

XRT Timeline to be uploaded on 2013/06/08

Period: 2013/06/08 11:09:00 - 2013/06/13 10:57:00

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Normal mode

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XOB #1998: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 60s cad With G-band and VLS closed												
Term	Pointing (x, y)		Comment									
06/08 11:22:00 - 06/08 17:23:30	Fixed (793.0, -513.0)		# OP start + 10min, West limb Flaring Region									
06/08 18:21:30 - 06/08 22:18:30	Fixed (793.0, -513.0)		# West Limb flaring AR cont.									
06/09 10:23:00 - 06/09 11:26:30	Track (437.3, 91.7) @ 06/09 10:20:00		# EIS observation of AR 11765									
06/10 10:58:00 - 06/10 12:03:30	Track (621.7, 90.9) @ 06/10 10:55:00		# EIS observation of AR 11765									
06/11 05:44:30 - 06/11 09:19:00	Track (739.1, 90.9) @ 06/11 05:41:30		# HOP 231 (Sunrise)									
PROG= 05 Inf.-time(s)												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 78 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Seqn= 62 4-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 63 52-time(s) 60.0sec												
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #199E: Synoptic Q95 2x2 - Al/mesh(12/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(33/362/1443) + G-b												
Term	Pointing (x, y)		Comment									
06/08 18:11:30 - 06/08 18:18:24	Fixed (0.0, 0.0)		synoptic, shifted 8.5 min									
06/09 05:59:00 - 06/09 06:05:54	Fixed (0.0, 0.0)		synoptic, shifted -4.0 min									
06/09 17:48:30 - 06/09 17:55:24	Fixed (0.0, 0.0)		synoptic, shifted -14.5 min									
06/10 06:11:30 - 06/10 06:15:24	Fixed (0.0, 0.0)		synoptic, shifted 5.5 min									
06/10 18:03:30 - 06/10 20:16:00	Fixed (0.0, 0.0)		synoptic, extended for SOT flat field, if Sunrise launch postponed									
06/11 05:34:30 - 06/11 05:41:24	Fixed (0.0, 0.0)		synoptic, shifted -28.5 min									
PROG= 16 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 82 1-time(s) 4.0sec												
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 83 1-time(s) 4.0sec												
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 66 1-time(s) 2.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
└─ Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1999: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 80s cad With G-band and VLS closed												
Term	Pointing (x, y)		Comment									
06/08 23:03:00 - 06/09 05:55:54	Track (-716.1, 160.6) @ 06/08 23:00:00		# East Limb AR.									
06/09 06:09:00 - 06/09 09:44:30	Track (-673.6, 160.0) @ 06/09 06:06:00		# Limb AR									
06/09 11:57:30 - 06/09 17:45:24	Track (-639.1, 159.5) @ 06/09 11:30:00		# Limb AR									
06/09 18:46:00 - 06/10 06:04:24	Track (-595.8, 158.9) @ 06/09 17:55:30		# Limb AR									
06/10 06:18:30 - 06/10 10:23:00	Track (-506.3, 157.6) @ 06/10 06:15:30		# Limb AR									
06/10 12:34:30 - 06/10 18:00:24	Track (-460.8, 157.0) @ 06/10 12:10:00		# Limb AR									
06/10 21:03:30 - 06/11 05:31:24	Track (-389.9, 156.1) @ 06/10 21:00:30		# Limb AR									
PROG= 11 Inf.-time(s)												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 78 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Seqn= 62 4-time(s) 2.0sec												

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 63 45-time(s) 80.0sec												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #199C: Flare Standard Obs. with eruptions mode-A (FW1=Open and VLS-Closed)

Term	Pointing (x, y)	Comment
06/08 11:22:00 - 06/08 17:23:30	Fixed (793.0, -513.0)	# OP start + 10min, West limb Flaring Region
06/08 18:21:30 - 06/08 22:18:30	Fixed (793.0, -513.0)	# West Limb flaring AR cont.
06/08 23:03:00 - 06/09 05:55:54	Track (-716.1, 160.6) ^{Ⓜ 06/08 23:00:00}	# East Limb AR.
06/09 06:09:00 - 06/09 09:44:30	Track (-673.6, 160.0) ^{Ⓜ 06/09 06:06:00}	# Limb AR
06/09 10:23:00 - 06/09 11:26:30	Track (437.3, 91.7) ^{Ⓜ 06/09 10:20:00}	# EIS observation of AR 11765
06/09 11:57:30 - 06/09 17:45:24	Track (-639.1, 159.5) ^{Ⓜ 06/09 11:30:00}	# Limb AR
06/09 18:46:00 - 06/10 06:04:24	Track (-595.8, 158.9) ^{Ⓜ 06/09 17:55:30}	# Limb AR
06/10 06:18:30 - 06/10 10:23:00	Track (-506.3, 157.6) ^{Ⓜ 06/10 06:15:30}	# Limb AR
06/10 10:58:00 - 06/10 12:03:30	Track (621.7, 90.9) ^{Ⓜ 06/10 10:55:00}	# EIS observation of AR 11765
06/10 12:34:30 - 06/10 18:00:24	Track (-460.8, 157.0) ^{Ⓜ 06/10 12:10:00}	# Limb AR
06/10 21:03:30 - 06/11 05:31:24	Track (-389.9, 156.1) ^{Ⓜ 06/10 21:00:30}	# Limb AR
06/11 05:44:30 - 06/11 09:19:00	Track (739.1, 90.9) ^{Ⓜ 06/11 05:41:30}	# HOP 231 (Sunrise)

PROG= 08 1-time(s)

Subr= 1 30-time(s) 20.0sec												
Seqn= 58 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 64 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 79 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 58 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 79 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 58 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 79 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 58 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4 24-time(s) 600.0sec												
Seqn= 61 1-time(s) 2.0sec												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec

Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

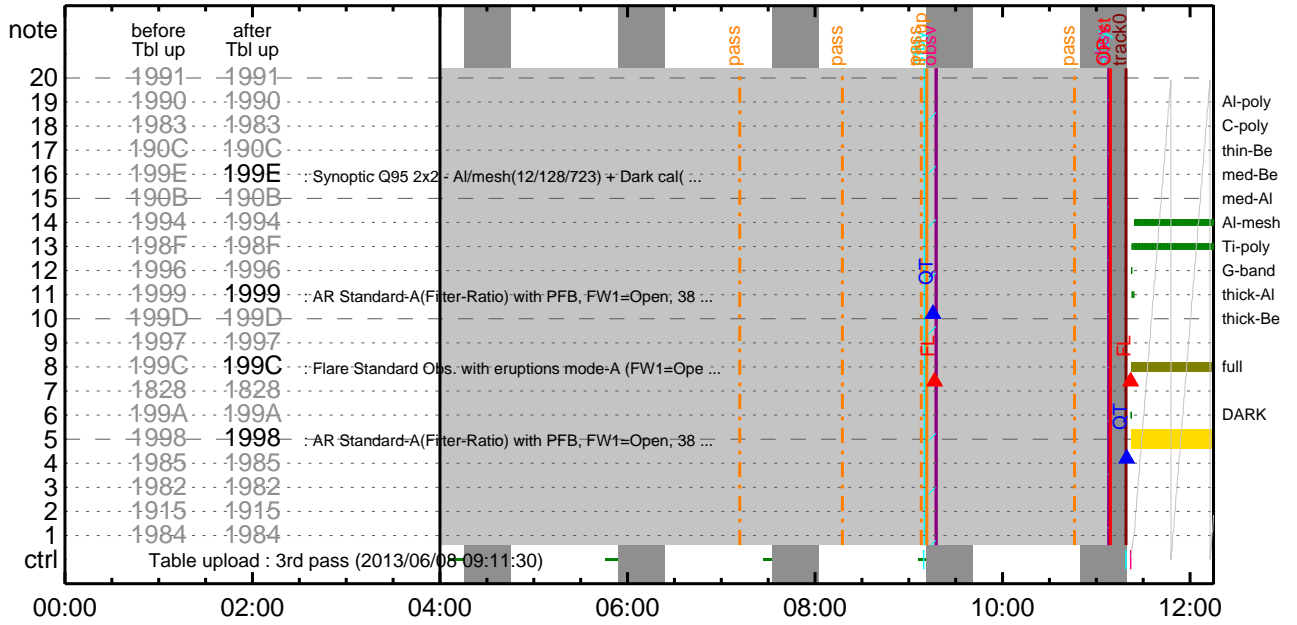
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Flare Detection

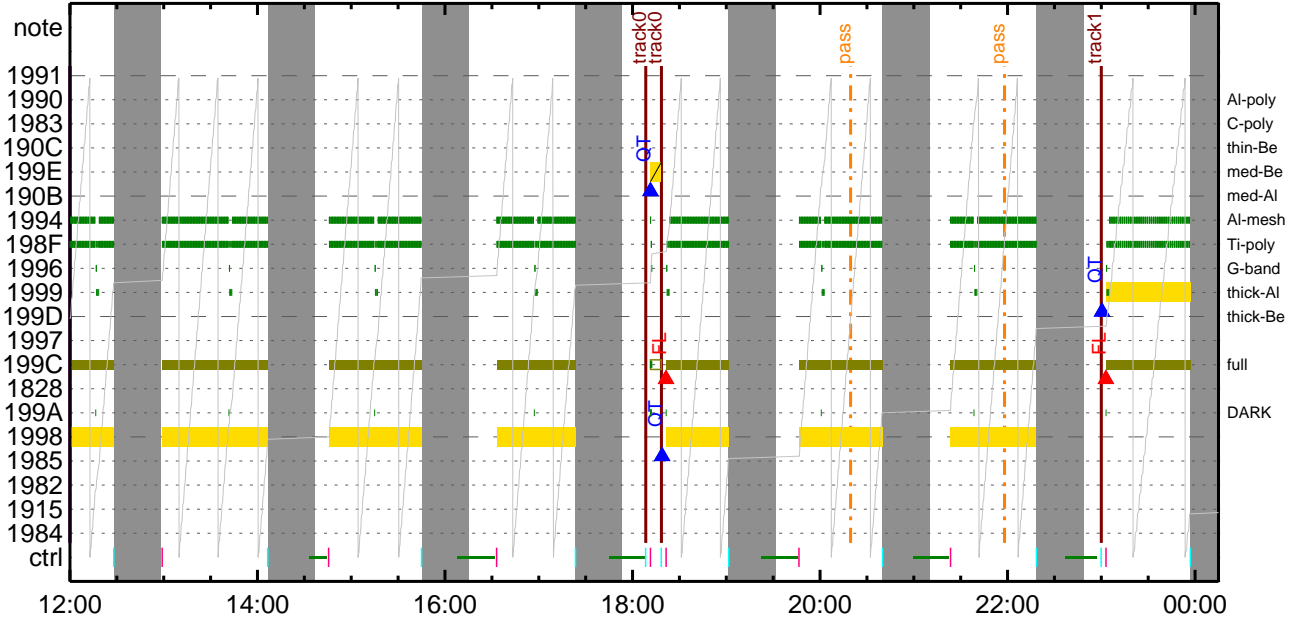
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FLD Patrol												
Term	Pointing (x, y)				Comment							
06/08 18:18:46 - 06/09 05:56:16	Fixed (793.0, -513.0)				# West Limb flaring AR cont.							
06/09 06:06:16 - 06/09 17:45:46	Track (-673.6, 160.0) @ 06/09 06:06:00				# Limb AR							
06/09 17:55:46 - 06/10 06:08:46	Track (-595.8, 158.9) @ 06/09 17:55:30				# Limb AR							
06/10 06:15:46 - 06/10 18:00:46	Track (-506.3, 157.6) @ 06/10 06:15:30				# Limb AR							
06/10 21:00:46 - 06/11 05:31:46	Track (-389.9, 156.1) @ 06/10 21:00:30				# Limb AR							
06/11 05:41:46 - 06/13 10:57:00	Track (739.1, 90.9) @ 06/11 05:41:30				# HOP 231 (Sunrise)							
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

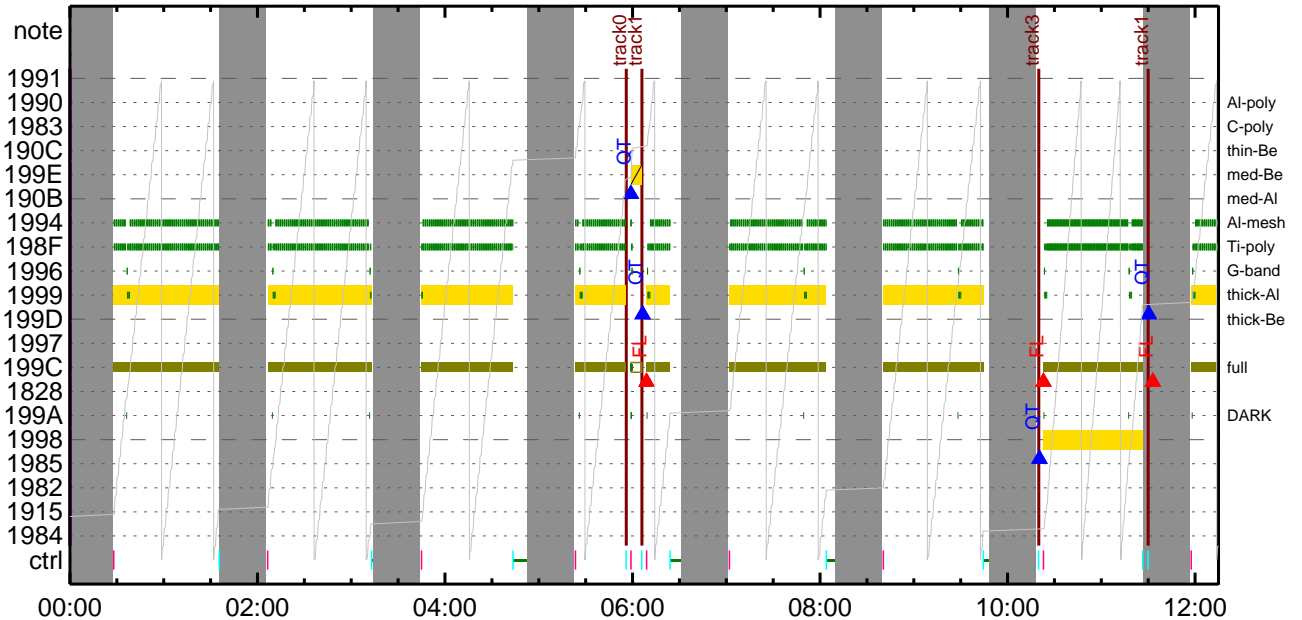
CMDI #0527 2013/06/08



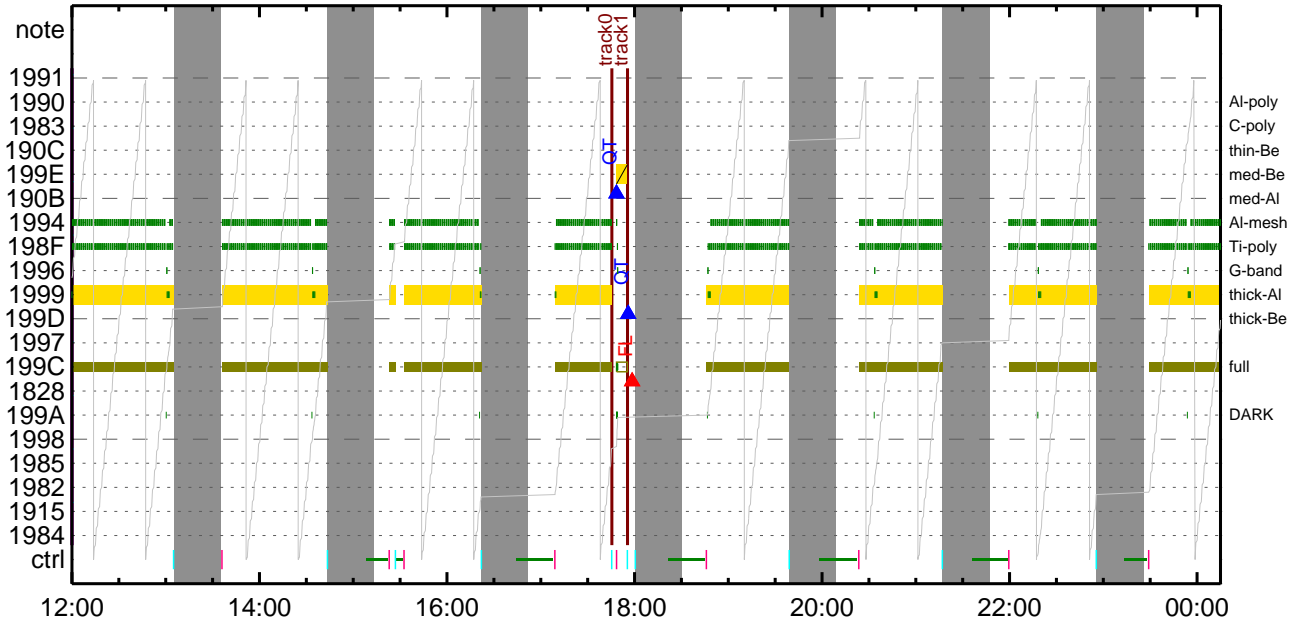
CMDI #0527 2013/06/08



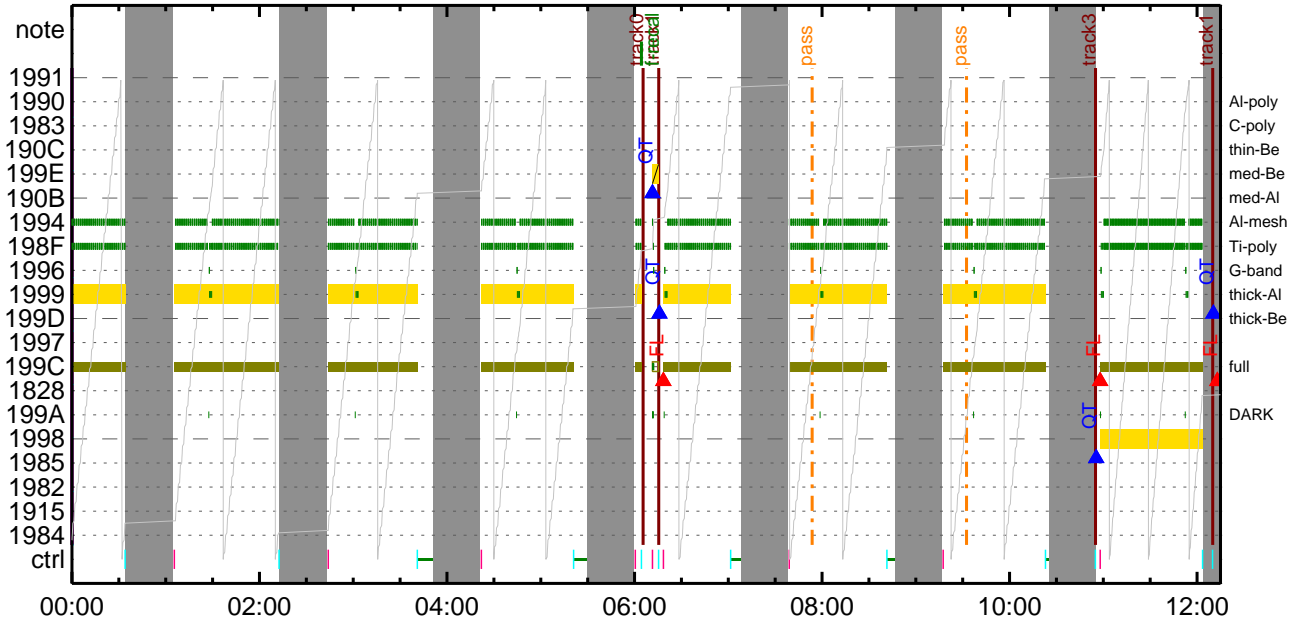
CMDI #0527 2013/06/09



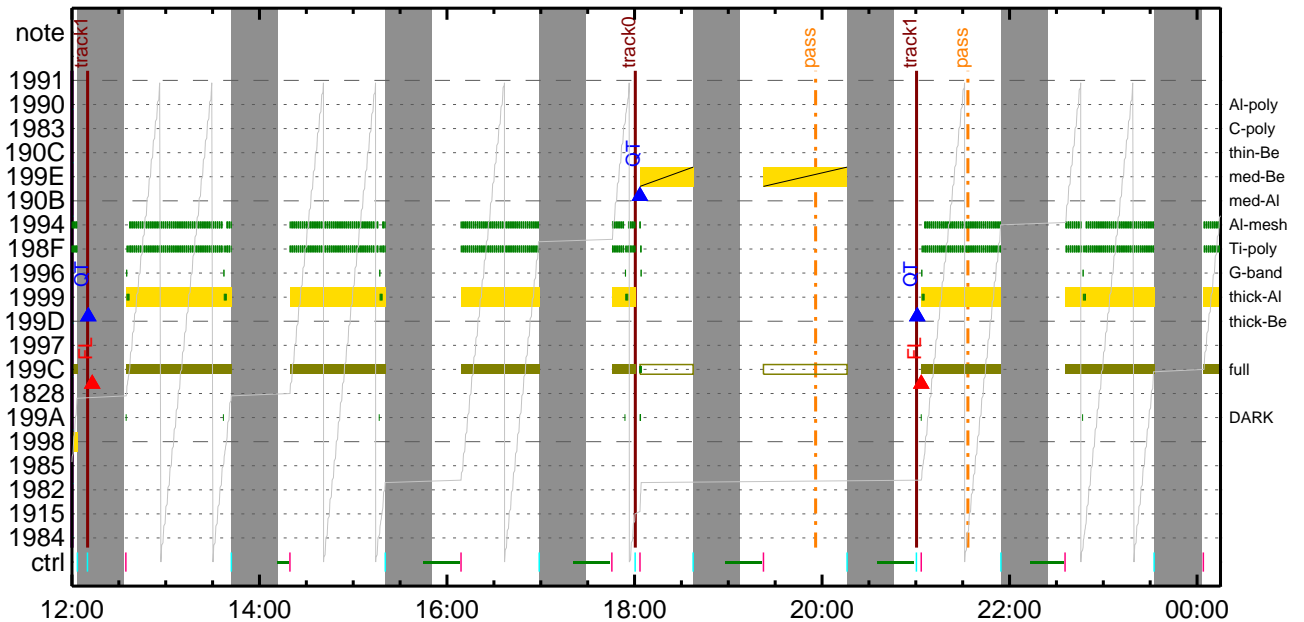
CMDI #0527 2013/06/09



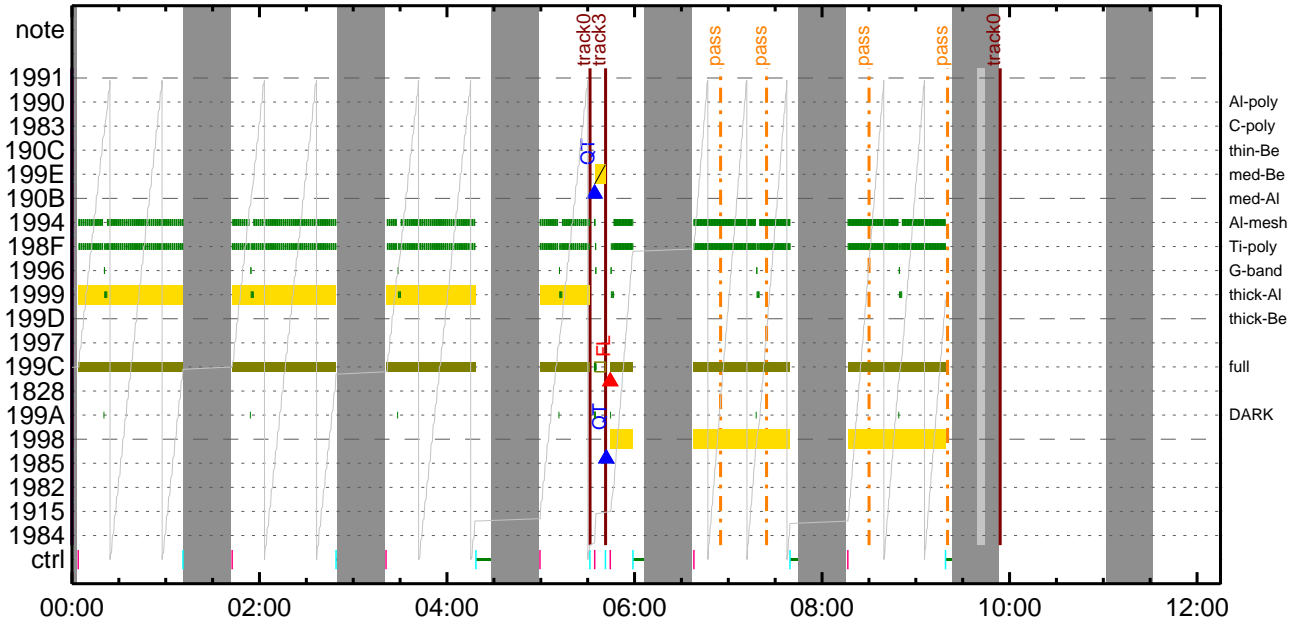
CMDI #0527 2013/06/10



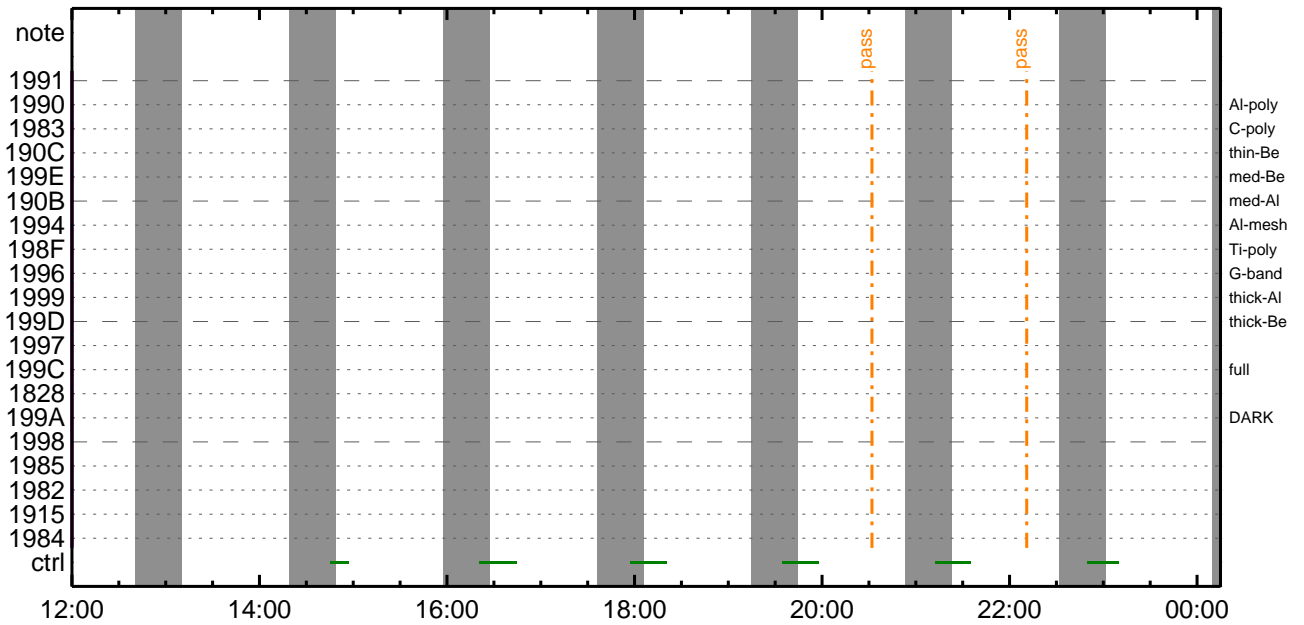
CMDI #0527 2013/06/10



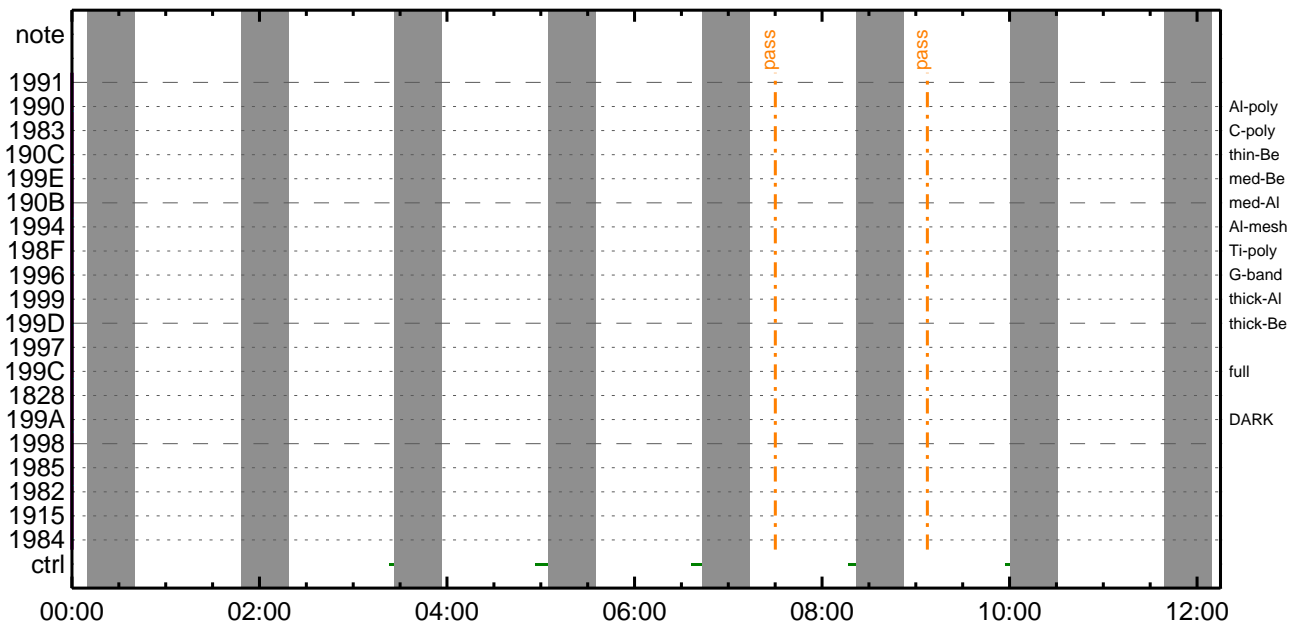
CMDI #0527 2013/06/11



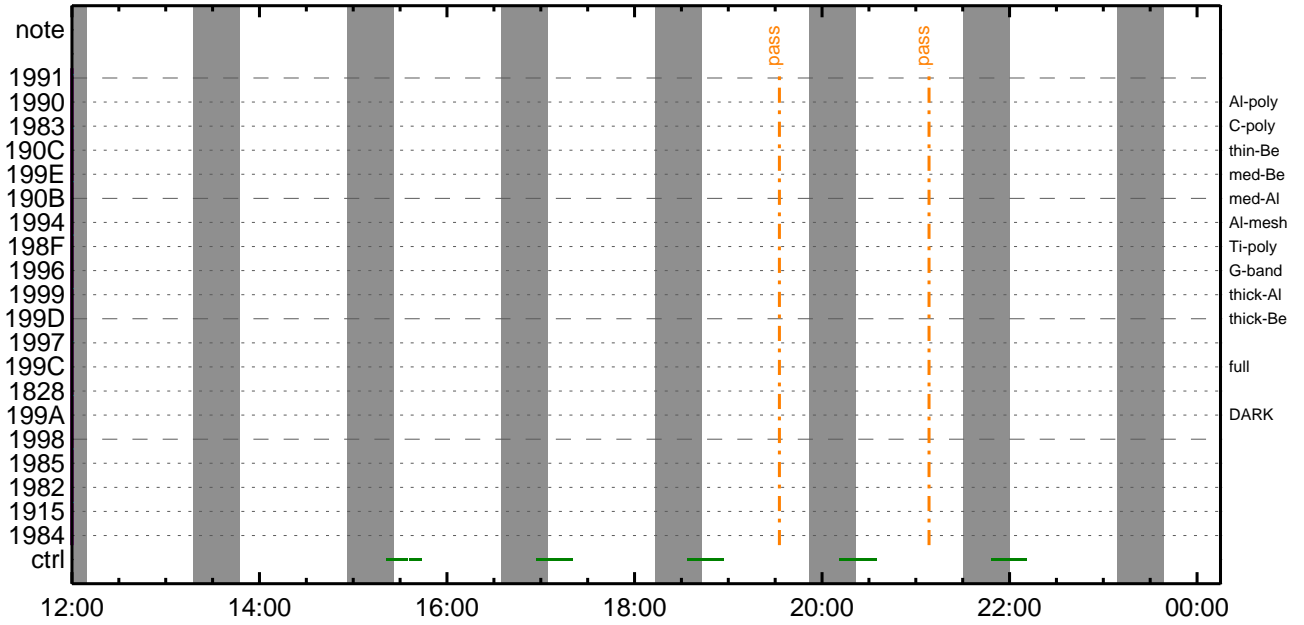
CMDI #0527 2013/06/11



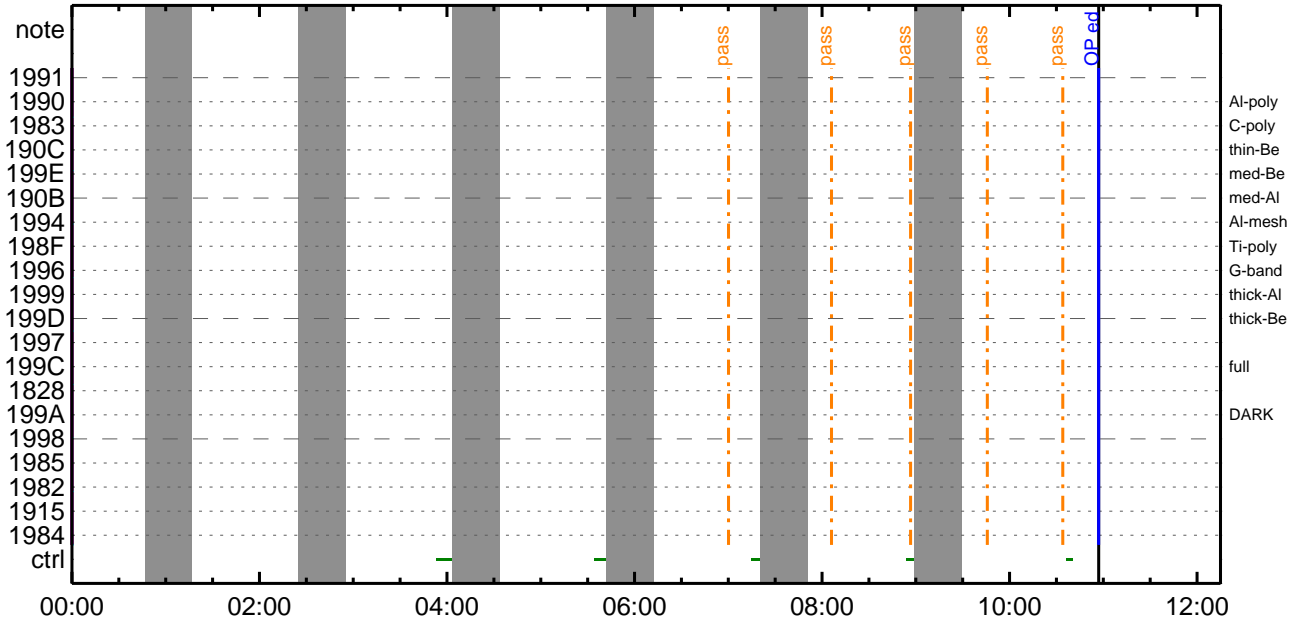
CMDI #0527 2013/06/12



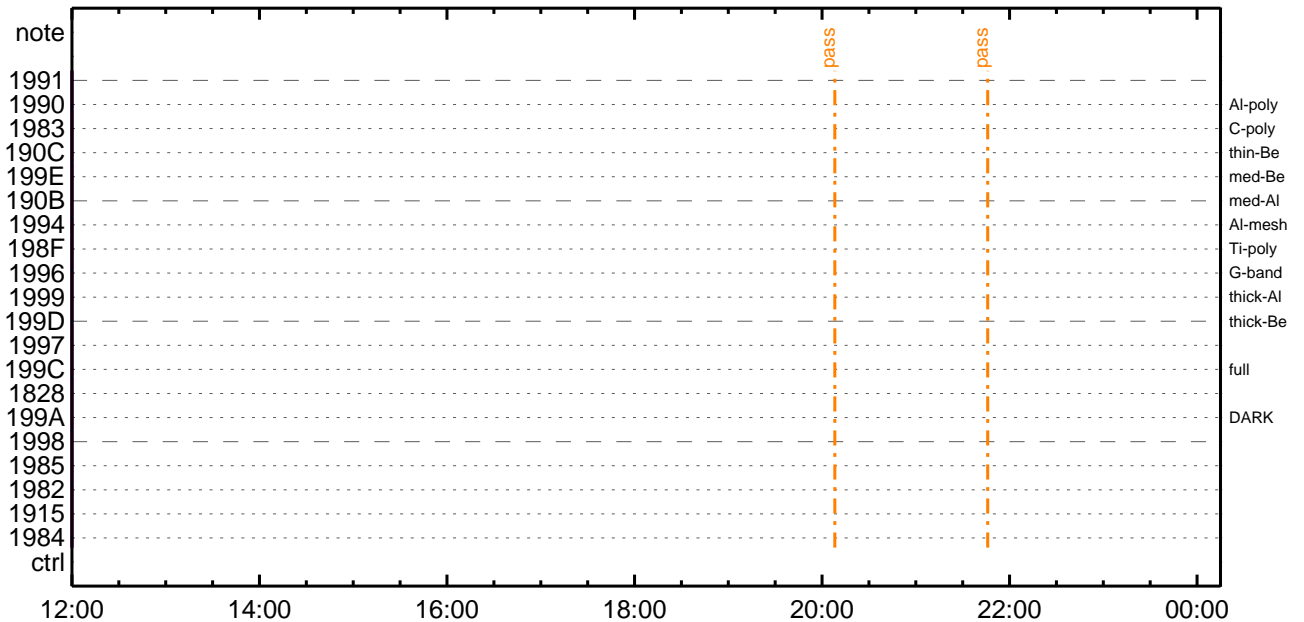
CMDI #0527 2013/06/12



CMDI #0527 2013/06/13



CMDI #0527 2013/06/13



(a) Spacecraft Operation Procedure (real-commands)

```
main-702 2013-06-08 15:18:06 205 33 SOLAR-B MAIN //
0001  C.
0002  . C. ***** AOS *****
0003  C.
0004  . C. ;ãAOSYÃY$YÃY-¼A»Û;ä
0005  C.
0006  C. YÃYB;¼Y³YFYöYÉA+¿©
0007  +. DC 00-00 NULL_DUMMY_CMD
0008  C.
0009  . C. ***** AOCs : Reload orbital element (send every contact) *****
0010  C.  Āí;Ë□¿□Ā□•μ°Ė»Ĭ×ĀÇ□ĬY□YÃY×Yí;¼YÉ;ËËë¼μ•íĒ;Ë□Ë¼°Ç□□•□¿¼Ĭ¹Ç□Ĭ;çĀ®, ù□¹□Ë□□□çĀ+¿©□•□Ē□□□³□Ë;f
0011  +. DC 02-8E AOCU_ORB_UPD
0012  C.
0013  C.
0014  . C. *****
0015  C. OP/OGYí;¼YÉ;|YÃYöYx
0016  C. *****
0017  C.
0018  . C. ;ãOP/OGYí;¼YÉ;ä
0019  . S. OP op-702:OP
0020  ()
0021  . S. OG og-702:OG
0022  ()
0023  C.
0024  . C. ;ãNMOG&OPĬ°ēYÃYöYx;ä
0025  C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027  BC (20 00 7f 01 02)
0028  C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029  C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030  C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031  C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032  C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033  +. DC 01-22 DHU_MODE_CHNG
0034  BC (07 0b f8)
0035  C. çç[HK1_PKT_FORM_NO] EQ 7
0036  C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037  C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038  C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039  C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040  . C. YÃYöYx¼ªª Ĭ»□□³ĬÇ§
0041  C. çç[HK1_DMP_CHK_FLG] EQ NON
0042  . C. RAM ID=NMOG□Ĭ¼Ē¹ç•ē²ĬOK□□³ĬÇ§
0043  C.
0044  C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046  BC (20 80 7f 01 02)
0047  C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048  C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049  C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050  C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051  C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052  +. DC 01-22 DHU_MODE_CHNG
0053  BC (07 0b f8)
0054  C. çç[HK1_PKT_FORM_NO] EQ 7
0055  C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056  C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057  C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058  C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059  . C. YÃYöYx¼ªª Ĭ»□□³ĬÇ§
0060  C. çç[HK1_DMP_CHK_FLG] EQ NON
0061  . C. RAM ID=NMOG□Ĭ¼Ē¹ç•ē²ĬOK□□³ĬÇ§
0062  C.
0063  C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065  BC (21 00 41 01 02)
0066  C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067  C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068  C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069  C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070  C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071  +. DC 01-22 DHU_MODE_CHNG
0072  BC (07 0b f8)
0073  C. çç[HK1_PKT_FORM_NO] EQ 7
0074  C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075  C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076  C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077  C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078  . C. YÃYöYx¼ªª Ĭ»□□³ĬÇ§
0079  C. çç[HK1_DMP_CHK_FLG] EQ NON
0080  . C. RAM ID=NMOG,RAM ID=OP□Ĭ¼Ē¹ç•ē²ĬOK□□³ĬÇ§
0081  C.
0082  . C. ***** °Ë²¼□Ĭ¼Ā´¶Ĭ°□ËË-□Ā+¿© (¼āμ-YÃYöYx¼ªªç□□đĀđĀç¼ªª □□¼Ĭ¹ç□ç□ā) *****
0083  C. DHUYã;¼YÉ;Ë¼Y¼; Yí;¼YÉ;Ë□đĬã□¹
0084  +. DC 01-22 DHU_MODE_CHNG
0085  BC (02 0a f8)
0086  C. çç[HK1_PKT_FORM_NO] EQ 2
0087  C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088  C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089  C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090  C.
0091  . C. *****
0092  C. TI-CMD SET (OPOG STOP/COPY/START)
0093  C. *****
0094  C.
0095  . C. NOTICE ;§ OPOG UPLOAD□-Ā+¿©NG□Ĭ¼Ĭ¹ç;ç°Ë²¼□ĬĬTI-CMDĀ+¿©□Ĭ¼ĬĀ¹Ĭ□□•□Ē□□□³□Ë;f
```



```
0096 C.                0302; SET EDUMPA 1E±°iYNY¹aÇ¹Öa|a³aE;f
0097 C.
0098 C. TTY³¥P¥ÖYÉaðÅDİ¿ (UT)
0099 +. TI 2013-06-08 11:04:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                00[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2013-06-08 11:04:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                00[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2013-06-08 11:04:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                00[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2013-06-08 11:08:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                00[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C. °E²¼aİÄè%îİÑaİYÁ¥§YÁY-¹àİÜ
0116 C.                00[HK1_TI_CMD_ENA/DIS]                EQ        ENA
0117 C.                00[HK1_TI_CMD_NUM]                EQ        4
0118 C.                00[HK1_NEXT_EXEC_PIM]                EQ        DHU
0119 C.                00[HK1_NEXT_EXEC_DC]                EQ        0xB3
0120 C.
0121 C. *****
0122 C. TIIİ°èYÁYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                00[HK1_DMP_TOP_ADRS_1]                EQ        07
0129 C.                00[HK1_DMP_TOP_ADRS_0]                EQ        2B
0130 C.                00[HK1_DMP_BLOCK_NUM]                EQ        3
0131 C.                00[HK1_DMP_REPEAT_NUM]                EQ        0
0132 C.                00[HK1_DMA_DMP_PIM]                EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                00[HK1_PKT_FORM_NO]                EQ        7
0136 C.                00[HK1_PKT_GEN_TIME]                EQ        0.25 s
0137 C.                00[HK1_S_TLM_BIT_RATE]                EQ        32k
0138 C.                00[HK1_X_TLM_BIT_RATE]                EQ        4M
0139 C.                00[HK1_DMP_CHK_FLG]                EQ        EXEC
0140 C.
0141 C. YÁYÖY×½¹İ»að³İÇ§
0142 C.                00[HK1_DMP_CHK_FLG]                EQ        NON
0143 C.
0144 C. RAM ID=TI_TBLaİ%È¹Ç•è²İOKað³İÇ§
0145 C.
0146 C. DHU¥â;¼YÉ;È¼Y¼,¥İ;¼YÈ;Èaðİāa¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                00[HK1_PKT_FORM_NO]                EQ        2
0150 C.                00[HK1_PKT_GEN_TIME]                EQ        0.5S
0151 C.                00[HK1_S_TLM_BIT_RATE]                EQ        32K
0152 C.                00[HK1_X_TLM_BIT_RATE]                EQ        4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2013-06-08 11:08:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 C.
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C.
0164 C. ***** SOT END *****
0165 C.
0166 C. ***** XRT START *****
0167 C. Execute, after the success of OP upload.
0168 +. TI 2013-06-08 11:08:00.0
0169 DC 07-F0 MDP_XRT_MODE_STBY
0170 BC (c3)
0171 C.                [ ] [HK1_TI_CMD_NUM]                EQ        1COUNTUP
0172 C.
0173 C. ***** XRT END *****
0174 C. Stop EIS observation and temporarily disable EIS mode changes
0175 C.
0176 C.
0177 C. ***** Start EIS operation (TI set) *****
0178 C. Execute, after the success of OP upload.
0179 C. Set EIS TI-commands
0180 +. TI 2013-06-08 11:08:30.0
0181 DC 07-FC EIS_MODE_MANU
0182 BC (21 02)
0183 +. TI 2013-06-08 11:08:40.0
0184 DC 07-FC EIS_MODE_CHG_DIS
0185 BC (22)
0186 C.                [ ] [HK1_TI_CMD_NUM]                EQ        2 COUNTUP
0187 C. ***** End EIS operation (TI set) *****
0188 C.
0189 C.
0190 C.
0191 C. ***** MDP ¼Äİaİ»ö¼YaÈÄa¹aèDCBC•x²è *****
0192 C. (%â°İYÖYÁYÉY¥P¥ÉYáYçYèaÈ¼¼a¼Ä»Üa¹aè)
0193 C. DC-BC dcbc-402:DCBC
```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOSŸÁŸSŸÃŸ-¼Â»Û;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. *****
0130 C. SOT table upload
0131 C. *****
0132 . C. < Stop FG table >
0133 +. DC 07-F0 MDP_FG_CTRL_MANU
0134 BC (51)
0135 . C. -----
0136 C. MDP_FG_CTRL_MODE = MANU [ ]
0137 C. -----
0138 C.
0139 . C. <Upload FG Observation Table>
0140 . S. RAM ram-269:MDP_OBS_F
0141 ( )
0142 C.
0143 . C. < Dump RAMID=MDP_OBS_F >
0144 +. DC 07-F0 MDP_DUMP_FGTBL
0145 BC (82 07 00 00 00 38 b8)
0146 C. -----
0147 C. MDP_OBS_F verify = OK/NG [ ]
0148 C. -----
0149 C.
0150 C. *****
0151 C. SOT TI command set
0152 C. *****
0153 C. Execute, after the success of TBL upload.
0154 +. TI 2013-06-08 11:08:18.0
0155 DC 07-F0 MDP_SOT_MODE_OBSV
0156 BC (40)
0157 . C. -----
0158 C. HKI_TI_CMD_NUM = 1 CNTUP [ ]
0159 C. -----
0160 C.
0161 C.
0162 . C. ***** MDP `úÃîï»ö%ÝðËÄð¹²³DCBC•x²è *****
0163 C. (%ã°îÿÓÿÄÿÈÿËÿËÿáÿçÿèè%¼ð¼Ä»Û¹²è)
0164 . S. DC-BC dcbc-402:DCBC
0165 (MDP_known_event)
0166 C.
0167 C.
0168 . C. ***** ¥Ðÿ¹•İ Daily±¿İñðË´Ø¹²³DCBC•x²è *****
0169 . S. DC-BC dcbc-153:DCBC
0170 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0171 C.
0172 C.
0173 . C. ¡ãLOS¥Á¥$ÿÃÿ-¼Ä»Û;ä
0174 C.
0175 . C. ***** LOS *****
0176 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-704 2013-06-08 15:18:06 118 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿øÀø•µ°È×ÁÇøíYçYÁY×Yí;¼YÉ;ÈÈè%µ•ííÉ;ÈøÈ¼°ÇÓø•ø¿¼í¹çøí;çÁ®, ùø¹øèøøøçÁ+¿®ø•øÈøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 80 80 20 20)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 08)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 08 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0f 80 80 06 06)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 10 80 80 08 08)
0054 + DC 07-F0 MDP_XRT_FLD_ENA
0055 BC (d8)
0056 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0057 BC (c8)
0058 + DC 07-F0 MDP_XRT_AEC_RESET
0059 BC (d0)
0060 + DC 07-F0 MDP_XRT_ARS_DIS
0061 BC (d5)
0062 + DC 07-F0 MDP_XRT_FLD_RESET
0063 BC (da)
0064 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0065 BC (c4 0b)
0066 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0067 BC (c5 08)
0068 . C. ----- Success Verify ? OK / NG ____
0069 C.
0070 C.
0071 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0072 C.
0073 +. DC 07-F0 MDP_XRT_MODE_OBSV
0074 BC (c2)
0075 +. TI 2013-06-08 11:08:02.0
0076 DC 07-F0 MDP_XRT_MODE_OBSV
0077 BC (c2)
0078 . C. ----- Success Verify ? OK / NG ____
0079 C.
0080 C. ***** XRT END *****
0081 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0082 +. DC 07-FC EIS_MODE_MANU
0083 BC (21 02)
0084 . C. Verify EIS in MANUAL mode
0085 . C. Estimated OBSTBL upload time is 36s
0086 C. *****
0087 C. EIS START OBSTBL LOAD
0088 C. *****
0089 . S. RAM ram-820:EIS_OBSTBL
0090 ( )
0091 +. DC 07-FC EIS_DUMP_OBSTBL
0092 BC (07 07 07 00 00 70 00)
0093 C.
0094 . C. Execute, after the success of OBSTBL upload.
0095 C. Set EIS TI-commands
```


(a) Spacecraft Operation Procedure (real-commands)

```
main-705 2013-06-08 15:18:06 29 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFYOYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿ªÁª•µ°Æ»Í×ÁÇªÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÍÉ;ÈÈÈ¼°ÇÔª•ª¿¼í¹çªÍ;çÀ®, ùª¹ªèªªçÁ+¿®ª•ªÈªªªªªªÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 . C. ***** MDP ´úÁÍªÍ»ò¼YªÈÁªª¹ªèDCBC•x²è *****
0016 C. (¼á°íYÓYÁYÈYªYÈYáYçYèªÈ¼ªª¼Á»Ûª¹ªè)
0017 . S. DC-BC dcbc-402:DCBC
0018 (MDP_known_event)
0019 C.
0020 C.
0021 . C. ***** YDŸ¹•İ Daily+¿ÍÑªÈ´Øª¹ªèDCBC•x²è *****
0022 . S. DC-BC dcbc-153:DCBC
0023 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0024 C.
0025 C.
0026 . C. ;ãLOSÁY$YÁY-¼Á»Û;ã
0027 C.
0028 . C. ***** LOS *****
0029 C.
```

Jun 08, 13 15:18

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*** OP Sequence for XRT ***

2013/06/08	11:18:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/06/08	11:18:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/06/08	11:19:00.0	AOCS_OrE-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 2d 99 b9 82		
2013/06/08	11:19:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/06/08	11:19:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/06/08	11:19:20.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/06/08	11:19:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/06/08	11:19:24.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/06/08	11:19:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2013/06/08	11:21:58.0	XRT_FL_PROG_SET_431_OG [0x1af]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08		
2013/06/08	11:22:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/06/08	12:28:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/06/08	12:28:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/06/08	12:28:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/06/08	12:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/06/08	12:58:00.0	XRT_Custom_430_OG [0x1ae]					
2013/06/08	12:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/06/08	14:06:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/06/08	14:06:32.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/06/08	14:06:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/06/08	14:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/06/08	14:44:30.0	XRT_Custom_430_OG [0x1ae]					
2013/06/08	14:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/06/08	15:45:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/06/08	15:45:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/06/08	15:45:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/06/08	15:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/06/08	16:32:00.0	XRT_Custom_430_OG [0x1ae]					
2013/06/08	16:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/06/08	17:23:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/06/08	17:23:32.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/06/08	17:23:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/06/08	17:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/06/08	18:08:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/06/08	18:08:26.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/06/08	18:08:30.0	AOCS_OrE-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2013/06/08	18:08:46.0	XRT_FLD_DIS_434_OG [0x1b2]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/06/08	18:11:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/06/08	18:11:26.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/06/08	18:11:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10		
2013/06/08	18:11:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/06/08	18:18:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/06/08	18:18:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/06/08	18:18:30.0	AOCS_OrE-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 2d 99 b9 82		
2013/06/08	18:18:46.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/06/08	18:18:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/06/08	18:18:50.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		

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2013/06/08	18:18:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/06/08	18:18:54.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/08	18:18:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2013/06/08	18:21:28.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2013/06/08	18:21:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/08	19:01:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/08	19:01:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/08	19:01:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/08	19:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/08	19:45:30.0	XRT_Custom_430_OG [0x1ae]							
2013/06/08	19:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/08	20:40:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/08	20:40:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/08	20:40:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/08	20:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/08	21:22:30.0	XRT_Custom_430_OG [0x1ae]							
2013/06/08	21:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/08	22:18:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/08	22:18:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/08	22:18:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/08	22:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/08	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/08	22:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/06/08	23:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01	00	00	00	00
2013/06/08	23:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/06/08	23:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/06/08	23:00:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/06/08	23:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/06/08	23:00:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/08	23:00:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2013/06/08	23:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2013/06/08	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/08	23:57:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/08	23:57:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/08	23:57:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/09	00:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/09	00:27:00.0	XRT_Custom_430_OG [0x1ae]							
2013/06/09	00:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/09	01:35:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/09	01:35:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/09	01:35:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/09	01:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/09	02:05:30.0	XRT_Custom_430_OG [0x1ae]							
2013/06/09	02:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/09	03:13:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/09	03:13:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/09	03:13:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/09	03:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/09	03:44:00.0	XRT_Custom_430_OG [0x1ae]							

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2013/06/09	03:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/09	04:43:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/09	04:43:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/09	04:43:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/09	04:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/09	05:22:30.0	XRT_Custom_430_OG [0x1ae]							
2013/06/09	05:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/09	05:55:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/09	05:55:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/06/09	05:56:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/06/09	05:56:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/06/09	05:58:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/06/09	05:58:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/06/09	05:58:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2013/06/09	05:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/09	06:05:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/09	06:05:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/06/09	06:06:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2013/06/09	06:06:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/06/09	06:06:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/06/09	06:06:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/06/09	06:06:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/06/09	06:06:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/09	06:06:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2013/06/09	06:08:58.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2013/06/09	06:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/09	06:24:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/09	06:24:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/09	06:24:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/09	06:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/09	07:01:00.0	XRT_Custom_430_OG [0x1ae]							
2013/06/09	07:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/09	08:04:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/09	08:04:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/09	08:04:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/09	08:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/09	08:39:30.0	XRT_Custom_430_OG [0x1ae]							
2013/06/09	08:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/09	09:44:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/09	09:44:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/09	09:44:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/09	09:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/09	10:19:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/09	10:19:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/06/09	10:20:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2013/06/09	10:20:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/06/09	10:20:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/06/09	10:20:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				

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2013/06/09	10:20:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/06/09	10:20:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/09	10:20:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2013/06/09	10:22:58.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2013/06/09	10:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/09	11:26:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/09	11:26:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/09	11:26:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/09	11:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/09	11:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/09	11:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/06/09	11:30:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2013/06/09	11:30:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/06/09	11:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/06/09	11:30:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/06/09	11:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/06/09	11:30:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/09	11:30:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2013/06/09	11:32:58.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2013/06/09	11:56:30.0	XRT_Custom_430_OG [0x1ae]							
2013/06/09	11:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/09	13:05:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/09	13:05:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/09	13:05:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/09	13:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/09	13:35:00.0	XRT_Custom_430_OG [0x1ae]							
2013/06/09	13:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/09	14:43:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/09	14:43:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/09	14:43:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/09	14:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/09	15:22:00.0	XRT_Custom_430_OG [0x1ae]							
2013/06/09	15:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/09	15:27:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/09	15:27:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/09	15:27:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/09	15:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/09	15:31:30.0	XRT_Custom_430_OG [0x1ae]							
2013/06/09	15:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/09	16:22:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/09	16:22:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/09	16:22:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/09	16:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/09	17:08:00.0	XRT_Custom_430_OG [0x1ae]							
2013/06/09	17:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/09	17:45:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/09	17:45:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/06/09	17:45:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/06/09	17:45:46.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				

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2013/06/09	17:48:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/06/09	17:48:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/06/09	17:48:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2013/06/09	17:48:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/09	17:55:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/09	17:55:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/06/09	17:55:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01	00	00	00	00
2013/06/09	17:55:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/06/09	17:55:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/06/09	17:55:50.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/06/09	17:55:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/06/09	17:55:54.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/09	17:55:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2013/06/09	17:58:28.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2013/06/09	18:00:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/09	18:00:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/09	18:00:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/09	18:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/09	18:45:00.0	XRT_Custom_430_OG [0x1ae]							
2013/06/09	18:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/09	19:39:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/09	19:39:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/09	19:39:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/09	19:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/09	20:22:30.0	XRT_Custom_430_OG [0x1ae]							
2013/06/09	20:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/09	21:17:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/09	21:17:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/09	21:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/09	21:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/09	21:58:30.0	XRT_Custom_430_OG [0x1ae]							
2013/06/09	21:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/09	22:55:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/09	22:55:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/09	22:55:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/09	22:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/09	23:28:00.0	XRT_Custom_430_OG [0x1ae]							
2013/06/09	23:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/10	00:34:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/10	00:34:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/10	00:34:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/10	00:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/10	01:04:30.0	XRT_Custom_430_OG [0x1ae]							
2013/06/10	01:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/10	02:12:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/10	02:12:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/10	02:12:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/10	02:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/10	02:43:00.0	XRT_Custom_430_OG [0x1ae]							
2013/06/10	02:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							

2013/06/10	03:41:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0					
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/06/10	03:41:02.0	XRT_FLD_RESET_415_OG [0x19f]									
			MDP_XRT_FLD_RESET	1	07-F0	da					
2013/06/10	03:41:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]									
			MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/06/10	03:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]									
			MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/06/10	04:21:00.0	XRT_Custom_430_OG [0x1ae]									
2013/06/10	04:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]									
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/06/10	05:21:00.0	XRT_CTRL_MANU_400_OG [0x190]									
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/06/10	05:21:02.0	XRT_FLD_RESET_415_OG [0x19f]									
			MDP_XRT_FLD_RESET	1	07-F0	da					
2013/06/10	05:21:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]									
			MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/06/10	05:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]									
			MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/06/10	05:59:30.0	XRT_Custom_430_OG [0x1ae]									
2013/06/10	06:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]									
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/06/10	06:04:24.0	XRT_CTRL_MANU_402_OG [0x192]									
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/06/10	06:04:26.0	XRT_FOCUS_RECALIBRATE_433_OG [0x1b1]									
			XRT_FOCUS_RECAL	2	07-F8	78	00				
2013/06/10	06:05:30.0	AOCS_Ore-point_Start_2_OG [0x098]									
			AOCU_NM	5	02-76	00	00	00	00	00	00
2013/06/10	06:08:26.0	XRT_FOCUS_POSITION_403_OG [0x193]									
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00		
2013/06/10	06:08:46.0	XRT_FLD_DIS_434_OG [0x1b2]									
			MDP_XRT_FLD_DIS	1	07-F0	d9					
2013/06/10	06:11:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]									
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2013/06/10	06:11:26.0	XRT_ARS_DIS_423_OG [0x1a7]									
			MDP_XRT_ARS_DIS	1	07-F0	d5					
2013/06/10	06:11:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]									
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	10				
2013/06/10	06:11:30.0	XRT_CTRL_AUTO_408_OG [0x198]									
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/06/10	06:15:24.0	XRT_CTRL_MANU_402_OG [0x192]									
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/06/10	06:15:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]									
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00		
2013/06/10	06:15:30.0	AOCS_Ore-point_Start_3_OG [0x099]									
			AOCU_NM	5	02-76	01	00	00	00	00	00
2013/06/10	06:15:46.0	XRT_FLD_ENA_411_OG [0x19b]									
			MDP_XRT_FLD_ENA	1	07-F0	d8					
2013/06/10	06:15:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]									
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2013/06/10	06:15:50.0	XRT_AEC_RESET_413_OG [0x19d]									
			MDP_XRT_AEC_RESET	1	07-F0	d0					
2013/06/10	06:15:52.0	XRT_ARS_DIS_423_OG [0x1a7]									
			MDP_XRT_ARS_DIS	1	07-F0	d5					
2013/06/10	06:15:54.0	XRT_FLD_RESET_415_OG [0x19f]									
			MDP_XRT_FLD_RESET	1	07-F0	da					
2013/06/10	06:15:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]									
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b				
2013/06/10	06:18:28.0	XRT_FL_PROG_SET_431_OG [0x1af]									
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	08				
2013/06/10	06:18:30.0	XRT_CTRL_AUTO_408_OG [0x198]									
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/06/10	07:01:30.5	XRT_CTRL_MANU_400_OG [0x190]									
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/06/10	07:01:32.5	XRT_FLD_RESET_415_OG [0x19f]									
			MDP_XRT_FLD_RESET	1	07-F0	da					
2013/06/10	07:01:34.5	XRT_PREFLR_STRT_418_OG [0x1a2]									
			MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/06/10	07:04:44.5	XRT_PREFLR_STOP_419_OG [0x1a3]									
			MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/06/10	07:38:00.0	XRT_Custom_430_OG [0x1ae]									
2013/06/10	07:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]									
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/06/10	08:41:30.0	XRT_CTRL_MANU_400_OG [0x190]									
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/06/10	08:41:32.0	XRT_FLD_RESET_415_OG [0x19f]									
			MDP_XRT_FLD_RESET	1	07-F0	da					
2013/06/10	08:41:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]									
			MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/06/10	08:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]									
			MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/06/10	09:16:30.0	XRT_Custom_430_OG [0x1ae]									
2013/06/10	09:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]									
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/06/10	10:23:00.0	XRT_CTRL_MANU_400_OG [0x190]									
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/06/10	10:23:02.0	XRT_FLD_RESET_415_OG [0x19f]									
			MDP_XRT_FLD_RESET	1	07-F0	da					
2013/06/10	10:23:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]									
			MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/06/10	10:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]									
			MDP_XRT_PREFLR_STOP	1	07-F0	e9					

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2013/06/10	10:54:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/06/10	10:54:56.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/06/10	10:55:00.0	AOCS_Or-e-point_Start_4_OG [0x09a] AOCU_NM	5	02-76	03 00 00 00 00
2013/06/10	10:55:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2013/06/10	10:55:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/06/10	10:55:20.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0	d0
2013/06/10	10:55:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2013/06/10	10:55:24.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2013/06/10	10:55:26.0	XRT_QT_PROG_SET_440_OG [0x1b8] MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2013/06/10	10:57:58.0	XRT_FL_PROG_SET_431_OG [0x1af] MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2013/06/10	10:58:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/06/10	12:03:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/06/10	12:03:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2013/06/10	12:03:34.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/06/10	12:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/06/10	12:09:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/06/10	12:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/06/10	12:10:00.0	AOCS_Or-e-point_Start_3_OG [0x099] AOCU_NM	5	02-76	01 00 00 00 00
2013/06/10	12:10:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2013/06/10	12:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/06/10	12:10:20.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0	d0
2013/06/10	12:10:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2013/06/10	12:10:24.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2013/06/10	12:10:26.0	XRT_QT_PROG_SET_422_OG [0x1a6] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2013/06/10	12:12:58.0	XRT_FL_PROG_SET_431_OG [0x1af] MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2013/06/10	12:33:30.0	XRT_Custom_430_OG [0x1ae]			
2013/06/10	12:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/06/10	13:42:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/06/10	13:42:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2013/06/10	13:42:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/06/10	13:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/06/10	14:18:30.0	XRT_Custom_430_OG [0x1ae]			
2013/06/10	14:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/06/10	15:20:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/06/10	15:20:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2013/06/10	15:20:34.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/06/10	15:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/06/10	16:08:00.0	XRT_Custom_430_OG [0x1ae]			
2013/06/10	16:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/06/10	16:59:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/06/10	16:59:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2013/06/10	16:59:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/06/10	17:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/06/10	17:44:30.0	XRT_Custom_430_OG [0x1ae]			
2013/06/10	17:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/06/10	18:00:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/06/10	18:00:26.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/06/10	18:00:30.0	AOCS_Or-e-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00 00
2013/06/10	18:00:46.0	XRT_FLD_DIS_434_OG [0x1b2]			

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2013/06/10	18:03:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/06/10	18:03:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/06/10	18:03:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2013/06/10	18:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/10	18:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/10	18:37:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/10	18:37:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/10	18:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/10	19:21:30.0	XRT_Custom_430_OG [0x1ae]								
2013/06/10	19:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/10	20:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/10	20:16:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/10	20:16:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/10	20:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/10	21:00:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/10	21:00:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/06/10	21:00:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01	00	00	00	00
2013/06/10	21:00:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/06/10	21:00:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/06/10	21:00:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/06/10	21:00:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/06/10	21:00:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/10	21:00:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2013/06/10	21:03:28.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2013/06/10	21:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/10	21:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/10	21:54:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/10	21:54:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/10	21:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/10	22:34:30.0	XRT_Custom_430_OG [0x1ae]								
2013/06/10	22:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/10	23:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/10	23:32:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/10	23:32:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/10	23:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/11	00:03:00.0	XRT_Custom_430_OG [0x1ae]								
2013/06/11	00:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/11	01:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/11	01:11:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/11	01:11:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/11	01:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/11	01:41:30.0	XRT_Custom_430_OG [0x1ae]								
2013/06/11	01:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/06/11	02:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/06/11	02:49:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/06/11	02:49:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/06/11	02:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/06/11	03:20:00.0	XRT_Custom_430_OG [0x1ae]								
2013/06/11	03:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								

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2013/06/11	04:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/06/11	04:18:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/06/11	04:18:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/06/11	04:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/06/11	04:58:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/06/11	04:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2013/06/11	05:31:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/06/11	05:31:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/06/11	05:31:30.5	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/06/11	05:31:46.0	XRT_FLD_DIS_434_OG [0x1b2]	AOCU_NM	5	02-76	00 00 00 00 00		
2013/06/11	05:34:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/06/11	05:34:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/06/11	05:34:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/06/11	05:34:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10		
2013/06/11	05:41:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/06/11	05:41:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/06/11	05:41:30.0	AOCS_Ore-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/06/11	05:41:46.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00		
2013/06/11	05:41:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/06/11	05:41:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/06/11	05:41:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/06/11	05:41:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/06/11	05:41:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/06/11	05:44:28.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2013/06/11	05:44:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08		
2013/06/11	05:59:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/06/11	05:59:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/06/11	05:59:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/06/11	06:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/06/11	06:37:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/06/11	06:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2013/06/11	07:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/06/11	07:39:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/06/11	07:39:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/06/11	07:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/06/11	08:15:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/06/11	08:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2013/06/11	09:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/06/11	09:19:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/06/11	09:19:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/06/11	09:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/06/11	09:54:00.0	AOCS_Ore-point_Start_2_OG [0x098]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
		AOCU_NM	XRT_CTRL_AUTO_424_OG [0x098]	5	02-76	00 00 00 00 00		