

# XRT Timeline to be uploaded on 2013/07/04

Period: 2013/07/04 10:48:00 - 2013/07/09 11:07:00

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Normal mode

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XOB #1999: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 80s cad With G-band and VLS closed												
Term	Pointing (x, y)						Comment					
07/04 14:55:30 - 07/04 15:22:30	Fixed ( 855.0, 290.0)						AR11781 on W-limb					
<b>PROG= 11 Inf.-time(s)</b>												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 78 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Seqn= 62 4-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 63 45-time(s) 80.0sec												
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #199E: Synoptic Q95 2x2 - Al/mesh(12/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(33/362/1443) + G-b												
Term	Pointing (x, y)						Comment					
07/04 21:35:30 - 07/04 21:42:24	Fixed ( 0.0, 0.0)						synoptic, shifted manually					
07/05 05:37:00 - 07/05 05:43:54	Fixed ( 0.0, 0.0)						synoptic, shifted -26.0 min					
07/05 18:26:30 - 07/05 19:16:30	Fixed ( 0.0, 0.0)						Extended synoptic, shifted 23.5 min + SOT SP flat field					
07/06 05:58:30 - 07/06 06:05:24	Fixed ( 0.0, 0.0)						synoptic, shifted -4.5 min					
<b>PROG= 16 1-time(s)</b>												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 82 1-time(s) 4.0sec												
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 83 1-time(s) 4.0sec												
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 66 1-time(s) 2.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
└─ Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1998: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 60s cad With G-band and VLS closed												
Term	Pointing (x, y)						Comment					
07/04 21:45:30 - 07/05 05:33:54	Track ( -623.9, -236.1) <sup>Ⓜ 07/04 21:42:30</sup>						AR11785					
07/05 05:47:00 - 07/05 08:59:54	Track ( -569.3, -239.2) <sup>Ⓜ 07/05 05:44:00</sup>						AR 11785					
07/05 09:03:00 - 07/05 17:38:00	Track ( -546.1, -240.4) <sup>Ⓜ 07/05 09:00:00</sup>						AR11785 for update					
07/05 21:38:31 - 07/06 05:55:24	Track ( -453.4, -244.8) <sup>Ⓜ 07/05 21:23:30</sup>						Cont.					
07/06 06:08:30 - 07/06 10:00:00	Track ( -384.2, -247.4) <sup>Ⓜ 07/06 06:05:30</sup>						Cont.					
<b>PROG= 05 Inf.-time(s)</b>												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 78 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Seqn= 62 4-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 63 52-time(s) 60.0sec												
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec

Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Flare mode

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XOB #199C: Flare Standard Obs. with eruptions mode-A (FW1=Open and VLS-Closed)												
Term	Pointing (x, y)							Comment				
07/04 14:55:30 - 07/04 15:22:30	Fixed ( 855.0, 290.0)							AR11781 on W-limb				
07/04 21:45:30 - 07/05 05:33:54	Track ( -623.9, -236.1) <sup>Ⓜ</sup> 07/04 21:42:30							AR11785				
07/05 05:47:00 - 07/05 08:59:54	Track ( -569.3, -239.2) <sup>Ⓜ</sup> 07/05 05:44:00							AR 11785				
07/05 09:03:00 - 07/05 17:38:00	Track ( -546.1, -240.4) <sup>Ⓜ</sup> 07/05 09:00:00							AR11785 for update				
07/05 21:38:31 - 07/06 05:55:24	Track ( -453.4, -244.8) <sup>Ⓜ</sup> 07/05 21:23:30							Cont.				
07/06 06:08:30 - 07/06 10:00:00	Track ( -384.2, -247.4) <sup>Ⓜ</sup> 07/06 06:05:30							Cont.				
<b>PROG= 08 1-time(s)</b>												
<b>Subr= 1 30-time(s) 20.0sec</b>												
<b>Seqn= 58 1-time(s) 2.0sec</b>												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 64 1-time(s) 2.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 79 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 3 30-time(s) 60.0sec</b>												
<b>Seqn= 58 1-time(s) 2.0sec</b>												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 60 1-time(s) 2.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 79 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 3 30-time(s) 60.0sec</b>												
<b>Seqn= 58 1-time(s) 2.0sec</b>												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 60 1-time(s) 2.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 79 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 3 30-time(s) 60.0sec</b>												
<b>Seqn= 58 1-time(s) 2.0sec</b>												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 60 1-time(s) 2.0sec</b>												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 4 24-time(s) 600.0sec</b>												
<b>Seqn= 61 1-time(s) 2.0sec</b>												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

\* \* \* \* \*

NOT USED

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### Flare Detection

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FLD Patrol												
Term	Pointing (x, y)							Comment				

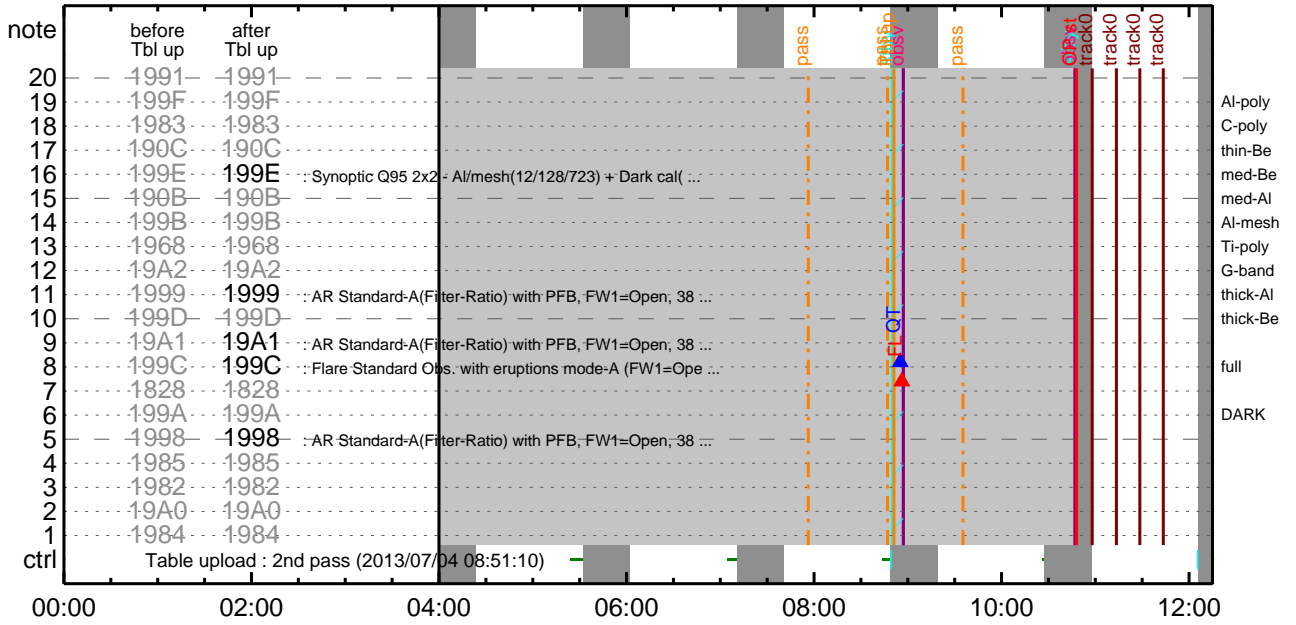
07/04 21:42:46 - 07/05 05:34:16 Track ( -623.9, -236.1) @ 07/04 21:42:30 AR11785  
 07/05 05:44:16 - 07/05 18:23:46 Track ( -569.3, -239.2) @ 07/05 05:44:00 AR 11785  
 07/05 21:23:46 - 07/06 05:55:46 Track ( -453.4, -244.8) @ 07/05 21:23:30 Cont.  
 07/06 06:05:46 - 07/09 11:07:00 Track ( -384.2, -247.4) @ 07/06 06:05:30 Cont.

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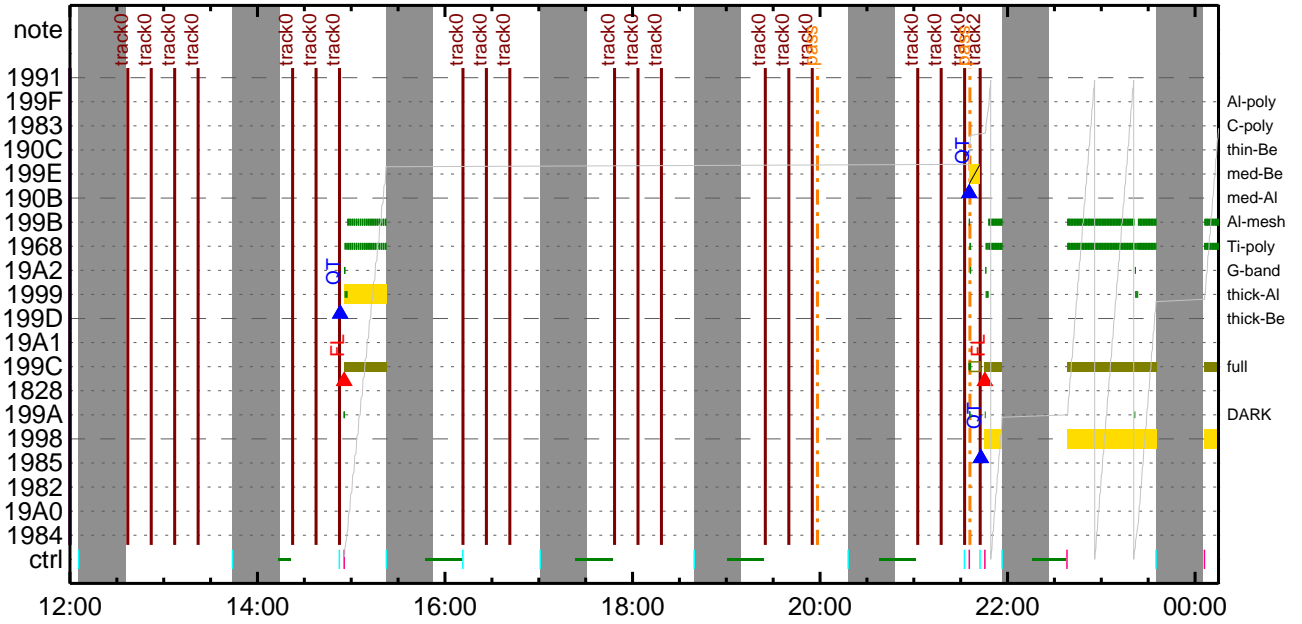
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

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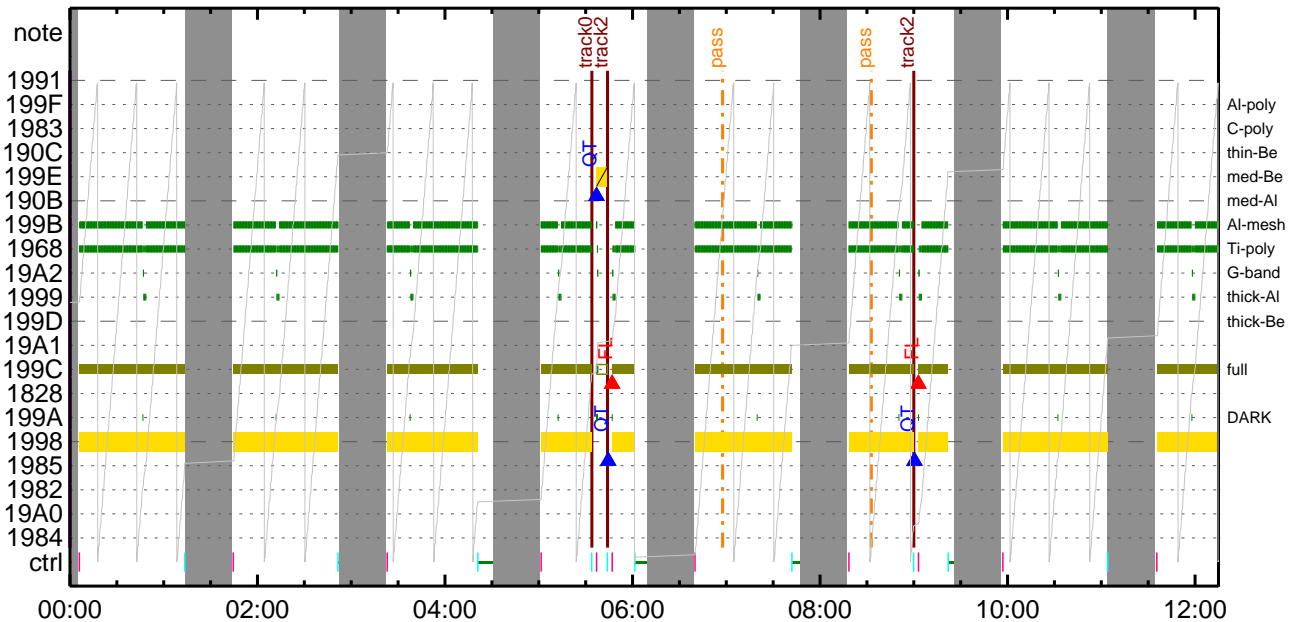
### CMDI #0579 2013/07/04



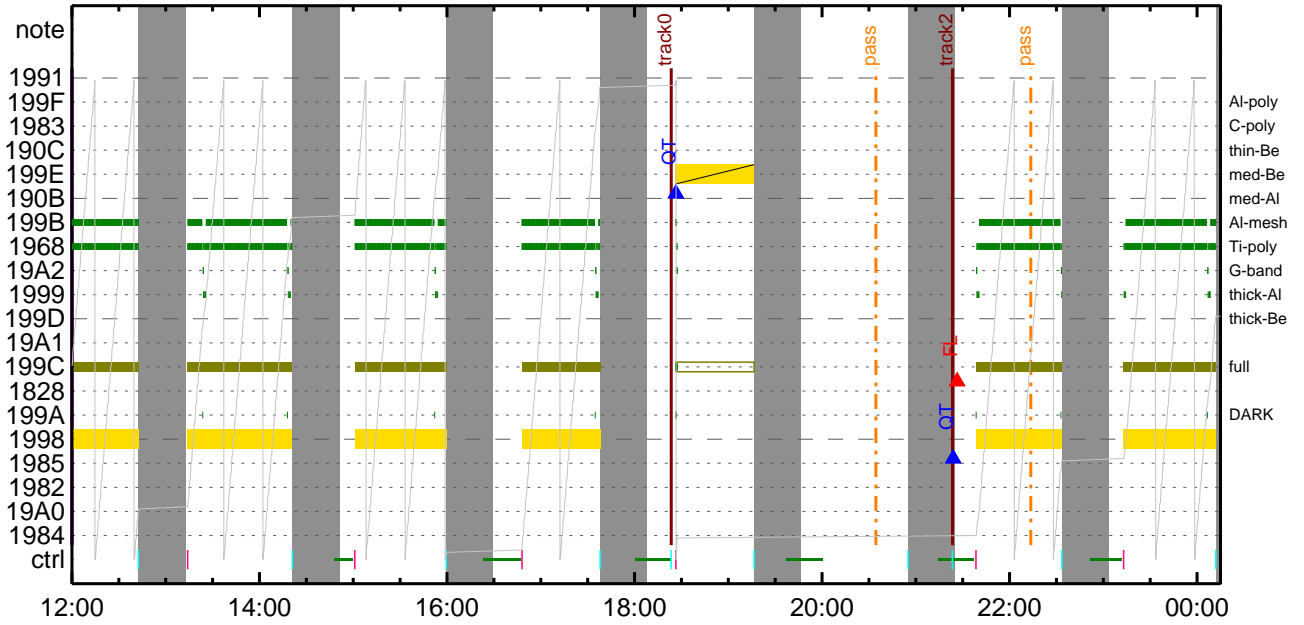
### CMDI #0579 2013/07/04



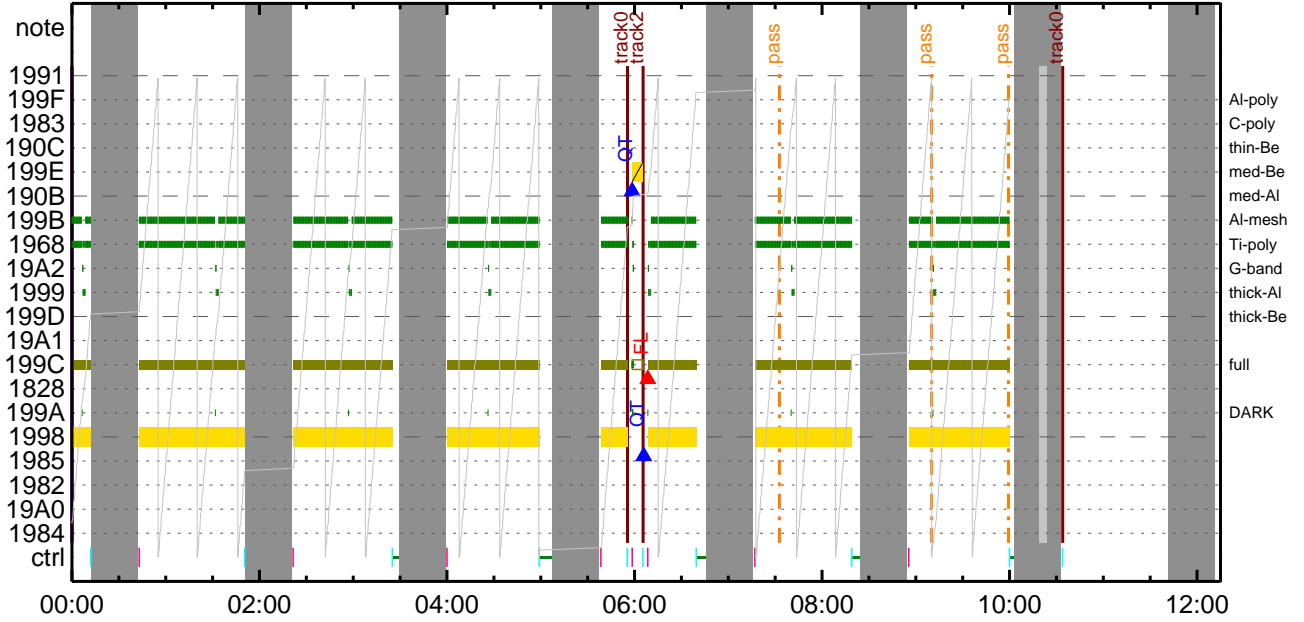
### CMDI #0579 2013/07/05



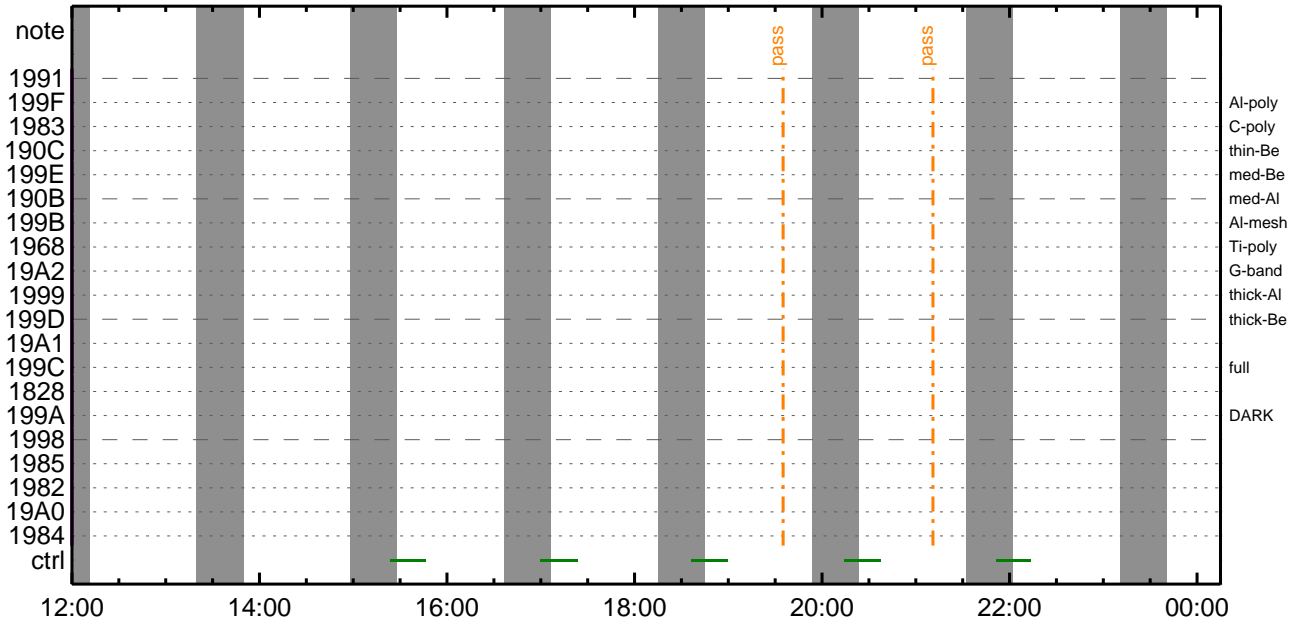
CMDI #0579 2013/07/05



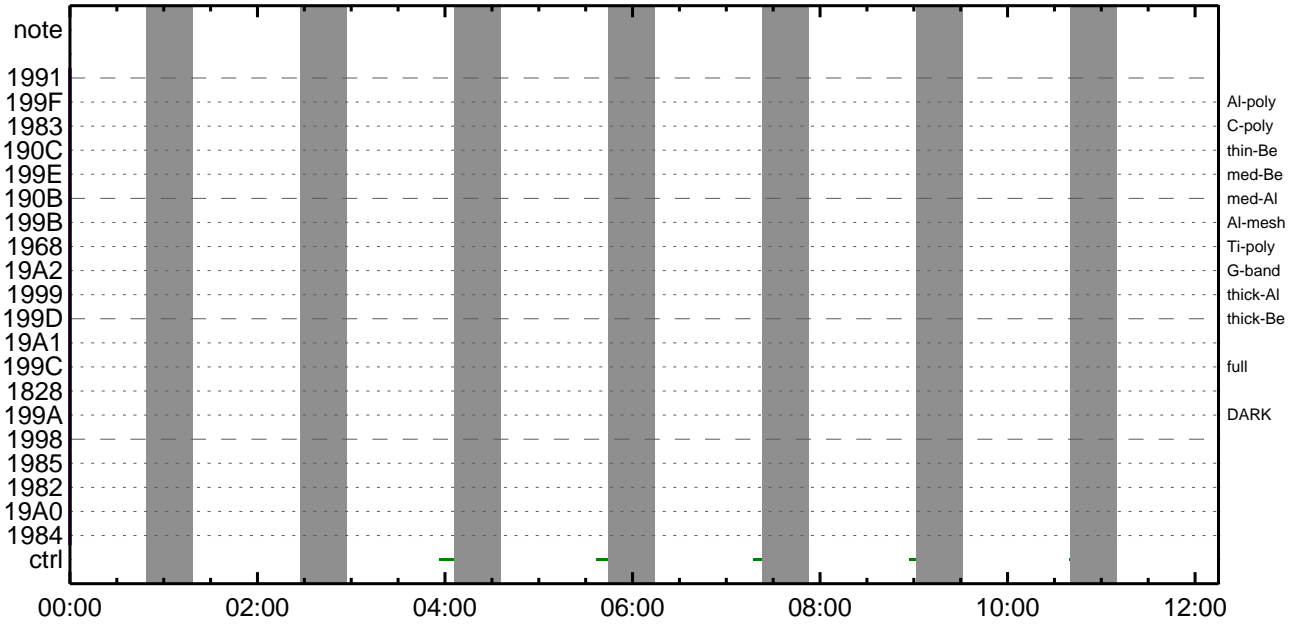
CMDI #0579 2013/07/06



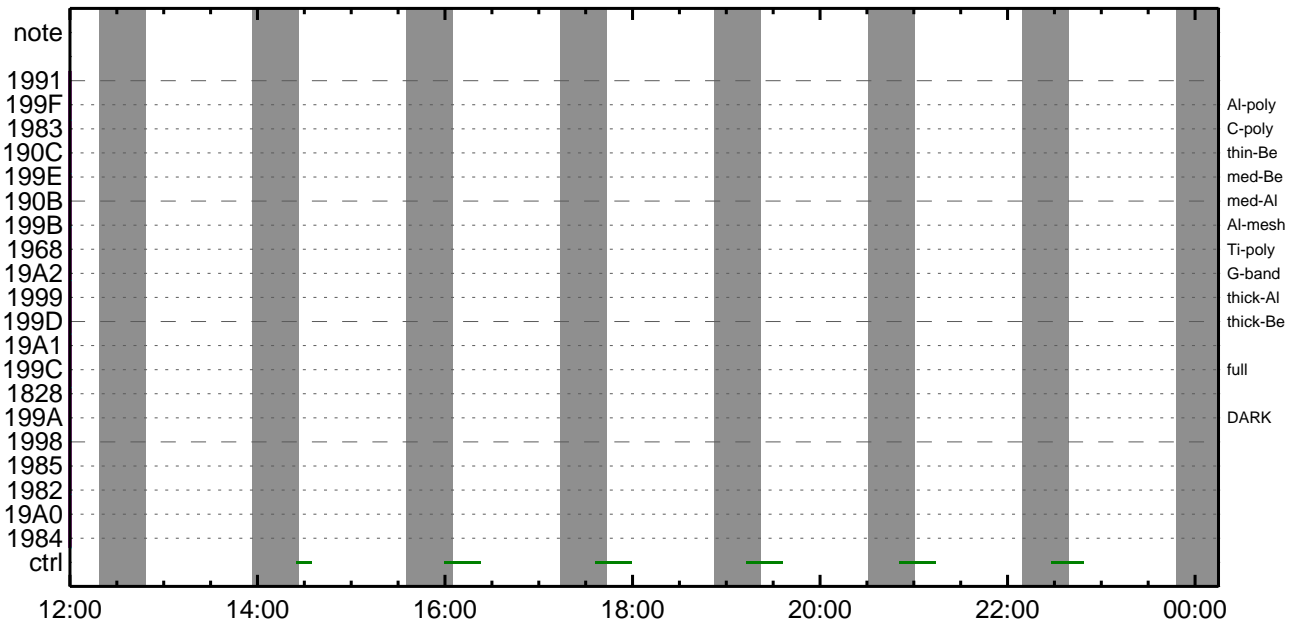
CMDI #0579 2013/07/06



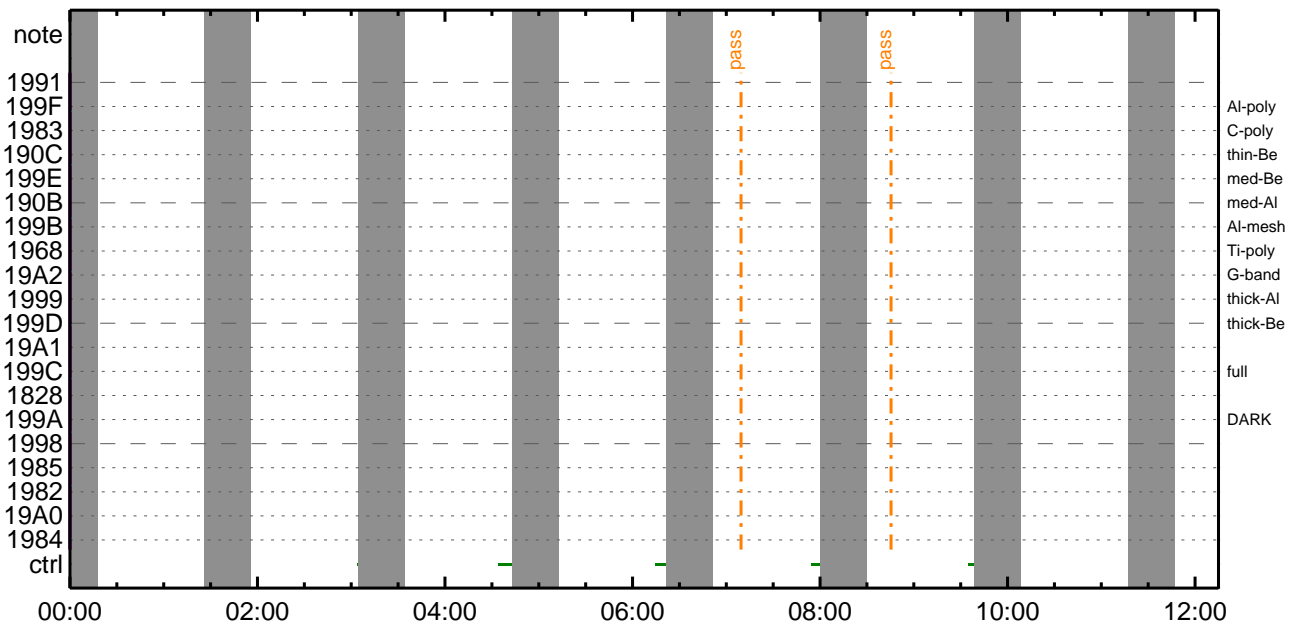
CMDI #0579 2013/07/07



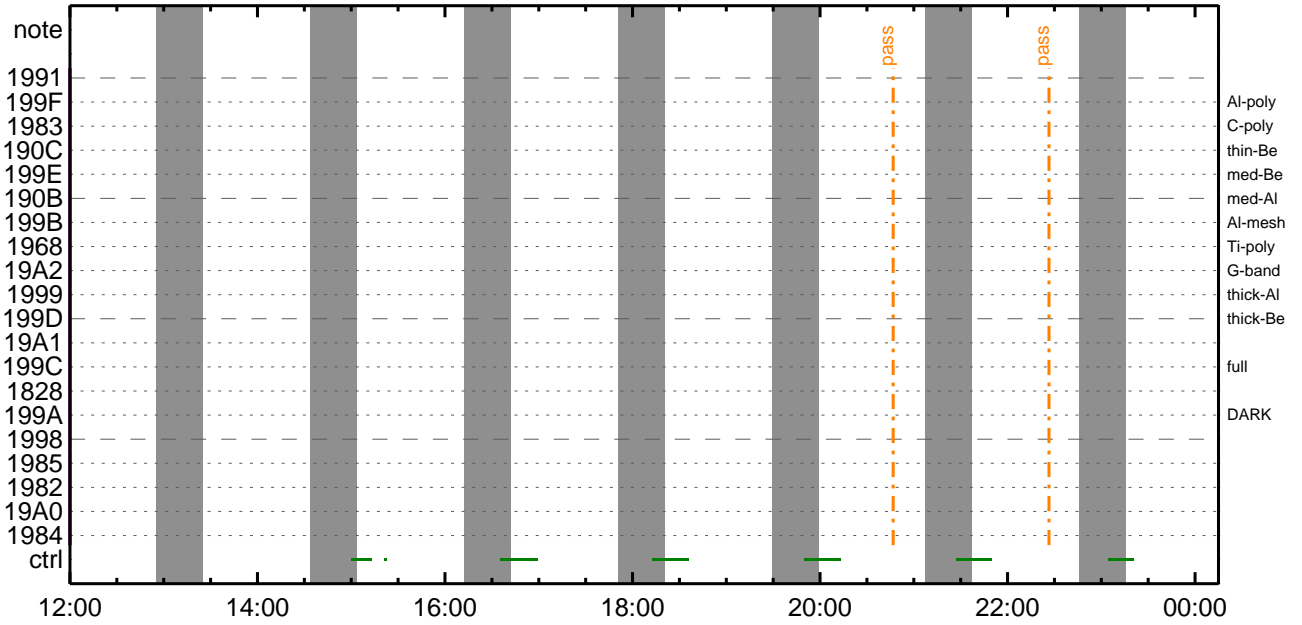
CMDI #0579 2013/07/07



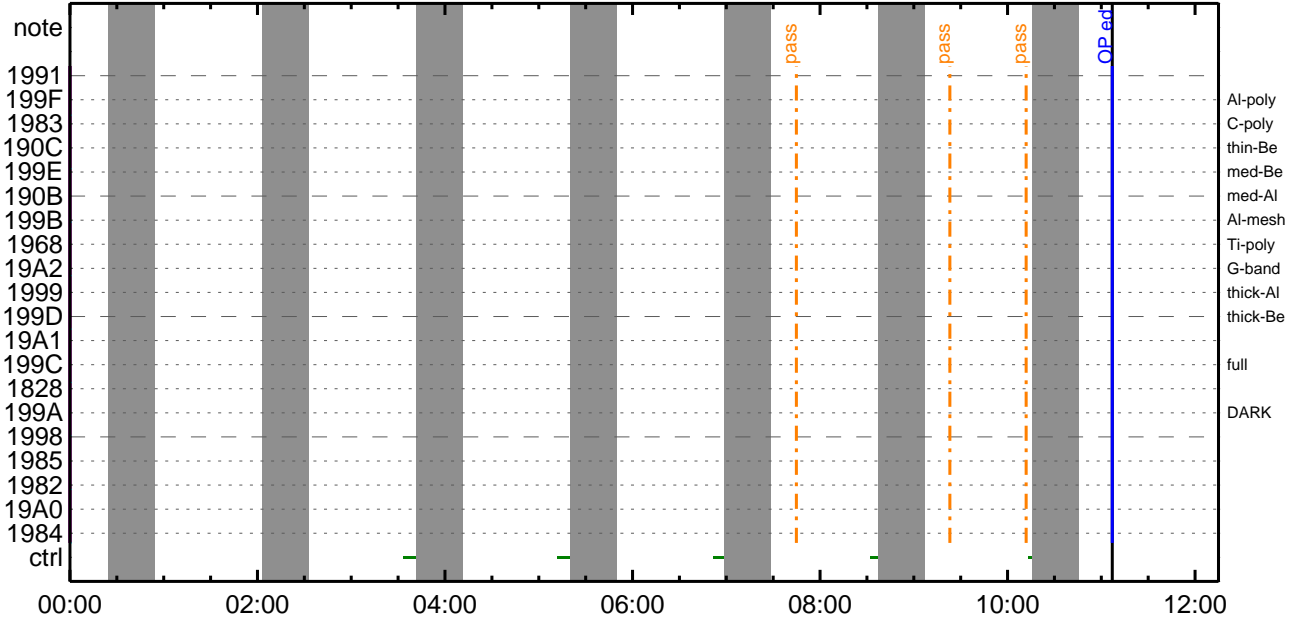
CMDI #0579 2013/07/08



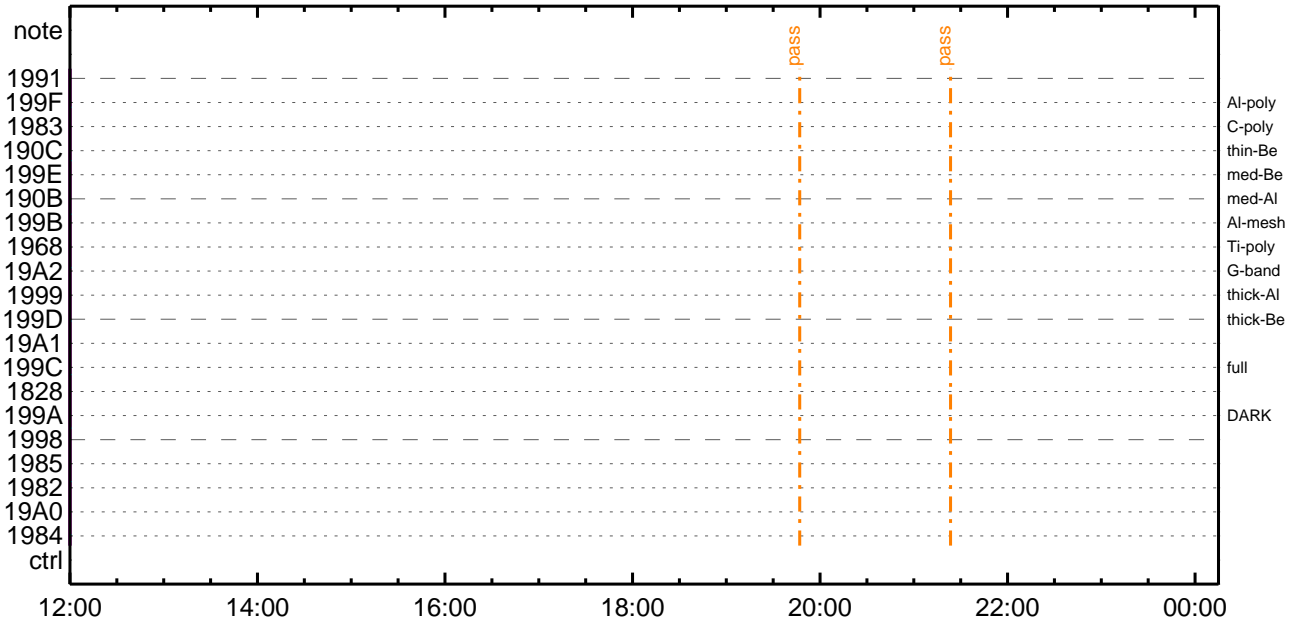
### CMDI #0579 2013/07/08



### CMDI #0579 2013/07/09



### CMDI #0579 2013/07/09







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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-756:OP
0104 ( )
0105 S. OG og-756:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ã´¶Á°òEÉ-ò°Á÷¿® (½âµ-YAYOYx½ê½çòðÁÓÆòÇ½ª°²òE½î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½, Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿®NG²î½î¹ç;ç°E²¼òî½TI-CMDÁ÷¿®²î½Á¹Ô²°²E²ò²³òE;f
0180 C. ²²ò¿;çSET²EEDUMP²î½±°²î½Y¹ç¹Ô²°²³òE;f
0181 C.
0182 C. TIY³Y²Y½E²òðÁDî¿(UT)
0183 +. TI 2013-07-04 10:43:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-07-04 10:43:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-07-04 10:43:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0096 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0097 BC (c8)
0098 + DC 07-F0 MDP_XRT_AEC_RESET
0099 BC (d0)
0100 + DC 07-F0 MDP_XRT_ARS_DIS
0101 BC (d5)
0102 + DC 07-F0 MDP_XRT_FLD_RESET
0103 BC (da)
0104 + DC 07-F0 MDP_XRT_QT_PROG_SET
0105 BC (c4 09)
0106 + DC 07-F0 MDP_XRT_FL_PROG_SET
0107 BC (c5 08)
0108 . C. ----- Success Verify ? OK / NG ____
0109 C.
0110 C.
0111 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0112 C.
0113 + DC 07-F0 MDP_XRT_MODE_OBSV
0114 BC (c2)
0115 + TI 2013-07-04 10:47:02.0
0116 DC 07-F0 MDP_XRT_MODE_OBSV
0117 BC (c2)
0118 . C. ----- Success Verify ? OK / NG ____
0119 C.
0120 C. ***** XRT END *****
0121 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0122 + DC 07-FC EIS_MODE_MANU
0123 BC (21 02)
0124 . C. Verify EIS in MANUAL mode
0125 . C. Estimated OBSTBL upload time is 1s
0126 C. *****
0127 C. EIS START OBSTBL LOAD
0128 C. *****
0129 . S. RAM ram-821:EIS_OBSTBL
0130 ( )
0131 + DC 07-FC EIS_DUMP_OBSTBL
0132 BC (07 07 07 00 00 70 00)
0133 C.
0134 C. Execute, after the success of OBSTBL upload.
0135 C. Set EIS TI-commands
0136 + TI 2013-07-04 10:47:50.0
0137 DC 07-FC EIS_MODE_CHG_ENA
0138 BC (20)
0139 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0140 C. *****
0141 C. EIS END OBSTBL LOAD
0142 C. *****
0143 C.
0144 . C. ***** MDP `úÃîï»ö%ÝðĒĀð¹ñēDCBC•x²è *****
0145 C. (%á°îÿÓŸĀŸĒŸpŸĒŸáŸçŸēñĒ%¼ñ¼Ā»Ūñ¹ñē)
0146 . S. DC-BC dcbc-402:DCBC
0147 (MDP_known_event)
0148 C.
0149 C.
0150 . C. ***** ŸDŸ¹•Ī Daily±ĵĪññĒ`Øñ¹ñēDCBC•x²è *****
0151 . S. DC-BC dcbc-153:DCBC
0152 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0153 C.
0154 C.
0155 . C. ;ãLOSŸĀŸŸŸĀŸ`¼Ā»Ū;ä
0156 C.
0157 . C. ***** LOS *****
0158 C.

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0096 C.
0097 C.
0098 C.
0099 . C. ***** MDP `ŭÃîŝî»ò¼ŷŝËÂĐŝ¹ŝéDCBC•x²è *****
0100 C. (¼á°îŷÓŷÃŷËŷŲŷËŷáŷçŷèŝ¼ŝŝ¼Â»Űŝ¹ŝé)
0101 . S. DC-BC dcbc-402:DCBC
0102 (MDP_known_event)
0103 C.
0104 C.
0105 . C. ***** ŷĐŷ¹•İ Daily±;İŃŝË´Ŧŝ¹ŝéDCBC•x²è *****
0106 . S. DC-BC dcbc-153:DCBC
0107 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0108 C.
0109 C.
0110 . C. ;ãLOSŷÁŷŲŷÃŷ¬¼Â»Ű;ã
0111 C.
0112 . C. ***** LOS *****
0113 C.
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\*\*\* OP Sequence for XRT \*\*\*

2013/07/04	10:58:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	03	be	4e	a7
2013/07/04	11:13:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	03	be	3c	fd
2013/07/04	11:28:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	03	be	2b	4b
2013/07/04	11:43:30.5	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	03	be	19	99
2013/07/04	12:05:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/07/04	12:05:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2013/07/04	12:05:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2013/07/04	12:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2013/07/04	12:37:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	03	be	07	e7
2013/07/04	12:52:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	03	be	f6	36
2013/07/04	13:07:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	03	be	e4	73
2013/07/04	13:22:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	03	be	d2	c1
2013/07/04	13:44:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/07/04	13:44:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2013/07/04	13:44:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2013/07/04	13:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2013/07/04	14:22:30.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	03	be	c1	0f
2013/07/04	14:37:30.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	03	be	af	65
2013/07/04	14:52:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/07/04	14:52:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97		00
2013/07/04	14:52:30.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	e6	36	b4	01
2013/07/04	14:52:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0					d8
2013/07/04	14:52:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0					c8
2013/07/04	14:52:50.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0					d0
2013/07/04	14:52:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2013/07/04	14:52:54.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2013/07/04	14:52:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0					0b
2013/07/04	14:55:28.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0					08
2013/07/04	14:55:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2013/07/04	15:22:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/07/04	15:22:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2013/07/04	15:22:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2013/07/04	15:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2013/07/04	16:11:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/07/04	16:11:30.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	ee	0d	51	b2
2013/07/04	16:26:30.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00	f0	b5	42	97
2013/07/04	16:41:30.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00	f1	b3	32	a7
2013/07/04	17:01:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/07/04	17:01:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2013/07/04	17:01:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2013/07/04	17:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2013/07/04	17:48:30.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00	f2	36	22	35
2013/07/04	18:03:30.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	00	f2	7f	11	58
2013/07/04	18:18:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	00	f2	98	00	18
2013/07/04	18:39:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/07/04	18:39:32.0	XRT_FLD_RESET_415_OG [0x19f]							

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2013/07/04	18:39:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/04	18:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/04	19:25:00.0	AOCS_OrE-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00	f2	7f	f0	19
2013/07/04	19:40:00.0	AOCS_OrE-point_Start_19_OG [0x0a9]	AOCU_NM	5	02-76	00	f2	36	df	34
2013/07/04	19:55:00.0	AOCS_OrE-point_Start_20_OG [0x0aa]	AOCU_NM	5	02-76	00	f1	b3	ce	c1
2013/07/04	20:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/04	20:18:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/04	20:18:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/04	20:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/04	21:02:30.0	AOCS_OrE-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00	f0	b5	be	da
2013/07/04	21:17:30.0	AOCS_OrE-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	00	ee	0d	af	bf
2013/07/04	21:32:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/04	21:32:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2013/07/04	21:32:30.0	AOCS_OrE-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00	00	00	00	00
2013/07/04	21:32:46.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/07/04	21:35:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/07/04	21:35:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/07/04	21:35:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2013/07/04	21:35:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/04	21:42:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/04	21:42:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/07/04	21:42:30.0	AOCS_OrE-point_Start_24_OG [0x0ae]	AOCU_NM	5	02-76	02	00	00	00	00
2013/07/04	21:42:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/07/04	21:42:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/07/04	21:42:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/07/04	21:42:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/07/04	21:42:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/04	21:42:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2013/07/04	21:45:28.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2013/07/04	21:45:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/04	21:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/04	21:56:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/04	21:56:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/04	21:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/04	22:37:01.0	XRT_Custom_430_OG [0x1ae]								
2013/07/04	22:38:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/04	23:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/04	23:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/04	23:35:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/04	23:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/05	00:05:00.0	XRT_Custom_430_OG [0x1ae]								
2013/07/05	00:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/05	01:13:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/05	01:13:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/05	01:13:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/05	01:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/05	01:43:30.0	XRT_Custom_430_OG [0x1ae]								
2013/07/05	01:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								



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2013/07/05	02:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/07/05	02:51:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	da		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/07/05	02:51:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/07/05	02:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/07/05	03:22:00.0	XRT_Custom_430_OG [0x1ae]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2013/07/05	03:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/07/05	04:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/07/05	04:21:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/07/05	04:21:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/07/05	04:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/07/05	05:00:30.0	XRT_Custom_430_OG [0x1ae]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2013/07/05	05:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/07/05	05:33:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/07/05	05:33:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/07/05	05:34:00.0	AOCS_Ore-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00 00 00 00 00		
			AOCU_NM	5	02-76	00 00 00 00 00		
2013/07/05	05:34:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9		
			MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/07/05	05:36:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/07/05	05:36:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/07/05	05:36:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10		
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 10		
2013/07/05	05:37:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/07/05	05:43:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/07/05	05:43:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/07/05	05:44:00.0	AOCS_Ore-point_Start_24_OG [0x0ae]	AOCU_NM	5	02-76	02 00 00 00 00		
			AOCU_NM	5	02-76	02 00 00 00 00		
2013/07/05	05:44:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/07/05	05:44:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/07/05	05:44:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/07/05	05:44:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/07/05	05:44:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/07/05	05:44:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2013/07/05	05:46:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08		
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 08		
2013/07/05	05:47:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/07/05	06:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/07/05	06:01:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/07/05	06:01:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/07/05	06:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/07/05	06:39:00.0	XRT_Custom_430_OG [0x1ae]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2013/07/05	06:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/07/05	07:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/07/05	07:42:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/07/05	07:42:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/07/05	07:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/07/05	08:17:30.0	XRT_Custom_430_OG [0x1ae]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2013/07/05	08:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/07/05	08:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/07/05	08:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/07/05	09:00:00.0	AOCS_Ore-point_Start_24_OG [0x0ae]	AOCU_NM	5	02-76	02 00 00 00 00		
			AOCU_NM	5	02-76	02 00 00 00 00		
2013/07/05	09:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/07/05	09:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		

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2013/07/05	09:00:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/07/05	09:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/07/05	09:00:24.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/05	09:00:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2013/07/05	09:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2013/07/05	09:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/05	09:22:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/05	09:22:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/05	09:22:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/07/05	09:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/05	09:56:00.5	XRT_Custom_430_OG [0x1ae]			
2013/07/05	09:57:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/05	11:04:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/05	11:04:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/05	11:04:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/07/05	11:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/05	11:34:30.0	XRT_Custom_430_OG [0x1ae]			
2013/07/05	11:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/05	12:42:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/05	12:42:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/05	12:42:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/07/05	12:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/05	13:13:00.0	XRT_Custom_430_OG [0x1ae]			
2013/07/05	13:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/05	14:21:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/05	14:21:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/05	14:21:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/07/05	14:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/05	15:00:00.0	XRT_Custom_430_OG [0x1ae]			
2013/07/05	15:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/05	15:59:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/05	15:59:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/05	15:59:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/07/05	16:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/05	16:47:00.0	XRT_Custom_430_OG [0x1ae]			
2013/07/05	16:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/05	17:38:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/05	17:38:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/05	17:38:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/07/05	17:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/05	18:23:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/05	18:23:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/07/05	18:23:30.0	AOCS_OrE-point_Start_23_OG [0x0ad]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/07/05	18:23:46.0	XRT_FLD_DIS_434_OG [0x1b2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/07/05	18:26:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/07/05	18:26:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/07/05	18:26:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2013/07/05	18:26:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/05	19:16:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

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2013/07/05	19:16:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/07/05	19:16:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/07/05	19:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/07/05	20:55:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/07/05	20:55:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/07/05	20:55:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/07/05	20:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/07/05	21:23:24.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/07/05	21:23:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2013/07/05	21:23:30.0	AOCS_OrE-point_Start_24_OG [0x0ae]								
		AOCU_NM	5	02-76	02 00 00 00 00					
2013/07/05	21:23:46.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2013/07/05	21:23:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2013/07/05	21:23:50.0	XRT_AEC_RESET_413_OG [0x19d]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2013/07/05	21:23:52.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2013/07/05	21:23:54.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/07/05	21:23:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05					
2013/07/05	21:26:28.0	XRT_FL_PROG_SET_431_OG [0x1af]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08					
2013/07/05	21:37:31.0	XRT_Custom_430_OG [0x1ae]								
2013/07/05	21:38:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/07/05	22:33:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/07/05	22:33:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/07/05	22:33:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/07/05	22:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/07/05	23:12:00.0	XRT_Custom_430_OG [0x1ae]								
2013/07/05	23:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/07/06	00:12:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/07/06	00:12:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/07/06	00:12:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/07/06	00:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/07/06	00:42:00.0	XRT_Custom_430_OG [0x1ae]								
2013/07/06	00:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/07/06	01:50:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/07/06	01:50:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/07/06	01:50:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/07/06	01:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/07/06	02:20:30.0	XRT_Custom_430_OG [0x1ae]								
2013/07/06	02:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/07/06	03:25:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/07/06	03:25:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/07/06	03:25:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/07/06	03:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/07/06	03:59:00.0	XRT_Custom_430_OG [0x1ae]								
2013/07/06	04:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/07/06	04:59:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/07/06	04:59:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/07/06	04:59:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/07/06	05:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/07/06	05:37:30.0	XRT_Custom_430_OG [0x1ae]								
2013/07/06	05:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/07/06	05:55:24.0	XRT_CTRL_MANU_402_OG [0x192]								

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2013/07/06	05:55:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/06	05:55:30.0	AOCs_OrE-point_Start_23_OG [0x0ad]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/07/06	05:55:46.0	XRT_FLD_DIS_434_OG [0x1b2]	AOCU_NM	5	02-76	00 00 00 00 00
2013/07/06	05:58:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2013/07/06	05:58:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/07/06	05:58:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/07/06	05:58:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2013/07/06	06:05:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/06	06:05:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/06	06:05:30.0	AOCs_OrE-point_Start_24_OG [0x0ae]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/07/06	06:05:46.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00
2013/07/06	06:05:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/07/06	06:05:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/07/06	06:05:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/07/06	06:05:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/07/06	06:05:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/06	06:08:28.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2013/07/06	06:08:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2013/07/06	06:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/06	06:39:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/06	06:39:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/06	06:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/07/06	07:16:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/06	07:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0
2013/07/06	08:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0
2013/07/06	08:19:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c1
2013/07/06	08:19:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/06	08:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/06	08:54:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/07/06	08:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/06	10:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0
2013/07/06	10:00:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/06	10:00:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/06	10:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/06	10:33:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/07/06	10:34:00.0	AOCs_OrE-point_Start_23_OG [0x0ad]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
			MDP_XRT_CTRL_MANU	1	07-F0	c1
			AOCU_NM	5	02-76	00 00 00 00 00