

XRT Timeline to be uploaded on 2013/07/13

Period: 2013/07/13 09:48:00 - 2013/07/18 10:08:00

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Normal mode

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XOB #1982: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh, Ti/Poly-long - w leak image-33 ms														
Term		Pointing (x, y)						Comment						
07/13 16:55:00 - 07/13 17:01:54		Fixed (-528.4, -528.4)						Quadra obs. #1						
PROG= 03 1-time(s)														
└─ Subr= 1 1-time(s) 12.0sec														
└─ Seqn= 1 1-time(s) 12.0sec														
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(1536, 1536)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(1536, 1536)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(1536, 1536)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(1536, 1536)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec														
└─ Seqn= 6 2-time(s) 2.0sec														
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 3 2-time(s) 2.0sec														
└─ Seqn= 12 1-time(s) 2.0sec														
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048	(1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 7 1-time(s) 2.0sec														
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1983: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh, Ti/Poly-long - w leak image-33 ms														
Term		Pointing (x, y)						Comment						
07/13 17:05:00 - 07/13 17:11:54		Fixed (528.4, -528.4)						Quadra obs. #2						
PROG= 18 1-time(s)														
└─ Subr= 1 1-time(s) 12.0sec														
└─ Seqn= 2 1-time(s) 12.0sec														
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(512, 1536)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(512, 1536)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(512, 1536)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(512, 1536)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec														
└─ Seqn= 6 2-time(s) 2.0sec														
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 3 2-time(s) 2.0sec														
└─ Seqn= 12 1-time(s) 2.0sec														
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048	(1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 7 1-time(s) 2.0sec														
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1984: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh, Ti/Poly-long - w leak image-33 ms														
Term		Pointing (x, y)						Comment						
07/13 17:15:00 - 07/13 17:21:54		Fixed (528.4, 528.4)						Quadra obs. #3						
PROG= 01 1-time(s)														
└─ Subr= 1 1-time(s) 12.0sec														
└─ Seqn= 3 1-time(s) 12.0sec														
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(512, 512)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(512, 512)	Q=90	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(512, 512)	Q=98	0	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024	(512, 512)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec														
└─ Seqn= 6 2-time(s) 2.0sec														
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 3 2-time(s) 2.0sec														
└─ Seqn= 12 1-time(s) 2.0sec														
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048	(1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 7 1-time(s) 2.0sec														
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval	

XOB #1985: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh, Ti/Poly-long - w leak image-33 ms														
Term		Pointing (x, y)						Comment						
07/13 17:25:00 - 07/13 17:31:54		Fixed (-528.4, 528.4)						Quadra obs. #4						
PROG= 04 1-time(s)														
└─ Subr= 1 1-time(s) 12.0sec														
└─ Seqn= 4 1-time(s) 12.0sec														
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(1536, 512)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	1024x1024	(1536, 512)	Q=90	0	0	2.0sec

Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Subr= 2		1-time(s)	2.0sec									
Seqn= 6		2-time(s)	2.0sec									
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3		1-time(s)	2.0sec									
Seqn= 12		1-time(s)	2.0sec									
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 7		1-time(s)	2.0sec									
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #199E: Synoptic Q95 2x2 - Al/mesh(12/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(33/362/1443) + G-b

Term	Pointing (x, y)	Comment
07/13 17:35:00 - 07/13 19:17:30	Fixed (0.0, 0.0)	SP flat and dark & XRT synoptic (18:30)
07/14 05:59:00 - 07/14 06:05:54	Fixed (0.0, 0.0)	synoptic, shifted -4.0 min
07/14 18:03:00 - 07/14 18:09:54	Fixed (0.0, 0.0)	synoptic
07/15 06:23:00 - 07/15 06:29:54	Fixed (0.0, 0.0)	synoptic, shifted 20.0 min
07/15 18:03:30 - 07/15 18:10:24	Fixed (0.0, 0.0)	synoptic, shifted 0.5 min
07/16 05:50:30 - 07/16 05:57:24	Fixed (0.0, 0.0)	synoptic, shifted -12.5 min

PROG= 16		1-time(s)										
Subr= 1		1-time(s)	12.0sec									
Seqn= 82		1-time(s)	4.0sec									
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5		1-time(s)	2.0sec									
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 83		1-time(s)	4.0sec									
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 66		1-time(s)	2.0sec									
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1999: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 80s cad With G-band and VLS closed

Term	Pointing (x, y)	Comment
07/13 20:02:30 - 07/14 05:55:54	Fixed (899.4, -233.2)	# AR 11785 obs
07/14 06:09:00 - 07/14 17:59:54	Fixed (899.4, -233.2)	#cont
07/14 18:13:00 - 07/15 06:19:54	Fixed (899.4, -233.2)	#cont.
07/15 06:33:00 - 07/15 17:14:30	Fixed (899.4, -233.2)	#cont.

PROG= 11		Inf.-time(s)										
Subr= 2		1-time(s)	2.0sec									
Seqn= 78		1-time(s)	2.0sec									
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Seqn= 62		4-time(s)	2.0sec									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 63		45-time(s)	80.0sec									
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #199B: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 180s cad With G-band and VLS closed

Term	Pointing (x, y)	Comment
07/15 18:13:30 - 07/16 05:47:24	Fixed (899.4, -233.2)	#cont.
07/16 06:00:30 - 07/16 07:55:30	Fixed (899.4, -233.2)	#cont.

PROG= 14		Inf.-time(s)										
Subr= 2		1-time(s)	2.0sec									
Seqn= 78		1-time(s)	2.0sec									
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Seqn= 62 4-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 63 40-time(s) 240.0sec												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #199C: Flare Standard Obs. with eruptions mode-A (FW1=Open and VLS-Closed)												
Term	Pointing (x, y)		Comment									
07/13 20:02:30 - 07/14 05:55:54	Fixed (899.4, -233.2)		# AR 11785 obs									
07/14 06:09:00 - 07/14 17:59:54	Fixed (899.4, -233.2)		#cont									
07/14 18:13:00 - 07/15 06:19:54	Fixed (899.4, -233.2)		#cont.									
07/15 06:33:00 - 07/15 17:14:30	Fixed (899.4, -233.2)		#cont.									
07/15 18:13:30 - 07/16 05:47:24	Fixed (899.4, -233.2)		#cont.									
07/16 06:00:30 - 07/16 07:55:30	Fixed (899.4, -233.2)		#cont.									
PROG= 08 1-time(s)												
Subr= 1 30-time(s) 20.0sec												
Seqn= 58 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 64 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 79 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 58 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 79 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 58 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 79 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 58 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4 24-time(s) 600.0sec												
Seqn= 61 1-time(s) 2.0sec												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

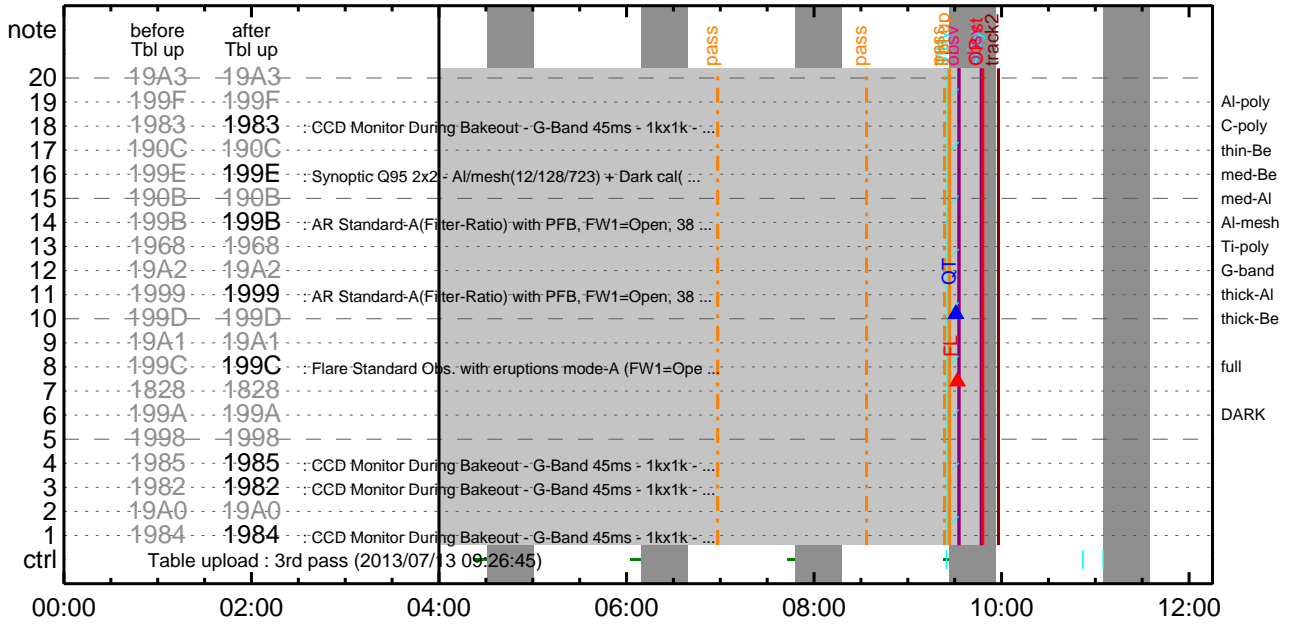
* * * * *

Flare Detection

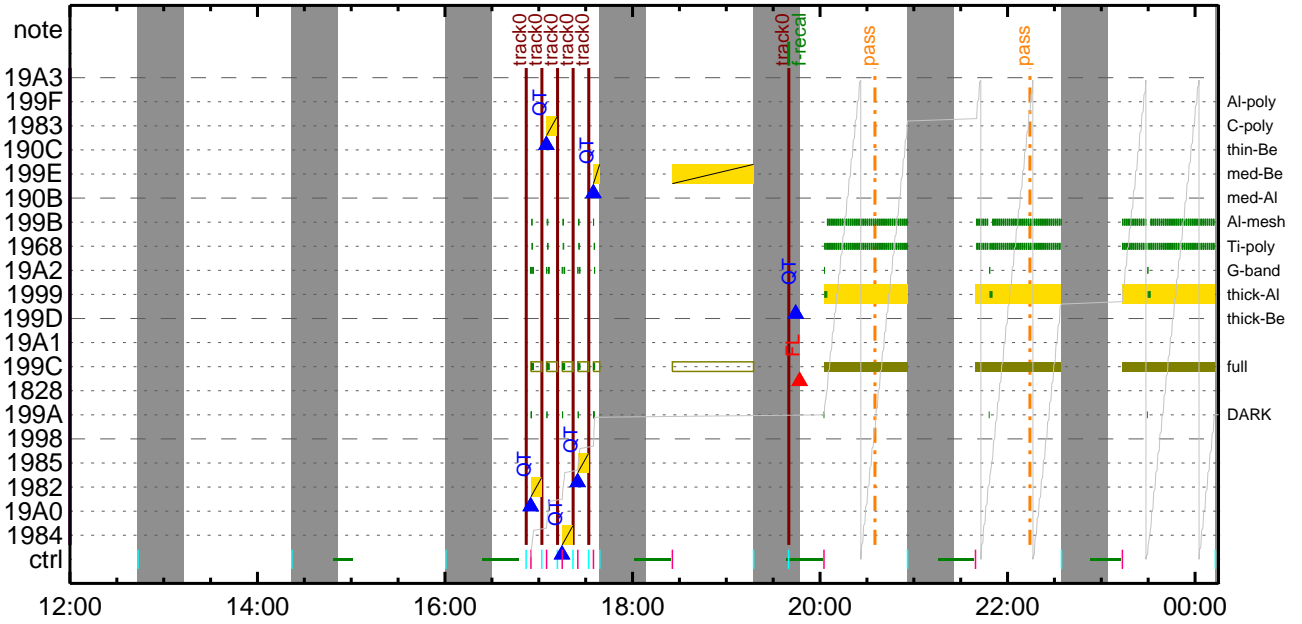
* * * * *

FLD Patrol												
Term		Pointing (x, y)						Comment				
07/13 19:44:16 - 07/14 05:56:16		Fixed (899.4, -233.2)				# AR 11785 obs						
07/14 06:06:16 - 07/14 18:00:16		Fixed (899.4, -233.2)				#cont						
07/14 18:10:16 - 07/15 06:20:16		Fixed (899.4, -233.2)				#cont.						
07/15 06:30:16 - 07/15 18:00:46		Fixed (899.4, -233.2)				#cont.						
07/15 18:10:46 - 07/16 05:47:46		Fixed (899.4, -233.2)				#cont.						
07/16 05:57:46 - 07/18 10:08:00		Fixed (899.4, -233.2)				#cont.						
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

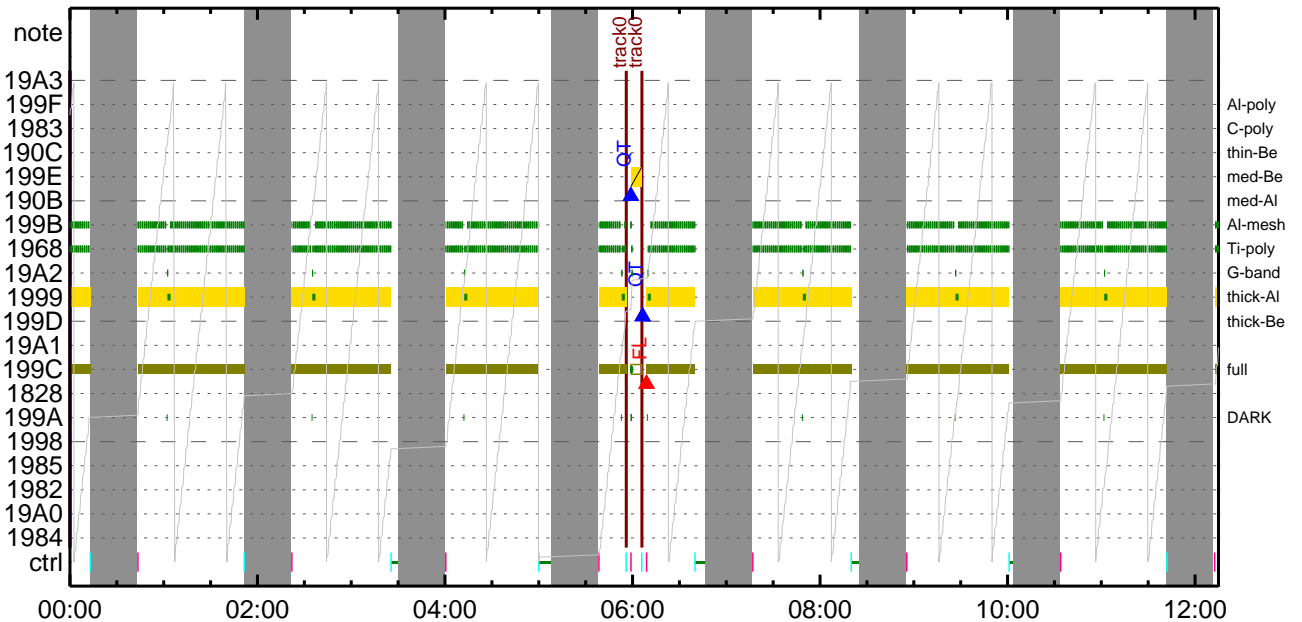
CMDI #0598 2013/07/13



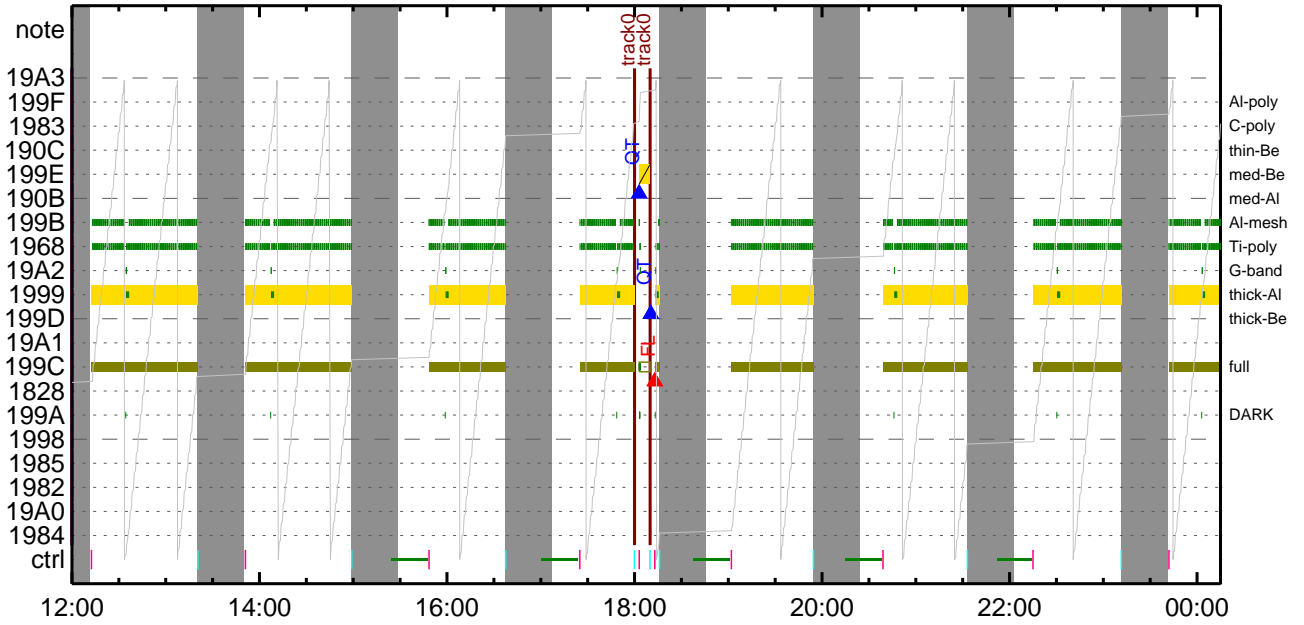
CMDI #0598 2013/07/13



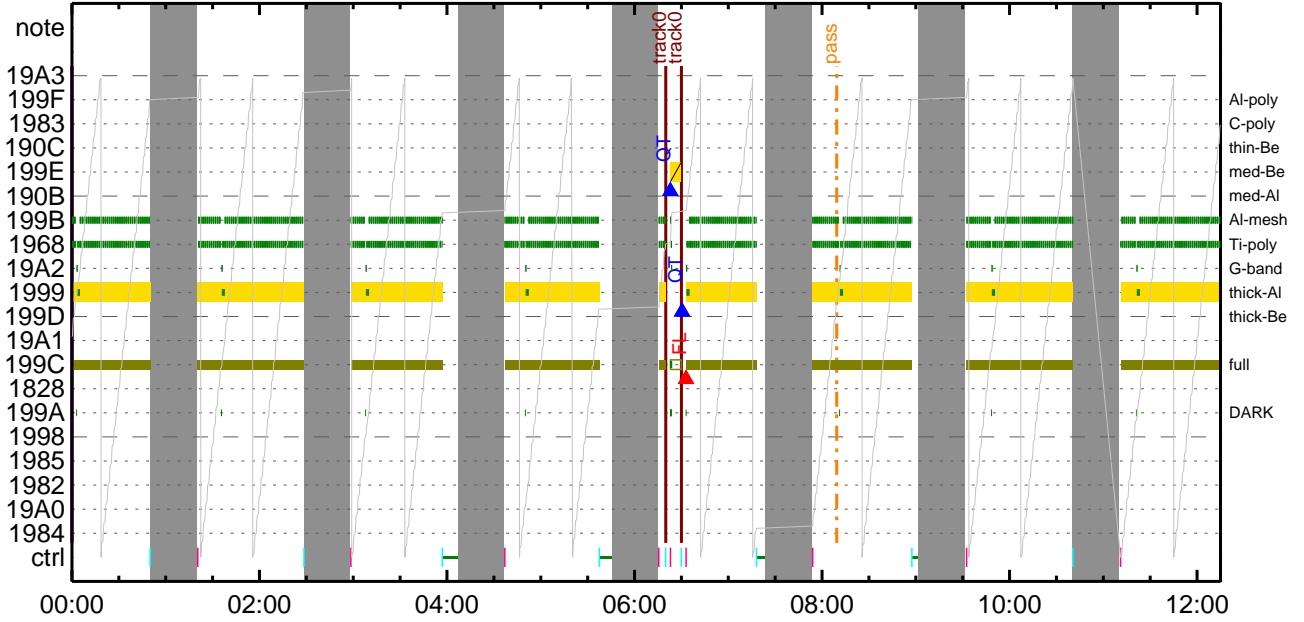
CMDI #0598 2013/07/14



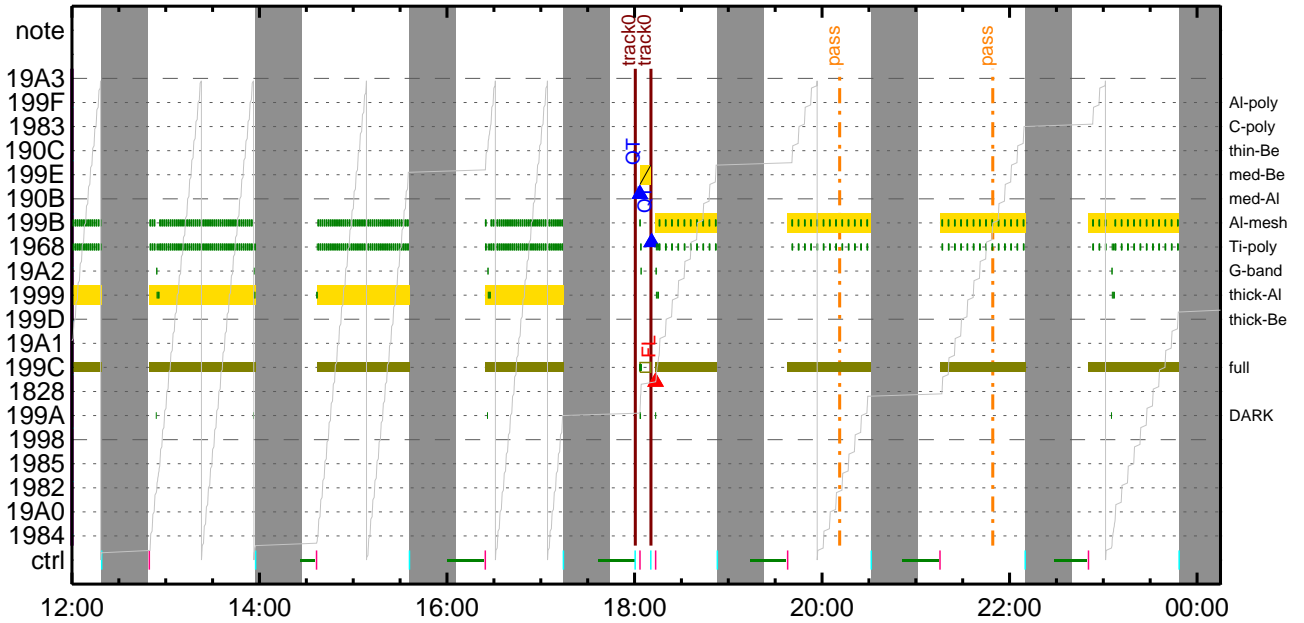
CMDI #0598 2013/07/14



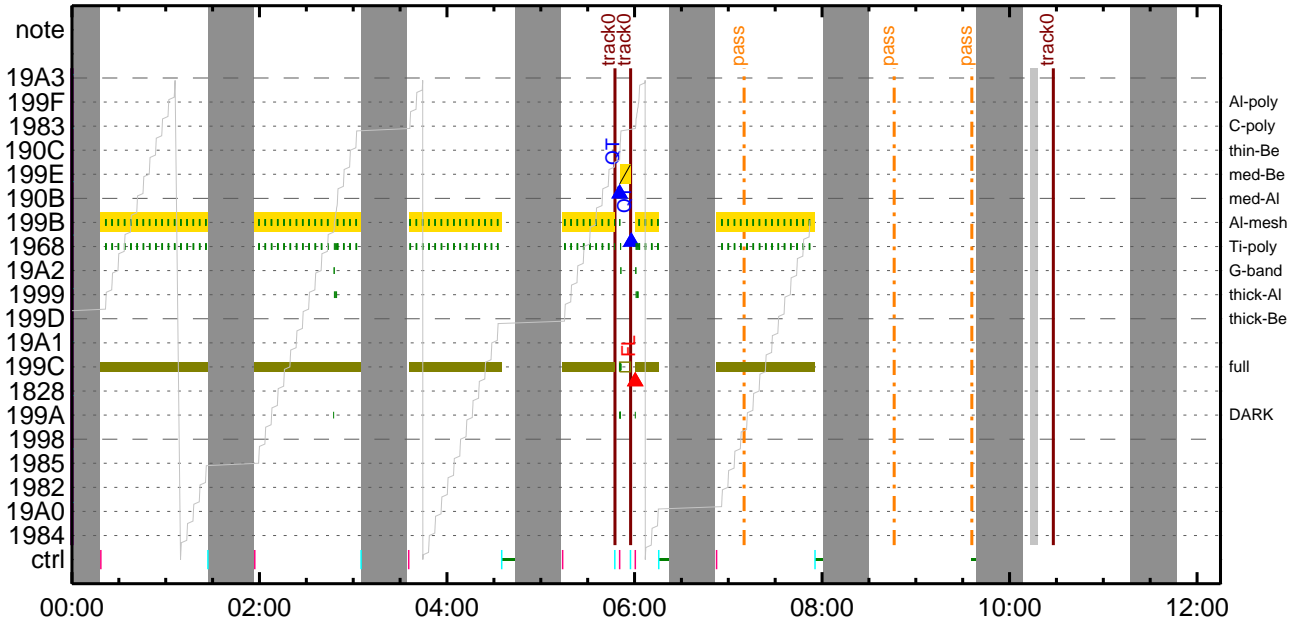
CMDI #0598 2013/07/15



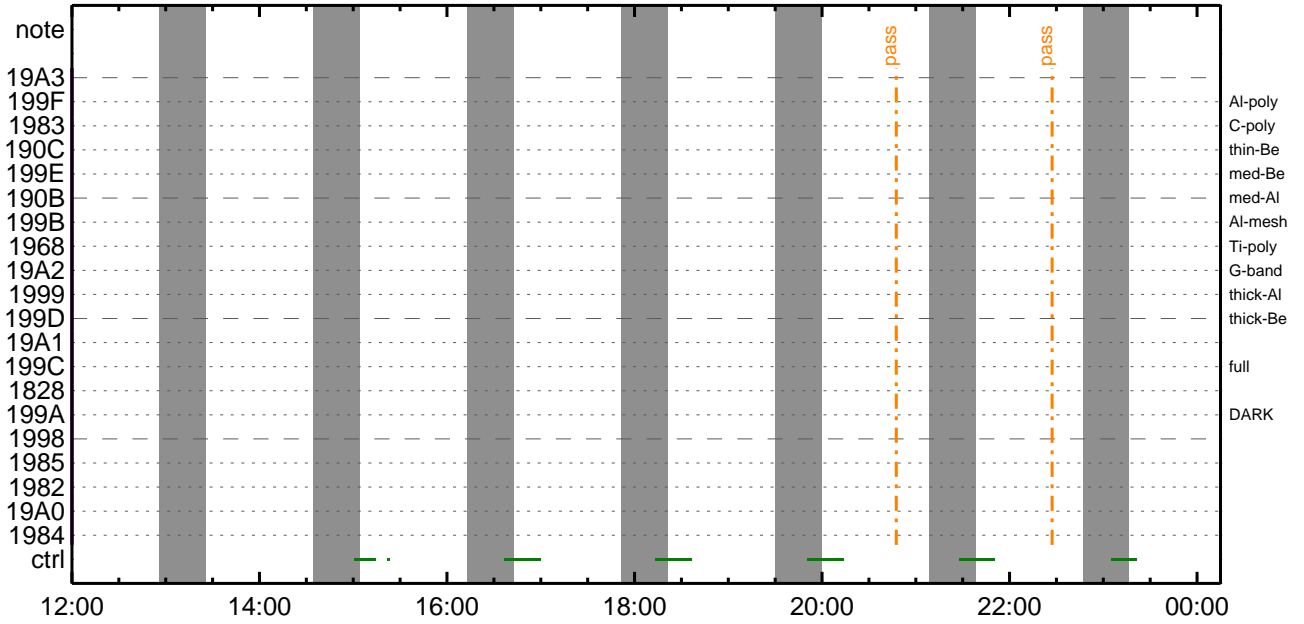
CMDI #0598 2013/07/15



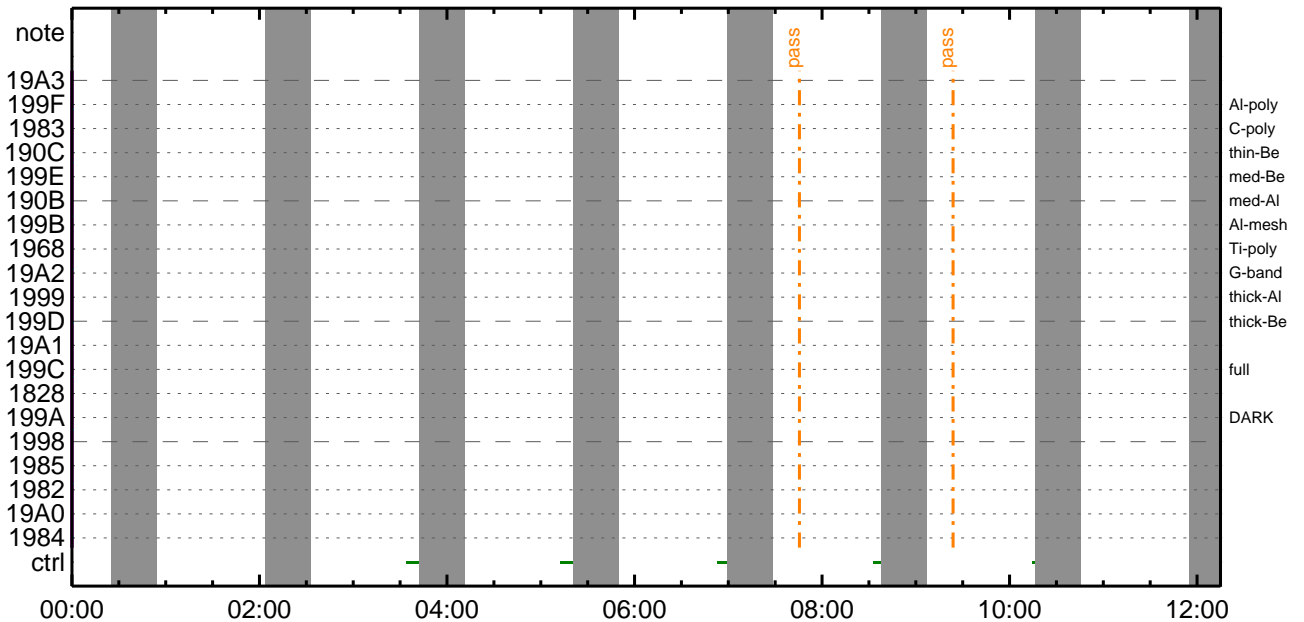
CMDI #0598 2013/07/16



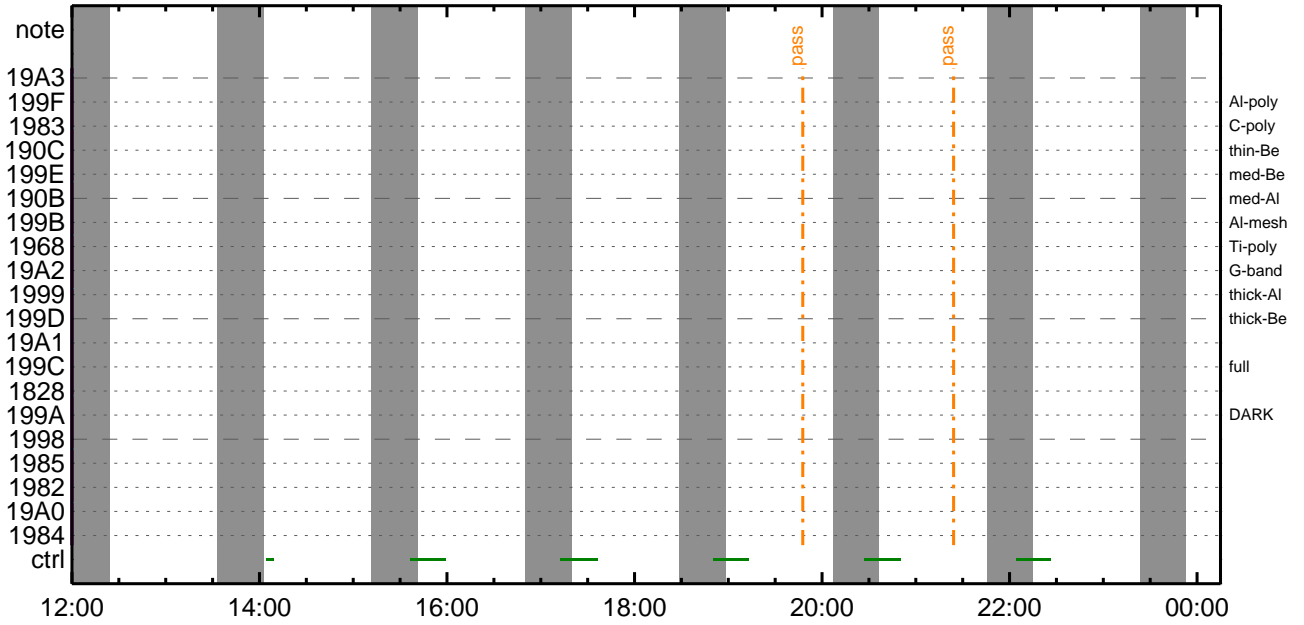
CMDI #0598 2013/07/16



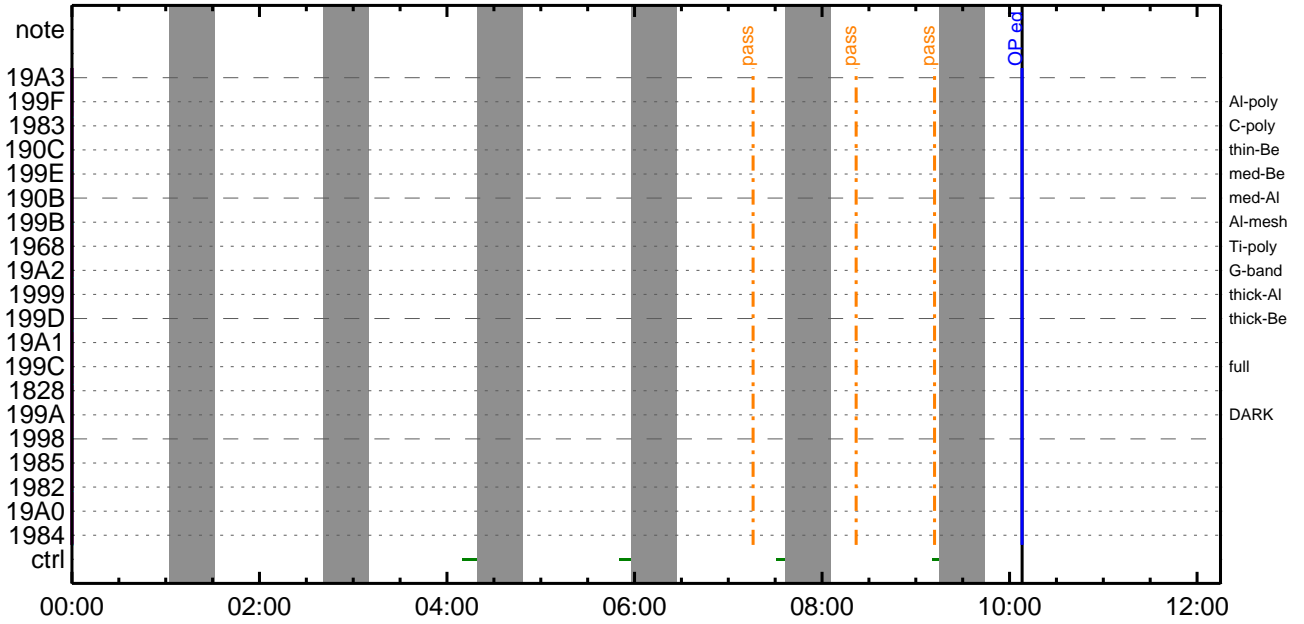
CMDI #0598 2013/07/17



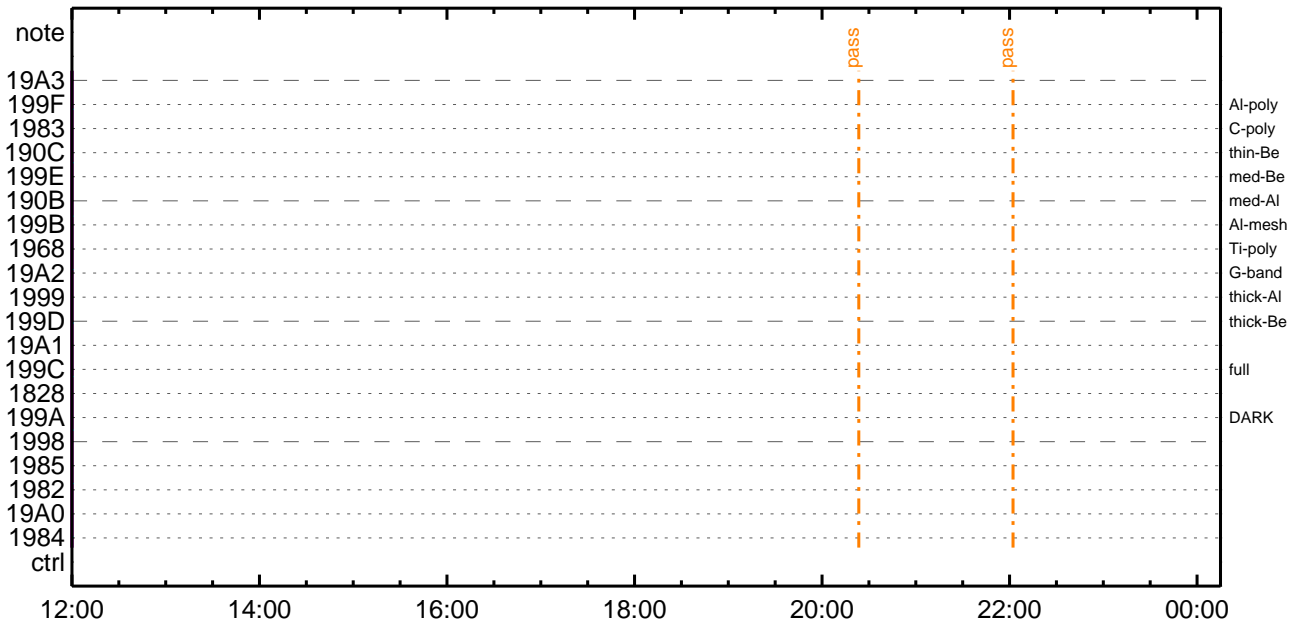
CMDI #0598 2013/07/17



CMDI #0598 2013/07/18



CMDI #0598 2013/07/18



(a) Spacecraft Operation Procedure (real-commands)

```
main-775 2013-07-13 13:00:11 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀY$;¼Y³YBYÓYÉÁ+¿@
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Éð¿ðÄð•µ°Æ»Í×ÁÇðíYÇYÁY×Yí;¼YÉ;ÉÈÈµ•ííÉ;ÈðÈ¼°ÇÓð•ð¿¼í¹Çðí;ÇÀ®, ùð¹ðÈððÇÁ+¿@ð•ðÈððð³ðÈ; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿@µ;ON
0016 C. *****
0017 C. Ç" °ÆÀ, í×ÈYðáLòSðBÇðí»p´Öðð¹íí, ð•; ÇÉÖí×ðÈXÁÓONðí¹ÒðÈðíðÈðð³ðÈ; f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYíYÁY-¾ÒÁÖð-°ÁÄÈð•ð¿ðÉ; Ç°È²¼ðí°ÆÀ, ¼È¾Çðð¼Á¹Òð¹ðÉ; f
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÆÀ,
0033 C. *****
0034 C. Ç" RESTART; ÈPT1; Èð•ð¿ð¼í¹Çðí; Ç°È²¼ðí°ÆÀ¹Òð»ð°; ÇDCBC-150ðØ¿Èðà; f
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»Û;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ò, ;¼Ú)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ú)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYÓYÉYÉÄÜÁÏ; ÈÁ•Á°²ÓÈð; È, áðí°ÆÀ, °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ò, ;¼Ú)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ú)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, ð-¼«Æ°Áä»ßð•ð¿, á; Ç°È²¼ðð¼Á¹Òð¹ðÉ; f
0055 C. YÇYÓYÉYÉÄÜÁÏðáÁ•Á°²ÓÈðð-¼áð¼í¹Çðí´°í»ð¹ðÈððÇÁÓðÄ; f
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÆÀ,
0059 C. *****
0060 C. Ç" RESTART; ÈPT2; Èð•ð¿ð¼í¹Çðí; Ç°È²¼ðí°ÆÀ¹Òð»ð°; ÇDCBC-151ðØ¿Èðà; f
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»Û;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ò, ;¼Ú)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ú)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYÓYÉYÉÄÜÁÏ; ÈÁ•Á°²ÓÈð; È, áðí°ÆÀ, °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ò, ;¼Ú)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Á¹Ò, ;¼Ú)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ò, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Áä»ß; ÇXÁ+¿@µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Áä»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+¿@µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
```

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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-775:OP
0104 ( )
0105 S. OG og-775:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;E½Y½, Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²²ò¿;çSET²EEDUMP²îE±°îYNY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²YóYÉòðÁDî¿(UT)
0183 +. TI 2013-07-13 09:43:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-07-13 09:43:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-07-13 09:43:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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(a) Spacecraft Operation Procedure (real-commands)

```
main-776 2013-07-13 13:00:11 166 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YBYóYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOC : Reload orbital element (send every contact) *****
0010 C. Áí;ÈçðÁáµ°Æ»Í×ÁÇçíYçYÁY×YÍ;¼YÉ;ÈÈè%µ•ííÉ;ÈáÈ¼°ÇÖá•çç¼l¹ççí;çÀ®, ùá¹áèáßáçÁ+ç®•áÈáááááè;æ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ç®µ;ON
0016 C. *****
0017 C. ç°°Ä, í×ÈYááLÒSáßáçáí»á¹á¹íí, á•; çÈÔí×áÈXÁÓONáí¹ááÈáááááè;æ
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÝóYÉYíYÁY-¾ÔÁÖáá•áççé; ç°È²¼áí°Ä, ¼Éççááá¹á¹á¹á¹;æ
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°Ä,
0033 C. *****
0034 C. ç°°° RESTART;ÈPT1;Èá•áççá¼l¹ççí; ç°È²¼áí°Ä¹á¹á¹á¹; çDCBC-150áááááá;æ
0035 C.
0036 . C. ;ãPT1°Ä, ³«»í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ô, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ô, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ô, ;¼Ú)
0045 C.
0046 . C. ;ãYçYóYÉYÉÁÚÁØ;ÈÁ•Á°²óÈð;È, áááááá, °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ô, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ô, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ô, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°Ä, á-¼«Æ°Äá»áá•áçç, á; ç°È²¼áááá¹á¹á¹á¹;æ
0055 C. YçYóYÉYÉÁÚÁØááÁ•Á°²óÈðá-¼ááá¼l¹ççí°í»á¹áááááçÁÓáá;æ
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°Ä,
0059 C. *****
0060 C. ç°°° RESTART;ÈPT2;Èá•áççá¼l¹ççí; ç°È²¼áí°Ä¹á¹á¹á¹; çDCBC-151áááááá;æ
0061 C.
0062 . C. ;ãPT2°Ä, ³«»í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ô, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ô, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ô, ;¼Ú)
0071 C.
0072 . C. ;ãYçYóYÉYÉÁÚÁØ;ÈÁ•Á°²óÈð;È, áááááá, °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ô, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ô, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ô, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°Ä, Áá»á;çXÁ+ç®µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°Ä, Áá»á;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ç®µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```


(a) Spacecraft Operation Procedure (real-commands)

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main-777 2013-07-13 13:00:11 154 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFYÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿øÅø•µ°È»Í×ÁÇøÍYçYÁY×Yí;¼YÉ;ÈÈ%µ•íÉ;ÈøÈ¼°ÇÖø•ø¿¼í¹çøÍ;çÀ®, ùø¹øÈøÈøÇÁ+¿®ø•øÈøøøø³øÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-269:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Stop SP table >
0036 +. DC 07-F0 MDP_SP_CTRL_MANU
0037 BC (61)
0038 C. -----
0039 C. MDP_SP_CTRL_MODE = MANU [ ]
0040 C. -----
0041 C.
0042 . C. <Upload SP Observation Table>
0043 . S. RAM ram-282:MDP_OBS_S
0044 ( )
0045 C.
0046 . C. < Dump RAMID=MDP_OBS_S >
0047 +. DC 07-F0 MDP_DUMP_SPTBL
0048 BC (83 07 00 00 00 38 b8)
0049 C. -----
0050 C. MDP_OBS_S verify = OK/NG [ ]
0051 C. -----
0052 C.
0053 C. *****
0054 C. SOT TI command set
0055 C. *****
0056 C. Execute, after the success of TBL upload.
0057 +. TI 2013-07-13 09:47:18.0
0058 DC 07-F0 MDP_SOT_MODE_OBSV
0059 BC (40)
0060 . C. -----
0061 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0062 C. -----
0063 C.
0064 C.
0065 C. ***** XRT START *****
0066 C.
0067 +. DC 07-F0 MDP_XRT_CTRL_MANU
0068 BC (c1)
0069 +. DC 07-F0 MDP_XRT_MODE_STBY
0070 BC (c3)
0071 . C. ----- Success Verify ? OK / NG_____
0072 C.
0073 C. XRT Obs. Table Upload
0074 . S. RAM ram-291:MDP_OBS_X
0075 ( )
0076 C.
0077 +. DC 07-F0 MDP_DUMP_XRTTBL
0078 BC (84 07 00 00 00 3a d4)
0079 . C. ----- Comparison Check ? OK / ERR _____
0080 C.
0081 C.
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 01 b1 b1 04 04)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 02 b1 b1 08 08)
0086 +. DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 03 b1 b1 08 08)
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 04 b1 b1 06 06)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 05 85 83 06 06)
0092 +. DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 06 85 83 06 06)
0094 +. DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 07 c0 c0 10 10)
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0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 08 80 80 20 20)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 09 40 c0 10 10)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 0a 40 40 10 10)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 0b c0 40 10 10)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 0c 80 80 20 08)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0d 80 80 08 20)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 0f 80 80 06 06)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 10 80 80 08 08)
0112 + DC 07-F0 MDP_XRT_FLD_ENA
0113 BC (d8)
0114 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0115 BC (c8)
0116 + DC 07-F0 MDP_XRT_AEC_RESET
0117 BC (d0)
0118 + DC 07-F0 MDP_XRT_ARS_DIS
0119 BC (d5)
0120 + DC 07-F0 MDP_XRT_FLD_RESET
0121 BC (da)
0122 + DC 07-F0 MDP_XRT_QT_PROG_SET
0123 BC (c4 0b)
0124 + DC 07-F0 MDP_XRT_FL_PROG_SET
0125 BC (c5 08)
0126 . C. ----- Success Verify ? OK / NG ____
0127 C.
0128 C.
0129 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0130 C.
0131 +. DC 07-F0 MDP_XRT_MODE_OBSV
0132 BC (c2)
0133 +. TI 2013-07-13 09:47:02.0
0134 DC 07-F0 MDP_XRT_MODE_OBSV
0135 BC (c2)
0136 . C. ----- Success Verify ? OK / NG ____
0137 C.
0138 C. ***** XRT END *****
0139 C.
0140 . C. ***** MDP 'uÃîâî»ö¼ýðËÄð¹ñèDCBC•x²è *****
0141 C. (%á°îÿÓÿÄÿËÿPÿËÿâÿçÿèñ¼ð¼Ä»Û¹ñè)
0142 . S. DC-BC dcbc-402:DCBC
0143 (MDP_known_event)
0144 C.
0145 C.
0146 . C. ***** ÿÐÿ¹•ï Daily±¿îÑñË´Ø¹ñèDCBC•x²è *****
0147 . S. DC-BC dcbc-153:DCBC
0148 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0149 C.
0150 C.
0151 . C. ;ãLOSÿÄÿSÿËÿËÿ-¼Ä»Û;ä
0152 C.
0153 . C. ***** LOS *****
0154 C.
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*** OP Sequence for XRT ***

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2013/07/13 09:58:00.0 AOCS_ORe-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 02 00 00 00 00
2013/07/13 10:52:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2013/07/13 10:52:02.0 XRT_TCIB_XRT_S_HTR_A_DIS_437_OG [0x1b5]
                        TCIB_XRT_S_HTR_A_DIS       0 04-C0
2013/07/13 11:05:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2013/07/13 11:05:02.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET          1 07-F0 da
2013/07/13 11:05:04.0 XRT_PREFLR_STRT_418_OG [0x1a2]
                        MDP_XRT_PREFLR_STRT        1 07-F0 e8
2013/07/13 11:08:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP        1 07-F0 e9
2013/07/13 12:43:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2013/07/13 12:43:32.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET          1 07-F0 da
2013/07/13 12:43:34.0 XRT_PREFLR_STRT_418_OG [0x1a2]
                        MDP_XRT_PREFLR_STRT        1 07-F0 e8
2013/07/13 12:46:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP        1 07-F0 e9
2013/07/13 14:22:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2013/07/13 14:22:02.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET          1 07-F0 da
2013/07/13 14:22:04.0 XRT_PREFLR_STRT_418_OG [0x1a2]
                        MDP_XRT_PREFLR_STRT        1 07-F0 e8
2013/07/13 14:25:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP        1 07-F0 e9
2013/07/13 16:00:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2013/07/13 16:00:32.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET          1 07-F0 da
2013/07/13 16:00:34.0 XRT_PREFLR_STRT_418_OG [0x1a2]
                        MDP_XRT_PREFLR_STRT        1 07-F0 e8
2013/07/13 16:03:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP        1 07-F0 e9
2013/07/13 16:51:54.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2013/07/13 16:52:00.0 AOCS_ORe-point_Start_2_OG [0x098]
                        AOCU_NM                    5 02-76 00 2e f9 2e f9
2013/07/13 16:54:32.0 XRT_FOCUS_POSITION_442_OG [0x1ba]
                        XRT_FOCUS_POSITION          4 07-F8 22 ff aa 00
2013/07/13 16:54:52.0 XRT_QT_PROG_SET_449_OG [0x1c1]
                        MDP_XRT_QT_PROG_SET        2 07-F0 c4 03
2013/07/13 16:54:54.0 XRT_FLD_DIS_404_OG [0x194]
                        MDP_XRT_FLD_DIS            1 07-F0 d9
2013/07/13 16:54:56.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS        1 07-F0 c9
2013/07/13 16:54:58.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS            1 07-F0 d5
2013/07/13 16:55:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO          1 07-F0 c0
2013/07/13 17:01:54.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2013/07/13 17:02:00.0 AOCS_ORe-point_Start_3_OG [0x099]
                        AOCU_NM                    5 02-76 00 2e f9 d1 07
2013/07/13 17:04:32.0 XRT_FOCUS_POSITION_442_OG [0x1ba]
                        XRT_FOCUS_POSITION          4 07-F8 22 ff aa 00
2013/07/13 17:04:52.0 XRT_QT_PROG_SET_447_OG [0x1bf]
                        MDP_XRT_QT_PROG_SET        2 07-F0 c4 12
2013/07/13 17:04:54.0 XRT_FLD_DIS_404_OG [0x194]
                        MDP_XRT_FLD_DIS            1 07-F0 d9
2013/07/13 17:04:56.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS        1 07-F0 c9
2013/07/13 17:04:58.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS            1 07-F0 d5
2013/07/13 17:05:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO          1 07-F0 c0
2013/07/13 17:11:54.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2013/07/13 17:12:00.0 AOCS_ORe-point_Start_4_OG [0x09a]
                        AOCU_NM                    5 02-76 00 d1 07 d1 07
2013/07/13 17:14:32.0 XRT_FOCUS_POSITION_442_OG [0x1ba]
                        XRT_FOCUS_POSITION          4 07-F8 22 ff aa 00
2013/07/13 17:14:52.0 XRT_QT_PROG_SET_426_OG [0x1aa]
                        MDP_XRT_QT_PROG_SET        2 07-F0 c4 01
2013/07/13 17:14:54.0 XRT_FLD_DIS_404_OG [0x194]
                        MDP_XRT_FLD_DIS            1 07-F0 d9
2013/07/13 17:14:56.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS        1 07-F0 c9
2013/07/13 17:14:58.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS            1 07-F0 d5
2013/07/13 17:15:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO          1 07-F0 c0
2013/07/13 17:21:54.0 XRT_CTRL_MANU_439_OG [0x1b7]
                        MDP_XRT_CTRL_MANU          1 07-F0 c1
2013/07/13 17:22:00.0 AOCS_ORe-point_Start_5_OG [0x09b]
                        AOCU_NM                    5 02-76 00 d1 07 2e f9
2013/07/13 17:24:32.0 XRT_FOCUS_POSITION_442_OG [0x1ba]

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2013/07/13	17:24:52.0	XRT_QT_PROG_SET_414_OG [0x19e]	4	07-F8	22 ff aa	00
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
2013/07/13	17:24:54.0	XRT_FLD_DIS_404_OG [0x194]	1	07-F0	d9	
		MDP_XRT_FLD_DIS	1	07-F0	c9	
2013/07/13	17:24:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	1	07-F0	c9	
		MDP_XRT_FLRCTRL_DIS	1	07-F0	d5	
2013/07/13	17:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0	
		MDP_XRT_CTRL_AUTO	1	07-F0	c1	
2013/07/13	17:31:54.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1	
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/13	17:31:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	4	07-F8	22 ff aa	00
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2013/07/13	17:32:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	5	02-76	00 00 00 00	00
		AOCU_NM	5	02-76	00 00 00 00	00
2013/07/13	17:32:16.0	XRT_FLD_DIS_434_OG [0x1b2]	1	07-F0	d9	
		MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/07/13	17:34:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	1	07-F0	c9	
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/07/13	17:34:56.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0	d5	
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/07/13	17:34:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]	2	07-F0	c4	10
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	10
2013/07/13	17:35:00.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0	c0	
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/13	17:39:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1	
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/13	17:39:02.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da	
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/13	17:39:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	1	07-F0	e8	
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/13	17:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9	
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/13	18:24:30.5	XRT_Custom_430_OG [0x1ae]				
2013/07/13	18:25:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0	
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/13	19:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1	
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/13	19:17:32.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da	
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/13	19:17:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	1	07-F0	e8	
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/13	19:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9	
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/13	19:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1	
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/13	19:39:56.0	XRT_FOCUS_RECALIBRATE_433_OG [0x1b1]	2	07-F8	78	00
		XRT_FOCUS_RECAL	2	07-F8	78	00
2013/07/13	19:40:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	5	02-76	00 14 bc b0	11
		AOCU_NM	5	02-76	00 14 bc b0	11
2013/07/13	19:43:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	4	07-F8	22 fe 97	00
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2013/07/13	19:44:16.0	XRT_FLD_ENA_411_OG [0x19b]	1	07-F0	d8	
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/07/13	19:44:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	1	07-F0	c8	
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/07/13	19:44:20.0	XRT_AEC_RESET_413_OG [0x19d]	1	07-F0	d0	
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/07/13	19:44:22.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0	d5	
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/07/13	19:44:24.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da	
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/13	19:44:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]	2	07-F0	c4	0b
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2013/07/13	19:46:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	2	07-F0	c5	08
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2013/07/13	20:01:30.0	XRT_Custom_430_OG [0x1ae]				
2013/07/13	20:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0	
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/13	20:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1	
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/13	20:56:02.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da	
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/13	20:56:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	1	07-F0	e8	
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/13	20:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9	
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/13	21:38:30.0	XRT_Custom_430_OG [0x1ae]				
2013/07/13	21:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0	
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/13	22:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1	
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/13	22:34:32.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da	
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/13	22:34:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	1	07-F0	e8	
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/13	22:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9	
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/13	23:12:30.0	XRT_Custom_430_OG [0x1ae]				
2013/07/13	23:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0	
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2013/07/14	00:13:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/07/14	00:13:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/07/14	00:13:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/07/14	00:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/07/14	00:42:30.0	XRT_Custom_430_OG [0x1ae]								
2013/07/14	00:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/07/14	01:51:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/07/14	01:51:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/07/14	01:51:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/07/14	01:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/07/14	02:21:00.0	XRT_Custom_430_OG [0x1ae]								
2013/07/14	02:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/07/14	03:25:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/07/14	03:25:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/07/14	03:25:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/07/14	03:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/07/14	03:59:30.0	XRT_Custom_430_OG [0x1ae]								
2013/07/14	04:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/07/14	05:00:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/07/14	05:00:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/07/14	05:00:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/07/14	05:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/07/14	05:37:30.0	XRT_Custom_430_OG [0x1ae]								
2013/07/14	05:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/07/14	05:55:54.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/07/14	05:55:56.0	XRT_FOCUS_POSITION_403_OG [0x193]								
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00					
2013/07/14	05:56:00.0	AOCS_OrE-point_Start_6_OG [0x09c]								
		AOCU_NM	5	02-76	00 00 00 00 00					
2013/07/14	05:56:16.0	XRT_FLD_DIS_434_OG [0x1b2]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					
2013/07/14	05:58:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2013/07/14	05:58:56.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2013/07/14	05:58:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10					
2013/07/14	05:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/07/14	06:05:54.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/07/14	06:05:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2013/07/14	06:06:00.0	AOCS_OrE-point_Start_7_OG [0x09d]								
		AOCU_NM	5	02-76	00 14 bc b0 11					
2013/07/14	06:06:16.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2013/07/14	06:06:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2013/07/14	06:06:20.0	XRT_AEC_RESET_413_OG [0x19d]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2013/07/14	06:06:22.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2013/07/14	06:06:24.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/07/14	06:06:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b					
2013/07/14	06:08:58.0	XRT_FL_PROG_SET_431_OG [0x1af]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08					
2013/07/14	06:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/07/14	06:40:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/07/14	06:40:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/07/14	06:40:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/07/14	06:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/07/14	07:16:00.0	XRT_Custom_430_OG [0x1ae]								
2013/07/14	07:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					

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2013/07/14	08:20:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/14	08:20:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/14	08:20:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/14	08:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/14	08:54:30.0	XRT_Custom_430_OG [0x1ae]							
2013/07/14	08:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/14	10:01:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/14	10:01:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/14	10:01:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/14	10:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/14	10:33:00.0	XRT_Custom_430_OG [0x1ae]							
2013/07/14	10:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/14	11:42:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/14	11:42:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/14	11:42:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/14	11:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/14	12:11:30.0	XRT_Custom_430_OG [0x1ae]							
2013/07/14	12:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/14	13:20:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/14	13:20:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/14	13:20:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/14	13:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/14	13:50:00.0	XRT_Custom_430_OG [0x1ae]							
2013/07/14	13:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/14	14:59:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/14	14:59:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/14	14:59:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/14	15:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/14	15:47:30.0	XRT_Custom_430_OG [0x1ae]							
2013/07/14	15:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/14	16:37:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/14	16:37:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/07/14	16:37:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/07/14	16:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/07/14	17:24:00.0	XRT_Custom_430_OG [0x1ae]							
2013/07/14	17:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/14	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/14	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/07/14	18:00:00.0	AOCS_OrE-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/07/14	18:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/07/14	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/07/14	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/07/14	18:02:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2013/07/14	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/07/14	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/07/14	18:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/07/14	18:10:00.0	AOCS_OrE-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 14 bc b0 11				
2013/07/14	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/07/14	18:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/07/14	18:10:20.0	XRT_AEC_RESET_413_OG [0x19d]							

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2013/07/14	18:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
			MDP_XRT_ARS_DIS	1	07-F0	d5
2013/07/14	18:10:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/14	18:10:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2013/07/14	18:12:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2013/07/14	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/14	18:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/14	18:16:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/14	18:16:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/07/14	18:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/14	19:01:00.0	XRT_Custom_430_OG [0x1ae]				
2013/07/14	19:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/14	19:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/14	19:54:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/14	19:54:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/07/14	19:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/14	20:38:00.0	XRT_Custom_430_OG [0x1ae]				
2013/07/14	20:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/14	21:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/14	21:33:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/14	21:33:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/07/14	21:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/14	22:14:00.0	XRT_Custom_430_OG [0x1ae]				
2013/07/14	22:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/14	23:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/14	23:11:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/14	23:11:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/07/14	23:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/14	23:41:00.0	XRT_Custom_430_OG [0x1ae]				
2013/07/14	23:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/15	00:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/15	00:50:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/15	00:50:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/07/15	00:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/15	01:19:30.0	XRT_Custom_430_OG [0x1ae]				
2013/07/15	01:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/15	02:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/15	02:28:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/15	02:28:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/07/15	02:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/15	02:57:30.0	XRT_Custom_430_OG [0x1ae]				
2013/07/15	02:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/15	03:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/15	03:57:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/15	03:57:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/07/15	04:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/15	04:36:00.0	XRT_Custom_430_OG [0x1ae]				
2013/07/15	04:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/15	05:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/15	05:37:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/15	05:37:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]				

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2013/07/15	05:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/07/15	06:14:30.0	XRT_Custom_430_OG [0x1ae]						
2013/07/15	06:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/07/15	06:19:54.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/07/15	06:19:56.0	XRT_FOCUS_POSITION_403_OG [0x193]						
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/07/15	06:20:00.0	AOCS_ORe-point_Start_6_OG [0x09c]						
			AOCU_NM	5	02-76	00 00 00 00 00		
2013/07/15	06:20:16.0	XRT_FLD_DIS_434_OG [0x1b2]						
			MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/07/15	06:22:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/07/15	06:22:56.0	XRT_ARS_DIS_423_OG [0x1a7]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/07/15	06:22:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 10		
2013/07/15	06:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/07/15	06:29:54.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/07/15	06:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/07/15	06:30:00.0	AOCS_ORe-point_Start_7_OG [0x09d]						
			AOCU_NM	5	02-76	00 14 bc b0 11		
2013/07/15	06:30:16.0	XRT_FLD_ENA_411_OG [0x19b]						
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/07/15	06:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/07/15	06:30:20.0	XRT_AEC_RESET_413_OG [0x19d]						
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/07/15	06:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/07/15	06:30:24.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/07/15	06:30:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b		
2013/07/15	06:32:58.0	XRT_FL_PROG_SET_431_OG [0x1af]						
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 08		
2013/07/15	06:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/07/15	07:18:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/07/15	07:18:02.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/07/15	07:18:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/07/15	07:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/07/15	07:53:00.0	XRT_Custom_430_OG [0x1ae]						
2013/07/15	07:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/07/15	08:57:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/07/15	08:57:32.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/07/15	08:57:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/07/15	09:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/07/15	09:31:30.0	XRT_Custom_430_OG [0x1ae]						
2013/07/15	09:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/07/15	10:40:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/07/15	10:40:32.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/07/15	10:40:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/07/15	10:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/07/15	11:10:00.0	XRT_Custom_430_OG [0x1ae]						
2013/07/15	11:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/07/15	12:19:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/07/15	12:19:02.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/07/15	12:19:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/07/15	12:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/07/15	12:48:30.0	XRT_Custom_430_OG [0x1ae]						
2013/07/15	12:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/07/15	13:57:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/07/15	13:57:32.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/07/15	13:57:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						

2013/07/15	14:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/07/15	14:35:30.0	XRT_Custom_430_OG [0x1ae]							
2013/07/15	14:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/07/15	15:36:00.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/07/15	15:36:02.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2013/07/15	15:36:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/07/15	15:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/07/15	16:23:30.0	XRT_Custom_430_OG [0x1ae]							
2013/07/15	16:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/07/15	17:14:30.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/07/15	17:14:32.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2013/07/15	17:14:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/07/15	17:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/07/15	18:00:24.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/07/15	18:00:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2013/07/15	18:00:30.0	AOCS_Ore-point_Start_6_OG [0x09c]							
			AOCU_NM	5	02-76	00 00 00 00 00			
2013/07/15	18:00:46.0	XRT_FLD_DIS_434_OG [0x1b2]							
			MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/07/15	18:03:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/07/15	18:03:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/07/15	18:03:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 10			
2013/07/15	18:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/07/15	18:10:24.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/07/15	18:10:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2013/07/15	18:10:30.0	AOCS_Ore-point_Start_7_OG [0x09d]							
			AOCU_NM	5	02-76	00 14 bc b0 11			
2013/07/15	18:10:46.0	XRT_FLD_ENA_411_OG [0x19b]							
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/07/15	18:10:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/07/15	18:10:50.0	XRT_AEC_RESET_413_OG [0x19d]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/07/15	18:10:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/07/15	18:10:54.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2013/07/15	18:10:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e			
2013/07/15	18:13:28.0	XRT_FL_PROG_SET_431_OG [0x1af]							
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 08			
2013/07/15	18:13:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/07/15	18:53:00.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/07/15	18:53:02.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2013/07/15	18:53:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/07/15	18:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/07/15	19:37:00.0	XRT_Custom_430_OG [0x1ae]							
2013/07/15	19:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/07/15	20:31:30.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/07/15	20:31:32.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2013/07/15	20:31:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/07/15	20:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/07/15	21:14:30.0	XRT_Custom_430_OG [0x1ae]							
2013/07/15	21:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/07/15	22:10:00.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/07/15	22:10:02.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2013/07/15	22:10:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/07/15	22:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			

2013/07/15	22:49:30.0	XRT_Custom_430_OG [0x1ae]				
2013/07/15	22:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/15	23:48:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/15	23:48:32.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/15	23:48:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/15	23:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/16	00:17:30.0	XRT_Custom_430_OG [0x1ae]				
2013/07/16	00:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/16	01:27:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/16	01:27:02.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/16	01:27:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/16	01:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/16	01:56:00.0	XRT_Custom_430_OG [0x1ae]				
2013/07/16	01:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/16	03:05:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/16	03:05:02.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/16	03:05:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/16	03:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/16	03:34:30.0	XRT_Custom_430_OG [0x1ae]				
2013/07/16	03:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/16	04:35:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/16	04:35:02.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/16	04:35:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/16	04:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/16	05:13:00.0	XRT_Custom_430_OG [0x1ae]				
2013/07/16	05:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/16	05:47:24.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/16	05:47:26.0	XRT_FOCUS_POSITION_403_OG [0x193]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/07/16	05:47:30.0	AOCS_ORe-point_Start_6_OG [0x09c]				
		AOCU_NM	5	02-76	00 00 00 00 00	
2013/07/16	05:47:46.0	XRT_FLD_DIS_434_OG [0x1b2]				
		MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/07/16	05:50:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]				
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/07/16	05:50:26.0	XRT_ARS_DIS_423_OG [0x1a7]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/07/16	05:50:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2013/07/16	05:50:30.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/16	05:57:24.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/16	05:57:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]				
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/07/16	05:57:30.0	AOCS_ORe-point_Start_7_OG [0x09d]				
		AOCU_NM	5	02-76	00 14 bc b0 11	
2013/07/16	05:57:46.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/07/16	05:57:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/07/16	05:57:50.0	XRT_AEC_RESET_413_OG [0x19d]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/07/16	05:57:52.0	XRT_ARS_DIS_423_OG [0x1a7]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/07/16	05:57:54.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/16	05:57:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2013/07/16	06:00:28.0	XRT_FL_PROG_SET_431_OG [0x1af]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08	
2013/07/16	06:00:30.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/16	06:15:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/16	06:15:32.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/16	06:15:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/16	06:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	

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2013/07/16	06:51:30.0	XRT_Custom_430_OG [0x1ae]								
2013/07/16	06:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/07/16	07:55:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/07/16	07:55:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2013/07/16	07:55:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/07/16	07:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/07/16	10:28:00.0	AOCS_ORe-point_Start_6_OG [0x09c]								
		AOCU_NM	5	02-76	00 00 00 00 00					