

Period: 2013/07/20 11:19:00 - 2013/07/25 10:37:00

Period: 2013/07/20 11:19:00 - 2013/07/25 10:37:00

XOB #1998: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 60s cad With G-band and VLS closed																
Term			Pointing (x, y)						Comment							
07/20 18:17:30 - 07/21 05:35:24	Track (	26.7, 230.9)	@ 07/20 18:14:30	# AR 11793.												
07/21 18:38:00 - 07/22 01:52:00	Track (	-648.1, -254.2)	@ 07/21 18:35:00	# AR 11799 obs.												
07/22 02:25:06 - 07/22 03:26:00	Track (	292.4, 233.2)	@ 07/22 02:00:00	# HOP 173 (AR 11793)												
07/22 04:13:00 - 07/22 07:59:54	Track (	-584.6, -259.6)	@ 07/22 04:10:00	# HOP 173 (11799)												
07/22 19:04:36 - 07/23 01:59:54	Track (	-482.9, -266.7)	@ 07/22 18:10:00	# AR cont.												
07/23 02:03:00 - 07/23 03:47:54	Track (	476.7, 239.6)	@ 07/23 02:00:00	# HOP 173 (AR 11793).												
PROG= 05 Inf.-time(s)																
Subr= 2		1-time(s)		2.0sec												
Seqn= 78		1-time(s)		2.0sec												
Open/Ti-poly		Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec		
Open/G-band		Open/G-band	open	Safe	Norm	12ms	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec		
Open/G-band		Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec		
Seqn= 62		4-time(s)		2.0sec												
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec		
Open/thick-Al		Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec		
Seqn= 63		52-time(s)		60.0sec												
Open/Al-mesh		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec		
Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec		
Open/Al-mesh		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	1	2.0sec		
Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	1	2.0sec		
Open/Al-mesh		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	2	2.0sec		
Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	2	2.0sec		
Open/Al-mesh		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	3	2.0sec		
Open/Ti-poly		Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	3	2.0sec		
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interv			

XOB #1999: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 80s cad With G-band and VLS closed														
Term		Pointing (x, y)					Comment							
07/21 05:48:30 - 07/21 17:40:00		Track (124.7, 230.9) @ 07/21 05:45:30					# AR cont.							
07/22 08:03:00 - 07/22 17:59:54		Track (-557.7, -261.7) @ 07/22 08:00:00					# Re-point to AR 11799 to allow possible pointing update on Monday.							
07/23 04:43:06 - 07/23 07:18:30		Track (-406.3, -270.9) @ 07/23 03:58:00					# HOP 173 (AR 11799)							
PROG= 11 Inf.-time(s)														
└─ Subr= 2 1-time(s) 2.0sec														
└─ Seqn= 78 1-time(s) 2.0sec														
└─┐ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec														
└─┐ Open/G-band Open/G-band open Safe Norm 12ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec														
└─┐ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec														
└─ Seqn= 62 4-time(s) 2.0sec														
└─┐ Open/Ti-poly Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec														
└─┐ Open/thick-Al Open/thick-Be close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec														
└─ Sean= 63 45-time(s) 80.0sec														

Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #199C: Flare Standard Obs. with eruptions mode-A (FW1=Open and VLS-Closed)													
Term		Pointing (x, y)					Comment						
07/20 18:17:30 - 07/21 05:35:24		Track (26.7, 230.9) @ 07/20 18:14:30					# AR 11793.						
07/21 05:48:30 - 07/21 17:40:00		Track (124.7, 230.9) @ 07/21 05:45:30					# AR cont.						
07/21 18:38:00 - 07/22 01:52:00		Track (-648.1, -254.2) @ 07/21 18:35:00					# AR 11799 obs.						
07/22 02:25:06 - 07/22 03:26:00		Track (292.4, 233.2) @ 07/22 02:00:00					# HOP 173 (AR 11793)						
07/22 04:13:00 - 07/22 07:59:54		Track (-584.6, -259.6) @ 07/22 04:10:00					# HOP 173 (11799)						
07/22 08:03:00 - 07/22 17:59:54		Track (-557.7, -261.7) @ 07/22 08:00:00					# Re-point to AR 11799 to allow possible pointing update on Monday.						
07/22 19:04:36 - 07/23 01:59:54		Track (-482.9, -266.7) @ 07/22 18:10:00					# AR cont.						
07/23 02:03:00 - 07/23 03:47:54		Track (476.7, 239.6) @ 07/23 02:00:00					# HOP 173 (AR 11793).						
07/23 04:43:06 - 07/23 07:18:30		Track (-406.3, -270.9) @ 07/23 03:58:00					# HOP 173 (AR 11799)						
PROG= 08 1-time(s)													
Subr= 1 30-time(s)		20.0sec											
Seqn= 58 1-time(s)		2.0sec											
Open/thick-Al		Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be		Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 64 1-time(s)		2.0sec											
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s)		2.0sec											
Seqn= 79 1-time(s)		2.0sec											
Open/G-band		Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band		Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Al		Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al		Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s)		60.0sec											
Seqn= 58 1-time(s)		2.0sec											
Open/thick-Al		Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be		Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s)		2.0sec											
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s)		2.0sec											
Seqn= 79 1-time(s)		2.0sec											
Open/G-band		Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band		Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Al		Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al		Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s)		60.0sec											
Seqn= 58 1-time(s)		2.0sec											
Open/thick-Al		Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be		Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s)		2.0sec											
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s)		2.0sec											
Seqn= 79 1-time(s)		2.0sec											
Open/G-band		Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band		Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Al		Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al		Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s)		60.0sec											
Seqn= 58 1-time(s)		2.0sec											
Open/thick-Al		Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be		Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 60 1-time(s)		2.0sec											
Open/Al-mesh		Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly		Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4 24-time(s)		600.0sec											
Seqn= 61 1-time(s)		2.0sec											
Open/Al-mesh		Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly		Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band		Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh		Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter		Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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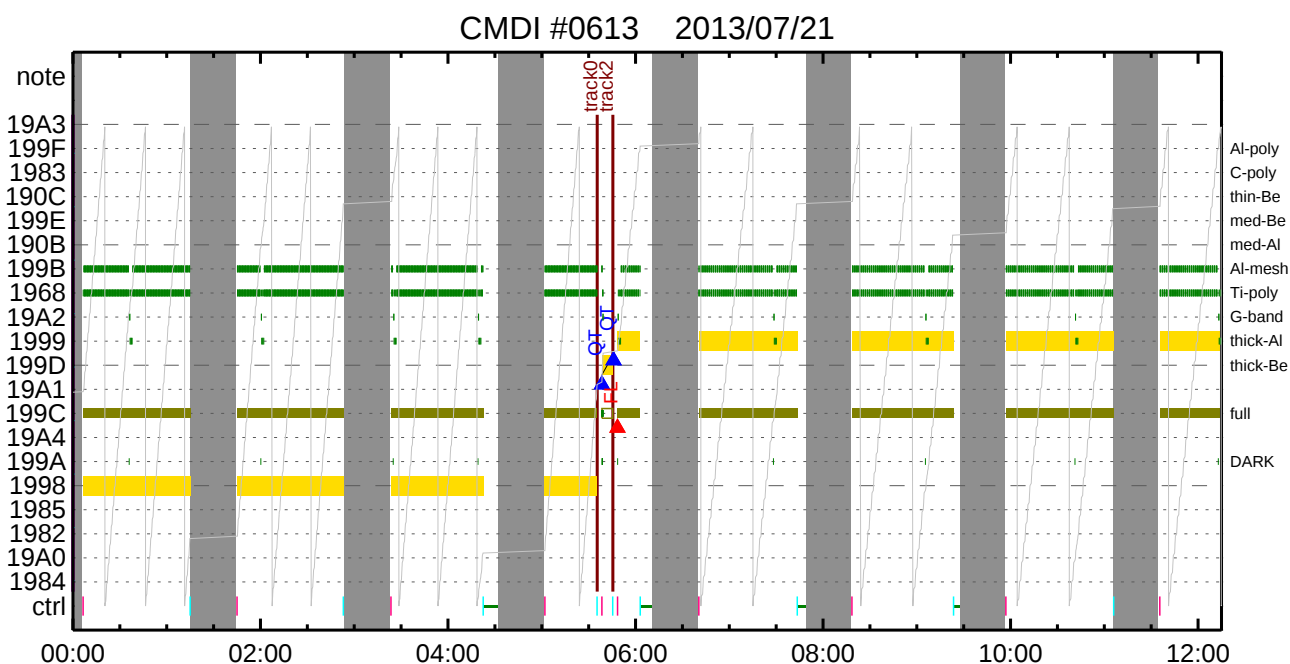
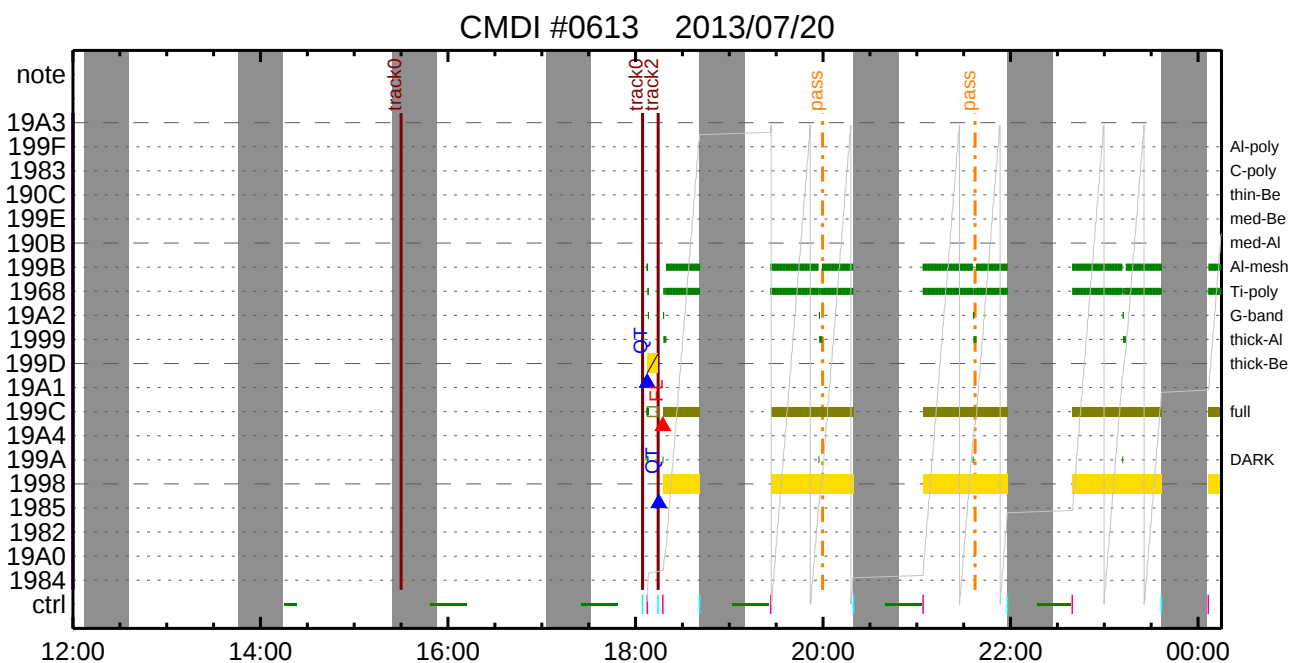
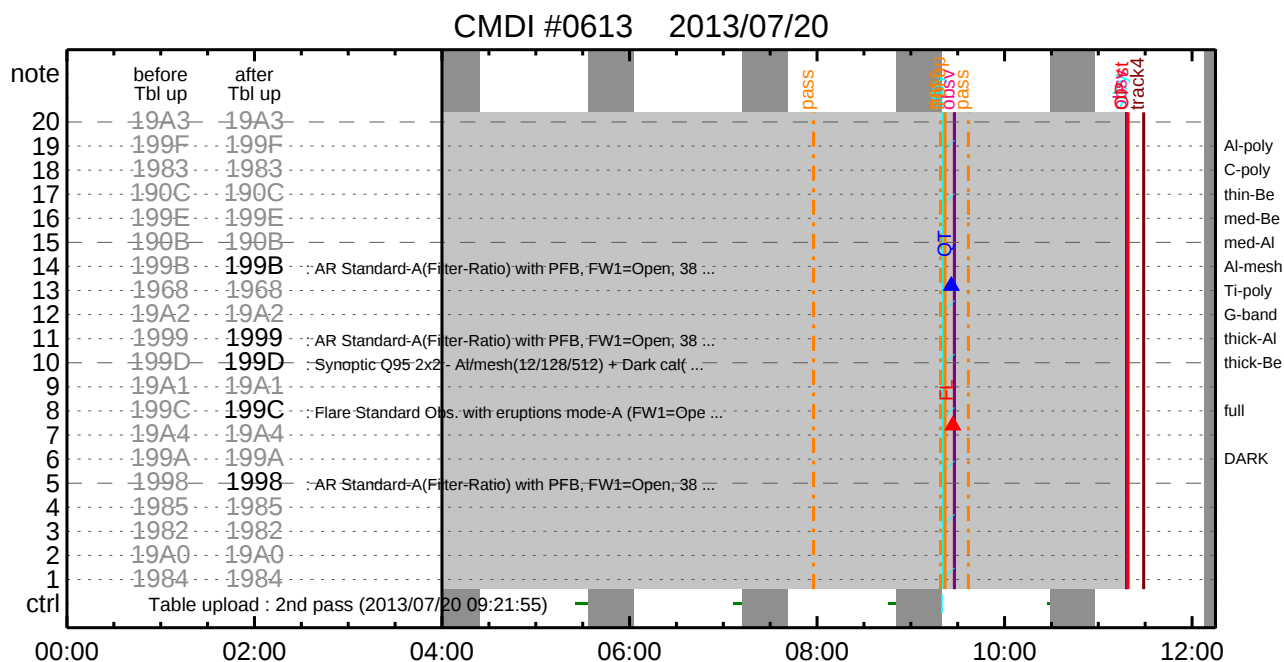
NOT USED

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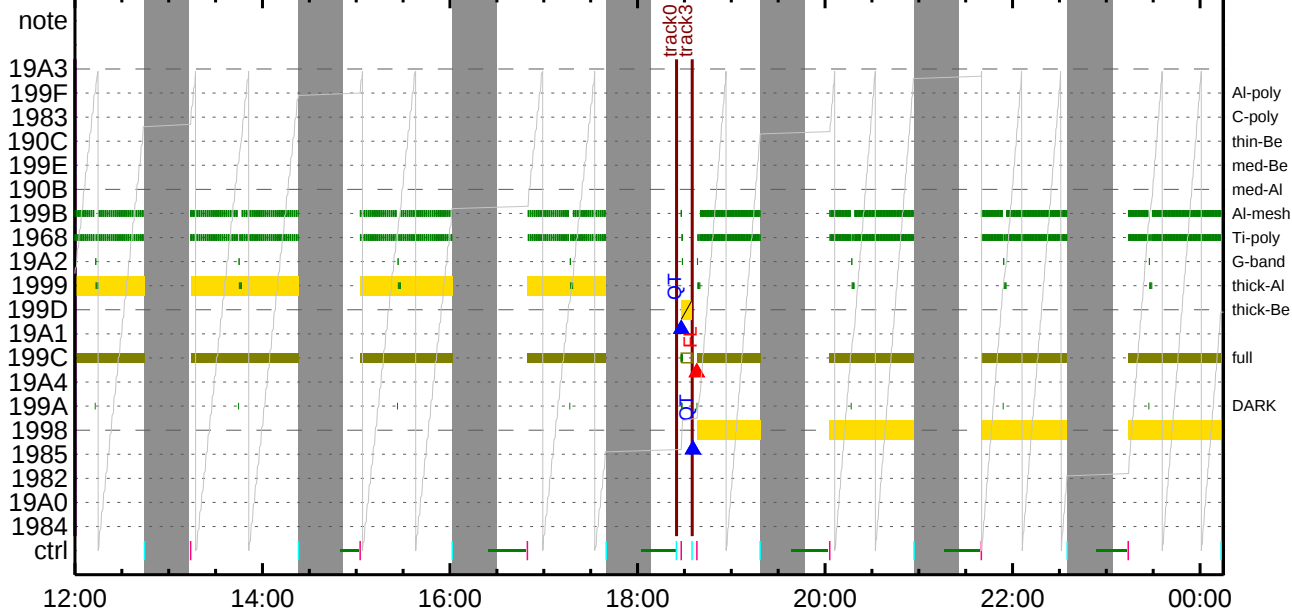
Flare Detection

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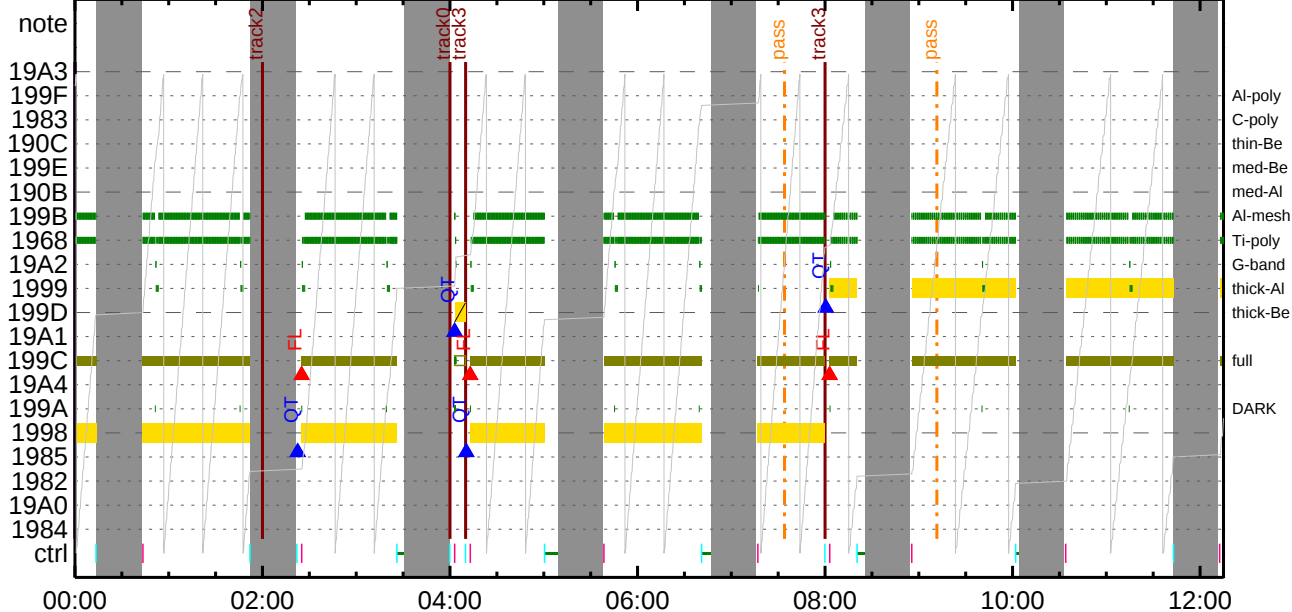
FLD Patrol												
Term			Pointing (x, y)					Comment				
07/20 18:14:46 - 07/21 05:35:46			Track (26.7, 230.9) @ 07/20 18:14:30					# AR 11793.				
07/21 05:45:46 - 07/21 18:25:16			Track (124.7, 230.9) @ 07/21 05:45:30					# AR cont.				
07/21 18:35:16 - 07/22 04:00:16			Track (-648.1, -254.2) @ 07/21 18:35:00					# AR 11799 obs.				
07/22 04:10:16 - 07/22 18:00:16			Track (-584.6, -259.6) @ 07/22 04:10:00					# HOP 173 (11799)				
07/22 19:01:52 - 07/23 03:48:16			Track (-482.9, -266.7) @ 07/22 18:10:00					# AR cont.				
07/23 04:40:22 - 07/25 10:37:00			Track (-406.3, -270.9) @ 07/23 03:58:00					# HOP 173 (AR 11799)				
Open/Ti-poly			Open/thick-Al	close	Safe	Norm	8ms	Obs 8x8	Q=50		30sec	
Default Filter			Thicker Filter	VLS	mode	image	Exp.	CCD Bin	ROI: size (center)		Comp.	AEC Buffer Interval



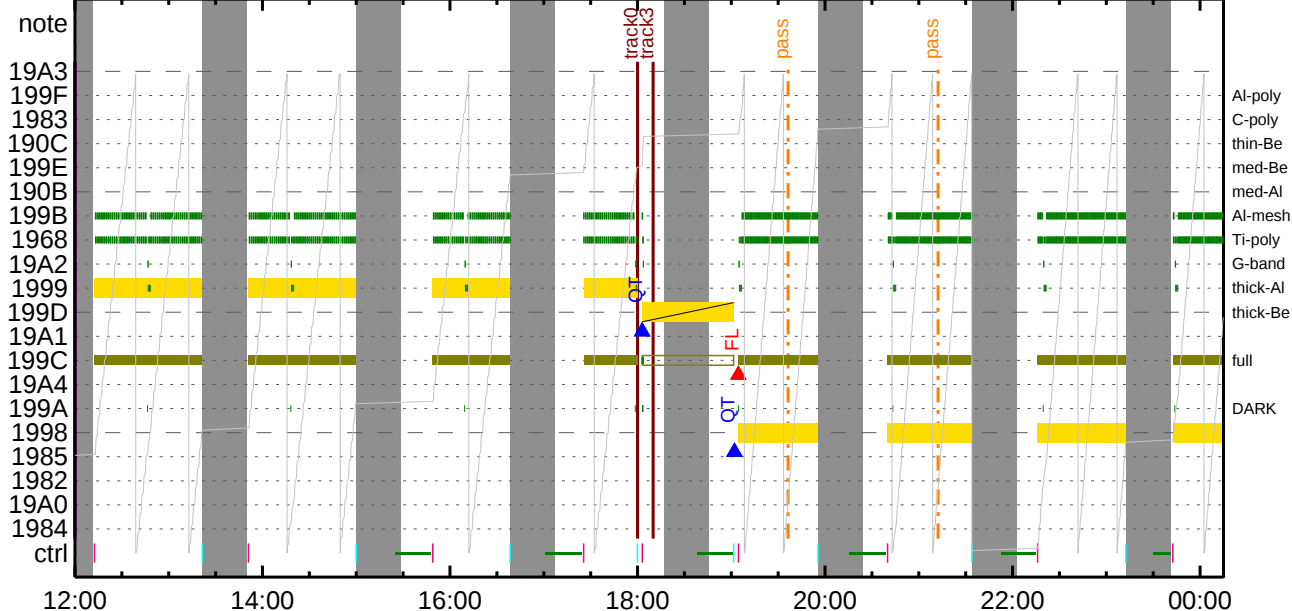
CMDI #0613 2013/07/21



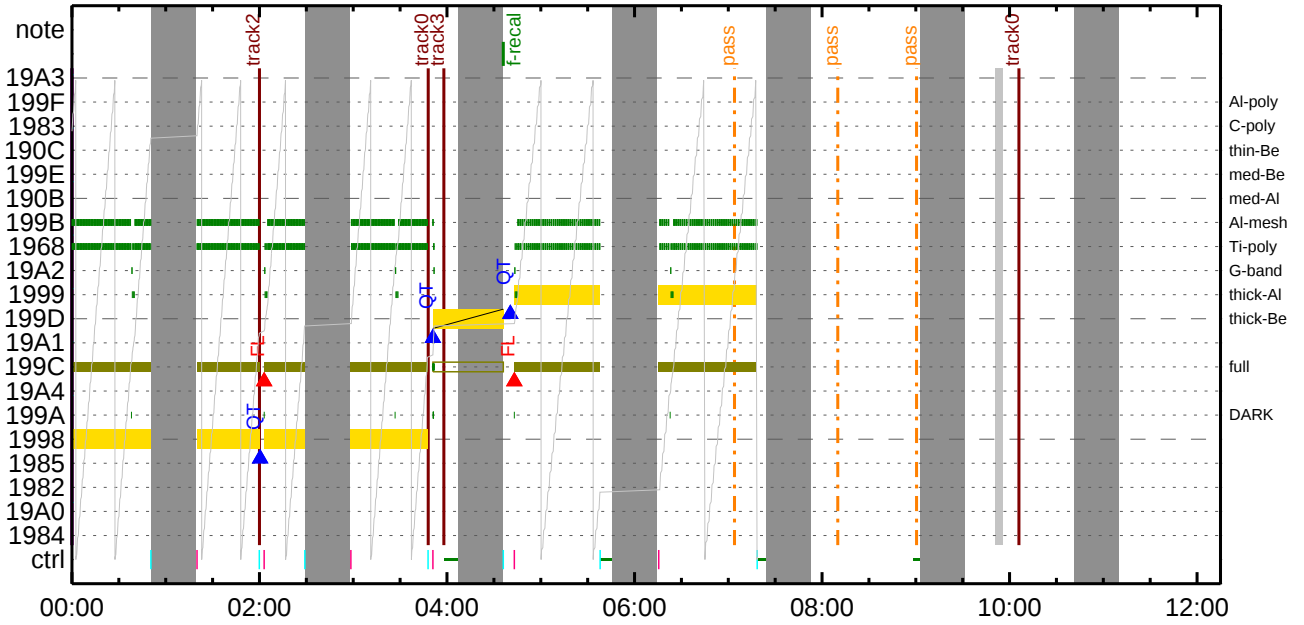
CMDI #0613 2013/07/22



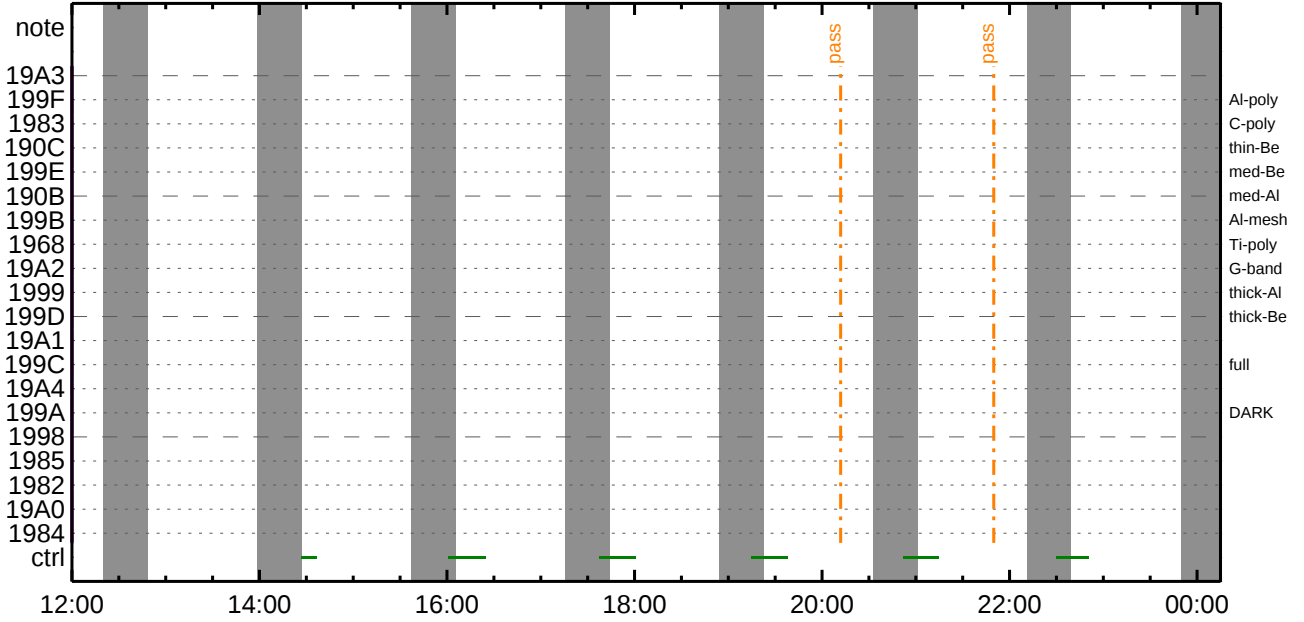
CMDI #0613 2013/07/22



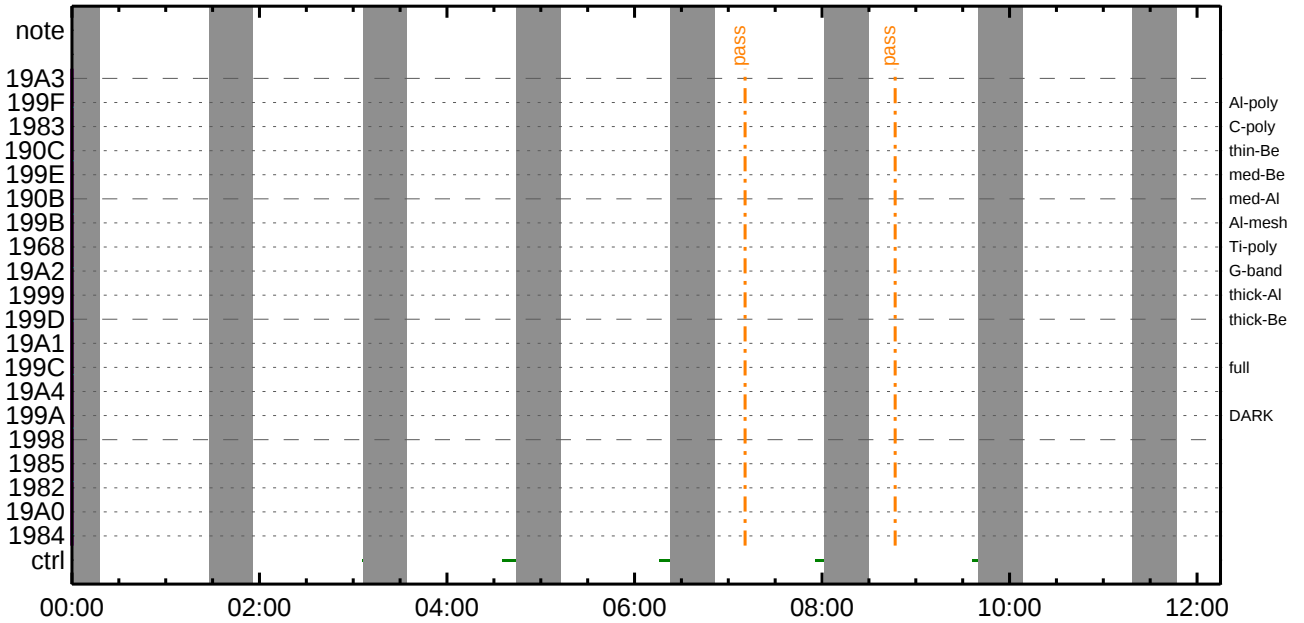
CMDI #0613 2013/07/23



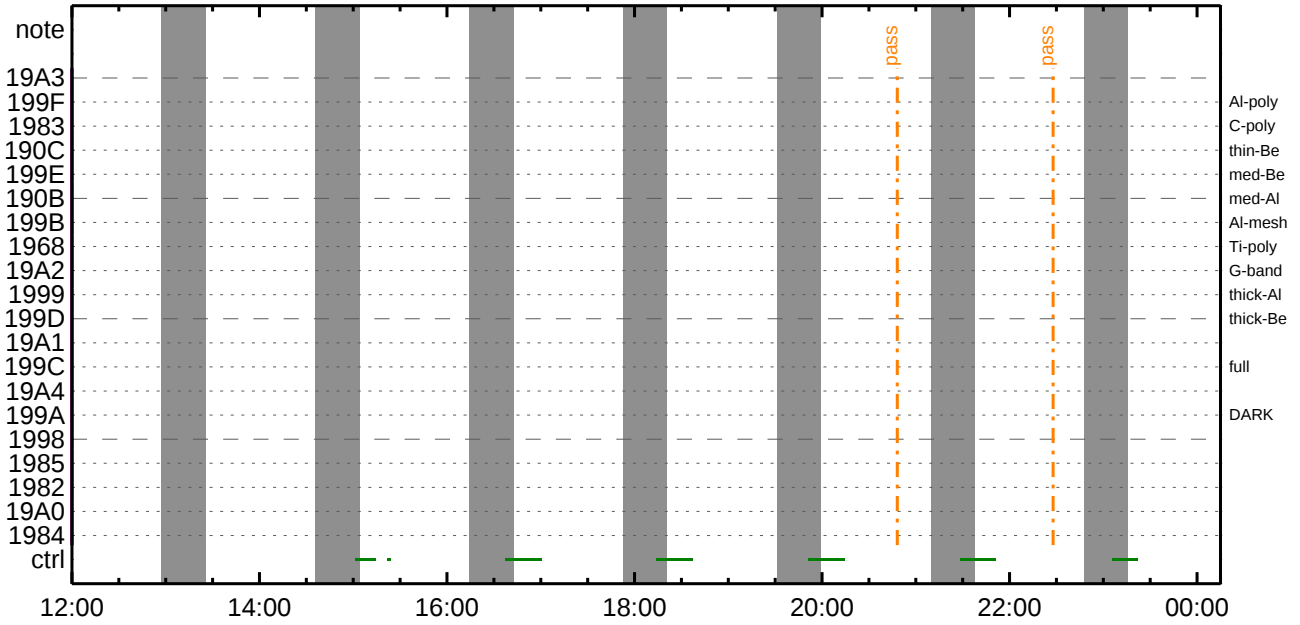
CMDI #0613 2013/07/23



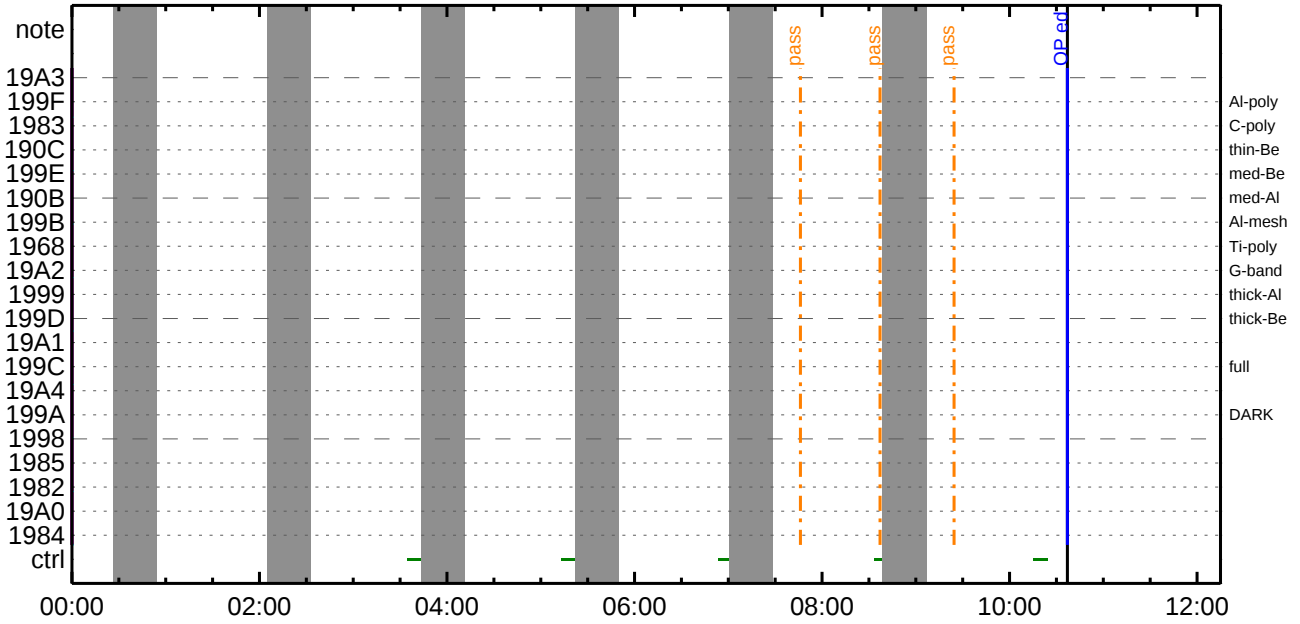
CMDI #0613 2013/07/24



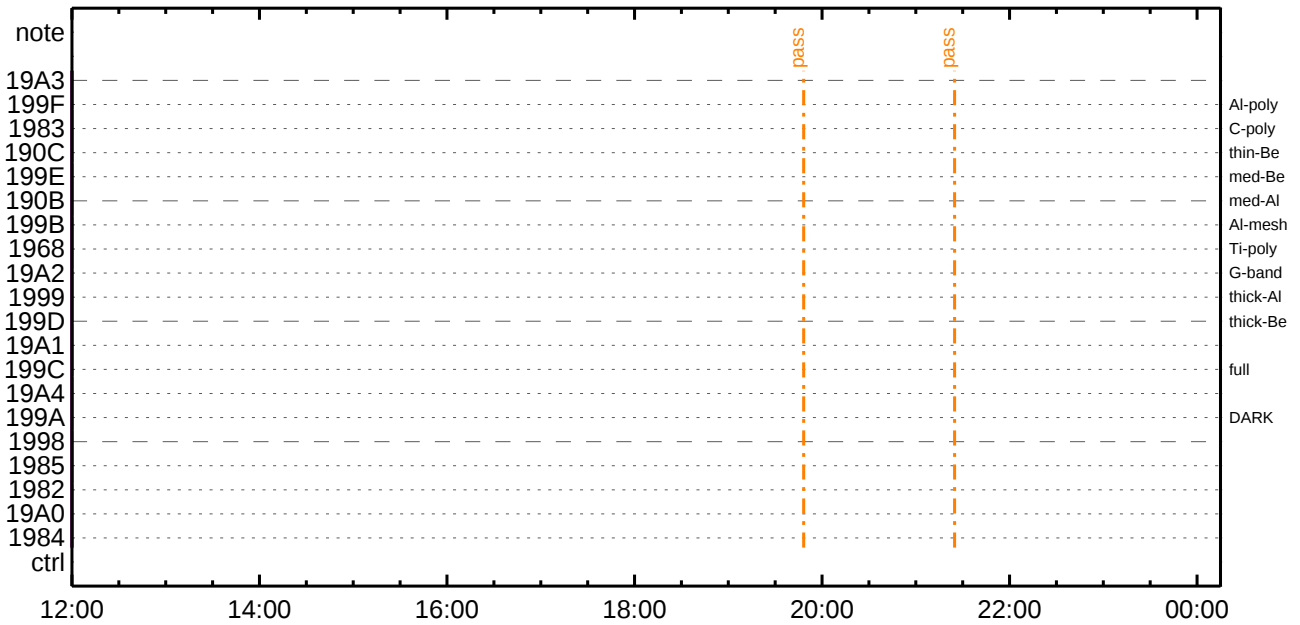
CMDI #0613 2013/07/24



CMDI #0613 2013/07/25



CMDI #0613 2013/07/25



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main-789 2013-07-20 13:27:25 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥$¥Ã¥¬¼Ä»Üjã
0005 C.
0006 C. ¥Ä¥ßj¼¥³¥Ð¥Ó¥ÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Äi j Ê¤¿¤À¤•µ°Æ»Î×Å¤¿¥¤¥Ä¥×¥i j¼¥É;ÊÊè½µ•îÊ;ÊºÊ¼°Ç¤¤•¤¿¼i¹Ç¤İ j ÇÄ®, ù¤¹¤è¤¸¤ÇÄ÷¿®¤¤•¤Ê¤¤¤³¤È j £
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÄ÷¿®µ j ON
0016 C. *****
0017 C. ¨ °ÆÄ, İ×ÊÝ¤àLOS¤¸¤Ç¤İ»Þ´Ö¤ò¹İİ, ¤• j ÇÊÔİ×¤ÊXÄÓON¤İ¹Ô¤Ê¤İ¤Ê¤¤¤³¤È j £
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/L0] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Ð¥Ó¥É¥İ¥Ä¥¬¼ÖÄÔ¤¬°ÄÄê¤¤•¤¿¤¤È j ¢°Ê²¼¤İ°ÆÄ, ¼ê½Ç¤ò¼Ä¹Ô¤¤¹¤è j £
0030 C.
0031 . C. *****
0032 C. DR PT1 Äİ¼i°ÆÄ
0033 C. *****
0034 C. ¨ RESTART j ÊPT1 j Ê¤¤•¤¿¤¤¼i¹Ç¤İ j ¢°Ê²¼¤İ¼Ä¹Ô¤¤¤¤¤ j ¢DCBC-150¤Ø¿Ê¤¤¤ j £
0035 C.
0036 . C. iãPT1°ÆÄ, ³«»İ j ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô, j¼Ú)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, j¼Ú)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, j¼Ú)
0045 C.
0046 . C. iã¥¢¥ó¥Æ¥ÊÄÜÄØ j ÊÄ•Äº²öÊò j Ê, ä¤İ°ÆÄ, °Æ³« j ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô, j¼Ú)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, j¼Ú)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, j¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÄ, ¤¬¼«Æ°Ää»ß¤¤•¤¿, ä j ¢°Ê²¼¤ò¼Ä¹Ô¤¤¹¤è j £
0055 C. ¥¢¥ó¥Æ¥ÊÄÜÄØ¤¤ÄÄ•Äº²öÊò¤¬¼á¤¤¼i¹Ç¤İ'°İ»¤¹¤è¤¸¤ÇÄÔ¤Ä j £
0056 C.
0057 . C. *****
0058 C. DR PT2 Äİ¼i°ÆÄ
0059 C. *****
0060 C. ¨ RESTART j ÊPT2 j Ê¤¤•¤¿¤¤¼i¹Ç¤İ j ¢°Ê²¼¤İ¼Ä¹Ô¤¤¤¤¤ j ¢DCBC-151¤Ø¿Ê¤¤¤ j £
0061 C.
0062 . C. iãPT2°ÆÄ, ³«»İ j ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô, j¼Ú)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, j¼Ú)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, j¼Ú)
0071 C.
0072 . C. iã¥¢¥ó¥Æ¥ÊÄÜÄØ j ÊÄ•Äº²öÊò j Ê, ä¤İ°ÆÄ, °Æ³« j ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô, j¼Ú)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, j¼Ú)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, j¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÄ, Ää»ß j ÇXÄ÷¿®µ j OFF
0081 C. *****
0082 C.
0083 . C. iãDR°ÆÄ, Ää»ß j ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. iãXÄ÷¿®µ j OFF j ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
```

```
0096 C.  
0097 C.  
0098 . C. *****  
0099 C. OP/OG¥í¼¥É;| ¥À¥ó×  
0100 C. *****  
0101 C.  
0102 C. iãOP/OG¥í¼¥É;iä  
0103 S. OP op-789:OP  
0104 (  
0105 S. OG og-789:OG  
0106 )  
0107 C.  
0108 C. iänMOG&OPÎî°è¥À¥ó×;iä  
0109 C. NMOG(0x200000-0x207FFF;$ 32 kbyte)  
+ DC 01-23 DHU_DMA_DMP_PRM_SET  
0111 BC (20 00 7f 01 02)  
0112 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40  
0113 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0  
0114 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127  
0115 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0  
0116 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU  
+ DC 01-22 DHU_MODE_CHNG  
0117 BC (07 0b f8)  
0118 C. ¢¢[HK1_PKT_FORM_NO] EQ 7  
0119 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s  
0120 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k  
0121 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M  
0122 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC  
0123 C.  
0124 C. ¥À¥ó×½ªÎ»ð³İÇŞ  
0125 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON  
0126 C. RAM ID=NMOG¤Î%Ê¹Ç•ë²İOK¤ð³İÇŞ  
0127 C.  
0128 C. NMOG(0x208000-0x20FFFF;$ 32 kbyte)  
+ DC 01-23 DHU_DMA_DMP_PRM_SET  
0130 BC (20 80 7f 01 02)  
0131 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41  
0132 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0  
0133 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127  
0134 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0  
0135 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU  
+ DC 01-22 DHU_MODE_CHNG  
0137 BC (07 0b f8)  
0138 C. ¢¢[HK1_PKT_FORM_NO] EQ 7  
0139 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s  
0140 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k  
0141 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M  
0142 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC  
0143 C. ¥À¥ó×½ªÎ»ð³İÇŞ  
0144 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON  
0145 C. RAM ID=NMOG¤Î%Ê¹Ç•ë²İOK¤ð³İÇŞ  
0146 C.  
0147 C. NMOG(0x210000-0x2100FF;$ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)  
+ DC 01-23 DHU_DMA_DMP_PRM_SET  
0149 BC (21 00 41 01 02)  
0150 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42  
0151 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0  
0152 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65  
0153 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0  
0154 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU  
+ DC 01-22 DHU_MODE_CHNG  
0155 BC (07 0b f8)  
0156 C. ¢¢[HK1_PKT_FORM_NO] EQ 7  
0157 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s  
0158 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k  
0159 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M  
0160 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC  
0161 C.  
0162 C. ¥À¥ó×½ªÎ»ð³İÇŞ  
0163 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON  
0164 C. RAM ID=NMOG,RAM ID=OP¤Î%Ê¹Ç•ë²İOK¤ð³İÇŞ  
0165 C.  
0166 C. **** °È²¼¤İ¾Á´¶Ā¤¤ËÉ¬¤Œ÷Ł® (%āµ-¥À¥ó×½ē¼Ç¤đÄŌǺæ¤Ç½¤¤¨¤ë¾ı₁Ç¤Ç¤â) ****  
0167 C. DHU¥â¼¥É;İ¼Y½, ¥ì¼¥Éjİ¤đİā¤¹  
+ DC 01-22 DHU_MODE_CHNG  
0169 BC (02 0a f8)  
0170 C. ¢¢[HK1_PKT_FORM_NO] EQ 2  
0171 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S  
0172 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K  
0173 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M  
0174 C.  
0175 C. *****  
0176 C. TI-CMD SET (OPOG STOP/COPY/START)  
0177 C. *****  
0178 C.  
0179 C. NOTICE İ$ OPOG UPLOAD¤-Ă÷Ł®NG¤İ¾ı₁Çj¢•È²¼¤İTI-CMDĂ÷Ł®İ¾Ā¹Ŏ¤¤È¤¤¤³¤Ëj£  
0180 C. ¤¤¤ŁjçSET¤EDUMP¤İÆ±°İN¥¹¤Ç₂Ŏ¤ı₃¤Ëj£  
0181 C.  
0182 C. Tİ¥³¥¤¥ó¥É¤đĐİŁ(UT)  
+ TI 2013-07-20 11:14:00.0  
0184 DC 01-B3 DHU_OP_STOP  
0185 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP  
0186 C.  
0187 + TI 2013-07-20 11:14:01.0  
0188 DC 01-B4 DHU_OP_COPY  
0189 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP  
0190 C.  
0191 + TI 2013-07-20 11:14:01.0  
0192 DC 01-B5 DHU_OPOG_COPY  
0193 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
```

0194	C.			
0195	+	TI	2013-07-20 11:18:59.5	
0196	DC	01-B2	DHU_OP_START	
0197	C.		☐☐[HK1_TI_CMD_NUM]	EQ 1COUNTUP
0198	C.			
0199	C.		°Ê²¼☐İÄê%İİÎ☐İ¥Á¥\$¥Á¥¹àİÜ	
0200	C.		☐☐[HK1_TI_CMD_ENA/DIS]	EQ ENA
0201	C.		☐☐[HK1_TI_CMD_NUM]	EQ 4
0202	C.		☐☐[HK1_NEXT_EXEC_PIM]	EQ DHU
0203	C.		☐☐[HK1_NEXT_EXEC_DC]	EQ 0xB3
0204	C.			
0205	C.		*****	
0206	C.		TIİİî°è¥Á¥0¥×	
0207	C.		*****	
0208	C.			
0209	C.		TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)	
0210	+	DC	01-23 DHU_DMA_DMP_PRM_SET	
0211	BC		(03 ab 03 01 02)	
0212	C.		☐☐[HK1_DMP_TOP_ADRS_1]	EQ 07
0213	C.		☐☐[HK1_DMP_TOP_ADRS_0]	EQ 2B
0214	C.		☐☐[HK1_DMP_BLOCK_NUM]	EQ 3
0215	C.		☐☐[HK1_DMP_REPEAT_NUM]	EQ 0
0216	C.		☐☐[HK1_DMA_DMP_PIM]	EQ DHU
0217	+	DC	01-22 DHU_MODE_CHNG	
0218	BC		(07 0b f8)	
0219	C.		☐☐[HK1_PKT_FORM_NO]	EQ 7
0220	C.		☐☐[HK1_PKT_GEN_TIME]	EQ 0.25 s
0221	C.		☐☐[HK1_S_TLM_BIT_RATE]	EQ 32k
0222	C.		☐☐[HK1_X_TLM_BIT_RATE]	EQ 4M
0223	C.		☐☐[HK1_DMP_CHK_FLG]	EQ EXEC
0224	C.			
0225	C.		¥Á¥0¥×½ªİ»☐ð³İÇ\$	
0226	C.		☐☐[HK1_DMP_CHK_FLG]	EQ NON
0227	C.			
0228	C.		RAM ID=TI_TBL☐İ%Ê¹Ç•è²İOK☐ð³İÇ\$	
0229	C.			
0230	C.		DHU¥â;¼¥Ê;Ê¼¥½, ¥İ;¼¥Ê;Ë☐ðİâ☐¹	
0231	+	DC	01-22 DHU_MODE_CHNG	
0232	BC		(02 0a f8)	
0233	C.		☐☐[HK1_PKT_FORM_NO]	EQ 2
0234	C.		☐☐[HK1_PKT_GEN_TIME]	EQ 0.5S
0235	C.		☐☐[HK1_S_TLM_BIT_RATE]	EQ 32K
0236	C.		☐☐[HK1_X_TLM_BIT_RATE]	EQ 4M
0237	C.			
0238	C.		*****	
0239	C.		SOT TI command set	
0240	C.		*****	
0241	C.		Execute, after the success of OP upload.	
0242	+	TI	2013-07-20 11:18:16.0	
0243	DC	07-F0	MDP_SOT_MODE_STBY	
0244	BC		(41)	
0245	C.		-----	
0246	C.		HK1_TI_CMD_NUM = 1 CNTUP []	
0247	C.		-----	
0248	C.		***** SOT END *****	
0249	C.		Stop EIS observation and temporarily disable EIS mode changes	
0250	C.			
0251	C.			
0252	C.		***** Start EIS operation (TI set) *****	
0253	C.		Execute, after the success of OP upload.	
0254	C.		Set EIS TI-commands	
0255	+	TI	2013-07-20 11:18:30.0	
0256	DC	07-FC	EIS_MODE_MANU	
0257	BC		(21 02)	
0258	+	TI	2013-07-20 11:18:40.0	
0259	DC	07-FC	EIS_MODE_CHG_DIS	
0260	BC		(22)	
0261	C.		[] [HK1_TI_CMD_NUM]	EQ 2 COUNTUP
0262	C.		***** End EIS operation (TI set) *****	
0263	C.			
0264	C.			
0265	C.			
0266	C.		***** XRT START *****	
0267	C.		Execute, after the success of OP upload.	
0268	+	TI	2013-07-20 11:18:00.0	
0269	DC	07-F0	MDP_XRT_MODE_STBY	
0270	BC		(c3)	
0271	C.		[] [HK1_TI_CMD_NUM]	EQ 1COUNTUP
0272	C.			
0273	C.		***** XRT END *****	
0274	C.			
0275	C.		***** MDP `ÜÄİ☐İ»0¼¥☐ĒÄð☐¹ēDCBC•×²è *****	
0276	C.		(%â°İ¥0¥Ä¥Ē¥ð¥Ē¥â¥Ç¥ē☐Ē¼☐¼A»Ü☐¹ē)	
0277	S.	DC-BC	dcbc-402:DCBC	
0278			(MDP_known_event)	
0279	C.			
0280	C.			
0281	C.		***** ¥ð¥¹•İ Daily±ÇİÎ☐Ē`ð☐¹ēDCBC•×²è *****	
0282	S.	DC-BC	dcbc-153:DCBC	
0283			(SPECIAL-CMD_DAILY_OPERATIN_DCB)	
0284	C.			
0285	C.			
0286	C.		j äLOS¥Ä¥\$¥Ä¥ ¼Ä»Üj ä	
0287	C.			
0288	C.		***** LOS *****	
0289	C.			

(a) Spacecraft Operation Procedure (real-commands)

```
main-790 2013-07-20 13:27:25 149 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Å»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ĀiĒ¿¿Aa•µ°Æ»İ×AÇaİ¥Ç¥A¥×¥i¼¥ÉiĒĒe½µ•iĒĒiĒE½°ÇÖa•a¿¼i¹ÇaİjÇÀ®, ũa¹aĒaPaÇÁ÷¿®a•aĒaPa³aĒiĒ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 C.
0046 C. ***** XRT START *****
0047 C.
0048 +. DC 07-F0 MDP_XRT_CTRL_MANU
0049 BC (c1)
0050 +. DC 07-F0 MDP_XRT_MODE_STBY
0051 BC (c3)
0052 . C. ----- Success Verify ? OK / NG____
0053 C.
0054 C. XRT Obs. Table Upload
0055 . S. RAM ram-291:MDP_OBS_X
0056 ( )
0057 C.
0058 +. DC 07-F0 MDP_DUMP_XRTTBL
0059 BC (84 07 00 00 00 3a d4)
0060 . C. ----- Comparison Check ? OK / ERR ____
0061 C.
0062 C.
0063 +. DC 07-F0 MDP_XRT_ROI_SET
0064 BC (cd 01 b1 b1 04 04)
0065 +. DC 07-F0 MDP_XRT_ROI_SET
0066 BC (cd 02 b1 b1 08 08)
0067 +. DC 07-F0 MDP_XRT_ROI_SET
0068 BC (cd 03 b1 b1 08 08)
0069 +. DC 07-F0 MDP_XRT_ROI_SET
0070 BC (cd 04 b1 b1 06 06)
0071 +. DC 07-F0 MDP_XRT_ROI_SET
0072 BC (cd 05 85 83 06 06)
0073 +. DC 07-F0 MDP_XRT_ROI_SET
0074 BC (cd 06 85 83 06 06)
0075 +. DC 07-F0 MDP_XRT_ROI_SET
0076 BC (cd 07 80 80 20 20)
0077 +. DC 07-F0 MDP_XRT_ROI_SET
0078 BC (cd 08 80 80 20 08)
0079 +. DC 07-F0 MDP_XRT_ROI_SET
0080 BC (cd 09 80 80 08 20)
0081 +. DC 07-F0 MDP_XRT_ROI_SET
0082 BC (cd 0f 80 80 06 06)
0083 +. DC 07-F0 MDP_XRT_ROI_SET
0084 BC (cd 10 80 80 08 08)
0085 +. DC 07-F0 MDP_XRT_FLD_ENA
0086 BC (d8)
0087 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0088 BC (c8)
0089 +. DC 07-F0 MDP_XRT_AEC_RESET
0090 BC (d0)
0091 +. DC 07-F0 MDP_XRT_ARS_DIS
0092 BC (d5)
0093 +. DC 07-F0 MDP_XRT_FLD_RESET
0094 BC (da)
0095 +. DC 07-F0 MDP_XRT_QT_PROG_SET
```

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0096 BC (c4 0e)
0097 + DC 07-F0 MDP_XRT_FL_PROG_SET
0098 BC (c5 08)
0099 . C. ----- Success Verify ? OK / NG ____
0100 C.
0101 C.
0102 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0103 C.
0104 +. DC 07-F0 MDP_XRT_MODE_OBSV
0105 BC (c2)
0106 +. TI 2013-07-20 11:18:02.0
0107 DC 07-F0 MDP_XRT_MODE_OBSV
0108 BC (c2)
0109 . C. ----- Success Verify ? OK / NG ____
0110 C.
0111 C. ***** XRT END *****
0112 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0113 +. DC 07-FC EIS_MODE_MANU
0114 BC (21 02)
0115 . C. Verify EIS in MANUAL mode
0116 . C. Estimated OBSTBL upload time is 48s
0117 C. *****
0118 C. EIS START OBSTBL LOAD
0119 C. *****
0120 . S. RAM ram-820:EIS_OBSTBL
0121 ( )
0122 +. DC 07-FC EIS_DUMP_OBSTBL
0123 BC (07 07 07 00 00 70 00)
0124 C.
0125 C. Execute, after the success of OBSTBL upload.
0126 C. Set EIS TI-commands
0127 +. TI 2013-07-20 11:18:50.0
0128 DC 07-FC EIS_MODE_CHG_ENA
0129 BC (20)
0130 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0131 C. *****
0132 C. EIS END OBSTBL LOAD
0133 C. *****
0134 C.
0135 . C. ***** MDP '0ÃÎ±Î»Ö%Ý±ÊÂÐ±¹±èDCBC•×²è *****
0136 C. (%ã°î¥0¥Ã¥Ê¥P¥Ê¥â¥C¥è±½¼±¼Ã»Û±¹±è)
0137 . S. DC-BC dcbc-402:DCBC
0138 (MDP_known_event)
0139 C.
0140 C.
0141 . C. ***** ¥Ð¥¹•İ Daily±¿İÑ±Ê´0±¹±èDCBC•×²è *****
0142 . S. DC-BC dcbc-153:DCBC
0143 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0144 C.
0145 C.
0146 . C. ¡ãLOS¥Á¥S¥Ã¥¬¼Ã»Û¡ä
0147 C.
0148 . C. ***** LOS *****
0149 C.

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0096 C.
0097 C.
0098 C. *****
0099 C. SOT table upload
0100 C. *****
0101 C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 C. <Upload FG Observation Table>
0109 S. RAM ram-263:MDP_OBS_F
0110 ( )
0111 C.
0112 C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2013-07-20 11:18:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** MDP `0Ãĩ0ĩ»0%Y0EÃ010E0DCBC•x2è *****
0132 C. (%ã°ĩ¥0¥Ã¥E¥P¥E¥ã¥C¥è0E½00¥A»010E)
0133 S. DC-BC dcbc-402:DCBC
0134 (MDP_known_event)
0135 C.
0136 C.
0137 C. ***** ¥D¥1•ĩ Daily±¿ĩN0E`0010E0DCBC•x2è *****
0138 S. DC-BC dcbc-153:DCBC
0139 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0140 C.
0141 C.
0142 C. jãLOS¥Á¥S¥Ã¥¬¼A»Üjã
0143 C.
0144 C. ***** LOS *****
0145 C.

```

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XRT_OGLIST_0613.chk

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*** OP Sequence for XRT ***

2013/07/20	11:29:00.0	AOCS_ORe-point_Start_1_OG [0x097]		
		AOCU_NM	5	02-76 04 00 00 00 00
2013/07/20	15:30:00.0	AOCS_ORe-point_Start_2_OG [0x098]		
		AOCU_NM	5	02-76 00 00 00 ac 00
2013/07/20	18:04:24.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/07/20	18:04:26.0	XRT_FOCUS_POSITION_403_OG [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2013/07/20	18:04:30.0	AOCS_ORe-point_Start_3_OG [0x099]		
		AOCU_NM	5	02-76 00 00 00 00 00
2013/07/20	18:04:46.0	XRT_FLD_DIS_434_OG [0x1b2]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2013/07/20	18:07:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2013/07/20	18:07:26.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2013/07/20	18:07:28.0	XRT_QT_PROG_SET_441_OG [0x1b9]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0a
2013/07/20	18:07:30.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/07/20	18:14:24.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/07/20	18:14:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2013/07/20	18:14:30.0	AOCS_ORe-point_Start_4_OG [0x09a]		
		AOCU_NM	5	02-76 02 00 00 00 00
2013/07/20	18:14:46.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2013/07/20	18:14:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2013/07/20	18:14:50.0	XRT_AEC_RESET_413_OG [0x19d]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2013/07/20	18:14:52.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2013/07/20	18:14:54.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/07/20	18:14:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 05
2013/07/20	18:17:28.0	XRT_FL_PROG_SET_431_OG [0x1af]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 08
2013/07/20	18:17:30.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/07/20	18:41:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/07/20	18:41:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/07/20	18:41:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/07/20	18:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/07/20	19:25:30.0	XRT_Custom_430_OG [0x1ae]		
2013/07/20	19:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/07/20	20:19:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/07/20	20:19:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/07/20	20:19:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/07/20	20:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/07/20	21:03:00.0	XRT_Custom_430_OG [0x1ae]		
2013/07/20	21:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/07/20	21:58:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/07/20	21:58:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/07/20	21:58:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/07/20	22:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/07/20	22:38:30.0	XRT_Custom_430_OG [0x1ae]		
2013/07/20	22:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/07/20	23:36:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/07/20	23:36:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/07/20	23:36:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/07/20	23:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/07/21	00:05:30.0	XRT_Custom_430_OG [0x1ae]		
2013/07/21	00:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/07/21	01:15:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/07/21	01:15:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/07/21	01:15:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		

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2013/07/21	01:18:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/21	01:44:00.0	XRT_Custom_430_0G [0x1ae]					
2013/07/21	01:45:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/21	02:53:00.0	XRT_CTRL_MANU_400_0G [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/21	02:53:02.0	XRT_FLD_RESET_415_0G [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/21	02:53:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/21	02:56:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/21	03:22:30.0	XRT_Custom_430_0G [0x1ae]					
2013/07/21	03:23:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/21	04:22:30.0	XRT_CTRL_MANU_400_0G [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/21	04:22:32.0	XRT_FLD_RESET_415_0G [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/21	04:22:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/21	04:25:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/21	05:01:00.0	XRT_Custom_430_0G [0x1ae]					
2013/07/21	05:02:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/21	05:35:24.0	XRT_CTRL_MANU_402_0G [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/21	05:35:26.0	XRT_FOCUS_POSITION_403_0G [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/07/21	05:35:30.5	AOCs_ORe-point_Start_3_0G [0x099]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2013/07/21	05:35:46.0	XRT_FLD_DIS_434_0G [0x1b2]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/07/21	05:38:24.0	XRT_FLRCTRL_DIS_405_0G [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/07/21	05:38:26.0	XRT_ARS_DIS_423_0G [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/07/21	05:38:28.0	XRT_QT_PROG_SET_441_0G [0x1b9]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2013/07/21	05:38:30.0	XRT_CTRL_AUTO_408_0G [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/21	05:45:24.0	XRT_CTRL_MANU_402_0G [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/21	05:45:26.0	XRT_FOCUS_POSITION_410_0G [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/07/21	05:45:30.0	AOCs_ORe-point_Start_4_0G [0x09a]					
			AOCU_NM	5	02-76	02 00 00 00 00	
2013/07/21	05:45:46.0	XRT_FLD_ENA_411_0G [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/07/21	05:45:48.0	XRT_FLRCTRL_ENA_412_0G [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/07/21	05:45:50.0	XRT_AEC_RESET_413_0G [0x19d]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/07/21	05:45:52.0	XRT_ARS_DIS_423_0G [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/07/21	05:45:54.0	XRT_FLD_RESET_415_0G [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/21	05:45:56.0	XRT_QT_PROG_SET_422_0G [0x1a6]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2013/07/21	05:48:28.0	XRT_FL_PROG_SET_431_0G [0x1af]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 08	
2013/07/21	05:48:30.0	XRT_CTRL_AUTO_408_0G [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/21	06:03:00.0	XRT_CTRL_MANU_400_0G [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/21	06:03:02.0	XRT_FLD_RESET_415_0G [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/21	06:03:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/21	06:06:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/21	06:39:30.0	XRT_Custom_430_0G [0x1ae]					
2013/07/21	06:40:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/21	07:43:30.0	XRT_CTRL_MANU_400_0G [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/21	07:43:32.0	XRT_FLD_RESET_415_0G [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/21	07:43:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/21	07:46:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/21	08:17:30.0	XRT_Custom_430_0G [0x1ae]					
2013/07/21	08:18:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/21	09:23:30.0	XRT_CTRL_MANU_400_0G [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/21	09:23:32.0	XRT_FLD_RESET_415_0G [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/21	09:23:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]					

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2013/07/21	09:26:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/07/21	09:56:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/21	09:57:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/21	11:06:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/21	11:06:02.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/21	11:06:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/07/21	11:09:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/21	11:34:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/21	11:35:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/21	12:44:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/21	12:44:32.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/07/21	12:44:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/21	12:47:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/21	13:13:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/21	13:14:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/21	14:23:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/21	14:23:02.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/07/21	14:23:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/21	14:26:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/21	15:01:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/21	15:02:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/21	16:01:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/21	16:01:32.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/07/21	16:01:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/21	16:04:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/21	16:48:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/21	16:49:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/21	17:40:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/21	17:40:02.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/07/21	17:40:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/21	17:43:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/07/21	18:24:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/21	18:24:56.0	XRT_FOCUS_POSITION_403_0G [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/07/21	18:25:00.0	AOCs_Ore-point_Start_3_0G [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2013/07/21	18:25:16.0	XRT_FLD_DIS_434_0G [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9
2013/07/21	18:27:54.0	XRT_FLRCTRL_DIS_405_0G [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/07/21	18:27:56.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/07/21	18:27:58.0	XRT_QT_PROG_SET_441_0G [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2013/07/21	18:28:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/07/21	18:34:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/07/21	18:34:56.0	XRT_FOCUS_POSITION_410_0G [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/07/21	18:35:00.0	AOCs_Ore-point_Start_5_0G [0x09b]	AOCU_NM	5	02-76	03 00 00 00 00
2013/07/21	18:35:16.0	XRT_FLD_ENA_411_0G [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/07/21	18:35:18.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/07/21	18:35:20.0	XRT_AEC_RESET_413_0G [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/07/21	18:35:22.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/07/21	18:35:24.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/07/21	18:35:26.0	XRT_QT_PROG_SET_440_0G [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2013/07/21	18:37:58.0	XRT_FL_PROG_SET_431_0G [0x1af]				

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2013/07/21	18:38:00.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2013/07/21	19:18:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/21	19:18:32.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/21	19:18:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/21	19:21:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/21	20:02:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/21	20:03:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]					
2013/07/21	20:57:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/21	20:57:02.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/21	20:57:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/21	21:00:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/21	21:39:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/21	21:40:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]					
2013/07/21	22:35:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/21	22:35:02.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/21	22:35:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/21	22:38:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/21	23:13:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/21	23:14:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]					
2013/07/22	00:13:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/22	00:13:32.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/22	00:13:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/22	00:16:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/22	00:42:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/22	00:43:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]					
2013/07/22	01:52:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/22	01:52:02.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/22	01:52:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/22	01:55:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/22	02:00:00.0	AOCs_Or-point_Start_4_0G [0x09a]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/22	02:22:00.0	XRT_CTRL_MANU_402_0G [0x192]	AOCU_NM	5	02-76	02	00 00 00 00
2013/07/22	02:22:02.0	XRT_FOCUS_POSITION_410_0G [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/22	02:22:22.0	XRT_FLD_ENA_411_0G [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2013/07/22	02:22:24.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/07/22	02:22:26.0	XRT_AEC_RESET_413_0G [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/07/22	02:22:28.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/07/22	02:22:30.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/07/22	02:22:32.0	XRT_QT_PROG_SET_440_0G [0x1b8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/22	02:25:04.0	XRT_FL_PROG_SET_431_0G [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05
2013/07/22	02:25:06.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2013/07/22	03:26:00.0	XRT_CTRL_MANU_400_0G [0x190]					
2013/07/22	03:26:02.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/07/22	03:26:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/22	03:29:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/07/22	03:59:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/07/22	03:59:56.0	XRT_FOCUS_POSITION_403_0G [0x193]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/07/22	04:00:00.0	AOCs_Or-point_Start_3_0G [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/07/22	04:00:16.0	XRT_FLD_DIS_434_0G [0x1b2]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
			AOCU_NM	5	02-76	00	00 00 00 00
			MDP_XRT_FLD_DIS	1	07-F0	d9	

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2013/07/22	04:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2013/07/22	04:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2013/07/22	04:02:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0a
2013/07/22	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/07/22	04:09:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/07/22	04:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2013/07/22	04:10:00.0	AOCS_ORe-point_Start_5_OG [0x09b]		
		AOCU_NM	5	02-76 03 00 00 00 00
2013/07/22	04:10:16.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2013/07/22	04:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2013/07/22	04:10:20.0	XRT_AEC_RESET_413_OG [0x19d]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2013/07/22	04:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2013/07/22	04:10:24.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/07/22	04:10:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 05
2013/07/22	04:12:58.0	XRT_FL_PROG_SET_431_OG [0x1af]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 08
2013/07/22	04:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/07/22	05:00:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/07/22	05:00:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/07/22	05:00:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/07/22	05:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/07/22	05:37:30.0	XRT_Custom_430_OG [0x1ae]		
2013/07/22	05:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/07/22	06:41:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/07/22	06:41:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/07/22	06:41:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/07/22	06:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/07/22	07:16:00.0	XRT_Custom_430_OG [0x1ae]		
2013/07/22	07:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/07/22	07:59:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/07/22	07:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2013/07/22	08:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b]		
		AOCU_NM	5	02-76 03 00 00 00 00
2013/07/22	08:00:16.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2013/07/22	08:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2013/07/22	08:00:20.0	XRT_AEC_RESET_413_OG [0x19d]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2013/07/22	08:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2013/07/22	08:00:24.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/07/22	08:00:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0b
2013/07/22	08:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 08
2013/07/22	08:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/07/22	08:20:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/07/22	08:20:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/07/22	08:20:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/07/22	08:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/07/22	08:54:30.0	XRT_Custom_430_OG [0x1ae]		
2013/07/22	08:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/07/22	10:02:00.5	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/07/22	10:02:02.5	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/07/22	10:02:04.5	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/07/22	10:05:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9

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2013/07/22	10:33:00.0	XRT_Custom_430_OG [0x1ae]		
2013/07/22	10:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/07/22	11:43:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/07/22	11:43:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/07/22	11:43:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/07/22	11:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/07/22	12:11:30.0	XRT_Custom_430_OG [0x1ae]		
2013/07/22	12:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/07/22	13:21:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/07/22	13:21:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/07/22	13:21:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/07/22	13:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/07/22	13:50:00.0	XRT_Custom_430_OG [0x1ae]		
2013/07/22	13:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/07/22	15:00:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/07/22	15:00:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/07/22	15:00:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/07/22	15:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/07/22	15:48:00.0	XRT_Custom_430_OG [0x1ae]		
2013/07/22	15:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/07/22	16:38:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/07/22	16:38:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/07/22	16:38:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/07/22	16:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/07/22	17:24:30.0	XRT_Custom_430_OG [0x1ae]		
2013/07/22	17:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/07/22	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/07/22	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2013/07/22	18:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]		
		AOCU_NM	5	02-76 00 00 00 00 00
2013/07/22	18:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2013/07/22	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2013/07/22	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2013/07/22	18:02:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0a
2013/07/22	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/07/22	18:10:00.0	AOCS_ORe-point_Start_5_OG [0x09b]		
		AOCU_NM	5	02-76 03 00 00 00 00
2013/07/22	19:01:30.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/07/22	19:01:32.0	XRT_FOCUS_POSITION_410_OG [0x19a]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2013/07/22	19:01:52.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2013/07/22	19:01:54.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2013/07/22	19:01:56.0	XRT_AEC_RESET_413_OG [0x19d]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2013/07/22	19:01:58.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2013/07/22	19:02:00.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/07/22	19:02:02.0	XRT_QT_PROG_SET_440_OG [0x1b8]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 05
2013/07/22	19:04:34.0	XRT_FL_PROG_SET_431_OG [0x1af]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 08
2013/07/22	19:04:36.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/07/22	19:55:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/07/22	19:55:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/07/22	19:55:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/07/22	19:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9

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2013/07/23	04:40:22.0	XRT_FLD_ENA_411_0G [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2013/07/23	04:40:24.0	XRT_FLRCTRL_ENA_412_0G [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2013/07/23	04:40:26.0	XRT_AEC_RESET_413_0G [0x19d]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2013/07/23	04:40:28.0	XRT_ARS_DIS_423_0G [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2013/07/23	04:40:30.0	XRT_FLD_RESET_415_0G [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/07/23	04:40:32.0	XRT_QT_PROG_SET_422_0G [0x1a6]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 0b
2013/07/23	04:43:04.0	XRT_FL_PROG_SET_431_0G [0x1af]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 08
2013/07/23	04:43:06.0	XRT_CTRL_AUTO_408_0G [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/07/23	05:38:00.0	XRT_CTRL_MANU_400_0G [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/07/23	05:38:02.0	XRT_FLD_RESET_415_0G [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/07/23	05:38:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/07/23	05:41:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/07/23	06:14:30.0	XRT_Custom_430_0G [0x1ae]		
2013/07/23	06:15:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/07/23	07:18:30.0	XRT_CTRL_MANU_400_0G [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/07/23	07:18:32.0	XRT_FLD_RESET_415_0G [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/07/23	07:18:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/07/23	07:21:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/07/23	10:06:00.0	AOCS_ORe-point_Start_3_0G [0x099]		
		AOCU_NM	5	02-76 00 00 00 00 00