

XRT Timeline to be uploaded on 2013/08/03

Period: 2013/08/03 09:04:00 - 2013/08/08 09:43:00

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Normal mode

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XOB #19A8: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 512x512 at 1064 1048, 100s cad With G-band and VLS closed												
Term	Pointing (x, y)	Comment										
08/03 09:44:00 - 08/03 15:34:54	Fixed (-920.0, -216.0)	# OP start + 10min, Limb AR observations for EIS.										
08/04 06:13:00 - 08/04 15:49:54	Track (-427.5, 70.6) <small>@ 08/04 06:10:00</small>	# AR 11809 obs. (HOP-234 from 10-12UT).										
08/05 06:17:00 - 08/05 18:05:54	Track (-224.4, 61.5) <small>@ 08/05 06:14:00</small>	# AR cont. (HOP-234 from 10-12UT).										
PROG= 02 Inf.-time(s)												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 91 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
└─ Seqn= 90 4-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 92 40-time(s) 100.0sec												
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #199E: Synoptic Q95 2x2 - Al/mesh(12/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(33/362/1443) + G-b												
Term	Pointing (x, y)	Comment										
08/03 15:38:00 - 08/03 15:44:54	Fixed (0.0, 0.0)	synoptic, shifted a few hours										
08/04 06:03:00 - 08/04 06:09:54	Fixed (0.0, 0.0)	Disk center flat-fields for SOT. XRT synoptic at 6UT.										
08/04 15:53:00 - 08/04 15:59:54	Fixed (0.0, 0.0)	synoptic, shifted a few hours										
08/05 06:07:00 - 08/05 06:13:54	Fixed (0.0, 0.0)	synoptic, shifted 4.0 min										
08/05 18:09:00 - 08/05 18:15:54	Fixed (0.0, 0.0)	synoptic, shifted 6.0 min										
08/06 05:39:30 - 08/06 05:46:24	Fixed (0.0, 0.0)	synoptic, shifted -23.5 min										
PROG= 16 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 82 1-time(s) 4.0sec												
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 83 1-time(s) 4.0sec												
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 66 1-time(s) 2.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
└─ Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #199A: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 100s cad With G-band and VLS closed												
Term	Pointing (x, y)	Comment										
08/03 16:38:00 - 08/03 21:59:54	Track (-538.7, 78.1) <small>@ 08/03 15:45:00</small>	# HOP-233 (AR 11809).										
08/04 16:03:00 - 08/05 05:26:30	Track (-346.8, 66.3) <small>@ 08/04 16:00:00</small>	# HOP-233 (AR 11809) between 16-22UT.										
08/05 18:19:00 - 08/06 05:36:24	Track (-117.5, 58.9) <small>@ 08/05 18:16:00</small>	# AR cont.										
08/06 05:49:30 - 08/06 08:31:00	Track (-13.8, 57.6) <small>@ 08/06 05:46:30</small>	# AR cont.										
PROG= 06 Inf.-time(s)												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 78 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Seqn= 62 4-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 63 40-time(s) 100.0sec												
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec

Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #190B: G-Band Alignment with North Pole Q90 2x2(G-band only) - 12msec - 5min cadence - Partial Sun-wNGT-2

Term	Pointing (x, y)	Comment
08/03 22:15:00 - 08/03 23:59:54	Fixed (0.0, 930.0)	# Coalignment at North Pole/
PROG= 15 1-time(s)		
└ Subr= 1 1-time(s) 360.0sec		
└└ Seqn= 32 24-time(s) 300.0sec		
└└└ Open/G-band Open/G-band open Safe Norm 12ms Obs 2x2 2048x1536 (1024, 768) Q=90 0 0 2.0sec		
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval		

XOB #190C: G-Band Alignment with East limb Q90 2x2 (G-band only) - 12msec- 8 min cadence-wNGT-2

Term	Pointing (x, y)	Comment
08/04 00:21:30 - 08/04 01:49:30	Fixed (-945.0, 0.0)	# Coalignment at East limb.
PROG= 17 1-time(s)		
└ Subr= 1 1-time(s) 360.0sec		
└└ Seqn= 33 15-time(s) 480.0sec		
└└└ Open/G-band Open/G-band open Safe Norm 12ms Obs 2x2 1536x2048 (1280, 1024) Q=90 0 0 2.0sec		
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval		

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Flare mode

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XOB #199C: Flare Standard Obs. with eruptions mode-A (FW1=Open and VLS-Closed)

Term	Pointing (x, y)	Comment
08/03 09:44:00 - 08/03 15:34:54	Fixed (-920.0, -216.0)	# OP start + 10min, Limb AR observations for EIS.
08/03 16:38:00 - 08/03 21:59:54	Track (-538.7, 78.1) @ 08/03 15:45:00	# HOP-233 (AR 11809).
08/04 06:13:00 - 08/04 15:49:54	Track (-427.5, 70.6) @ 08/04 06:10:00	# AR 11809 obs. (HOP-234 from 10-12UT).
08/04 16:03:00 - 08/05 05:26:30	Track (-346.8, 66.3) @ 08/04 16:00:00	# HOP-233 (AR 11809) between 16-22UT.
08/05 06:17:00 - 08/05 18:05:54	Track (-224.4, 61.5) @ 08/05 06:14:00	# AR cont. (HOP-234 from 10-12UT).
08/05 18:19:00 - 08/06 05:36:24	Track (-117.5, 58.9) @ 08/05 18:16:00	# AR cont.
08/06 05:49:30 - 08/06 08:31:00	Track (-13.8, 57.6) @ 08/06 05:46:30	# AR cont.

PROG= 08 1-time(s)												
└ Subr= 1 30-time(s) 20.0sec												
└└ Seqn= 58 1-time(s) 2.0sec												
└└└ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└└└ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└└ Seqn= 64 1-time(s) 2.0sec												
└└└ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec												
└└└ Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec												
└ Subr= 2 1-time(s) 2.0sec												
└└ Seqn= 79 1-time(s) 2.0sec												
└└└ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└└└ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 384x384 (1024, 1024) DPCM 0 0 2.0sec												
└└└ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└└└ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec												
└ Subr= 3 30-time(s) 60.0sec												
└└ Seqn= 58 1-time(s) 2.0sec												
└└└ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└└└ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└└ Seqn= 60 1-time(s) 2.0sec												
└└└ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec												
└└└ Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec												
└ Subr= 2 1-time(s) 2.0sec												
└└ Seqn= 79 1-time(s) 2.0sec												
└└└ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└└└ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 384x384 (1024, 1024) DPCM 0 0 2.0sec												
└└└ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└└└ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec												
└ Subr= 3 30-time(s) 60.0sec												
└└ Seqn= 58 1-time(s) 2.0sec												
└└└ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└└└ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec												
└└ Seqn= 60 1-time(s) 2.0sec												
└└└ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec												
└└└ Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec												
└ Subr= 2 1-time(s) 2.0sec												
└└ Seqn= 79 1-time(s) 2.0sec												
└└└ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└└└ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 384x384 (1024, 1024) DPCM 0 0 2.0sec												
└└└ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec												
└└└ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec												

		Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
		Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
		Seqn= 60	1-time(s)	2.0sec										
		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
		Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
		Subr= 4	24-time(s)	600.0sec										
		Seqn= 61	1-time(s)	2.0sec										
		Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
		Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
		Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

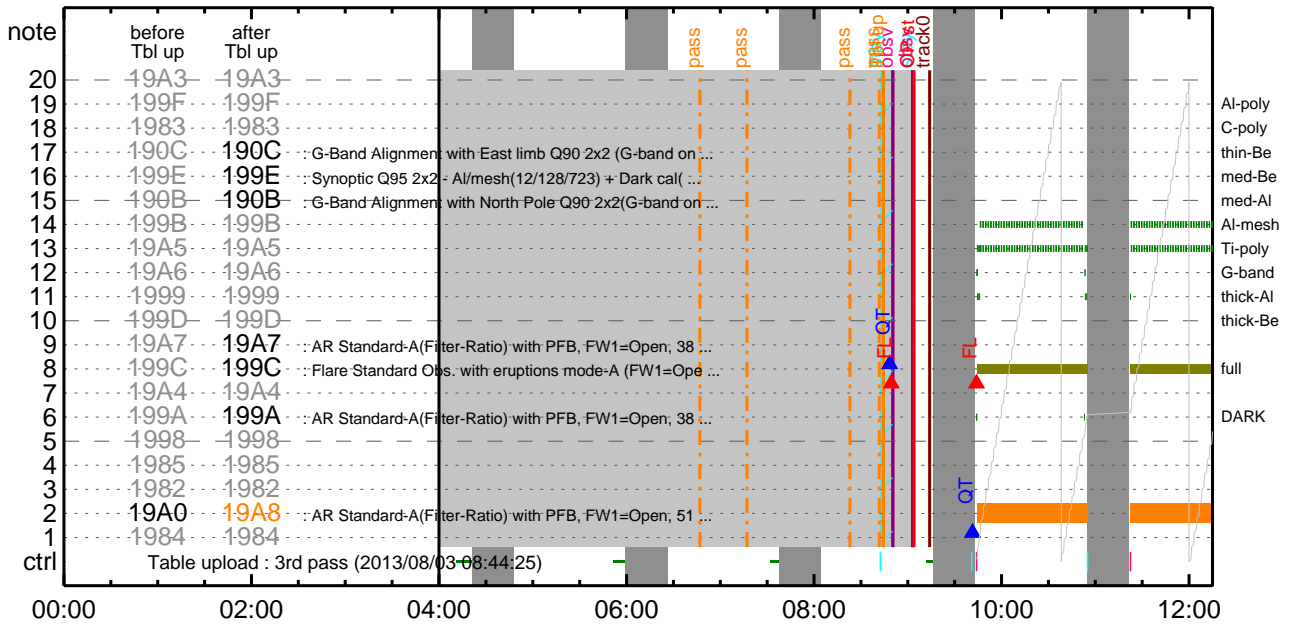
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Flare Detection

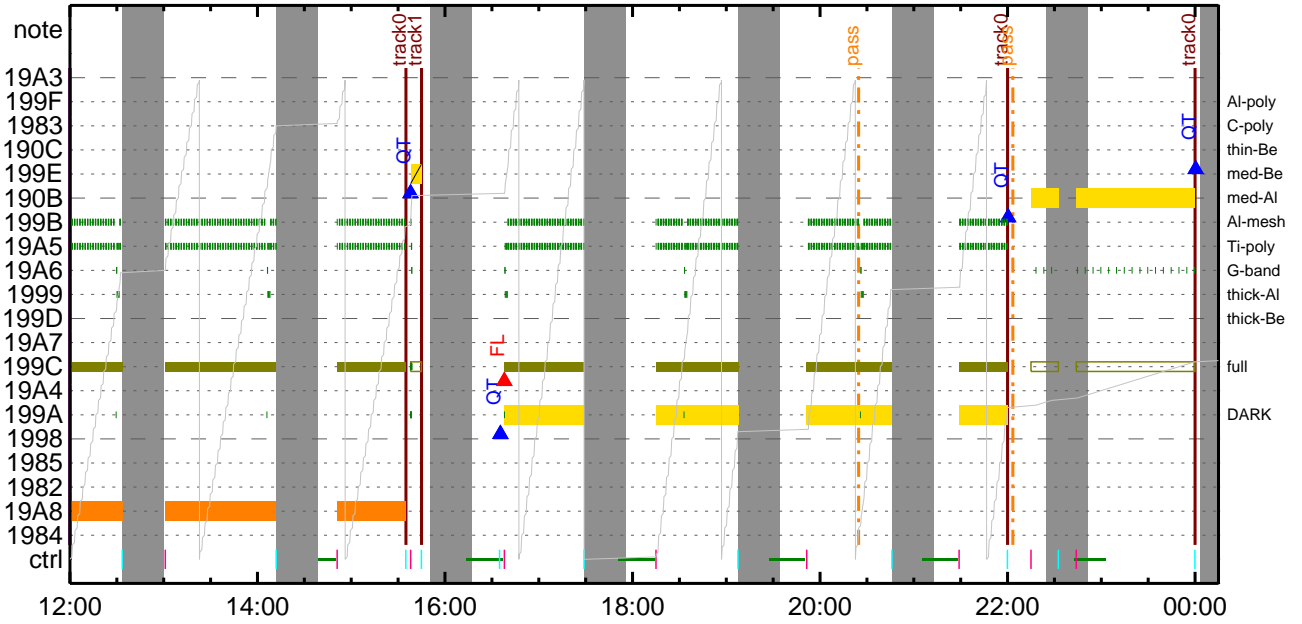
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FLD Patrol													
Term		Pointing (x, y)						Comment					
08/03	16:35:16 - 08/03 22:00:16	Track (-538.7,	78.1)	@ 08/03 15:45:00	# HOP-233 (AR 11809).								
08/04	06:10:16 - 08/04 15:50:16	Track (-427.5,	70.6)	@ 08/04 06:10:00	# AR 11809 obs. (HOP-234 from 10-12UT).								
08/04	16:00:16 - 08/05 06:04:16	Track (-346.8,	66.3)	@ 08/04 16:00:00	# HOP-233 (AR 11809) between 16-22UT.								
08/05	06:14:16 - 08/05 18:06:16	Track (-224.4,	61.5)	@ 08/05 06:14:00	# AR cont. (HOP-234 from 10-12UT).								
08/05	18:16:16 - 08/06 05:36:46	Track (-117.5,	58.9)	@ 08/05 18:16:00	# AR cont.								
08/06	05:46:46 - 08/08 09:43:00	Track (-13.8,	57.6)	@ 08/06 05:46:30	# AR cont.								
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50			30sec	
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

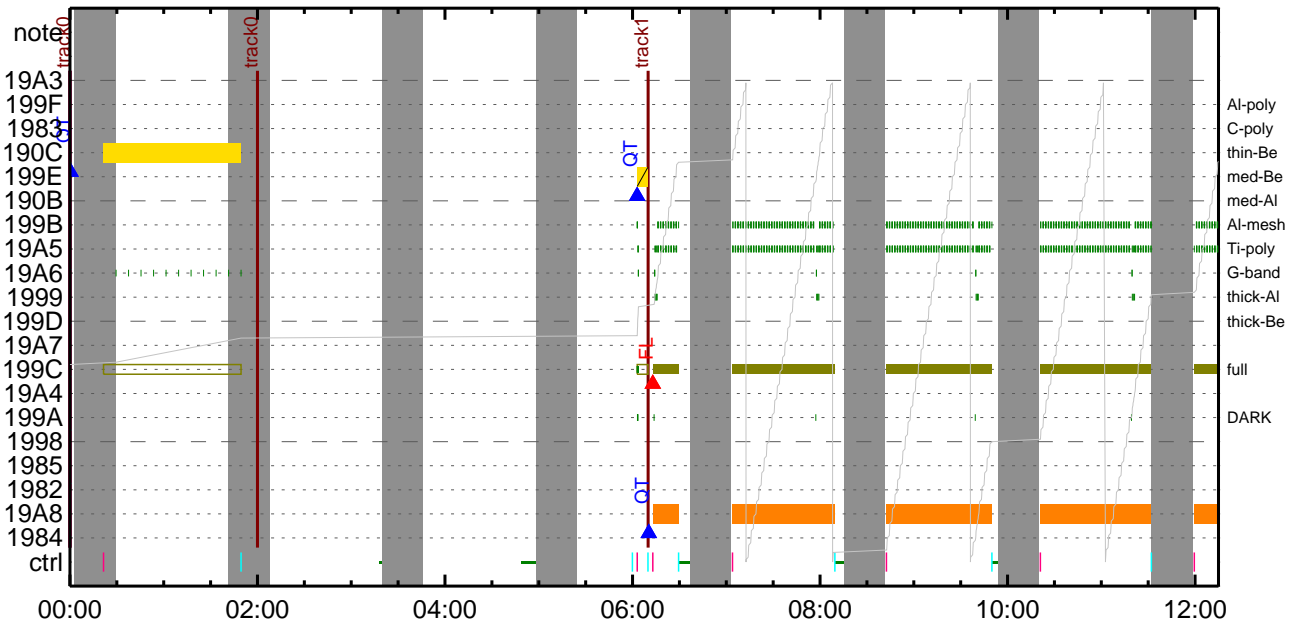
CMDI #0641 2013/08/03



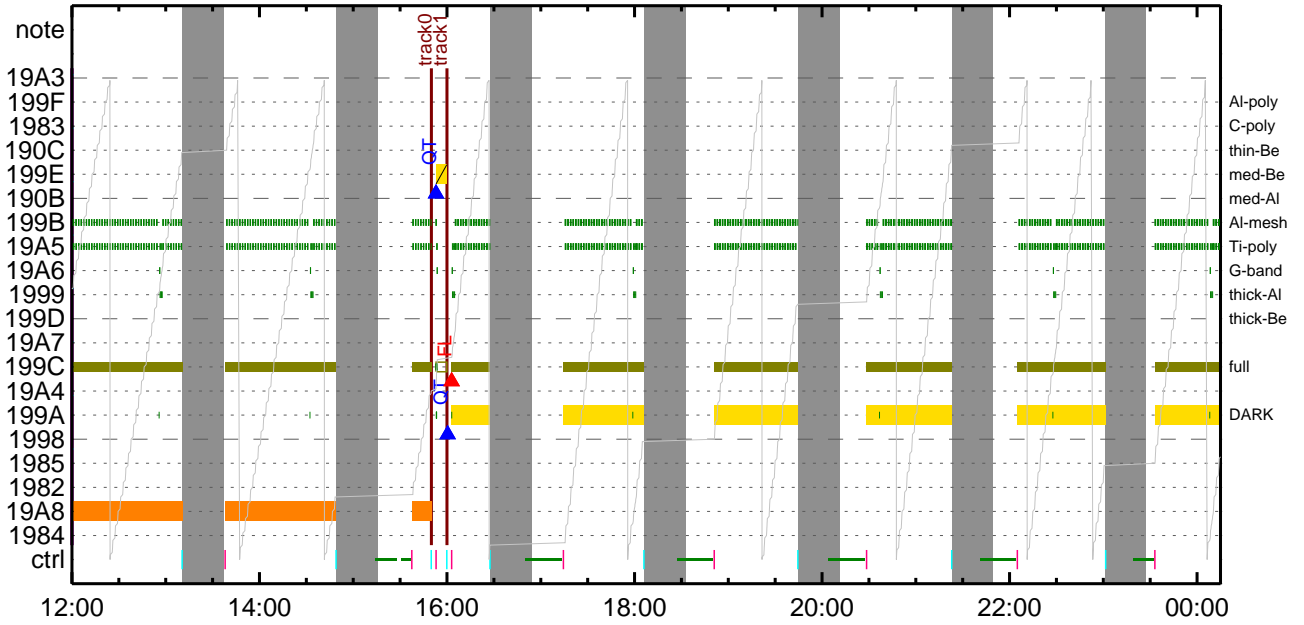
CMDI #0641 2013/08/03



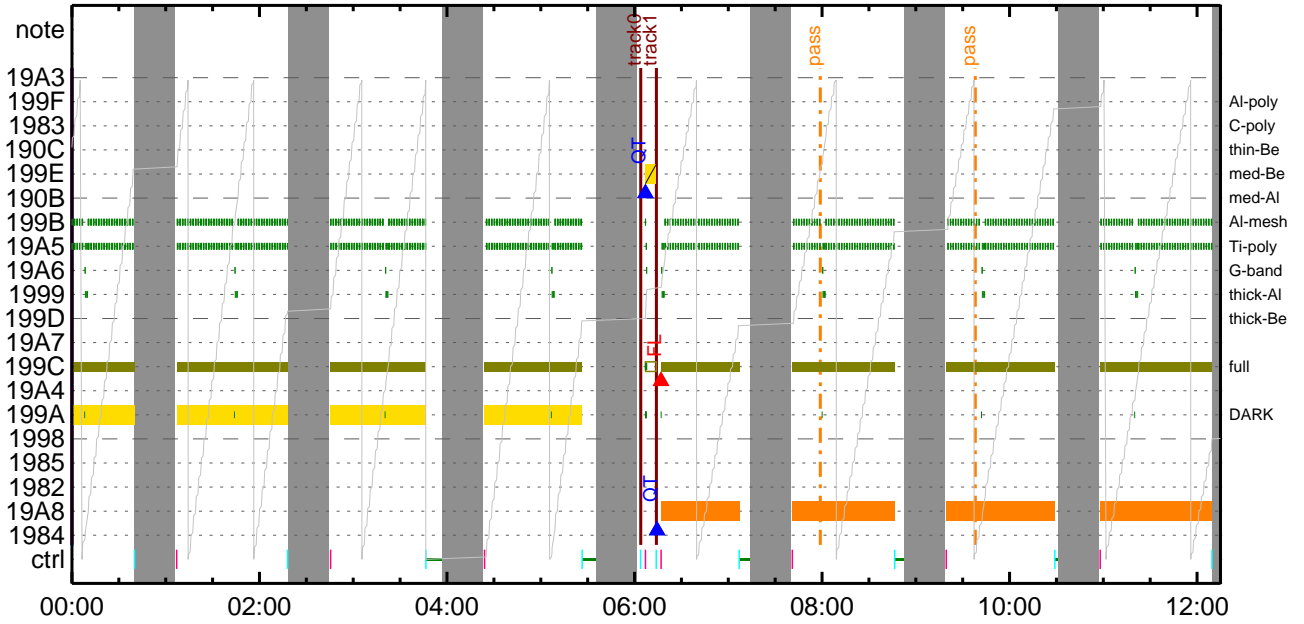
CMDI #0641 2013/08/04



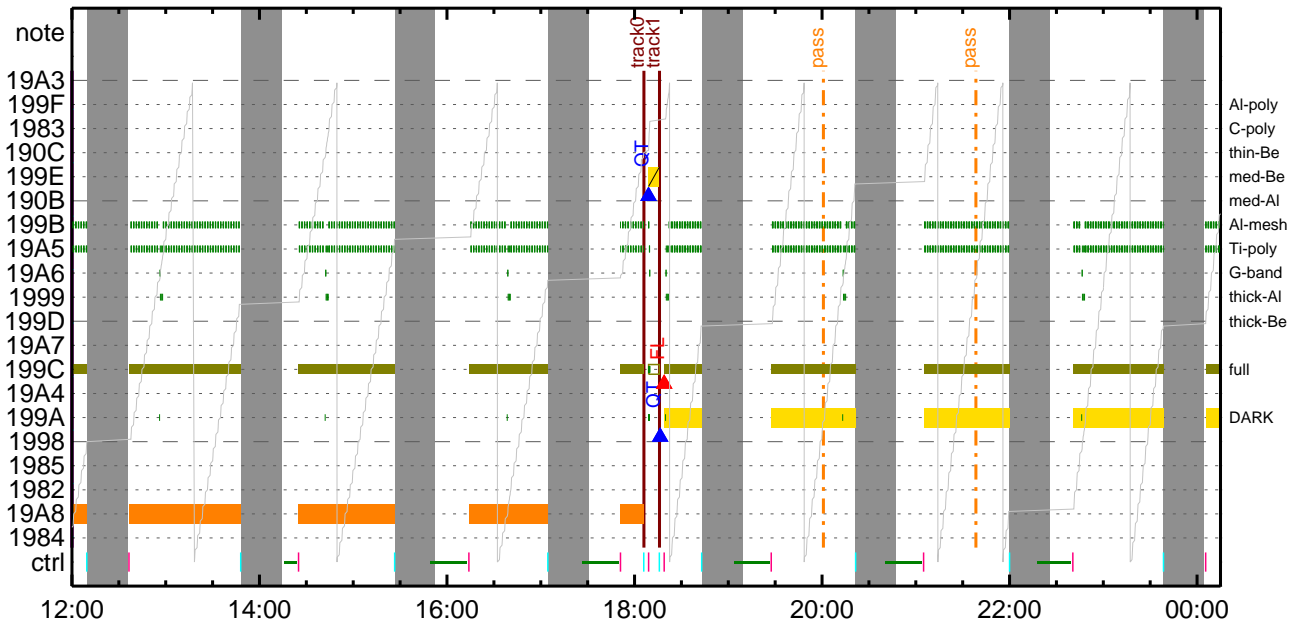
CMDI #0641 2013/08/04



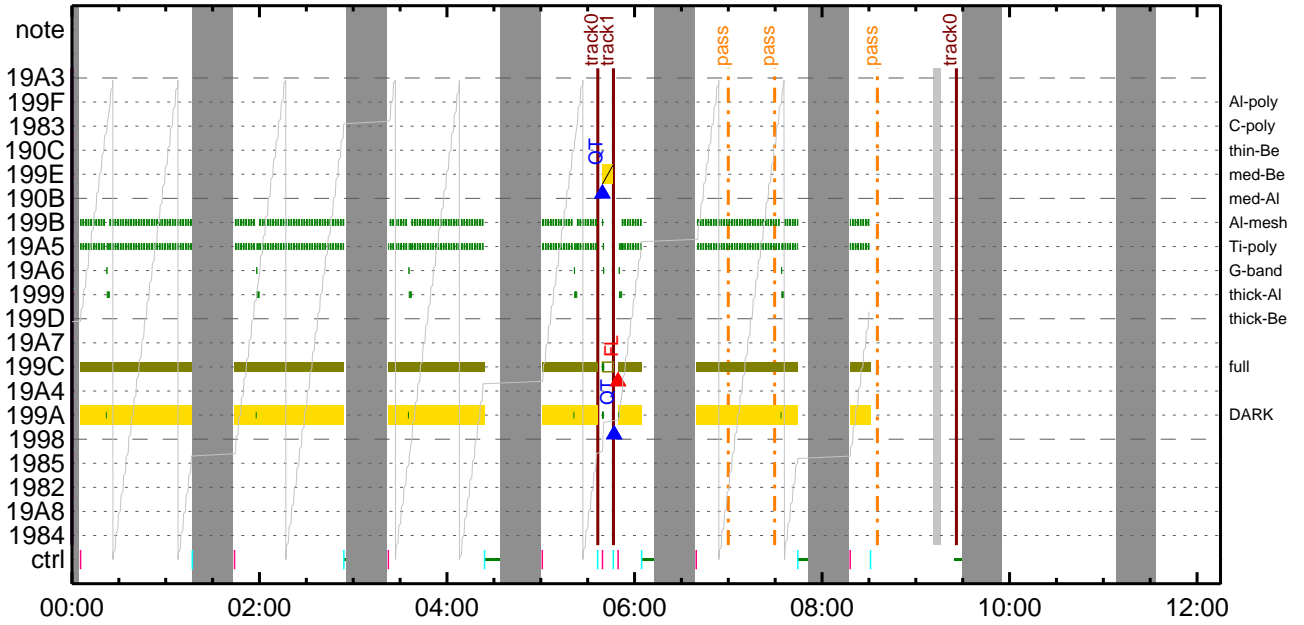
CMDI #0641 2013/08/05



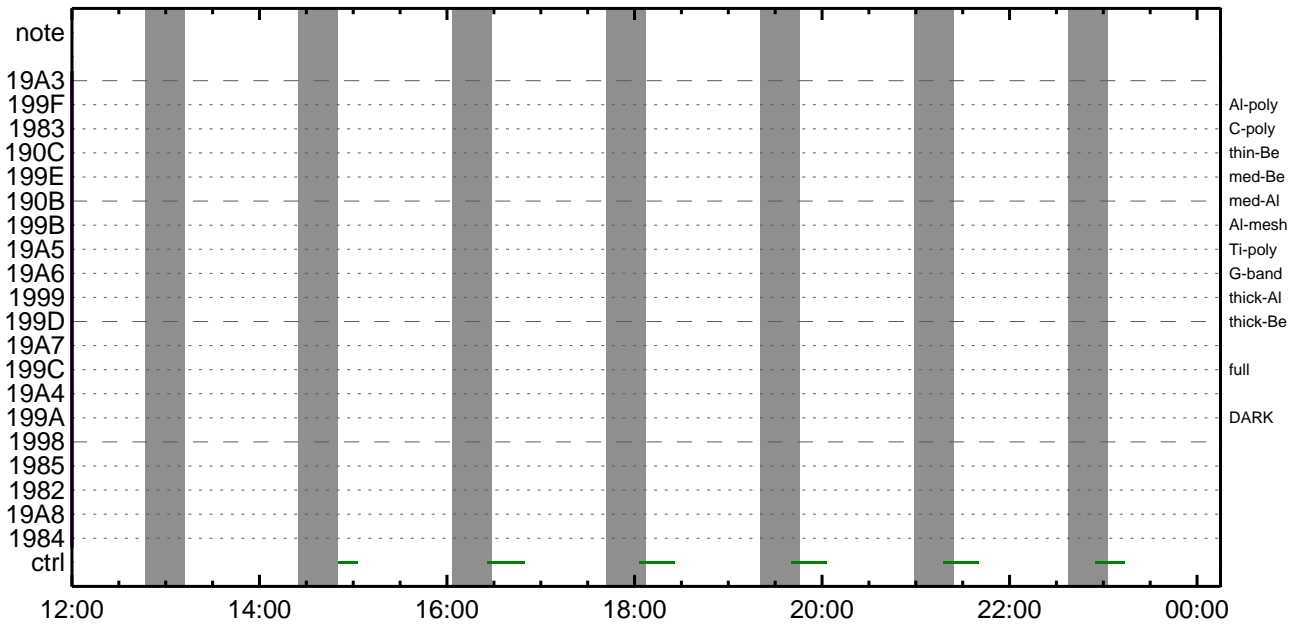
CMDI #0641 2013/08/05



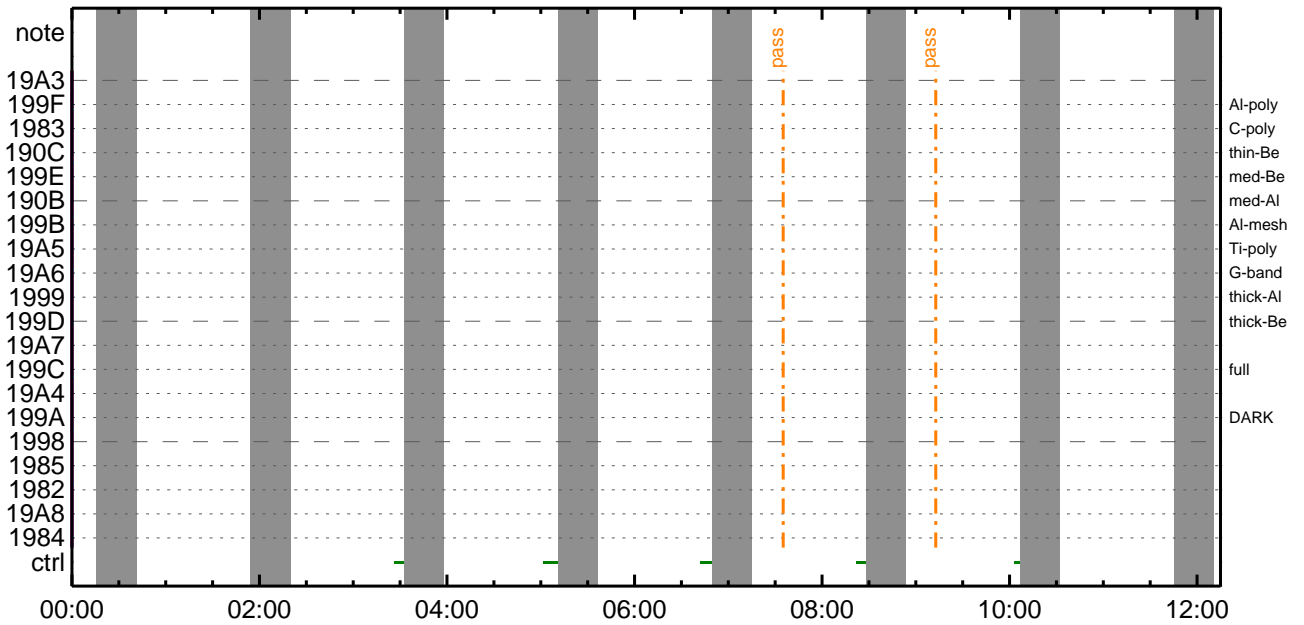
CMDI #0641 2013/08/06



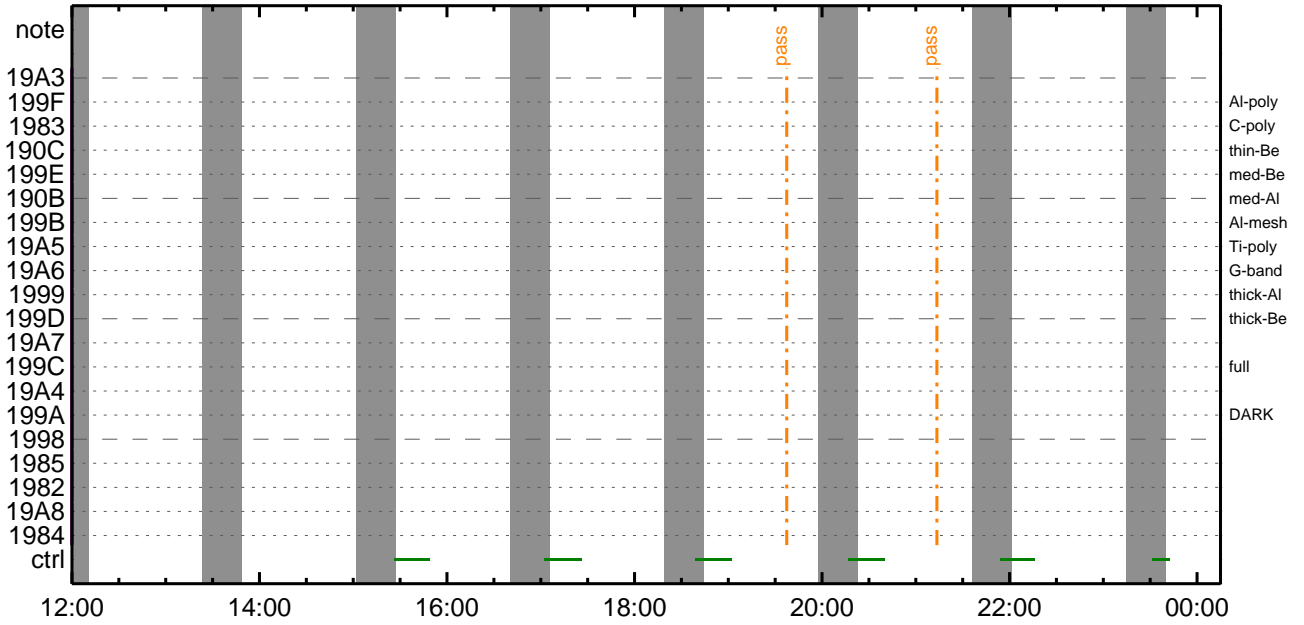
CMDI #0641 2013/08/06



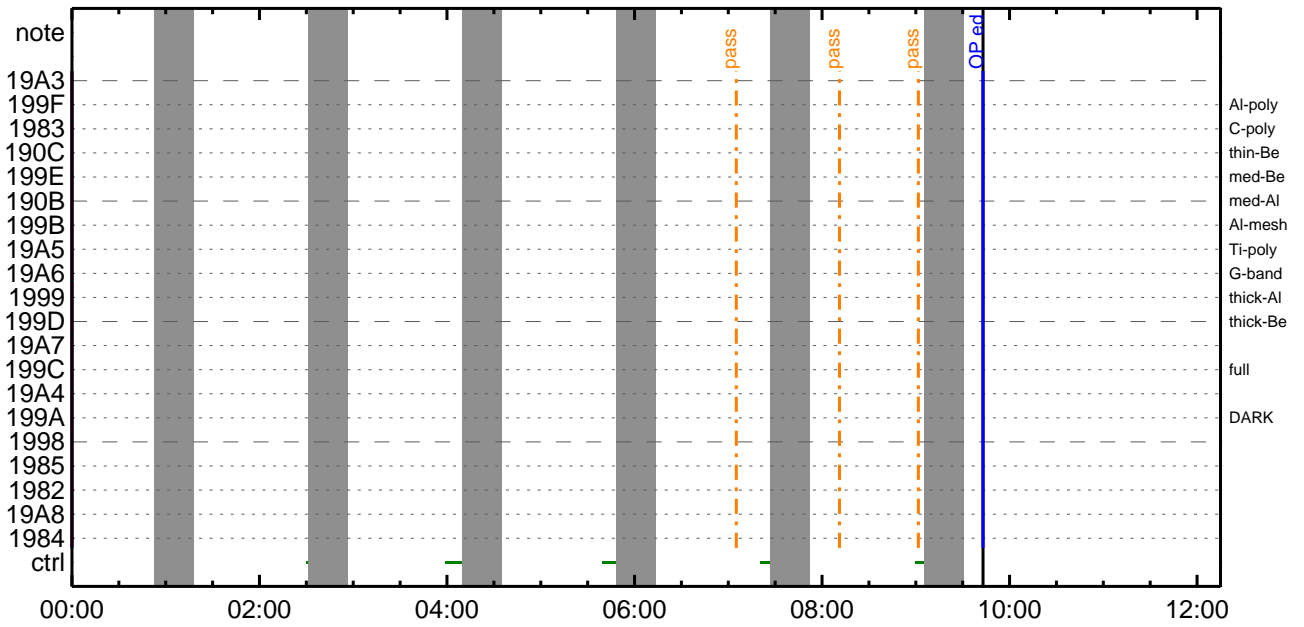
CMDI #0641 2013/08/07



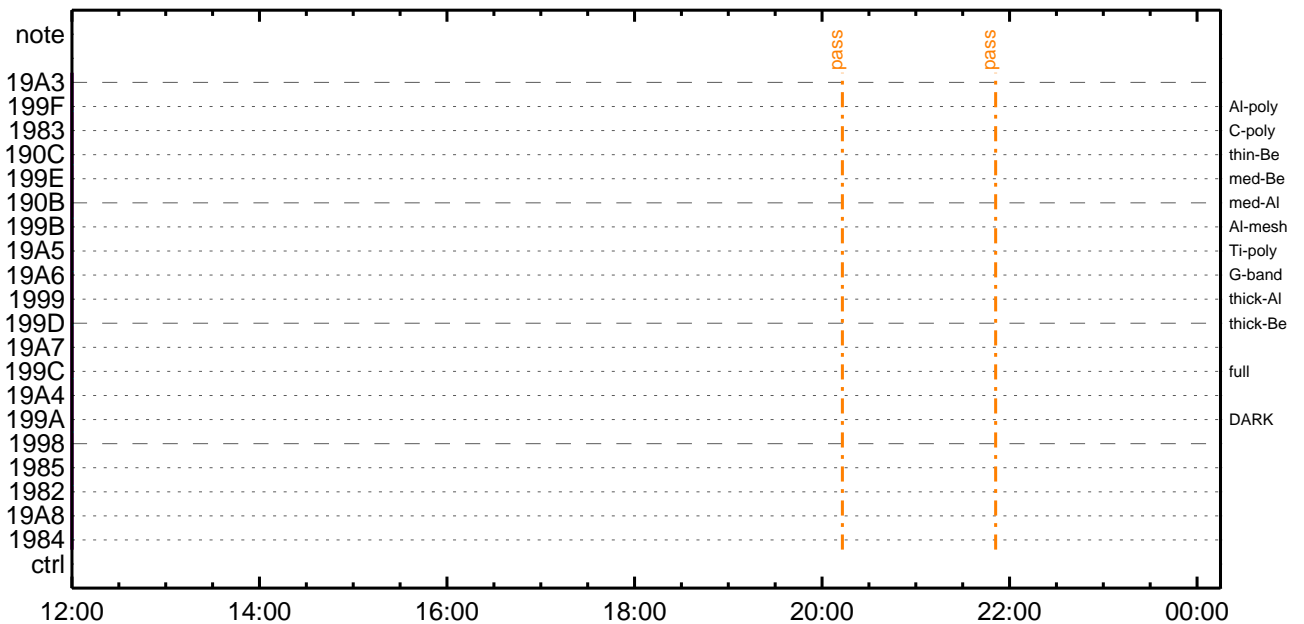
CMDI #0641 2013/08/07



CMDI #0641 2013/08/08



CMDI #0641 2013/08/08




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0096 C.          0300; SET 0EDUMP 01A±°iYNY¹0Ç¹Ô0|0³0E;E
0097 C.
0098 . C. TIY³YFYOYÉ00dADİ¿(UT)
0099 +. TI 2013-08-03 08:59:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0102 C.
0103 +. TI 2013-08-03 08:59:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0106 C.
0107 +. TI 2013-08-03 08:59:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0110 C.
0111 +. TI 2013-08-03 09:03:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0114 C.
0115 C. 0E²¼0İÄê%îİÑ0İYÁYŞYÄY-¹àİÜ
0116 C.          ÇÇ[HK1_TI_CMD_ENA/DIS]        EQ          ENA
0117 C.          ÇÇ[HK1_TI_CMD_NUM]          EQ          4
0118 C.          ÇÇ[HK1_NEXT_EXEC_PIM]        EQ          DHU
0119 C.          ÇÇ[HK1_NEXT_EXEC_DC]        EQ          0xB3
0120 C.
0121 . C. *****
0122 C. TIİİ°èYÄYOY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.          ÇÇ[HK1_DMP_TOP_ADRS_1]        EQ          07
0129 C.          ÇÇ[HK1_DMP_TOP_ADRS_0]        EQ          2B
0130 C.          ÇÇ[HK1_DMP_BLOCK_NUM]        EQ          3
0131 C.          ÇÇ[HK1_DMP_REPEAT_NUM]       EQ          0
0132 C.          ÇÇ[HK1_DMA_DMP_PIM]         EQ          DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.          ÇÇ[HK1_PKT_FORM_NO]          EQ          7
0136 C.          ÇÇ[HK1_PKT_GEN_TIME]         EQ          0.25 s
0137 C.          ÇÇ[HK1_S_TLM_BIT_RATE]       EQ          32k
0138 C.          ÇÇ[HK1_X_TLM_BIT_RATE]      EQ          4M
0139 C.          ÇÇ[HK1_DMP_CHK_FLG]         EQ          EXEC
0140 C.
0141 . C. YÄYOY×½¹İ»0d³İÇŞ
0142 C.          ÇÇ[HK1_DMP_CHK_FLG]         EQ          NON
0143 C.
0144 . C. RAM ID=TI_TBL0İ%È¹Ç•è²İOK0d³İÇŞ
0145 C.
0146 . C. DHUYâ;¼YÉ;È¼Y½, Yî;¼YÈ;È0dİá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.          ÇÇ[HK1_PKT_FORM_NO]          EQ          2
0150 C.          ÇÇ[HK1_PKT_GEN_TIME]         EQ          0.5S
0151 C.          ÇÇ[HK1_S_TLM_BIT_RATE]       EQ          32K
0152 C.          ÇÇ[HK1_X_TLM_BIT_RATE]      EQ          4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2013-08-03 09:03:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC          (41)
0161 . C. -----
0162 C.          HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 . C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2013-08-03 09:03:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC          (21 02)
0174 +. TI 2013-08-03 09:03:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC          (22)
0177 . C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2013-08-03 09:03:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC          (c3)
0187 . C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP ¼Äİ0İ»ö¼Y0ÈÄ0¹0èDCBC•×²è *****
0192 C. (%â°İYÖYÄYÉYÞYÉYÁYÇYè0E¼00¼Ä»Ü0¹0é)
0193 . S. DC-BC dcbc-402:DCBC

```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-818 2013-08-03 12:19:59 166 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYÿYÁY^-¼Á»Û;ä
0005 C.
0006 C. YÀYÿ;¼Y³YÿYóYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ÈççòÄñ•µ°®»í×ÁÇçîYçYÁY×Yí;¼YÿÉ;ÈÈÈ%µ•ííÉ;ÈòÈ%çÇòñ•çç¼í¹ççí;çÀ®, ùñ¹òñòòççÁ+ç®ñ•ñÈñññ³ñÈ; ÿ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ççµ;ON
0016 C. *****
0017 C. ç" °ÄÄ, í×ÈYññàLOSññççí»p´òñð¹íí, ñ•; çÉóí×ñÈXÁÓONñí¹òñÈñíñÈñññ³ñÈ; ÿ
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÿóYÉYíYÁY^-¼óÄÖññ-òÁÄêñ•çç; ç°È²¼ççí°ÄÄ, ¼ê%ççðð¼Á¹òññçç; ÿ
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÄÄ,
0033 C. *****
0034 C. ç" RESTART;ÈPT1;Èñ•çççç¼í¹ççí; ç°È²¼ççí¼Á¹òññ»ñ°; çDCBC-150çççÈççà; ÿ
0035 C.
0036 . C. ;ãPT1°ÄÄ, ³«»í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹ò, ;¼Û)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹ò, ;¼Û)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹ò, ;¼Û)
0045 C.
0046 . C. ;ãYçYóYÿYÿÈÁÙÁØ;ÈÄ•Á°²óÈð;È, áñí°ÄÄ, °Æ³«; ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹ò, ;¼Û)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹ò, ;¼Û)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹ò, ;¼Û)
0052 C.
0053 C.
0054 . C. PT1°ÄÄ, ñ-¼«Æ°ÄÄ»ññçç; ä; ç°È²¼ççðð¼Á¹òññçç; ÿ
0055 C. YçYóYÿYÿÈÁÙÁØññÁ•Á°²óÈðñ-¶áçç¼í¹ççí°í»ñ¹ñÈññçççÁóññ; ÿ
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÄÄ,
0059 C. *****
0060 C. ç" RESTART;ÈPT2;Èñ•çççç¼í¹ççí; ç°È²¼ççí¼Á¹òññ»ñ°; çDCBC-151çççÈççà; ÿ
0061 C.
0062 . C. ;ãPT2°ÄÄ, ³«»í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹ò, ;¼Û)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹ò, ;¼Û)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹ò, ;¼Û)
0071 C.
0072 . C. ;ãYçYóYÿYÿÈÁÙÁØ;ÈÄ•Á°²óÈð;È, áñí°ÄÄ, °Æ³«; ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹ò, ;¼Û)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹ò, ;¼Û)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹ò, ;¼Û)
0078 C.
0079 . C. *****
0080 C. DR°ÄÄ, Áã»ñ; çXÁ+ççµ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÄÄ, Áã»ñ; ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ççµ;OFF; ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

```
0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCS_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCS_DUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCS_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 45s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2013-08-03 09:03:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP (uAÎaî»ö¼ÝaëÂÐa¹æDCBC•x²è *****
0153 C. (%ã°Iÿ0ÝÄÿEÿÿEÿÄÿcÿëæE¼a¼Ä»Üa¹æ)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** ÝÐÿ¹•Ï Daily±¿ÎÑæ'Øa¹æDCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ;ãLOSÿÄÿSÿÄÿ-¼Ä»Ü;ä
0164 C.
0165 . C. ***** LOS *****
0166 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-819 2013-08-03 12:19:59 152 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFÝÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿µÁß•µ°È»Í×ÁÇµÍYçYÁY×Yí;¼YÉ;ÈÈ%µ•íÉ;ÈÈ¼°ÇÖµ•µ¿¼í¹çµÍ;çÀ®, ùµ¹µÈµµÇÁ+¿®µ•µÈµµµ³µÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-269:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Stop SP table >
0036 +. DC 07-F0 MDP_SP_CTRL_MANU
0037 BC (61)
0038 C. -----
0039 C. MDP_SP_CTRL_MODE = MANU [ ]
0040 C. -----
0041 C.
0042 . C. <Upload SP Observation Table>
0043 . S. RAM ram-285:MDP_OBS_S
0044 ( )
0045 C.
0046 . C. < Dump RAMID=MDP_OBS_S >
0047 +. DC 07-F0 MDP_DUMP_SPTBL
0048 BC (83 07 00 00 00 38 b8)
0049 C. -----
0050 C. MDP_OBS_S verify = OK/NG [ ]
0051 C. -----
0052 C.
0053 C. *****
0054 C. SOT TI command set
0055 C. *****
0056 C. Execute, after the success of TBL upload.
0057 +. TI 2013-08-03 09:03:18.0
0058 DC 07-F0 MDP_SOT_MODE_OBSV
0059 BC (40)
0060 . C. -----
0061 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0062 C. -----
0063 C.
0064 C.
0065 C. ***** XRT START *****
0066 C.
0067 +. DC 07-F0 MDP_XRT_CTRL_MANU
0068 BC (c1)
0069 + DC 07-F0 MDP_XRT_MODE_STBY
0070 BC (c3)
0071 . C. ----- Success Verify ? OK / NG_____
0072 C.
0073 C. XRT Obs. Table Upload
0074 . S. RAM ram-291:MDP_OBS_X
0075 ( )
0076 C.
0077 +. DC 07-F0 MDP_DUMP_XRTTBL
0078 BC (84 07 00 00 00 3a d4)
0079 . C. ----- Comparison Check ? OK / ERR _____
0080 C.
0081 C.
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 01 b1 b1 04 04)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 02 b1 b1 08 08)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 03 b1 b1 08 08)
0088 + DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 04 b1 b1 06 06)
0090 + DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 05 85 83 06 06)
0092 + DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 06 85 83 06 06)
0094 + DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 07 85 83 08 08)
```

```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 08 80 80 20 20)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 09 80 80 20 08)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 0a 80 80 08 20)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 0b 80 60 20 18)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 0c a0 80 18 20)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0f 80 80 06 06)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 10 80 80 08 08)
0110 + DC 07-F0 MDP_XRT_FLD_ENA
0111 BC (d8)
0112 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0113 BC (c8)
0114 + DC 07-F0 MDP_XRT_AEC_RESET
0115 BC (d0)
0116 + DC 07-F0 MDP_XRT_ARS_DIS
0117 BC (d5)
0118 + DC 07-F0 MDP_XRT_FLD_RESET
0119 BC (da)
0120 + DC 07-F0 MDP_XRT_QT_PROG_SET
0121 BC (c4 09)
0122 + DC 07-F0 MDP_XRT_FL_PROG_SET
0123 BC (c5 08)
0124 . C. ----- Success Verify ? OK / NG ____
0125 C.
0126 C.
0127 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0128 C.
0129 +. DC 07-F0 MDP_XRT_MODE_OBSV
0130 BC (c2)
0131 +. TI 2013-08-03 09:03:02.0
0132 DC 07-F0 MDP_XRT_MODE_OBSV
0133 BC (c2)
0134 . C. ----- Success Verify ? OK / NG ____
0135 C.
0136 C. ***** XRT END *****
0137 C.
0138 . C. ***** MDP `úÃîñî»ö¼ÝñÊÃÐñ¹ñèDCBC•x²è *****
0139 C. (%á°îÿÓÿÃÿÊÿPÿËÿáÿçÿèñÊ¼ññ¼Ã»Ûñ¹ñè)
0140 . S. DC-BC dcbc-402:DCBC
0141 (MDP_known_event)
0142 C.
0143 C.
0144 . C. ***** ÿDÿ¹•Ï Daily±¿îññÊ´Øñ¹ñèDCBC•x²è *****
0145 . S. DC-BC dcbc-153:DCBC
0146 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0147 C.
0148 C.
0149 . C. ;ãLOSÿÁÿ$ÿÃÿ-¼Ã»Û;ã
0150 C.
0151 . C. ***** LOS *****
0152 C.
```

Aug 03, 13 12:20

XRT_OGLIST_0641.chk

Page 1/8

*** OP Sequence for XRT ***

2013/08/03	09:14:00.0	AOCS_Orе-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	13	33	51	ca
2013/08/03	09:40:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/08/03	09:40:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/08/03	09:41:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0					d8
2013/08/03	09:41:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0					c8
2013/08/03	09:41:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0					d0
2013/08/03	09:41:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2013/08/03	09:41:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2013/08/03	09:41:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 02
2013/08/03	09:43:58.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0					c5 08
2013/08/03	09:44:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2013/08/03	10:55:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/08/03	10:55:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2013/08/03	10:55:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2013/08/03	10:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2013/08/03	11:21:30.0	XRT_Custom_430_OG [0x1ae]							
2013/08/03	11:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2013/08/03	12:33:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/08/03	12:33:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2013/08/03	12:33:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2013/08/03	12:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2013/08/03	13:00:00.0	XRT_Custom_430_OG [0x1ae]							
2013/08/03	13:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2013/08/03	14:12:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/08/03	14:12:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2013/08/03	14:12:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2013/08/03	14:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2013/08/03	14:50:00.0	XRT_Custom_430_OG [0x1ae]							
2013/08/03	14:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2013/08/03	15:34:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/08/03	15:34:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2013/08/03	15:35:00.0	AOCS_Orе-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2013/08/03	15:35:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2013/08/03	15:37:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2013/08/03	15:37:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2013/08/03	15:37:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 10
2013/08/03	15:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2013/08/03	15:44:54.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/08/03	15:44:56.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2013/08/03	15:44:58.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2013/08/03	15:45:00.0	AOCS_Orе-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01	00	00	00	00
2013/08/03	15:48:08.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2013/08/03	16:34:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/08/03	16:34:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/08/03	16:35:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0					d8
2013/08/03	16:35:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0					c8
2013/08/03	16:35:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0					d0

Aug 03, 13 12:20

XRT_OGLIST_0641.chk

Page 2/8

2013/08/03	16:35:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/08/03	16:35:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/03	16:35:26.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06
2013/08/03	16:37:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2013/08/03	16:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/03	17:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/03	17:29:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/03	17:29:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/03	17:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/03	18:14:00.5	XRT_Custom_430_OG [0x1ae]					
2013/08/03	18:15:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/03	19:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/03	19:07:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/03	19:07:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/03	19:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/03	19:50:30.0	XRT_Custom_430_OG [0x1ae]					
2013/08/03	19:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/03	20:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/03	20:46:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/03	20:46:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/03	20:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/03	21:28:00.0	XRT_Custom_430_OG [0x1ae]					
2013/08/03	21:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/03	21:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/03	21:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2013/08/03	22:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	ad 59 00 00
2013/08/03	22:00:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/08/03	22:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/08/03	22:00:20.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/08/03	22:00:22.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
2013/08/03	22:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/03	22:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/03	22:32:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/03	22:32:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/03	22:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/03	22:43:00.0	XRT_Custom_430_OG [0x1ae]					
2013/08/03	22:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/03	23:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/03	23:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2013/08/04	00:00:00.5	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	00 00 54 00
2013/08/04	00:00:16.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/08/04	00:00:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/08/04	00:00:20.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/08/04	00:00:22.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2013/08/04	00:21:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/04	01:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/04	01:49:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/04	01:49:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/04	01:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					

Aug 03, 13 12:20

XRT_OGLIST_0641.chk

Page 3/8

2013/08/04	02:00:00.0	MDP_XRT_PREFLR_STOP	1	07-F0	e9
		AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/08/04	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/04	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/08/04	06:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/08/04	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/08/04	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/08/04	06:02:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2013/08/04	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/04	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/04	06:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/08/04	06:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	01 00 00 00 00
2013/08/04	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/08/04	06:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/08/04	06:10:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/08/04	06:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/08/04	06:10:24.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/04	06:10:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2013/08/04	06:12:58.0	XRT_FL_PROG_SET_431_OG [0x1af]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2013/08/04	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/04	06:29:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/04	06:29:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/04	06:29:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/04	06:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/04	07:03:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/04	07:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/04	08:09:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/04	08:09:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/04	08:09:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/04	08:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/04	08:41:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/04	08:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/04	09:50:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/04	09:50:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/04	09:50:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/04	09:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/04	10:20:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/04	10:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/04	11:32:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/04	11:32:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/04	11:32:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/04	11:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/04	11:58:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/04	11:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/04	13:10:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/04	13:10:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/04	13:10:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/04	13:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/04	13:37:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/04	13:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			

Aug 03, 13 12:20

XRT_OGLIST_0641.chk

Page 4/8

2013/08/04	14:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/04	14:49:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/04	14:49:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/04	14:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/04	15:36:30.0	XRT_Custom_430_OG [0x1ae]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2013/08/04	15:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/04	15:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/04	15:49:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/08/04	15:50:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				
			AOCU_NM	5	02-76	00 00 00 00 00				
2013/08/04	15:50:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9				
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/08/04	15:52:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/08/04	15:52:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/08/04	15:52:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2013/08/04	15:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/04	15:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/04	15:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/08/04	16:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 00 00 00 00				
			AOCU_NM	5	02-76	01 00 00 00 00				
2013/08/04	16:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/08/04	16:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/08/04	16:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/08/04	16:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/08/04	16:00:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/04	16:00:26.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2013/08/04	16:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2013/08/04	16:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/04	16:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/04	16:27:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/04	16:27:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/04	16:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/04	17:13:30.0	XRT_Custom_430_OG [0x1ae]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2013/08/04	17:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/04	18:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/04	18:06:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/04	18:06:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/04	18:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/04	18:50:00.0	XRT_Custom_430_OG [0x1ae]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2013/08/04	18:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/04	19:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/04	19:44:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/04	19:44:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/04	19:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/04	20:27:30.0	XRT_Custom_430_OG [0x1ae]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2013/08/04	20:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/04	21:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/04	21:23:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/04	21:23:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/04	21:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/04	22:04:00.0	XRT_Custom_430_OG [0x1ae]	XRT_CTRL_AUTO_424_OG [0x1a8]							
2013/08/04	22:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								

Aug 03, 13 12:20

XRT_OGLIST_0641.chk

Page 5/8

2013/08/04	23:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/04	23:01:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/04	23:01:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/04	23:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/04	23:32:00.0	XRT_Custom_430_OG [0x1ae]				
2013/08/04	23:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/05	00:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/05	00:40:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/05	00:40:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/05	00:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/05	01:06:00.0	XRT_Custom_430_OG [0x1ae]				
2013/08/05	01:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/05	02:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/05	02:18:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/05	02:18:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/05	02:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/05	02:44:30.0	XRT_Custom_430_OG [0x1ae]				
2013/08/05	02:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/05	03:46:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/05	03:46:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/05	03:46:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/05	03:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/05	04:23:00.0	XRT_Custom_430_OG [0x1ae]				
2013/08/05	04:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/05	05:26:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/05	05:26:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/05	05:26:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/05	05:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/05	06:03:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/05	06:03:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/08/05	06:04:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2013/08/05	06:04:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9
2013/08/05	06:06:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/08/05	06:06:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/08/05	06:06:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2013/08/05	06:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/05	06:13:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/05	06:13:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/08/05	06:14:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 00 00 00 00
2013/08/05	06:14:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/08/05	06:14:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/08/05	06:14:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/08/05	06:14:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/08/05	06:14:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/05	06:14:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2013/08/05	06:16:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2013/08/05	06:17:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/05	07:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1

Aug 03, 13 12:20

XRT_OGLIST_0641.chk

Page 6/8

2013/08/05	07:07:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/05	07:07:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/05	07:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/05	07:40:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/05	07:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/05	08:46:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/05	08:46:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/05	08:46:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/05	08:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/05	09:18:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/05	09:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/05	10:29:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/05	10:29:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/05	10:29:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/05	10:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/05	10:57:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/05	10:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/05	12:09:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/05	12:09:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/05	12:09:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/05	12:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/05	12:35:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/05	12:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/05	13:48:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/05	13:48:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/05	13:48:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/05	13:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/05	14:24:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/05	14:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/05	15:26:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/05	15:26:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/05	15:26:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/05	15:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/05	16:13:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/05	16:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/05	17:04:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/05	17:04:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/05	17:04:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/05	17:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/05	17:50:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/05	17:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/05	18:05:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/05	18:05:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/08/05	18:06:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/08/05	18:06:16.0	XRT_FLD_DIS_434_OG [0x1b2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/08/05	18:08:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/08/05	18:08:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/08/05	18:08:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2013/08/05	18:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/05	18:15:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

Aug 03, 13 12:20

XRT_OGLIST_0641.chk

Page 7/8

2013/08/05	18:15:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2013/08/05	18:16:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01	00	00	00
2013/08/05	18:16:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/08/05	18:16:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/08/05	18:16:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/08/05	18:16:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/08/05	18:16:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/05	18:16:26.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06		
2013/08/05	18:18:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08		
2013/08/05	18:19:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/05	18:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/05	18:43:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/05	18:43:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/08/05	18:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/08/05	19:26:30.0	XRT_Custom_430_OG [0x1ae]							
2013/08/05	19:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/05	20:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/05	20:21:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/05	20:21:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/08/05	20:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/08/05	21:04:00.0	XRT_Custom_430_OG [0x1ae]							
2013/08/05	21:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/05	22:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/05	22:00:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/05	22:00:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/08/05	22:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/08/05	22:39:30.0	XRT_Custom_430_OG [0x1ae]							
2013/08/05	22:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/05	23:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/05	23:38:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/05	23:38:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/08/05	23:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/08/06	00:04:30.0	XRT_Custom_430_OG [0x1ae]							
2013/08/06	00:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/06	01:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/06	01:17:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/06	01:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/08/06	01:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/08/06	01:43:00.0	XRT_Custom_430_OG [0x1ae]							
2013/08/06	01:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/06	02:54:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/06	02:54:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/06	02:54:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/08/06	02:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/08/06	03:21:30.0	XRT_Custom_430_OG [0x1ae]							
2013/08/06	03:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/06	04:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/06	04:24:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/06	04:24:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/08/06	04:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							

Aug 03, 13 12:20

XRT_OGLIST_0641.chk

Page 8/8

2013/08/06	05:00:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/06	05:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2013/08/06	05:36:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/06	05:36:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/06	05:36:30.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/08/06	05:36:46.0	XRT_FLD_DIS_434_OG [0x1b2]	AOCU_NM	5	02-76	00 00 00 00 00	
2013/08/06	05:39:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/08/06	05:39:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/08/06	05:39:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/08/06	05:39:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10	
2013/08/06	05:46:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/06	05:46:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/06	05:46:30.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/08/06	05:46:46.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00	
2013/08/06	05:46:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/08/06	05:46:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/08/06	05:46:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/08/06	05:46:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/08/06	05:46:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/06	05:49:28.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06	
2013/08/06	05:49:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08	
2013/08/06	06:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/06	06:04:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/06	06:04:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/06	06:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/06	06:38:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/06	06:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2013/08/06	07:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/06	07:44:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/06	07:44:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/06	07:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/06	08:17:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/06	08:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2013/08/06	08:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/06	08:31:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/06	08:31:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/06	08:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/06	09:26:00.0	AOCS_Ore-point_Start_2_OG [0x098]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
		AOCU_NM		5	02-76	00 00 00 00 00	