

XRT Timeline to be uploaded on 2013/08/13

Period: 2013/08/13 10:51:00 - 2013/08/17 10:03:00

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Normal mode

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XOB #199A: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 100s cad With G-band and VLS closed												
Term	Pointing (x, y)		Comment									
08/13 11:04:00 - 08/13 18:06:24	Track (-66.7, -459.6) @ 08/13 11:01:00		# OP start + 10min Track AR 11817									
08/13 18:19:30 - 08/13 21:59:54	Track (-6.4, -460.2) @ 08/13 18:16:30		Track AR 11817									
08/14 03:21:30 - 08/14 05:36:54	Track (66.0, -460.2) @ 08/14 03:00:00		Track AR 11817									
08/14 09:55:00 - 08/14 17:43:00	Track (122.0, -459.7) @ 08/14 09:47:00		Track AR 11817									
08/14 18:39:30 - 08/14 21:59:54	Track (194.1, -458.5) @ 08/14 18:36:30		Track AR 11817									
08/15 03:03:00 - 08/15 05:58:24	Track (261.4, -456.6) @ 08/15 03:00:00		Track AR 11817									
08/15 06:11:30 - 08/15 09:30:00	Track (286.2, -455.7) @ 08/15 06:08:30		Track AR 11817									
PROG= 06 Inf.-time(s)												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 78 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Seqn= 62 4-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 63 40-time(s) 100.0sec												
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #199E: Synoptic Q95 2x2 - Al/mesh(12/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(33/362/1443) + G-b												
Term	Pointing (x, y)		Comment									
08/13 18:09:30 - 08/13 18:16:24	Fixed (0.0, 0.0)		synoptic, shifted 6.5 min									
08/14 05:40:00 - 08/14 05:46:54	Fixed (0.0, 0.0)		synoptic, shifted -23.0 min									
08/14 18:29:30 - 08/14 18:36:24	Fixed (0.0, 0.0)		synoptic, shifted 26.5 min									
08/15 06:01:30 - 08/15 06:08:24	Fixed (0.0, 0.0)		synoptic, shifted -1.5 min									
PROG= 16 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 82 1-time(s) 4.0sec												
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 83 1-time(s) 4.0sec												
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 66 1-time(s) 2.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
└─ Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19AC: HOP128- AR - Al/mesh with PFB(Al-mesh Ti-poly), G-band - 512x512 45ms with G-band VLS closed 33ms												
Term	Pointing (x, y)		Comment									
08/13 22:41:00 - 08/14 02:54:30	Track (-48.1, -462.9) @ 08/13 22:00:00		HOP 128 sunspot									
PROG= 19 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 84 2-time(s) 2.0sec												
└─ Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 95 1-time(s) 30.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	512x512 (1064, 1048)	Q=90	0	0	2.0sec
└─ Subr= 3 30-time(s) 2.0sec												
└─ Seqn= 96 2-time(s) 30.0sec												
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec

Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19A9: G-Band Alignment with North Pole Q90 2x2(G-band only) - 8msec - 5min cadence - Partial Sun-wNGT-2

Term	Pointing (x, y)	Comment
08/14 06:02:00 - 08/14 07:46:54	Fixed (0.0, 945.0)	Co-alignment N pole
PROG= 11 1-time(s)		
└─ Subr= 1 1-time(s) 360.0sec		
└─ Seqn= 93 24-time(s) 300.0sec		
Open/G-band	Open/G-band	open Safe Norm 8ms Obs 2x2 2048x1536 (1024, 768) Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #19AA: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8msec- 8 min cadence-wNGT-2

Term	Pointing (x, y)	Comment
08/14 08:02:00 - 08/14 09:46:54	Fixed (-945.0, 0.0)	Co-alignment E limb
PROG= 10 1-time(s)		
└─ Subr= 1 1-time(s) 360.0sec		
└─ Seqn= 94 15-time(s) 480.0sec		
Open/G-band	Open/G-band	open Safe Norm 8ms Obs 2x2 1536x2048 (1280, 1024) Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #19AD: HOP128-long - Al/mesh 2s, G-band - 512x512 45ms with G-band VLS closed 33ms

Term	Pointing (x, y)	Comment
08/14 22:03:00 - 08/15 02:59:54	Track (627.6, -531.6) ^{Ⓜ 08/14 22:00:00}	HOP128 dark filament
PROG= 07 Inf.-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 84 2-time(s) 2.0sec		
Open/G-band	Open/G-band	close Safe Norm 32ms Obs 1x1 512x512 (1064, 1048) DPCM 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 95 1-time(s) 30.0sec		
Open/G-band	Open/G-band	open Safe Norm 44ms Obs 1x1 512x512 (1064, 1048) Q=90 0 0 2.0sec
└─ Subr= 3 30-time(s) 2.0sec		
└─ Seqn= 97 2-time(s) 30.0sec		
Open/Al-mesh	Open/Ti-poly	close Safe Norm 2.00s Obs 1x1 512x512 (1064, 1048) Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #199C: Flare Standard Obs. with eruptions mode-A (FW1=Open and VLS-Closed)

Term	Pointing (x, y)	Comment
08/13 11:04:00 - 08/13 18:06:24	Track (-66.7, -459.6) ^{Ⓜ 08/13 11:01:00}	# OP start + 10min Track AR 11817
08/13 18:19:30 - 08/13 21:59:54	Track (-6.4, -460.2) ^{Ⓜ 08/13 18:16:30}	Track AR 11817
08/13 22:41:00 - 08/14 02:54:30	Track (-48.1, -462.9) ^{Ⓜ 08/13 22:00:00}	HOP 128 sunspot
08/14 03:21:30 - 08/14 05:36:54	Track (66.0, -460.2) ^{Ⓜ 08/14 03:00:00}	Track AR 11817
08/14 09:55:00 - 08/14 17:43:00	Track (122.0, -459.7) ^{Ⓜ 08/14 09:47:00}	Track AR 11817
08/14 18:39:30 - 08/14 21:59:54	Track (194.1, -458.5) ^{Ⓜ 08/14 18:36:30}	Track AR 11817
08/14 22:03:00 - 08/15 02:59:54	Track (627.6, -531.6) ^{Ⓜ 08/14 22:00:00}	HOP128 dark filament
08/15 03:03:00 - 08/15 05:58:24	Track (261.4, -456.6) ^{Ⓜ 08/15 03:00:00}	Track AR 11817
08/15 06:11:30 - 08/15 09:30:00	Track (286.2, -455.7) ^{Ⓜ 08/15 06:08:30}	Track AR 11817
PROG= 08 1-time(s)		
└─ Subr= 1 30-time(s) 20.0sec		
└─ Seqn= 58 1-time(s) 2.0sec		
Open/thick-Al	Open/thick-Al	close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be	close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ Seqn= 64 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Open/Ti-poly	Open/Ti-poly	close Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 79 1-time(s) 2.0sec		
Open/G-band	Open/G-band	open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/G-band	Open/G-band	close Safe Norm 32ms Obs 1x1 384x384 (1024, 1024) DPCM 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
└─ Subr= 3 30-time(s) 60.0sec		
└─ Seqn= 58 1-time(s) 2.0sec		
Open/thick-Al	Open/thick-Al	close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be	close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ Seqn= 60 1-time(s) 2.0sec		
Open/Al-mesh	Open/Al-mesh	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Open/Ti-poly	Open/Ti-poly	close Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 79 1-time(s) 2.0sec		
Open/G-band	Open/G-band	open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/G-band	Open/G-band	close Safe Norm 32ms Obs 1x1 384x384 (1024, 1024) DPCM 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec

Subr= 3 30-time(s) 60.0sec													
└─ Seqn= 58 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 60 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 79 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec													
└─ Seqn= 58 1-time(s) 2.0sec													
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 60 1-time(s) 2.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4 24-time(s) 600.0sec													
└─ Seqn= 61 1-time(s) 2.0sec													
	Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

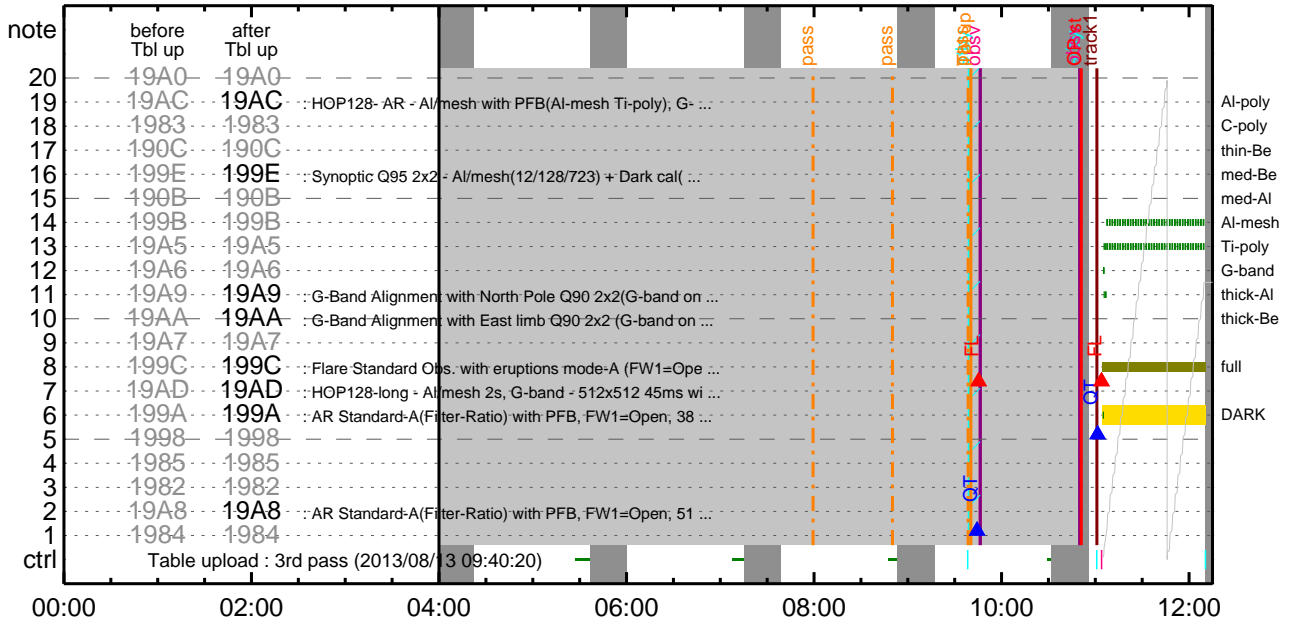
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Flare Detection

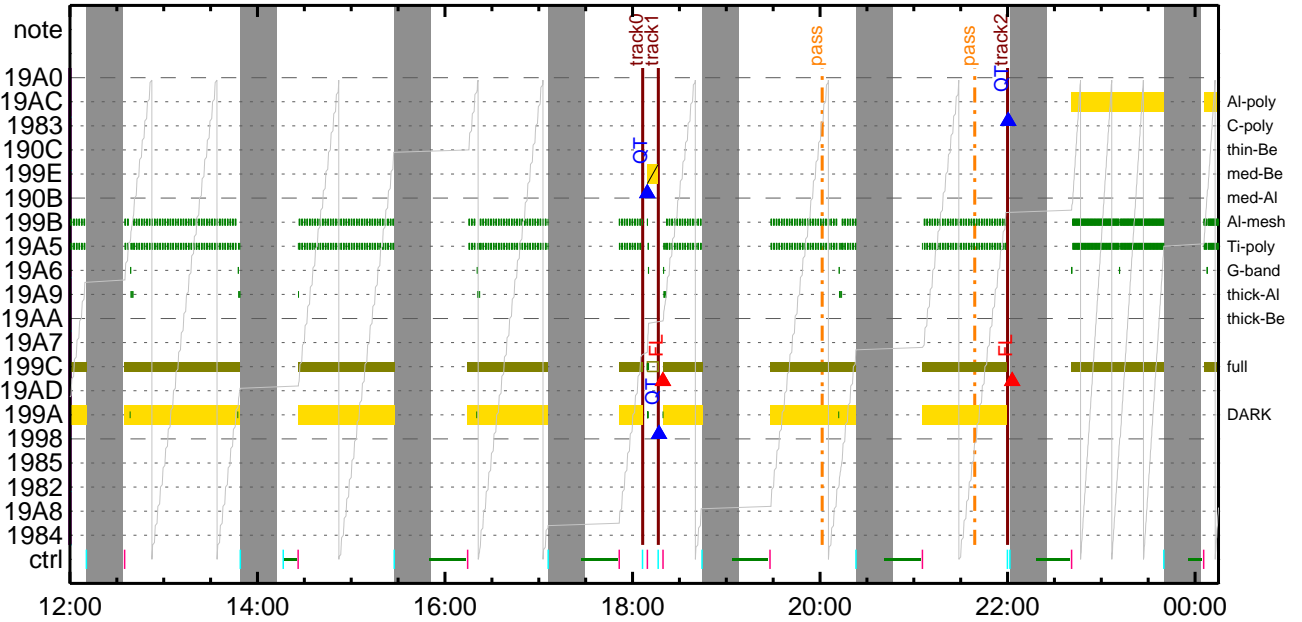
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FLD Patrol												
Term	Pointing (x, y)								Comment			
08/13 18:16:46 - 08/14 05:37:16	Track (-6.4,	-460.2)	<small>Ⓢ 08/13 18:16:30</small>	Track	AR	11817					
08/14 09:47:16 - 08/14 18:26:46	Track (122.0,	-459.7)	<small>Ⓢ 08/14 09:47:00</small>	Track	AR	11817					
08/14 18:36:46 - 08/15 05:58:46	Track (194.1,	-458.5)	<small>Ⓢ 08/14 18:36:30</small>	Track	AR	11817					
08/15 06:08:46 - 08/17 10:03:00	Track (286.2,	-455.7)	<small>Ⓢ 08/15 06:08:30</small>	Track	AR	11817					
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

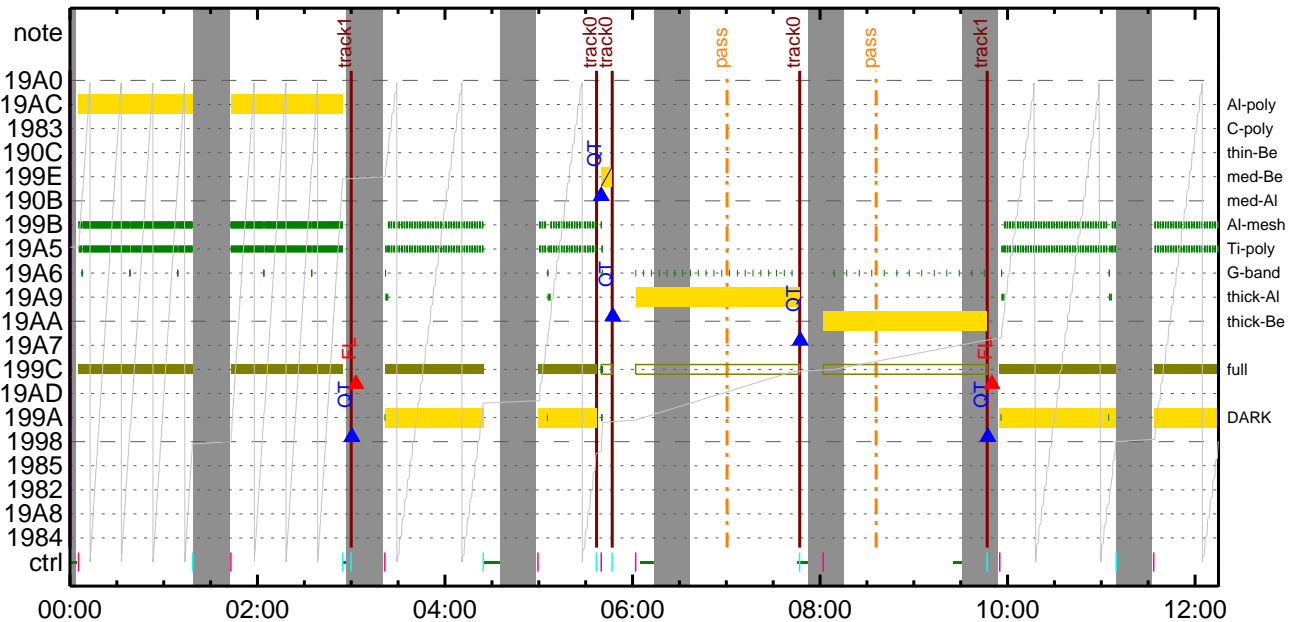
CMDI #0666 2013/08/13



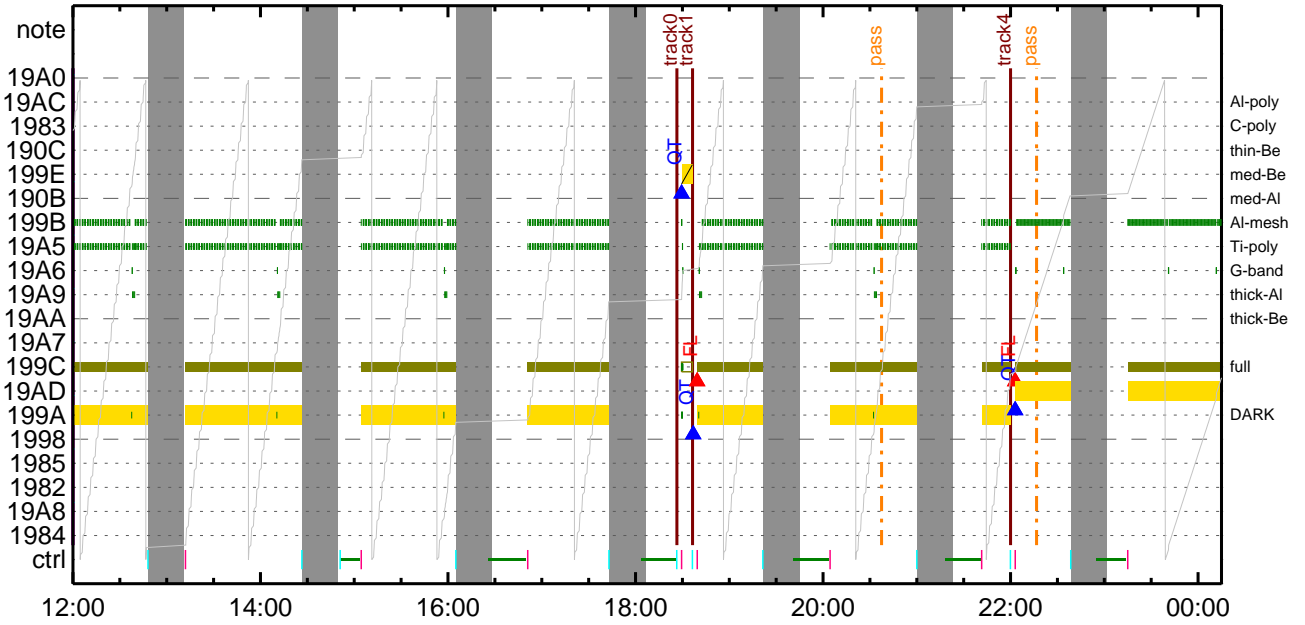
CMDI #0666 2013/08/13



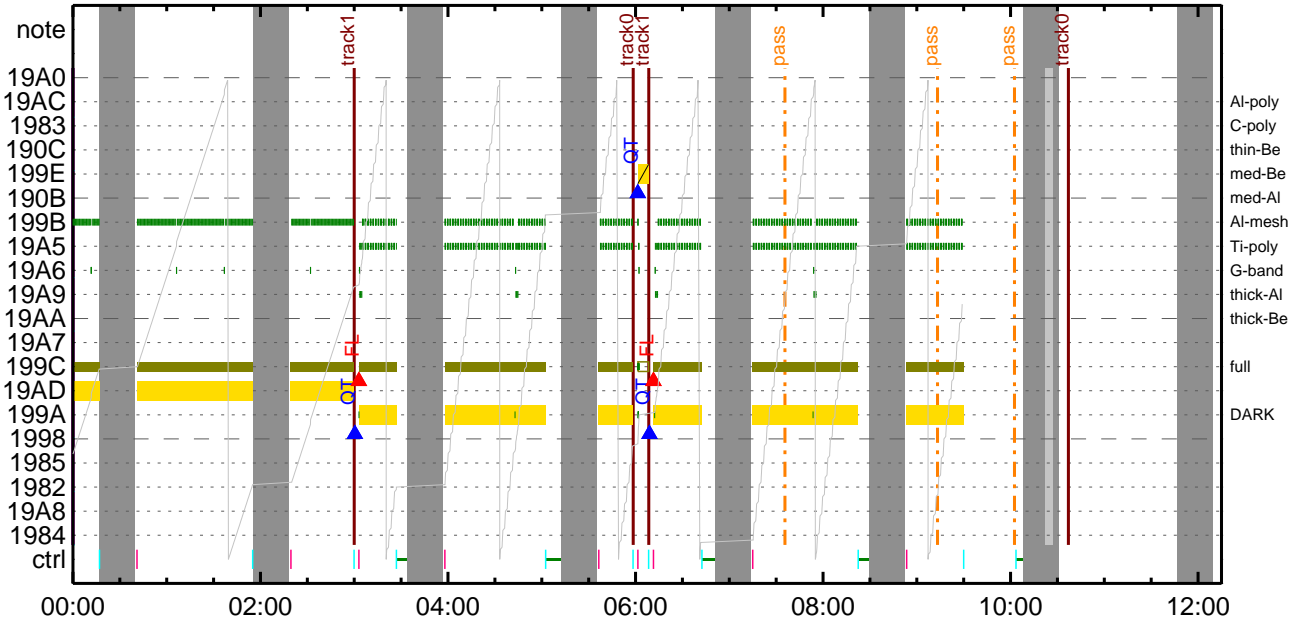
CMDI #0666 2013/08/14



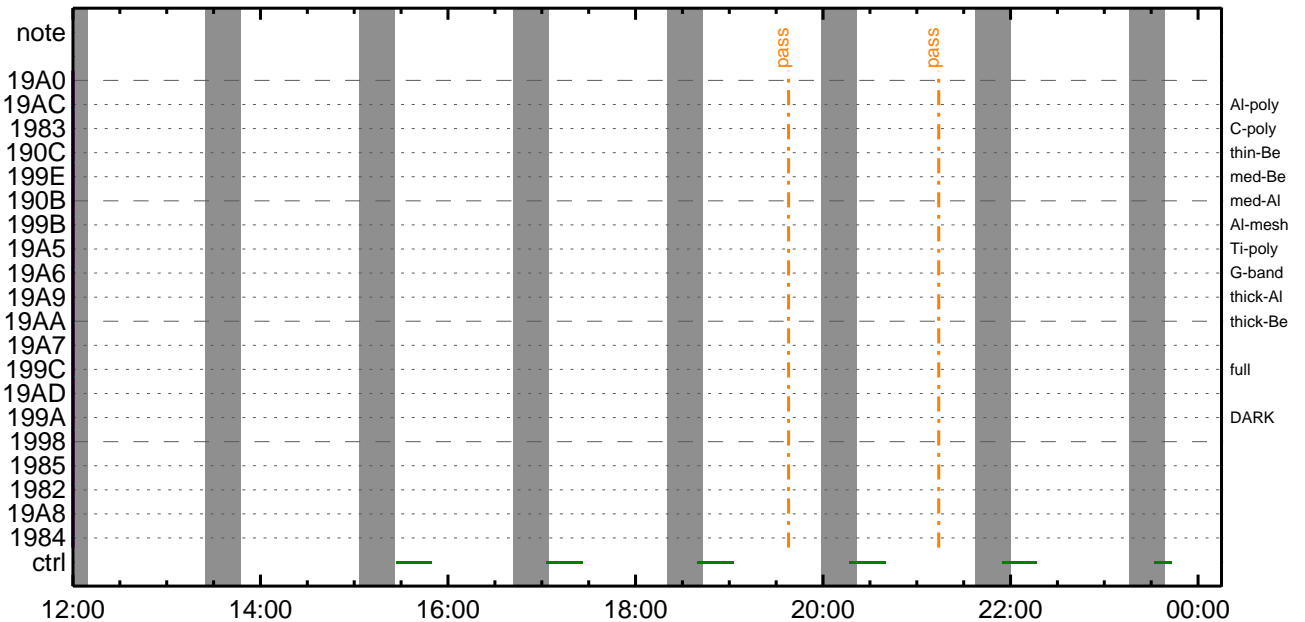
CMDI #0666 2013/08/14



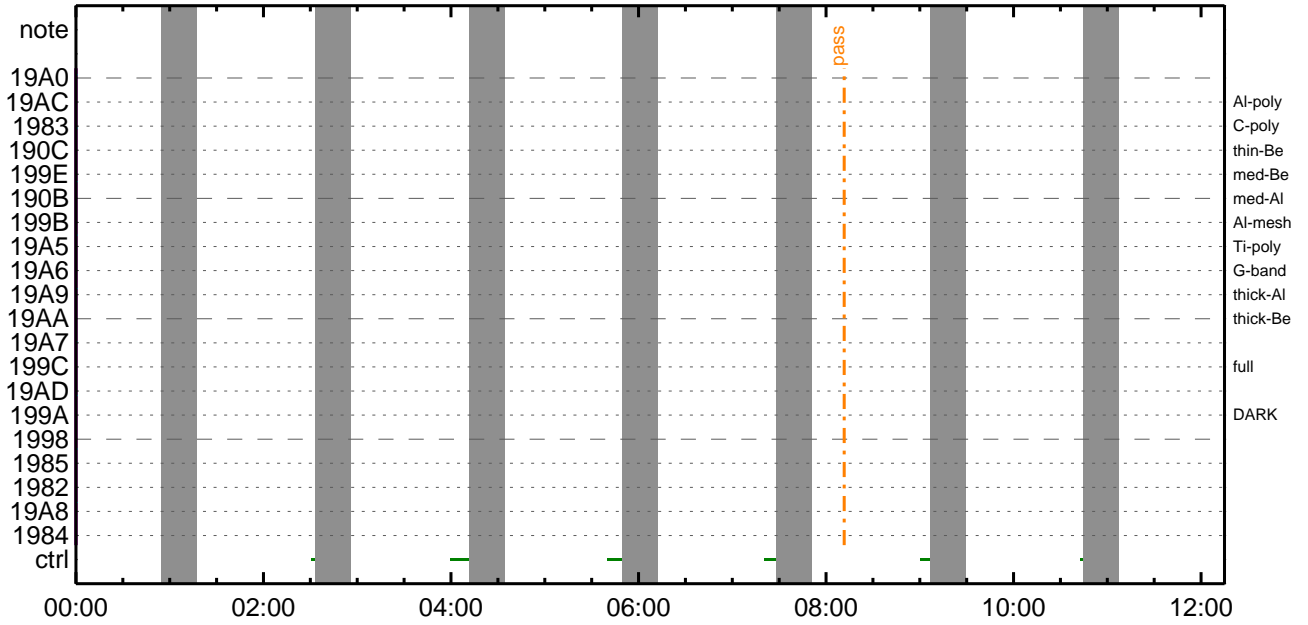
CMDI #0666 2013/08/15



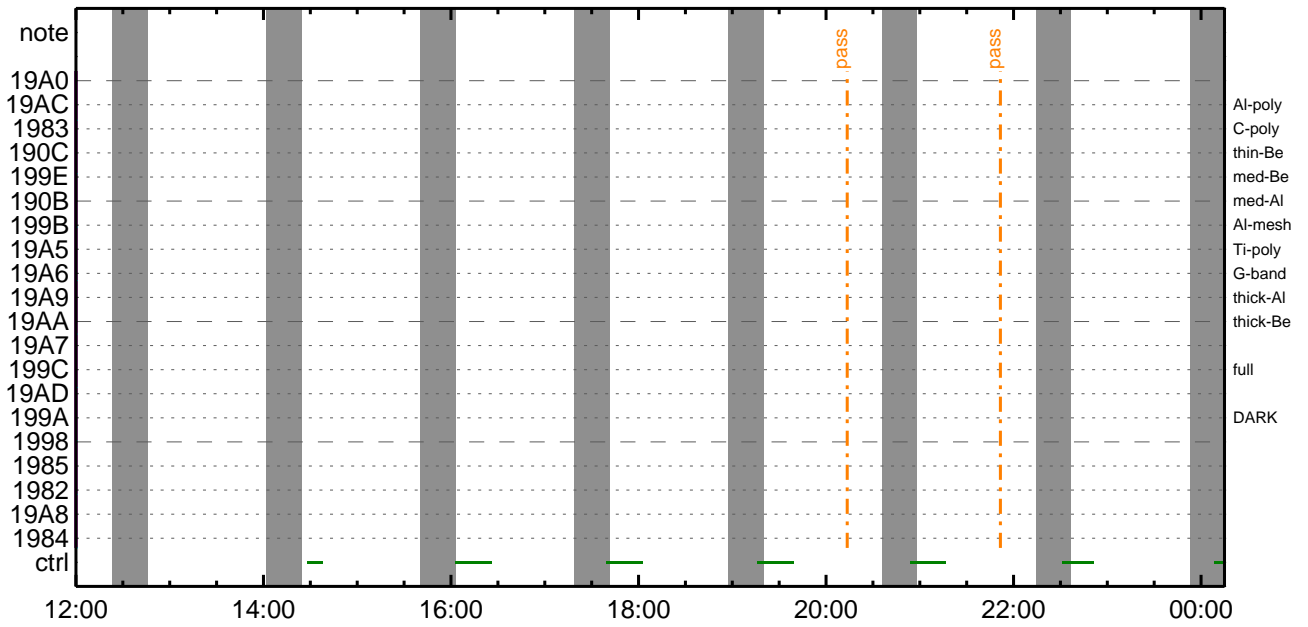
CMDI #0666 2013/08/15



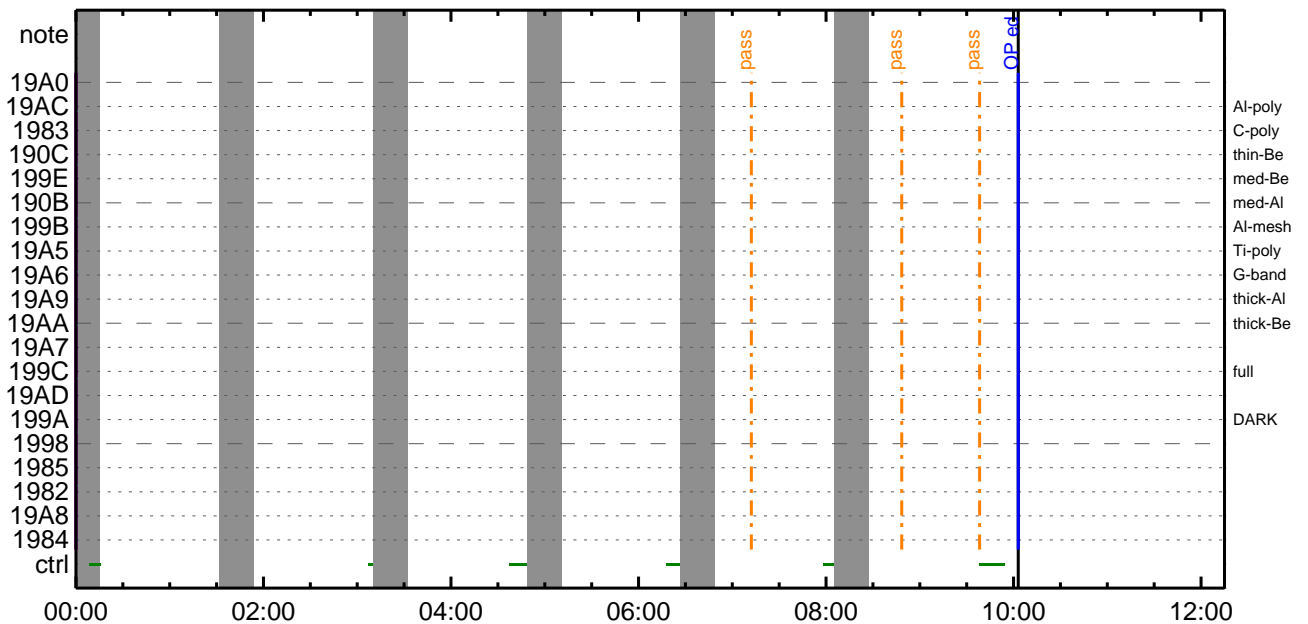
CMDI #0666 2013/08/16

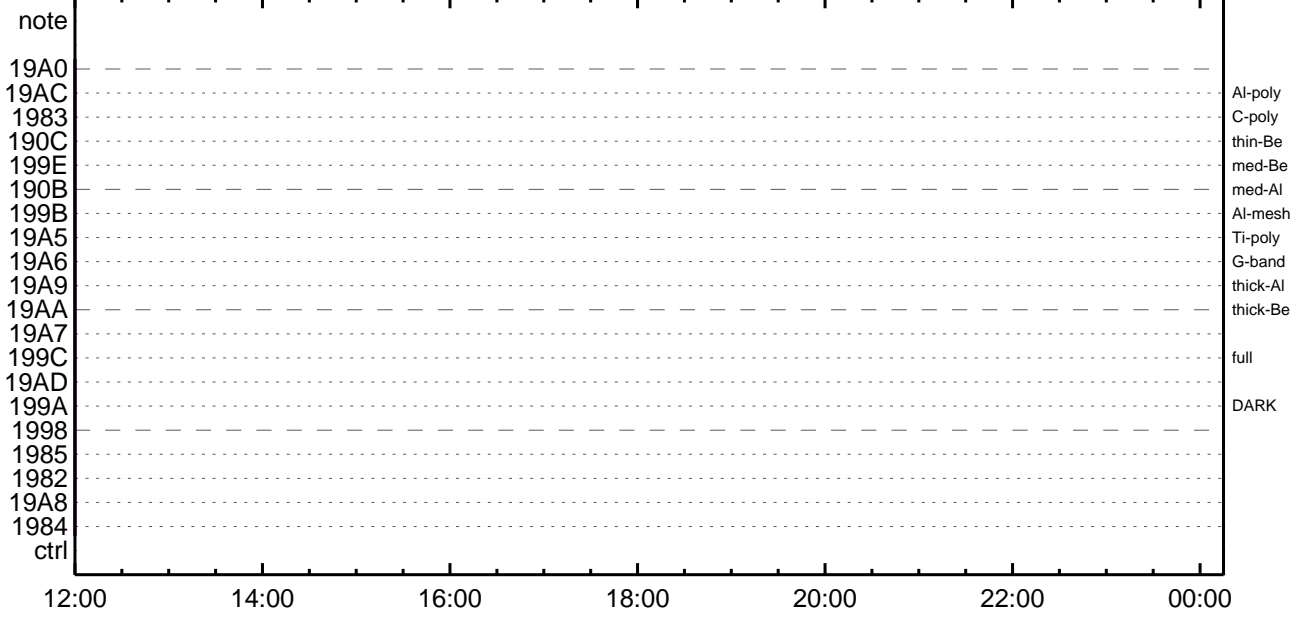


CMDI #0666 2013/08/16



CMDI #0666 2013/08/17






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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-837:OP
0104 ( )
0105 S. OG og-837:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶Á°òEÈ-ò°Á÷¿@ (¼âµ-YAYOYx½ê½çòðÁÔæòÇ¼ª°²òè¼i¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½i¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ôª²òE²²²³òE;f
0180 C. ²²²¿;çSET²EDUMP²î½±²î½Y¹²ç¹Ôª²²³²²E;f
0181 C.
0182 C. TIY³Y²YóYÈ²òðÁDî¿(UT)
0183 +. TI 2013-08-13 10:46:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-08-13 10:46:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-08-13 10:46:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-264:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Upload DPL table >
0120 C.
0121 C. ¥ç¥Ã¥×¥í;¥É°âÊSTS_CHKðOFFðËð¹ðë
0122 C.
0123 . S. RAM ram-271:MDP_DPL
0124 ( )
0125 C.
0126 . C. < Dump RAMID=MDP_DPL >
0127 +. DC 07-F0 MDP_DUMP_FGTBL
0128 BC (82 07 00 38 b8 00 40)
0129 C. -----
0130 C. MDP_DPL verify = OK [ ]
0131 C. -----
0132 C.
0133 C. STS_CHKðONðËð¹ðë
0134 C.
0135 . C. < Update MDP DSC PAR1 >
0136 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0137 BC (4c)
0138 C. MDP_CMD_CODE = F04C0700[ ]
0139 C. MDP_CMD_CNT (count-up 1) [ ]
0140 C. -----
0141 C.
0142 . C.
0143 C. *****
0144 C. SOT TI command set
0145 C. *****
0146 C. Execute, after the success of TBL upload.
0147 +. TI 2013-08-13 10:50:18.0
0148 DC 07-F0 MDP_SOT_MODE_OBSV
0149 BC (40)
0150 . C. -----
0151 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0152 C. -----
0153 C.
0154 C.
0155 C. ***** XRT START *****
0156 C.
0157 +. DC 07-F0 MDP_XRT_CTRL_MANU
0158 BC (c1)
0159 + DC 07-F0 MDP_XRT_MODE_STBY
0160 BC (c3)
0161 . C. ----- Success Verify ? OK / NG_____
0162 C.
0163 C. XRT Obs. Table Upload
0164 . S. RAM ram-291:MDP_OBS_X
0165 ( )
0166 C.
0167 +. DC 07-F0 MDP_DUMP_XRTTBL
0168 BC (84 07 00 00 00 3a d4)
0169 . C. ----- Comparison Check ? OK / ERR _____
0170 C.
0171 C.
0172 +. DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 01 b1 b1 04 04)
0174 + DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 02 b1 b1 08 08)
0176 + DC 07-F0 MDP_XRT_ROI_SET
0177 BC (cd 03 b1 b1 08 08)
0178 + DC 07-F0 MDP_XRT_ROI_SET
0179 BC (cd 04 b1 b1 06 06)
0180 + DC 07-F0 MDP_XRT_ROI_SET
0181 BC (cd 05 85 83 06 06)
0182 + DC 07-F0 MDP_XRT_ROI_SET
0183 BC (cd 06 85 83 06 06)
0184 + DC 07-F0 MDP_XRT_ROI_SET
0185 BC (cd 07 85 83 08 08)
0186 + DC 07-F0 MDP_XRT_ROI_SET
0187 BC (cd 08 80 80 20 20)
0188 + DC 07-F0 MDP_XRT_ROI_SET
0189 BC (cd 09 80 80 20 08)
0190 + DC 07-F0 MDP_XRT_ROI_SET
0191 BC (cd 0a 80 80 08 20)
0192 + DC 07-F0 MDP_XRT_ROI_SET
0193 BC (cd 0b 80 60 20 18)

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0194 + DC 07-F0 MDP_XRT_ROI_SET
0195 BC (cd 0c a0 80 18 20)
0196 + DC 07-F0 MDP_XRT_ROI_SET
0197 BC (cd 0f 80 80 06 06)
0198 + DC 07-F0 MDP_XRT_ROI_SET
0199 BC (cd 10 80 80 08 08)
0200 + DC 07-F0 MDP_XRT_FLD_ENA
0201 BC (d8)
0202 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0203 BC (c8)
0204 + DC 07-F0 MDP_XRT_AEC_RESET
0205 BC (d0)
0206 + DC 07-F0 MDP_XRT_ARS_DIS
0207 BC (d5)
0208 + DC 07-F0 MDP_XRT_FLD_RESET
0209 BC (da)
0210 + DC 07-F0 MDP_XRT_QT_PROG_SET
0211 BC (c4 02)
0212 + DC 07-F0 MDP_XRT_FL_PROG_SET
0213 BC (c5 08)
0214 . C. ----- Success Verify ? OK / NG ____
0215 C.
0216 C.
0217 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0218 C.
0219 +. DC 07-F0 MDP_XRT_MODE_OBSV
0220 BC (c2)
0221 +. TI 2013-08-13 10:50:02.0
0222 DC 07-F0 MDP_XRT_MODE_OBSV
0223 BC (c2)
0224 . C. ----- Success Verify ? OK / NG ____
0225 C.
0226 C. ***** XRT END *****
0227 C.
0228 . C. ***** MDP 'uAÎaî»ô%ÿaÊÂDâ¹aèDCBC•x²è *****
0229 C. (%â°îÿÓÿÃÿÈÿPÿËÿâÿçÿèaE%¼a¼Ã»Ûa¹aè)
0230 . S. DC-BC dcbc-402:DCBC
0231 (MDP_known_event)
0232 C.
0233 C.
0234 . C. ***** ÿDÿ¹.Ï Daily±;îÑaÊ´Øa¹aèDCBC•x²è *****
0235 . S. DC-BC dcbc-153:DCBC
0236 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0237 C.
0238 C.
0239 . C. ;ãLOSÿÃÿSÿËÿ¼Ã»Û;ã
0240 C.
0241 . C. ***** LOS *****
0242 C.
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*** OP Sequence for XRT ***

2013/08/13	11:00:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/13	11:00:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/08/13	11:01:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2013/08/13	11:01:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/08/13	11:01:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/08/13	11:01:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/08/13	11:01:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/08/13	11:01:24.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/13	11:01:26.0	XRT_QT_PROG_SET_401_OG [0x191]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2013/08/13	11:03:58.0	XRT_FL_PROG_SET_431_OG [0x1af]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2013/08/13	11:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/13	12:10:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/13	12:10:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/13	12:10:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/13	12:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/13	12:34:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/13	12:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/13	13:49:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/13	13:49:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/13	13:49:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/13	13:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/13	14:16:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/13	14:16:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/13	14:16:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/13	14:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/13	14:25:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/13	14:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/13	15:27:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/13	15:27:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/13	15:27:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/13	15:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/13	16:13:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/13	16:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/13	17:06:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/13	17:06:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/13	17:06:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/13	17:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/13	17:50:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/13	17:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/13	18:06:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/13	18:06:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/08/13	18:06:30.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/08/13	18:06:46.0	XRT_FLD_DIS_434_OG [0x1b2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/08/13	18:09:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/08/13	18:09:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/08/13	18:09:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2013/08/13	18:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/13	18:16:24.0	XRT_CTRL_MANU_402_OG [0x192]			

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2013/08/13	18:16:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/13	18:16:30.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/08/13	18:16:46.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00
2013/08/13	18:16:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/08/13	18:16:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/08/13	18:16:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/08/13	18:16:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/08/13	18:16:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/13	18:19:28.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2013/08/13	18:19:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2013/08/13	18:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/13	18:44:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/13	18:44:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/13	18:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/13	19:27:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/13	19:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2013/08/13	20:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/13	20:23:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/13	20:23:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/13	20:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/13	21:04:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/13	21:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2013/08/13	21:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/13	21:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/13	22:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/08/13	22:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00
2013/08/13	22:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/08/13	22:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/08/13	22:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/08/13	22:00:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/08/13	22:00:26.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/13	22:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2013/08/13	22:01:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/13	22:01:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/13	22:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/13	22:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2013/08/13	22:40:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/13	22:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2013/08/13	23:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/13	23:40:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/13	23:40:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/13	23:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/14	00:04:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/14	00:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2013/08/14	01:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/14	01:18:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/14	01:18:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/14	01:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2013/08/14	01:42:00.0	XRT_Custom_430_OG [0x1ae]							
2013/08/14	01:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/14	02:54:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/14	02:54:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/14	02:54:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/14	02:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/14	02:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/14	02:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/08/14	03:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2013/08/14	03:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/08/14	03:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/08/14	03:00:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/08/14	03:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/08/14	03:00:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/14	03:00:26.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2013/08/14	03:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2013/08/14	03:20:30.0	XRT_Custom_430_OG [0x1ae]							
2013/08/14	03:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/14	04:24:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/14	04:24:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/14	04:24:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/14	04:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/14	04:58:30.0	XRT_Custom_430_OG [0x1ae]							
2013/08/14	04:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/14	05:36:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/14	05:36:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/08/14	05:37:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/08/14	05:37:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/08/14	05:39:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/08/14	05:39:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/08/14	05:39:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2013/08/14	05:40:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/14	05:46:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/14	05:46:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/08/14	05:47:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 ac 00 00 00				
2013/08/14	05:47:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/08/14	05:47:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/08/14	05:47:20.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/08/14	05:47:22.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2013/08/14	06:02:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/14	07:46:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/14	07:46:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/08/14	07:47:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 00 00 54 00				
2013/08/14	07:47:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/08/14	07:47:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/08/14	07:47:20.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/08/14	07:47:22.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2013/08/14	08:02:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2013/08/14	09:46:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/14	09:46:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/08/14	09:47:00.0	AOCS_Or-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00
2013/08/14	09:47:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/08/14	09:47:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/08/14	09:47:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/08/14	09:47:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/08/14	09:47:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/14	09:47:26.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2013/08/14	09:49:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2013/08/14	09:54:00.0	XRT_Custom_430_OG [0x1ae]				
2013/08/14	09:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/14	11:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/14	11:09:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/14	11:09:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/14	11:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/14	11:32:30.0	XRT_Custom_430_OG [0x1ae]				
2013/08/14	11:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/14	12:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/14	12:48:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/14	12:48:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/14	12:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/14	13:11:00.0	XRT_Custom_430_OG [0x1ae]				
2013/08/14	13:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/14	14:26:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/14	14:26:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/14	14:26:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/14	14:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/14	14:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/14	14:51:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/14	14:51:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/14	14:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/14	15:03:30.0	XRT_Custom_430_OG [0x1ae]				
2013/08/14	15:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/14	16:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/14	16:05:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/14	16:05:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/14	16:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/14	16:50:00.0	XRT_Custom_430_OG [0x1ae]				
2013/08/14	16:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/14	17:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/14	17:43:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/14	17:43:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/14	17:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/14	18:26:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/14	18:26:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/08/14	18:26:30.0	AOCS_Or-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2013/08/14	18:26:46.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9
2013/08/14	18:29:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9

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2013/08/14	18:29:26.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/08/14	18:29:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	10	
2013/08/14	18:29:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/08/14	18:36:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/08/14	18:36:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2013/08/14	18:36:30.0	AOCS_ORe-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	01	00 00 00 00	
2013/08/14	18:36:46.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/08/14	18:36:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/08/14	18:36:50.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/08/14	18:36:52.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/08/14	18:36:54.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/08/14	18:36:56.0	XRT_QT_PROG_SET_401_OG [0x191]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	06	
2013/08/14	18:39:28.0	XRT_FL_PROG_SET_431_OG [0x1af]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	08	
2013/08/14	18:39:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/08/14	19:21:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/08/14	19:21:32.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/08/14	19:21:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/08/14	19:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/08/14	20:03:30.0	XRT_Custom_430_OG [0x1ae]					
2013/08/14	20:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/08/14	21:00:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/08/14	21:00:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/08/14	21:00:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/08/14	21:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/08/14	21:40:30.0	XRT_Custom_430_OG [0x1ae]					
2013/08/14	21:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/08/14	21:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/08/14	21:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2013/08/14	22:00:00.0	AOCS_ORe-point_Start_6_OG [0x09c]					
		AOCU_NM	5	02-76	04	00 00 00 00	
2013/08/14	22:00:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/08/14	22:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/08/14	22:00:20.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/08/14	22:00:22.0	XRT_ARS_DIS_417_OG [0x1a1]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/08/14	22:02:54.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/08/14	22:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	07	
2013/08/14	22:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	08	
2013/08/14	22:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/08/14	22:38:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/08/14	22:38:32.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/08/14	22:38:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/08/14	22:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/08/14	23:14:00.0	XRT_Custom_430_OG [0x1ae]					
2013/08/14	23:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/08/15	00:17:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/08/15	00:17:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/08/15	00:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/08/15	00:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/08/15	00:40:00.0	XRT_Custom_430_OG [0x1ae]					
2013/08/15	00:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					

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2013/08/15	01:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/08/15	01:55:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	da		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/08/15	01:55:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/08/15	01:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/08/15	02:18:30.0	XRT_Custom_430_OG [0x1ae]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2013/08/15	02:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/08/15	02:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/08/15	02:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/08/15	03:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00		
			AOCU_NM	5	02-76	01 00 00 00 00		
2013/08/15	03:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/08/15	03:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/08/15	03:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/08/15	03:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/08/15	03:00:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/08/15	03:00:26.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2013/08/15	03:02:58.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08		
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 08		
2013/08/15	03:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/08/15	03:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/08/15	03:27:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/08/15	03:27:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/08/15	03:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/08/15	03:57:00.0	XRT_Custom_430_OG [0x1ae]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2013/08/15	03:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/08/15	05:02:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/08/15	05:02:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/08/15	05:02:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/08/15	05:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/08/15	05:35:30.0	XRT_Custom_430_OG [0x1ae]	XRT_CTRL_AUTO_424_OG [0x1a8]					
2013/08/15	05:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/08/15	05:58:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/08/15	05:58:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/08/15	05:58:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
			AOCU_NM	5	02-76	00 00 00 00 00		
2013/08/15	05:58:46.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9		
			MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/08/15	06:01:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/08/15	06:01:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/08/15	06:01:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10		
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 10		
2013/08/15	06:01:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/08/15	06:08:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/08/15	06:08:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/08/15	06:08:30.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00		
			AOCU_NM	5	02-76	01 00 00 00 00		
2013/08/15	06:08:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/08/15	06:08:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/08/15	06:08:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/08/15	06:08:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/08/15	06:08:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/08/15	06:08:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2013/08/15	06:11:28.0	XRT_FL_PROG_SET_431_OG [0x1af]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08		
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 08		
2013/08/15	06:11:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		

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2013/08/15	06:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/15	06:42:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/15	06:42:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/15	06:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/15	07:14:00.0	XRT_Custom_430_OG [0x1ae]				
2013/08/15	07:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/15	08:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/15	08:22:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/15	08:22:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/15	08:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/15	08:52:30.0	XRT_Custom_430_OG [0x1ae]				
2013/08/15	08:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/15	09:30:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/15	10:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/15	10:03:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/15	10:03:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/15	10:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/15	10:37:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00