

# XRT Timeline to be uploaded on 2013/08/17

Period: 2013/08/17 10:03:00 - 2013/08/22 09:51:00

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Normal mode

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## XOB #19B3: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band

Term	Pointing (x, y)	Comment
08/17 10:16:00 - 08/17 17:45:54	Track ( 384.7, -213.8) <small>Ⓢ 08/17 10:13:00</small>	# OP start + 10min, AR11818
08/17 18:40:00 - 08/17 21:13:00	Track ( 447.9, -210.3) <small>Ⓢ 08/17 17:56:00</small>	# AR11818
08/18 01:03:00 - 08/18 05:59:54	Track ( 503.7, -206.6) <small>Ⓢ 08/18 01:00:00</small>	# AR11818
08/19 01:29:30 - 08/19 05:52:30	Track ( 673.7, -190.8) <small>Ⓢ 08/19 01:00:00</small>	AR 11818
08/19 18:28:00 - 08/19 21:59:54	Track ( 774.7, -176.4) <small>Ⓢ 08/19 18:25:00</small>	# AR11818
08/20 01:03:00 - 08/20 06:00:24	Track ( 807.3, -170.4) <small>Ⓢ 08/20 01:00:00</small>	AR11818

**PROG= 17 Inf.-time(s)**

<b>Subr= 1 1-time(s) 2.0sec</b>	
<b>Seqn= 8 2-time(s) 2.0sec</b>	
Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec	
<b>Subr= 2 2-time(s) 2.0sec</b>	
<b>Seqn= 24 1-time(s) 2.0sec</b>	
Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec	
Open/G-band Open/G-band open Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec	
<b>Seqn= 98 4-time(s) 2.0sec</b>	
Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	
Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec	
thin-Be/Open med-Be/Open close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec	
Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	
<b>Seqn= 21 15-time(s) 180.0sec</b>	
thin-Be/Open Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	
Al-poly/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec	
thin-Be/Open Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec	
Al-poly/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec	
thin-Be/Open Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec	
Al-poly/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec	
thin-Be/Open Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec	
Al-poly/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec	

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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## XOB #19AE: Synoptic Q95 2x2 - Al/mesh(12/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(33/362/1443) + Thi

Term	Pointing (x, y)	Comment
08/17 17:49:00 - 08/17 17:55:54	Fixed ( 0.0, 0.0)	synoptic, shifted -14.0 min
08/18 06:03:00 - 08/18 06:09:54	Fixed ( 0.0, 0.0)	synoptic
08/18 19:23:00 - 08/18 19:29:54	Fixed ( 0.0, 0.0)	synoptic, shifted because of EIS CH obs
08/19 06:31:30 - 08/19 06:38:24	Fixed ( 0.0, 0.0)	synoptic, shifted 28.5 min
08/19 18:18:00 - 08/19 18:24:54	Fixed ( 0.0, 0.0)	synoptic, shifted 15.0 min
08/20 06:03:30 - 08/20 06:10:24	Fixed ( 0.0, 0.0)	synoptic, shifted 0.5 min

**PROG= 18 1-time(s)**

<b>Subr= 1 1-time(s) 12.0sec</b>	
<b>Seqn= 82 1-time(s) 4.0sec</b>	
Open/Al-mesh Open/Al-mesh close Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
<b>Seqn= 5 1-time(s) 2.0sec</b>	
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec	
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec	
Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec	
<b>Seqn= 83 1-time(s) 4.0sec</b>	
Open/Ti-poly Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Open/Ti-poly Open/Ti-poly close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
<b>Seqn= 99 1-time(s) 2.0sec</b>	
thin-Be/Open thin-Be/Open close Safe Norm 86ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
thin-Be/Open thin-Be/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
thin-Be/Open thin-Be/Open close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec	
<b>Seqn= 66 1-time(s) 2.0sec</b>	
Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec	
Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec	

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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## XOB #19AF: HOP128- AR - Al/mesh with PFB(Thin Be), G-band - 512x512 45ms with G-band VLS closed 33ms

Term	Pointing (x, y)	Comment
08/17 22:03:00 - 08/18 00:30:00	Track ( 480.1, -193.6) <small>Ⓢ 08/17 22:00:00</small>	# AR11818 for HOP128
08/18 22:29:30 - 08/18 23:29:00	Fixed ( 819.6, -365.3)	AR 11817 for HOP128
08/19 22:07:00 - 08/20 00:59:54	Track ( 793.7, -158.5) <small>Ⓢ 08/19 22:00:00</small>	AR11818 for HOP128

**PROG= 01 Inf.-time(s)**

**Subr= 1 1-time(s) 2.0sec**

Seqn= 84		2-time(s)		2.0sec													
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	512x512 (1064, 1048)		DPCM	0	0	2.0sec				
Subr= 2		1-time(s)		2.0sec													
Seqn= 95		1-time(s)		30.0sec													
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	512x512 (1064, 1048)		Q=90	0	0	2.0sec				
Subr= 3		30-time(s)		2.0sec													
Seqn=100		2-time(s)		30.0sec													
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	2.0sec				
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	1	2.0sec				
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	2	2.0sec				
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	3	2.0sec				
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval					

**XOB #19B4: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band**

Term	Pointing (x, y)	Comment
08/18 06:13:00 - 08/18 14:59:54	Track ( 542.9, -203.6) @ 08/18 06:10:00	# AR11818
08/19 06:41:30 - 08/19 17:32:00	Track ( 708.7, -186.3) @ 08/19 06:38:30	# AR11818

**PROG= 09 Inf.-time(s)**

Subr= 1		1-time(s)		2.0sec													
Seqn= 8		2-time(s)		2.0sec													
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec				
Subr= 2		2-time(s)		2.0sec													
Seqn= 24		1-time(s)		2.0sec													
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec				
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec				
Seqn= 98		4-time(s)		2.0sec													
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec				
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	2.0sec				
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	2.0sec				
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec				
Seqn= 21		12-time(s)		240.0sec													
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec				
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec				
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	1	2.0sec				
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	1	2.0sec				
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	2	2.0sec				
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	2	2.0sec				
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	3	2.0sec				
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	3	2.0sec				
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval					

**XOB #19A6: CME watch - 4x4 - AEC 2 - Ti/poly- 60s cad**

Term	Pointing (x, y)	Comment
08/18 15:03:00 - 08/18 19:19:54	Track ( 0.0, -61.2) @ 08/18 15:00:00	CH for EIS

**PROG= 12 Inf.-time(s)**

Subr= 1		15-time(s)		60.0sec													
Seqn= 47		1-time(s)		4.0sec													
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	2	0	2.0sec				
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval					

**XOB #19B0: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band**

Term	Pointing (x, y)	Comment
08/18 19:33:00 - 08/18 21:50:30	Track ( 637.7, -194.8) @ 08/18 19:30:00	# AR 11818

**PROG= 04 Inf.-time(s)**

Subr= 1		1-time(s)		2.0sec													
Seqn= 8		2-time(s)		2.0sec													
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)		DPCM	0	0	2.0sec				
Subr= 2		2-time(s)		2.0sec													
Seqn= 24		1-time(s)		2.0sec													
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec				
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)		Q=98	0	0	2.0sec				
Seqn= 98		4-time(s)		2.0sec													
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec				
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	2.0sec				
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)		Q=95	3	0	2.0sec				
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec				
Seqn= 21		45-time(s)		60.0sec													
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec				
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	0	2.0sec				
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	1	2.0sec				
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	1	2.0sec				
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	2	2.0sec				
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	2	2.0sec				
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)		Q=95	3	3	2.0sec				
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)		Q=95	3	3	2.0sec				
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval					

**XOB #19B2: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band**

Term	Pointing (x, y)	Comment
08/20 06:13:30 - 08/20 08:10:30	Track ( 830.5, -165.6) <sup>Ⓜ</sup> 08/20 06:10:30	# AR11818
<b>PROG= 15 Inf.-time(s)</b>		
Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 8 2-time(s) 2.0sec		
Open/G-band	Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048)	DPCM 0 0 2.0sec
Subr= 2 2-time(s) 2.0sec		
└─ Seqn= 24 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048)	Q=98 0 0 2.0sec
Open/G-band	Open/G-band open Safe Norm 32ms Obs 1x1 384x384 (1064, 1048)	Q=98 0 0 2.0sec
└─ Seqn= 98 4-time(s) 2.0sec		
Al-poly/Open	thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Be close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048)	Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048)	Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
└─ Seqn= 21 25-time(s) 120.0sec		
thin-Be/Open	Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
Al-poly/Open	Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
thin-Be/Open	Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048)	Q=95 3 1 2.0sec
Al-poly/Open	Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 1 2.0sec
thin-Be/Open	Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048)	Q=95 3 2 2.0sec
Al-poly/Open	Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 2 2.0sec
thin-Be/Open	Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048)	Q=95 3 3 2.0sec
Al-poly/Open	Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 3 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

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**Flare mode**

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**XOB #1980: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops-2**

Term	Pointing (x, y)	Comment
08/17 10:16:00 - 08/17 17:45:54	Track ( 384.7, -213.8) <sup>Ⓜ</sup> 08/17 10:13:00	# OP start + 10min, AR11818
08/17 18:40:00 - 08/17 21:13:00	Track ( 447.9, -210.3) <sup>Ⓜ</sup> 08/17 17:56:00	# AR11818
08/17 22:03:00 - 08/18 00:30:00	Track ( 480.1, -193.6) <sup>Ⓜ</sup> 08/17 22:00:00	# AR11818 for HOP128
08/18 01:03:00 - 08/18 05:59:54	Track ( 503.7, -206.6) <sup>Ⓜ</sup> 08/18 01:00:00	# AR11818
08/18 06:13:00 - 08/18 14:59:54	Track ( 542.9, -203.6) <sup>Ⓜ</sup> 08/18 06:10:00	# AR11818
08/18 15:03:00 - 08/18 19:19:54	Track ( 0.0, -61.2) <sup>Ⓜ</sup> 08/18 15:00:00	CH for EIS
08/18 19:33:00 - 08/18 21:50:30	Track ( 637.7, -194.8) <sup>Ⓜ</sup> 08/18 19:30:00	# AR 11818
08/18 22:29:30 - 08/18 23:29:00	Fixed ( 819.6, -365.3)	AR 11817 for HOP128
08/19 01:29:30 - 08/19 05:52:30	Track ( 673.7, -190.8) <sup>Ⓜ</sup> 08/19 01:00:00	AR 11818
08/19 06:41:30 - 08/19 17:32:00	Track ( 708.7, -186.3) <sup>Ⓜ</sup> 08/19 06:38:30	# AR11818
08/19 18:28:00 - 08/19 21:59:54	Track ( 774.7, -176.4) <sup>Ⓜ</sup> 08/19 18:25:00	# AR11818
08/19 22:07:00 - 08/20 00:59:54	Track ( 793.7, -158.5) <sup>Ⓜ</sup> 08/19 22:00:00	AR11818 for HOP128
08/20 01:03:00 - 08/20 06:00:24	Track ( 807.3, -170.4) <sup>Ⓜ</sup> 08/20 01:00:00	AR11818
08/20 06:13:30 - 08/20 08:10:30	Track ( 830.5, -165.6) <sup>Ⓜ</sup> 08/20 06:10:30	# AR11818
<b>PROG= 03 15-time(s)</b>		
Subr= 1 45-time(s) 10.0sec		
└─ Seqn= 9 1-time(s) 2.0sec		
thin-Be/Open	med-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024)	Q=95 3 0 2.0sec
Subr= 2 1-time(s) 10.0sec		
└─ Seqn= 10 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024)	Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024)	Q=95 3 0 2.0sec
└─ Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024)	Q=95 2 0 2.0sec
└─ Seqn= 15 1-time(s) 2.0sec		
Open/G-band	Open/G-band open Safe Norm 44ms Obs 1x1 384x384 (1024, 1024)	Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024)	Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024)	Q=98 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

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**Active Region Search**

\* \* \* \* \*

NOT USED

\* \* \* \* \*

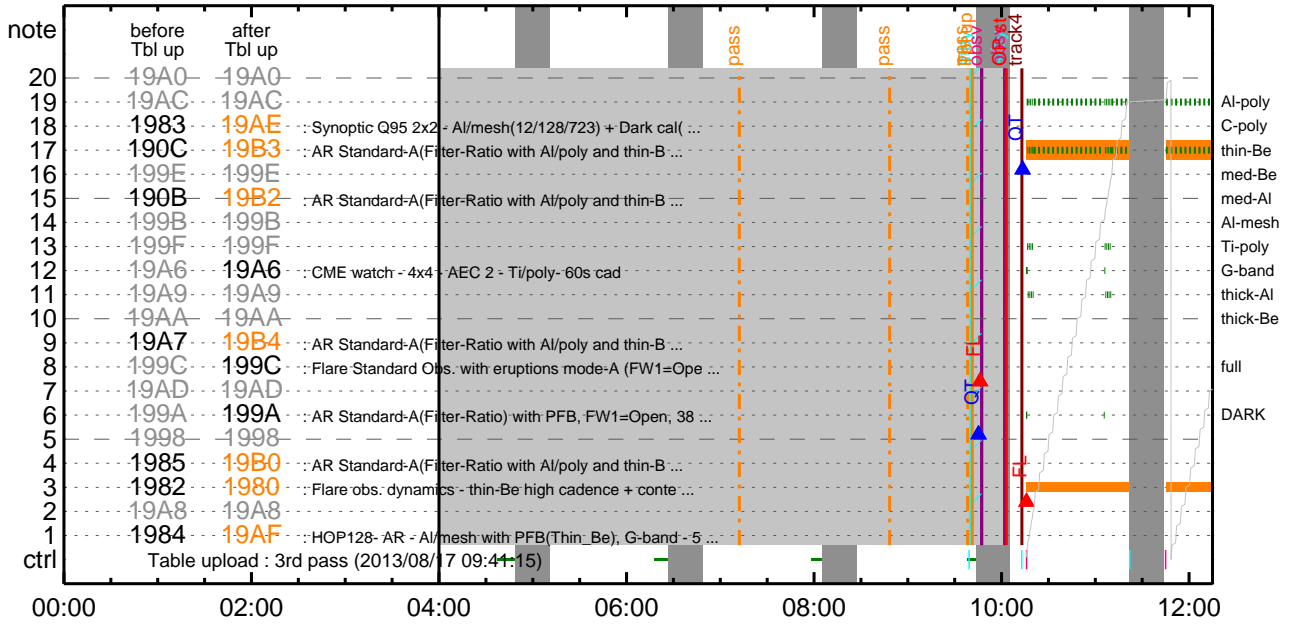
**Flare Detection**

\* \* \* \* \*

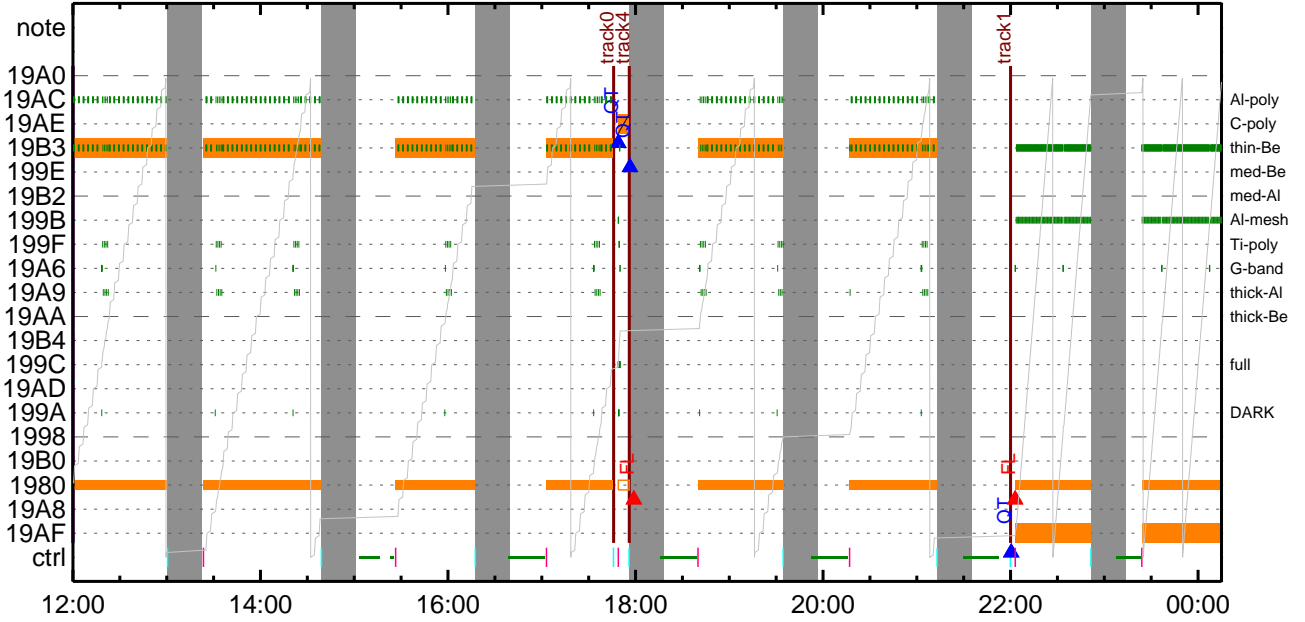
**FLD Patrol**

Term	Pointing (x, y)	Comment
08/17 17:56:16 - 08/18 06:00:16	Track ( 447.9, -210.3) <sup>Ⓜ</sup> 08/17 17:56:00	# AR11818
08/18 06:10:16 - 08/18 19:20:16	Track ( 542.9, -203.6) <sup>Ⓜ</sup> 08/18 06:10:00	# AR11818
08/18 19:30:16 - 08/19 06:28:46	Track ( 637.7, -194.8) <sup>Ⓜ</sup> 08/18 19:30:00	# AR 11818
08/19 06:38:46 - 08/19 18:15:16	Track ( 708.7, -186.3) <sup>Ⓜ</sup> 08/19 06:38:30	# AR11818
08/19 18:25:16 - 08/20 06:00:46	Track ( 774.7, -176.4) <sup>Ⓜ</sup> 08/19 18:25:00	# AR11818
08/20 06:10:46 - 08/22 09:51:00	Track ( 830.5, -165.6) <sup>Ⓜ</sup> 08/20 06:10:30	# AR11818
Open/Ti-poly	Open/thick-Al close Safe Norm 8ms Obs 8x8	Q=50 30sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

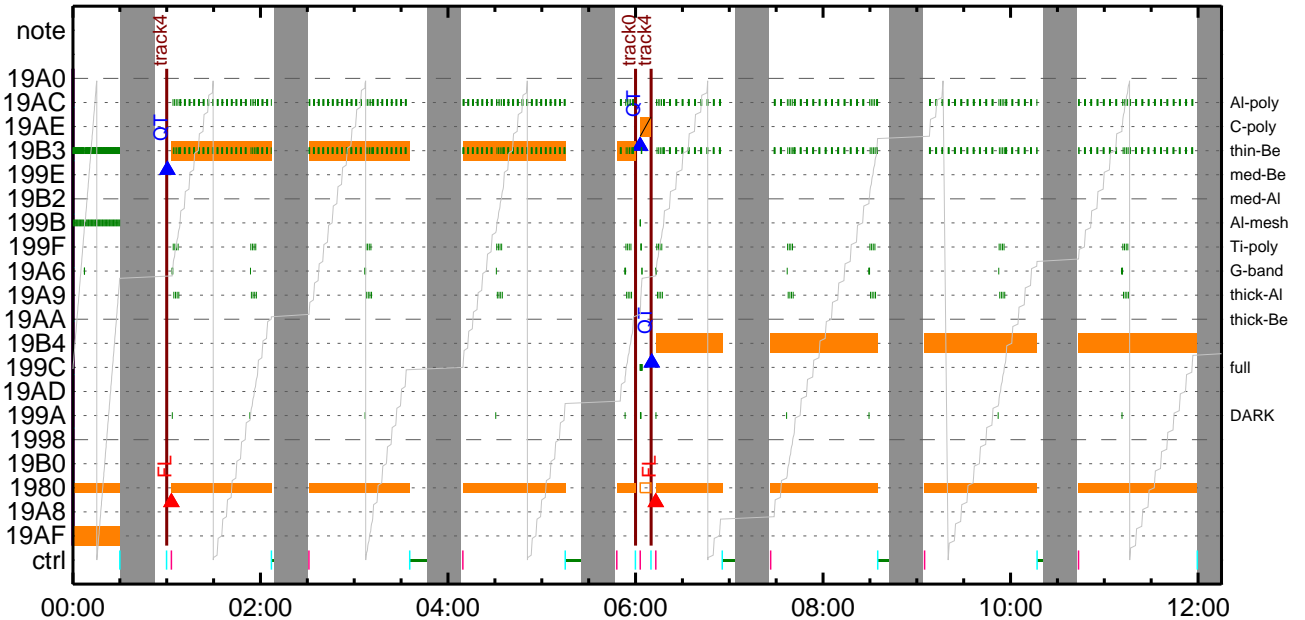
### CMDI #0676 2013/08/17



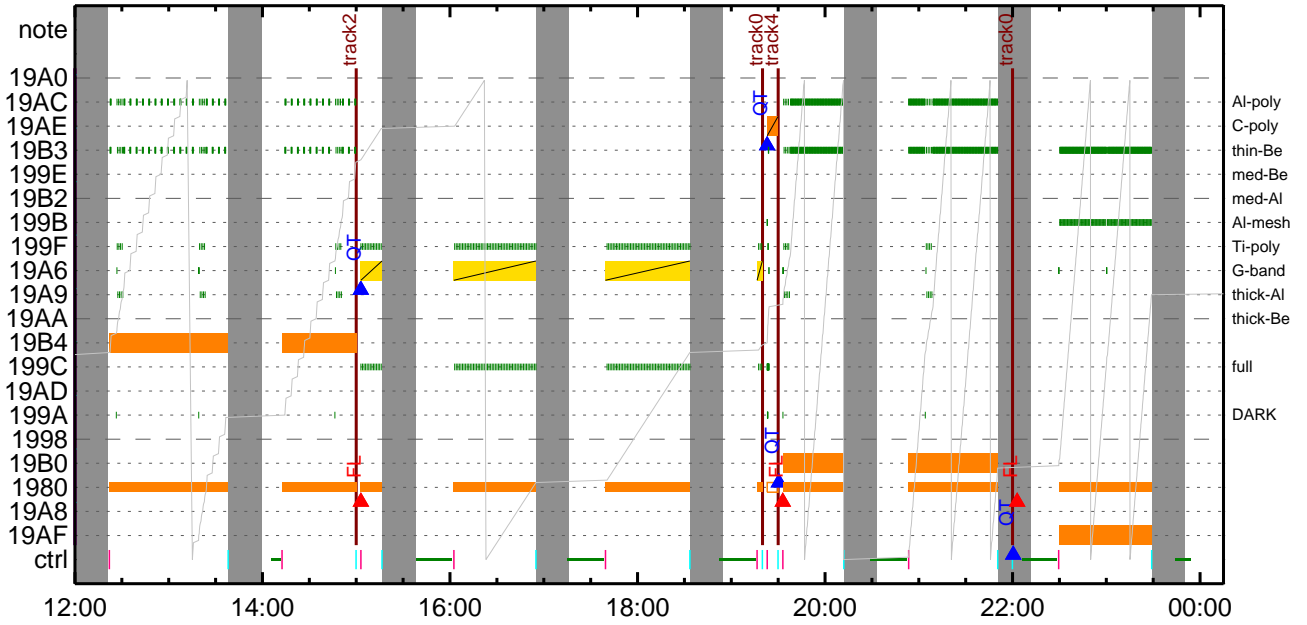
### CMDI #0676 2013/08/17



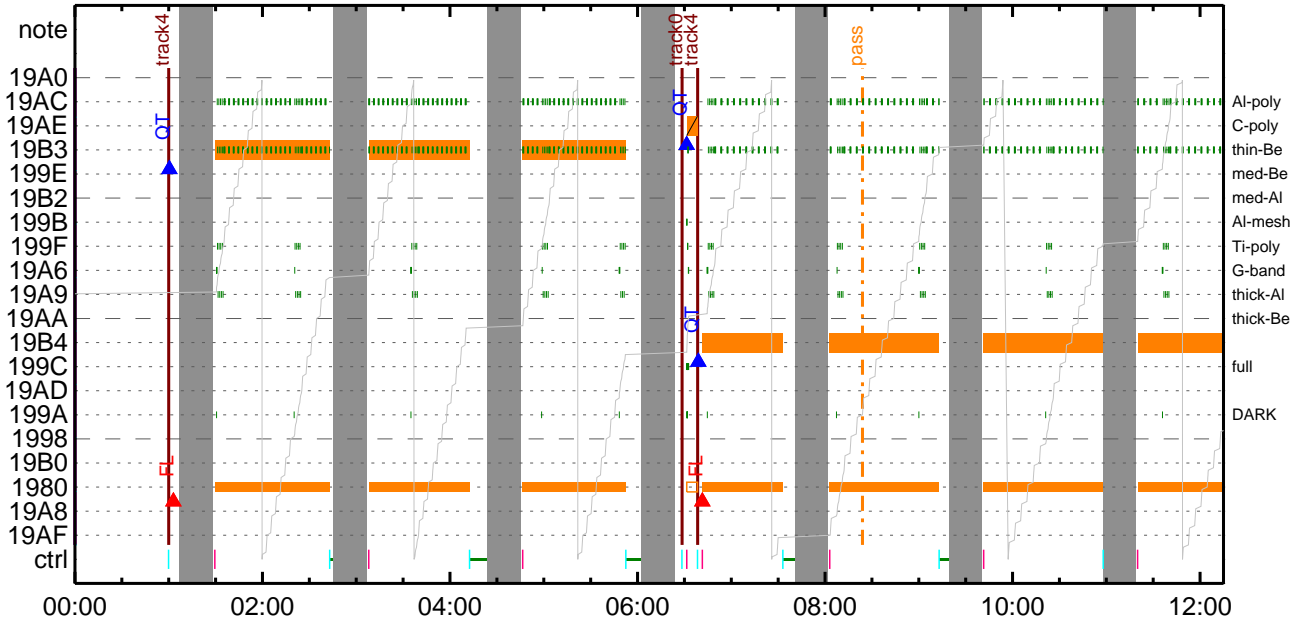
### CMDI #0676 2013/08/18



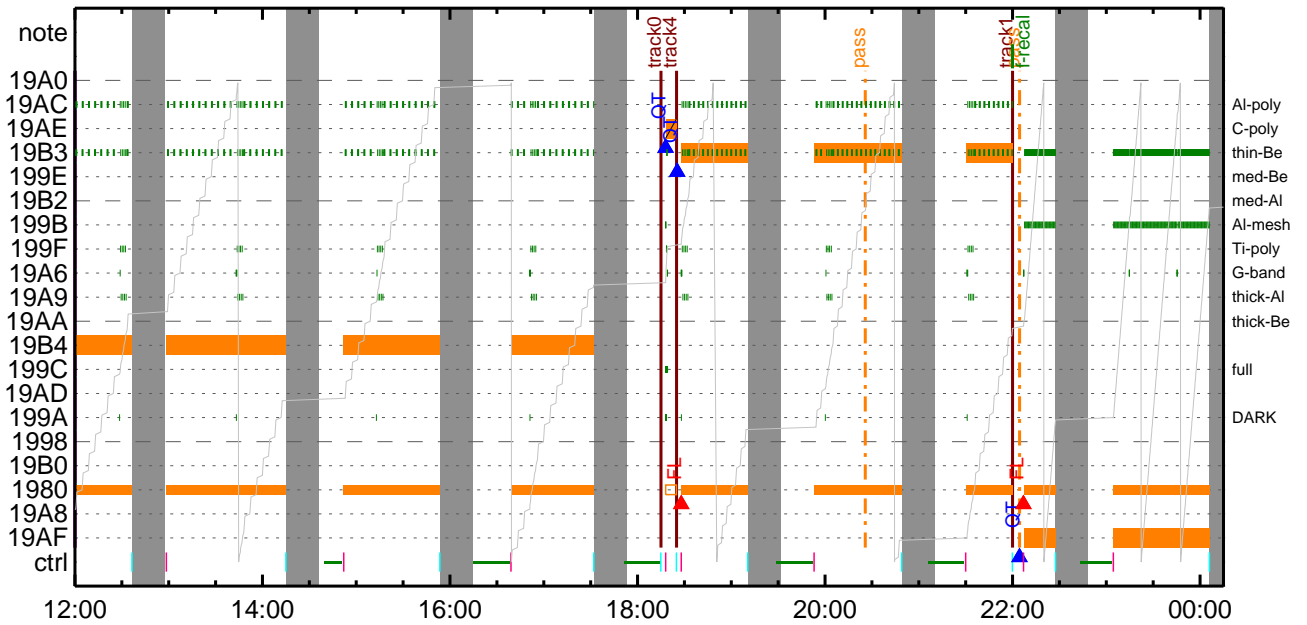
### CMDI #0676 2013/08/18



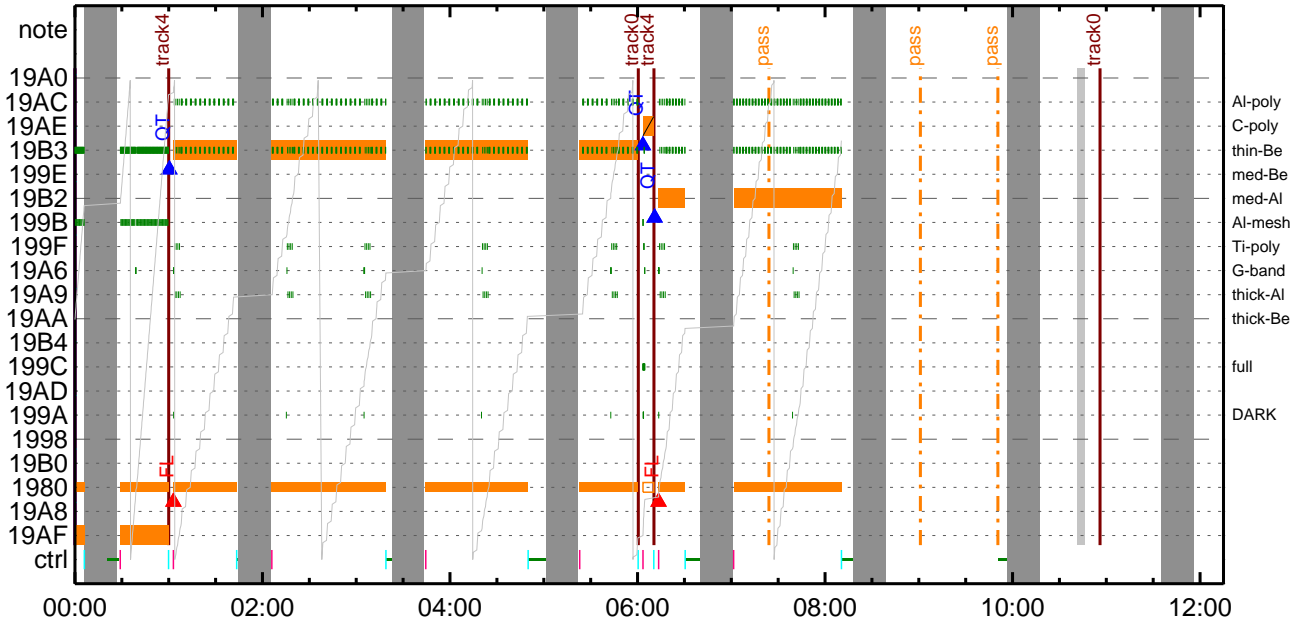
### CMDI #0676 2013/08/19



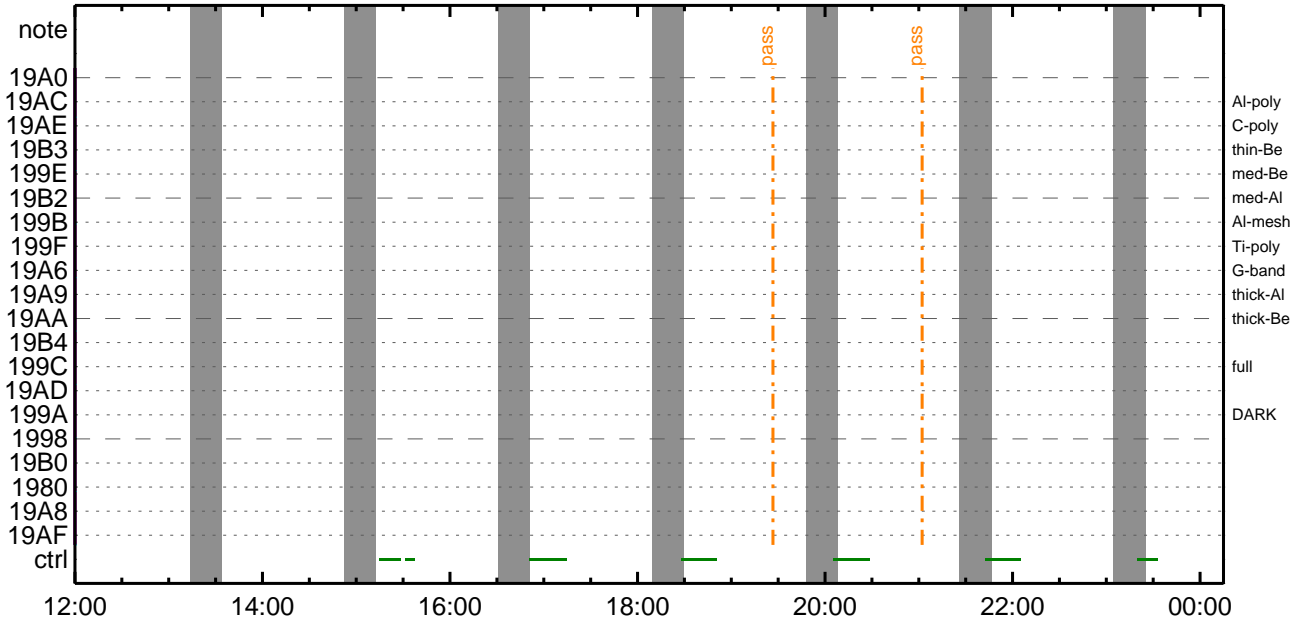
### CMDI #0676 2013/08/19



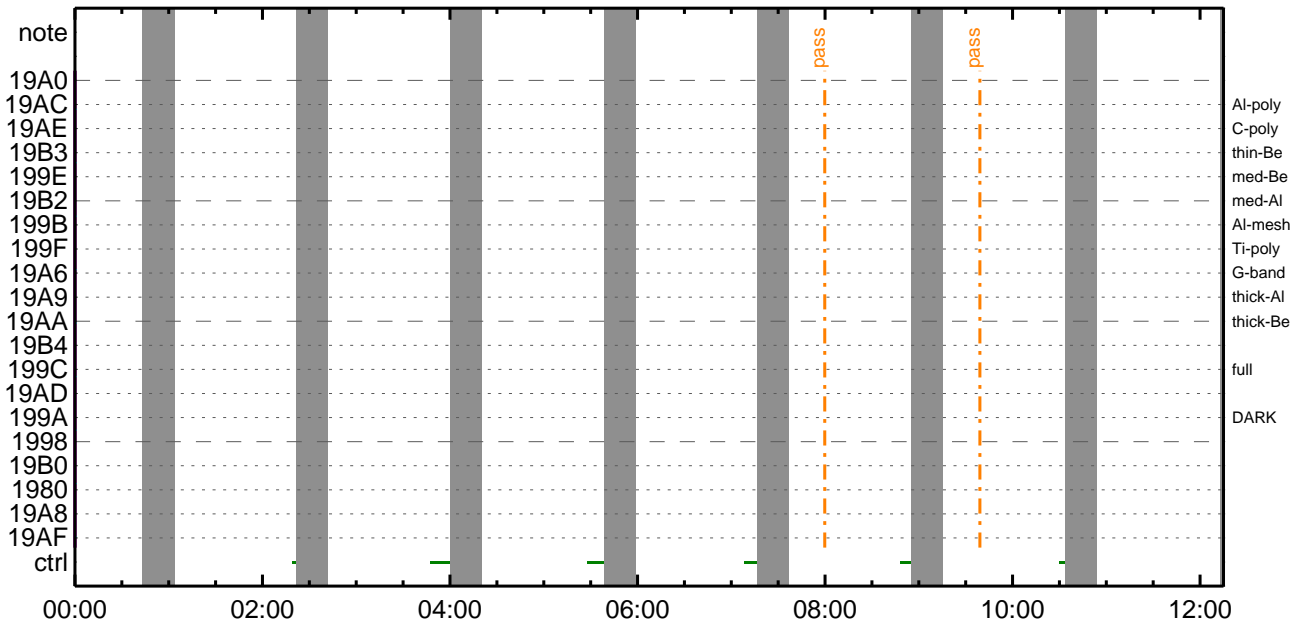
CMDI #0676 2013/08/20



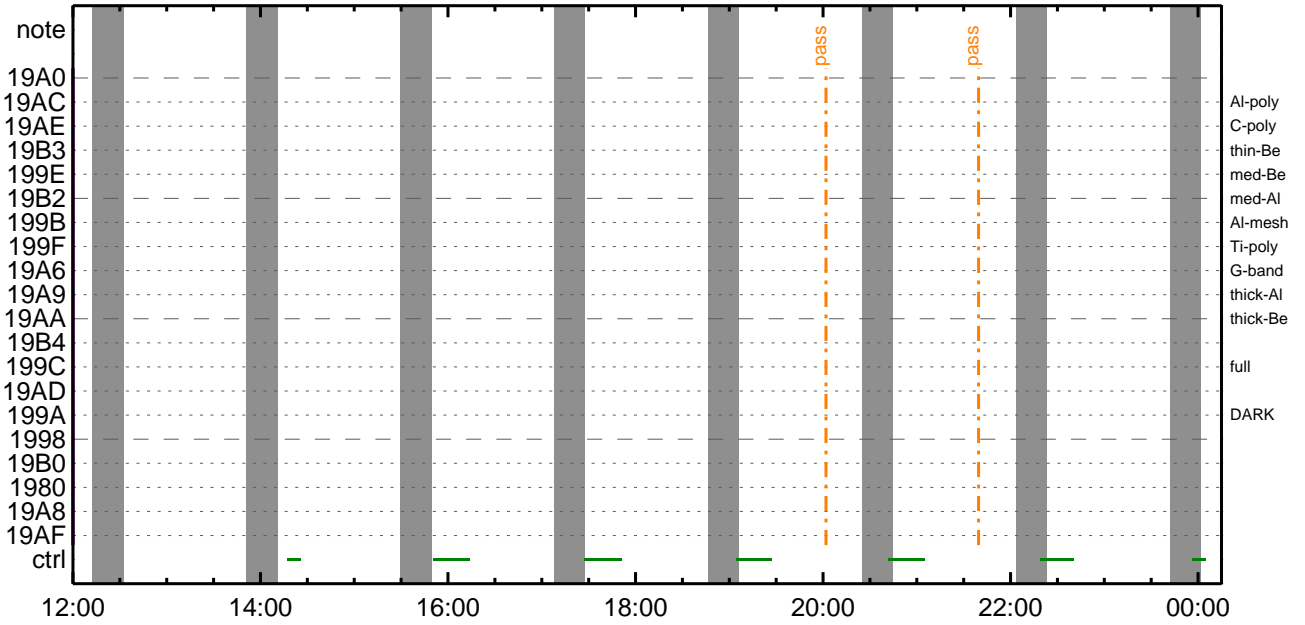
CMDI #0676 2013/08/20



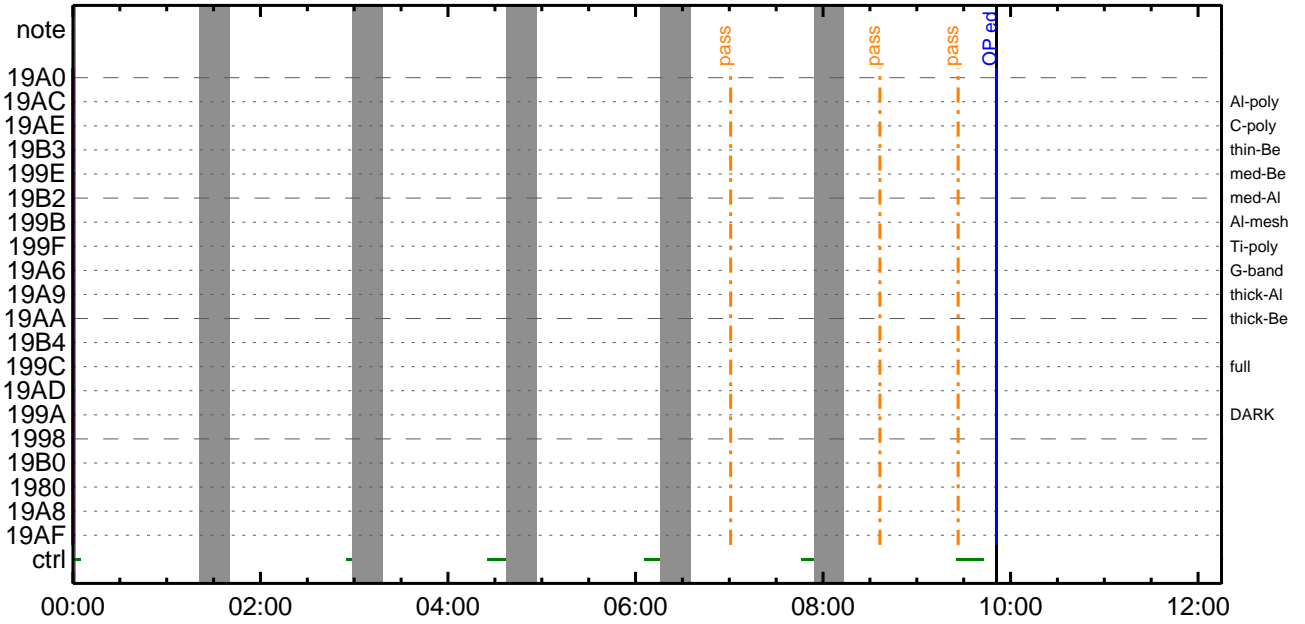
CMDI #0676 2013/08/21



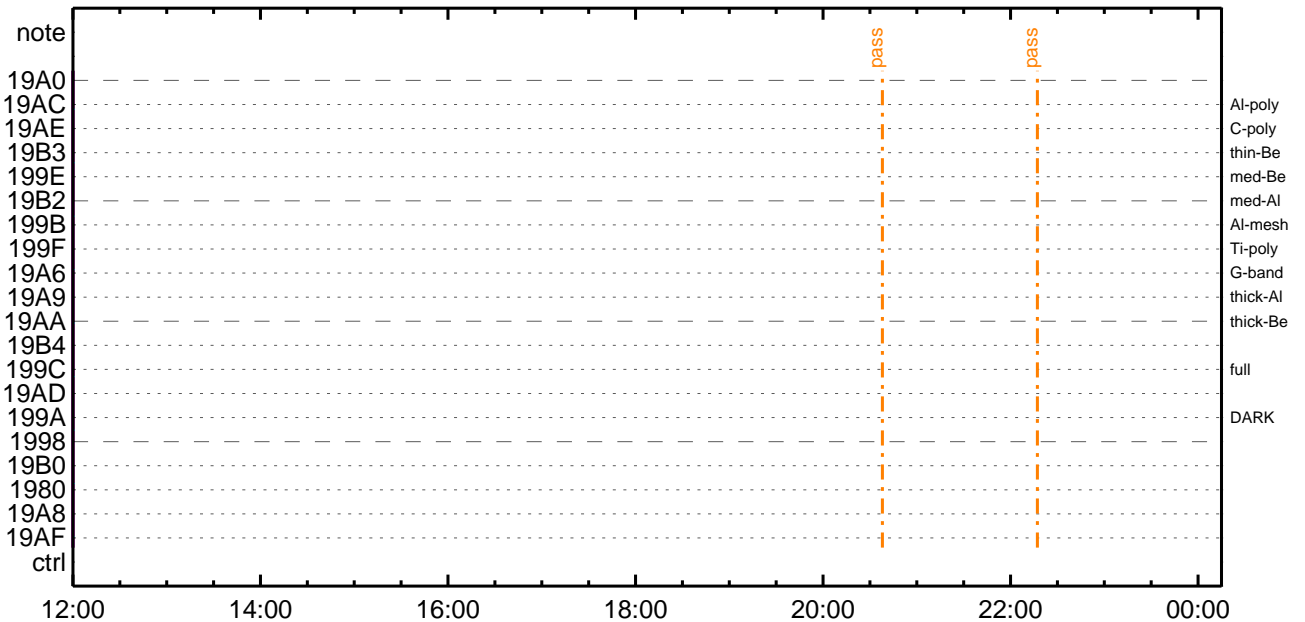
CMDI #0676 2013/08/21



CMDI #0676 2013/08/22



CMDI #0676 2013/08/22







```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-846:OP
0104 ( )
0105 S. OG og-846:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî¼Ã´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOYx½ê¼çòðÁÔæòÇ¼ª°¬òE¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E¼Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î¼î¹ç;ç°E²¼òî¼TI-CMDÁ÷¿@²î¼Á¹Ôª°¬E²ò³òE;f
0180 C. ²ò³òE;çSET²EEDUMP²î¼±°îYÑY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²Y¼YE²òðÁDî¿(UT)
0183 +. TI 2013-08-17 09:58:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-08-17 09:58:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-08-17 09:58:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2013-08-17 10:02:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼□îÄë%îíñ□îŷÄŷ§ŷÄŷ-¹àîü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]        EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîŷ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]        EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]        EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»□ð³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]        EQ      NON
0227 C.
0228 C. RAM ID=TI_TBL□îŷ°è¹ç•è²îOK□ð³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÉ;Ê¼ŷ¼. ŷî;¼ŷÉ;Ê□ðîâ□¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]        EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2013-08-17 10:02:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C.   HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2013-08-17 10:02:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2013-08-17 10:02:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2013-08-17 10:02:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîâî»ö¼ŷ□èÄð□¹èDCBC•x²è *****
0276 C. (¼ª°îŷÖŷÄŷÉŷŷŷÉŷ¼ŷçŷèè¼□□¼ª»Û□¹è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷÐŷ¹•î Daily±çîñ□è´Ø□¹èDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼ª»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

```



```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 41s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2013-08-17 10:02:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP 'úÃîîî»ö¼ÝñÊÄðñ¹ñèDCBC•x²è *****
0153 C. (%ã°îÿÖÿÄÿËÿPÿËÿÄÿÇÿËÿñÊ¼ñ¼Ä»Ûñ¹ñè)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** ÿDÿ¹•Ï Daily±¿Îññ'Øñ¹ñèDCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ;ãLOSÿÄÿSÿÄÿÿ¼Ä»Û;ã
0164 C.
0165 . C. ***** LOS *****
0166 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-848 2013-08-17 12:01:43 148 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYŞ;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èççãã•µ°È»ÍxÁÇçíYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈèÈ¼°ÇÓã•ã¿¼í¹çãí;çÁ®, ùã¹ãèãããçÁ+¿®ã•ãÈããã³ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 85 83 06 06)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 85 83 08 08)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 20)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 20 08)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 80 80 08 20)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0f 80 80 06 06)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 10 80 80 08 08)
0056 + DC 07-F0 MDP_XRT_FLD_ENA
0057 BC (d8)
0058 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0059 BC (c8)
0060 + DC 07-F0 MDP_XRT_AEC_RESET
0061 BC (d0)
0062 + DC 07-F0 MDP_XRT_ARS_DIS
0063 BC (d5)
0064 + DC 07-F0 MDP_XRT_FLD_RESET
0065 BC (da)
0066 + DC 07-F0 MDP_XRT_QT_PROG_SET
0067 BC (c4 06)
0068 + DC 07-F0 MDP_XRT_FL_PROG_SET
0069 BC (c5 08)
0070 . C. ----- Success Verify ? OK / NG ____
0071 C.
0072 C.
0073 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0074 C.
0075 +. DC 07-F0 MDP_XRT_MODE_OBSV
0076 BC (c2)
0077 +. TI 2013-08-17 10:02:02.0
0078 DC 07-F0 MDP_XRT_MODE_OBSV
0079 BC (c2)
0080 . C. ----- Success Verify ? OK / NG ____
0081 C.
0082 C. ***** XRT END *****
0083 C. *****
0084 C. SOT table upload
0085 C. *****
0086 . C. < Stop FG table >
0087 +. DC 07-F0 MDP_FG_CTRL_MANU
0088 BC (51)
0089 . C. -----
0090 C. MDP_FG_CTRL_MODE = MANU [ ]
0091 C. -----
0092 C.
0093 . C. <Upload FG Observation Table>
0094 . S. RAM ram-266:MDP_OBS_F
0095 ( )
```

```
0096 C.
0097 . C. < Dump RAMID=MDP_OBS_F >
0098 +. DC 07-F0 MDP_DUMP_FGTBL
0099 BC (82 07 00 00 00 38 b8)
0100 C. -----
0101 C. MDP_OBS_F verify = OK/NG [ ]
0102 C. -----
0103 C.
0104 . C. < Stop SP table >
0105 +. DC 07-F0 MDP_SP_CTRL_MANU
0106 BC (61)
0107 C. -----
0108 C. MDP_SP_CTRL_MODE = MANU [ ]
0109 C. -----
0110 C.
0111 . C. <Upload SP Observation Table>
0112 . S. RAM ram-287:MDP_OBS_S
0113 ( )
0114 C.
0115 . C. < Dump RAMID=MDP_OBS_S >
0116 +. DC 07-F0 MDP_DUMP_SPTBL
0117 BC (83 07 00 00 00 38 b8)
0118 C. -----
0119 C. MDP_OBS_S verify = OK/NG [ ]
0120 C. -----
0121 C.
0122 C. *****
0123 C. SOT TI command set
0124 C. *****
0125 C. Execute, after the success of TBL upload.
0126 +. TI 2013-08-17 10:02:18.0
0127 DC 07-F0 MDP_SOT_MODE_OBSV
0128 BC (40)
0129 . C. -----
0130 C. HKI_TI_CMD_NUM = 1 CNTUP [ ]
0131 C. -----
0132 C.
0133 C.
0134 . C. ***** MDP 'ûÃîåî»ö¼ÝáëÂð¹²³DCBC•×²è *****
0135 C. (¼á°îÿÓÿÄÿËÿÌÿÍÿÎÿÏÿÐÿÑÿÒÿÓÿÔÿÕÿÖÿ×ÿØÿ¹ÿºÿ»ÿ¼ÿ½ÿ¾ÿ¿ÿÀÿ¹²³)
0136 . S. DC-BC dcbc-402:DCBC
0137 (MDP_known_event)
0138 C.
0139 C.
0140 . C. ***** ¼Ðÿ¹•î Daily±¿îÑå´Ø¹²³DCBC•×²è *****
0141 . S. DC-BC dcbc-153:DCBC
0142 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0143 C.
0144 C.
0145 . C. ;ãLOSÿÁÿËÿÏÿÑÿ½Â»Û;ä
0146 C.
0147 . C. ***** LOS *****
0148 C.
```

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\*\*\* OP Sequence for XRT \*\*\*

2013/08/17	10:12:54.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/17	10:12:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]				
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/08/17	10:13:00.0	AOCS_Orе-point_Start_1_OG [0x097]				
		AOCU_NM	5	02-76	04 00 00 00 00	
2013/08/17	10:13:16.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/08/17	10:13:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/08/17	10:13:20.0	XRT_AEC_RESET_413_OG [0x19d]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/08/17	10:13:22.0	XRT_ARS_DIS_423_OG [0x1a7]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/08/17	10:13:24.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/17	10:13:26.0	XRT_QT_PROG_SET_446_OG [0x1be]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2013/08/17	10:15:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2013/08/17	10:16:00.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/17	11:22:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/17	11:22:02.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/17	11:22:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/17	11:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/17	11:44:00.0	XRT_Custom_430_OG [0x1ae]				
2013/08/17	11:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/17	13:00:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/17	13:00:32.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/17	13:00:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/17	13:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/17	13:22:30.0	XRT_Custom_430_OG [0x1ae]				
2013/08/17	13:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/17	14:39:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/17	14:39:02.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/17	14:39:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/17	14:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/17	15:25:30.0	XRT_Custom_430_OG [0x1ae]				
2013/08/17	15:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/17	16:17:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/17	16:17:32.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/17	16:17:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/17	16:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/17	17:02:00.0	XRT_Custom_430_OG [0x1ae]				
2013/08/17	17:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/17	17:45:54.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/17	17:45:56.0	XRT_FOCUS_POSITION_403_OG [0x193]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/08/17	17:46:00.0	AOCS_Orе-point_Start_2_OG [0x098]				
		AOCU_NM	5	02-76	00 00 00 00 00	
2013/08/17	17:46:16.0	XRT_FLD_DIS_434_OG [0x1b2]				
		MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/08/17	17:48:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]				
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/08/17	17:48:56.0	XRT_ARS_DIS_423_OG [0x1a7]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/08/17	17:48:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2013/08/17	17:49:00.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/17	17:55:54.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/17	17:55:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]				
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/08/17	17:56:00.0	AOCS_Orе-point_Start_1_OG [0x097]				
		AOCU_NM	5	02-76	04 00 00 00 00	
2013/08/17	17:56:16.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/08/17	17:56:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				

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2013/08/17	17:56:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/08/17	17:56:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/08/17	17:56:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/17	17:56:26.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2013/08/17	17:58:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2013/08/17	18:39:00.5	XRT_Custom_430_OG [0x1ae]					
2013/08/17	18:40:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/17	19:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/17	19:34:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/17	19:34:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/17	19:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/17	20:16:00.0	XRT_Custom_430_OG [0x1ae]					
2013/08/17	20:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/17	21:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/17	21:13:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/17	21:13:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/17	21:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/17	21:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/17	21:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2013/08/17	22:00:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01	00 00 00 00
2013/08/17	22:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/08/17	22:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/08/17	22:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/08/17	22:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/08/17	22:00:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/17	22:00:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2013/08/17	22:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2013/08/17	22:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/17	22:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/17	22:51:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/17	22:51:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/17	22:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/17	23:23:00.0	XRT_Custom_430_OG [0x1ae]					
2013/08/17	23:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/18	00:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/18	00:30:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/18	00:30:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/18	00:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/18	00:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/18	00:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2013/08/18	01:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04	00 00 00 00
2013/08/18	01:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/08/18	01:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/08/18	01:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/08/18	01:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/08/18	01:00:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/18	01:00:26.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2013/08/18	01:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]					



2013/08/18	01:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/18	02:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/18	02:07:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/18	02:07:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/18	02:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/18	02:30:00.0	XRT_Custom_430_OG [0x1ae]					
2013/08/18	02:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/18	03:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/18	03:35:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/18	03:35:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/18	03:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/18	04:08:30.0	XRT_Custom_430_OG [0x1ae]					
2013/08/18	04:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/18	05:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/18	05:15:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/18	05:15:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/18	05:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/18	05:47:00.0	XRT_Custom_430_OG [0x1ae]					
2013/08/18	05:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/18	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/18	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/08/18	06:00:00.0	AOCS_Or-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2013/08/18	06:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/08/18	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/08/18	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/08/18	06:02:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2013/08/18	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/18	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/18	06:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/08/18	06:10:00.0	AOCS_Or-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00	
2013/08/18	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/08/18	06:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/08/18	06:10:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/08/18	06:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/08/18	06:10:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/18	06:10:26.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2013/08/18	06:12:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2013/08/18	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/18	06:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/18	06:55:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/18	06:55:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/18	06:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/18	07:25:30.0	XRT_Custom_430_OG [0x1ae]					
2013/08/18	07:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/18	08:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/18	08:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/18	08:35:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/18	08:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	

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2013/08/18	09:04:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/18	09:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/18	10:17:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/18	10:17:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/18	10:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/18	10:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/18	10:42:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/18	10:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/18	11:59:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/18	11:59:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/18	11:59:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/18	12:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/18	12:21:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/18	12:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/18	13:38:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/18	13:38:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/18	13:38:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/18	13:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/18	14:11:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/18	14:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/18	14:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/18	14:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/08/18	15:00:00.0	AOCS_Or-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	02 00 00 00 00
2013/08/18	15:00:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/08/18	15:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/08/18	15:00:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/08/18	15:00:22.0	XRT_ARS_DIS_417_OG [0x1a1]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/08/18	15:02:54.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/18	15:02:56.0	XRT_QT_PROG_SET_438_OG [0x1b6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2013/08/18	15:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2013/08/18	15:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/18	15:16:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/18	15:16:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/18	15:16:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/18	15:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/18	16:01:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/18	16:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/18	16:55:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/18	16:55:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/18	16:55:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/18	16:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/18	17:38:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/18	17:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/18	18:33:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/18	18:33:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/18	18:33:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/18	18:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/18	19:15:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/18	19:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/18	19:19:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

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2013/08/18	19:19:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2013/08/18	19:20:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2013/08/18	19:20:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/08/18	19:22:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/08/18	19:22:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/08/18	19:22:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2013/08/18	19:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/18	19:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/18	19:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/08/18	19:30:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04	00	00	00	00
2013/08/18	19:30:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/08/18	19:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/08/18	19:30:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/08/18	19:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/08/18	19:30:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/18	19:30:26.0	XRT_QT_PROG_SET_414_OG [0x19e]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2013/08/18	19:32:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2013/08/18	19:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/18	20:12:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/18	20:12:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/18	20:12:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/18	20:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/18	20:52:30.0	XRT_Custom_430_OG [0x1ae]							
2013/08/18	20:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/18	21:50:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/18	21:50:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/18	21:50:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/18	21:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/18	21:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/18	21:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/08/18	22:00:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	20	7a	b7	23
2013/08/18	22:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/08/18	22:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/08/18	22:00:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/08/18	22:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/08/18	22:00:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/18	22:00:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	01			
2013/08/18	22:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2013/08/18	22:28:30.0	XRT_Custom_430_OG [0x1ae]							
2013/08/18	22:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/18	23:29:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/18	23:29:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/18	23:29:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/18	23:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/19	00:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/19	00:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/08/19	01:00:00.5	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04	00	00	00	00
2013/08/19	01:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							

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2013/08/19	01:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/08/19	01:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/08/19	01:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/08/19	01:00:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/19	01:00:26.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2013/08/19	01:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2013/08/19	01:28:30.0	XRT_Custom_430_OG [0x1ae]								
2013/08/19	01:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/19	02:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/19	02:43:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/19	02:43:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/19	02:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/19	03:07:00.0	XRT_Custom_430_OG [0x1ae]								
2013/08/19	03:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/19	04:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/19	04:12:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/19	04:12:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/19	04:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/19	04:45:30.0	XRT_Custom_430_OG [0x1ae]								
2013/08/19	04:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/19	05:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/19	05:52:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/19	05:52:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/19	05:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/19	06:28:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/19	06:28:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00			
2013/08/19	06:28:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00			
2013/08/19	06:28:46.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/08/19	06:31:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/08/19	06:31:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/08/19	06:31:28.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2013/08/19	06:31:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/19	06:38:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/19	06:38:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2013/08/19	06:38:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04	00 00 00 00			
2013/08/19	06:38:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/08/19	06:38:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/08/19	06:38:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/08/19	06:38:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/08/19	06:38:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/19	06:38:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09			
2013/08/19	06:41:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2013/08/19	06:41:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/19	07:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/19	07:33:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/19	07:33:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/19	07:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/19	08:02:00.0	XRT_Custom_430_OG [0x1ae]								

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2013/08/19	08:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/19	09:13:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/19	09:13:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/19	09:13:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/19	09:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/19	09:40:30.0	XRT_Custom_430_OG [0x1ae]							
2013/08/19	09:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/19	10:58:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/19	10:58:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/19	10:58:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/19	11:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/19	11:19:00.0	XRT_Custom_430_OG [0x1ae]							
2013/08/19	11:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/19	12:36:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/19	12:36:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/19	12:36:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/19	12:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/19	12:57:30.0	XRT_Custom_430_OG [0x1ae]							
2013/08/19	12:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/19	14:15:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/19	14:15:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/19	14:15:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/19	14:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/19	14:51:00.0	XRT_Custom_430_OG [0x1ae]							
2013/08/19	14:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/19	15:53:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/19	15:53:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/19	15:53:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/19	15:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/19	16:38:00.0	XRT_Custom_430_OG [0x1ae]							
2013/08/19	16:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/19	17:32:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/19	17:32:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/19	17:32:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/19	17:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/19	18:14:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/19	18:14:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/08/19	18:15:00.0	AOCS_Or-e-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/08/19	18:15:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/08/19	18:17:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/08/19	18:17:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/08/19	18:17:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2013/08/19	18:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/19	18:24:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/19	18:24:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/08/19	18:25:00.0	AOCS_Or-e-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2013/08/19	18:25:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/08/19	18:25:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/08/19	18:25:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				

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2013/08/19	18:25:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/08/19	18:25:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/19	18:25:26.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2013/08/19	18:27:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2013/08/19	18:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/19	19:10:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/19	19:10:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/19	19:10:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/19	19:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/19	19:52:00.0	XRT_Custom_430_OG [0x1ae]							
2013/08/19	19:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/19	20:49:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/19	20:49:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/19	20:49:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/19	20:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/19	21:29:00.0	XRT_Custom_430_OG [0x1ae]							
2013/08/19	21:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/19	21:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/19	21:59:56.0	XRT_FOCUS_RECALIBRATE_433_OG [0x1b1]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2013/08/19	22:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2013/08/19	22:03:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/08/19	22:04:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/08/19	22:04:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/08/19	22:04:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/08/19	22:04:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/08/19	22:04:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/19	22:04:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2013/08/19	22:06:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2013/08/19	22:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/19	22:27:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/19	22:27:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/19	22:27:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/19	22:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/19	23:03:30.0	XRT_Custom_430_OG [0x1ae]							
2013/08/19	23:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/20	00:06:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/20	00:06:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/20	00:06:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/20	00:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/20	00:28:00.0	XRT_Custom_430_OG [0x1ae]							
2013/08/20	00:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/20	00:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/20	00:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/08/20	01:00:00.5	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2013/08/20	01:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/08/20	01:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/08/20	01:00:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/08/20	01:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/08/20	01:00:24.0	XRT_FLD_RESET_415_OG [0x19f]							

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2013/08/20	01:00:26.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_FLD_RESET	1	07-F0	da				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2013/08/20	01:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2013/08/20	01:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/20	01:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/20	01:43:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/20	01:43:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/20	01:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/20	02:05:00.0	XRT_Custom_430_OG [0x1ae]								
2013/08/20	02:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/20	03:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/20	03:19:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/20	03:19:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/20	03:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/20	03:43:30.0	XRT_Custom_430_OG [0x1ae]								
2013/08/20	03:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/20	04:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/20	04:50:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/20	04:50:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/20	04:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/20	05:22:00.0	XRT_Custom_430_OG [0x1ae]								
2013/08/20	05:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/20	06:00:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/20	06:00:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2013/08/20	06:00:30.0	AOCs_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00	00
2013/08/20	06:00:46.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/08/20	06:03:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/08/20	06:03:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/08/20	06:03:28.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2013/08/20	06:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/20	06:10:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/20	06:10:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/08/20	06:10:30.0	AOCs_Orе-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04	00	00	00	00
2013/08/20	06:10:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/08/20	06:10:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/08/20	06:10:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/08/20	06:10:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/08/20	06:10:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/20	06:10:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2013/08/20	06:13:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03			
2013/08/20	06:13:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/20	06:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/20	06:30:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/20	06:30:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/20	06:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/20	07:00:30.0	XRT_Custom_430_OG [0x1ae]								
2013/08/20	07:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/20	08:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/20	08:10:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				

2013/08/20	08:10:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]										
			MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2013/08/20	08:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]										
			MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2013/08/20	10:56:00.0	AOCS_ORe-point_Start_2_OG [0x098]										
			AOCU_NM	5	02-76	00 00 00 00 00 00						