

# XRT Timeline to be uploaded on 2013/08/22

Period: 2013/08/22 09:51:00 - 2013/08/26 10:39:00

\* \* \* \* \*

Normal mode

\* \* \* \* \*

## XOB #19BA: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment
08/24 01:33:00 - 08/24 01:39:54	Fixed ( -528.4, -528.4)	XRT quad pointing 1
<b>PROG= 16 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 12.0sec</b>		
└─ <b>Seqn= 1 1-time(s) 12.0sec</b>		
└─ Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 23 2-time(s) 2.0sec</b>		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ <b>Subr= 3 2-time(s) 2.0sec</b>		
└─ <b>Seqn= 12 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ <b>Seqn= 7 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band open	Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

## XOB #19B9: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment
08/24 01:43:00 - 08/24 01:49:54	Fixed ( 528.4, -528.4)	XRT quad pointing 2
<b>PROG= 07 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 12.0sec</b>		
└─ <b>Seqn= 2 1-time(s) 12.0sec</b>		
└─ Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 23 2-time(s) 2.0sec</b>		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ <b>Subr= 3 2-time(s) 2.0sec</b>		
└─ <b>Seqn= 12 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ <b>Seqn= 7 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band open	Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

## XOB #19B8: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment
08/24 01:53:00 - 08/24 01:59:54	Fixed ( 528.4, 528.4)	XRT quad pointing 3
<b>PROG= 02 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 12.0sec</b>		
└─ <b>Seqn= 3 1-time(s) 12.0sec</b>		
└─ Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 23 2-time(s) 2.0sec</b>		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ <b>Subr= 3 2-time(s) 2.0sec</b>		
└─ <b>Seqn= 12 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ <b>Seqn= 7 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band open	Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center)	Comp. AEC Buffer Interval

## XOB #19B7: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment
08/24 02:03:00 - 08/24 02:09:54	Fixed ( -528.4, 528.4)	XRT quad pointing 4
<b>PROG= 20 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 12.0sec</b>		
└─ <b>Seqn= 4 1-time(s) 12.0sec</b>		
└─ Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec

Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
<b>Subr= 2</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
<b>Seqn= 23</b>		<b>2-time(s)</b>	<b>2.0sec</b>									
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Subr= 3</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
<b>Seqn= 12</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
<b>Seqn= 7</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #19BC: AR standard-B (thin-Be) with PFB (384FOV), 512x512 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band (33ms/45ms lead)**

Term	Pointing (x, y)		Comment									
08/24 02:13:00 - 08/24 06:16:24	Track ( -95.0, -419.1) @ 08/24 02:10:00		# AR 11827									
<b>PROG= 19</b>		<b>Inf.-time(s)</b>										
<b>Subr= 1</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
<b>Seqn= 44</b>		<b>2-time(s)</b>	<b>2.0sec</b>									
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
<b>Subr= 2</b>		<b>2-time(s)</b>	<b>2.0sec</b>									
<b>Seqn= 18</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
<b>Seqn= 22</b>		<b>4-time(s)</b>	<b>2.0sec</b>									
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 42</b>		<b>80-time(s)</b>	<b>20.0sec</b>									
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #19BB: Synoptic Q95 1x1 - Al/mesh(45/512/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(128/1443/5795ms lead)**

Term	Pointing (x, y)		Comment									
08/24 06:19:30 - 08/24 07:21:00	Fixed ( 0.0, 0.0)		synoptic, shifted 16.5 min, extended for SOT flat fielding									
<b>PROG= 13</b>		<b>1-time(s)</b>										
<b>Subr= 1</b>		<b>1-time(s)</b>	<b>12.0sec</b>									
<b>Seqn= 27</b>		<b>1-time(s)</b>	<b>4.0sec</b>									
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	44ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 5</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
<b>Seqn= 34</b>		<b>1-time(s)</b>	<b>4.0sec</b>									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	125ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	5.66s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 39</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16.0s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 35</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Flare mode**

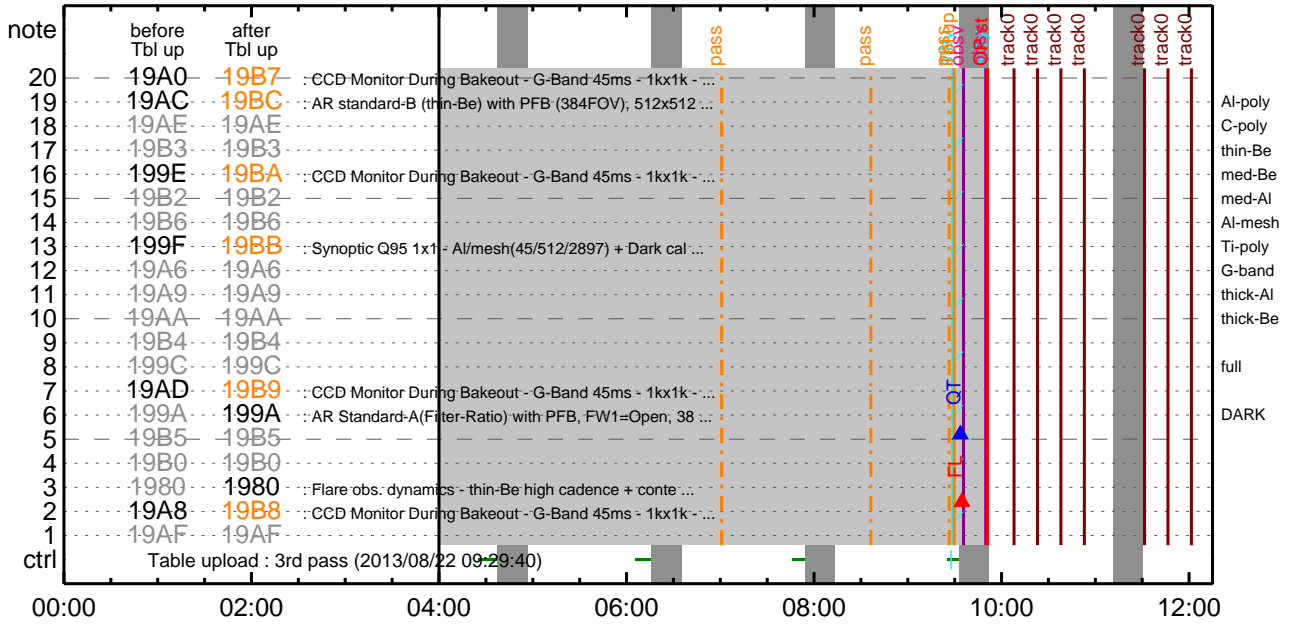
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**XOB #1980: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops-2**

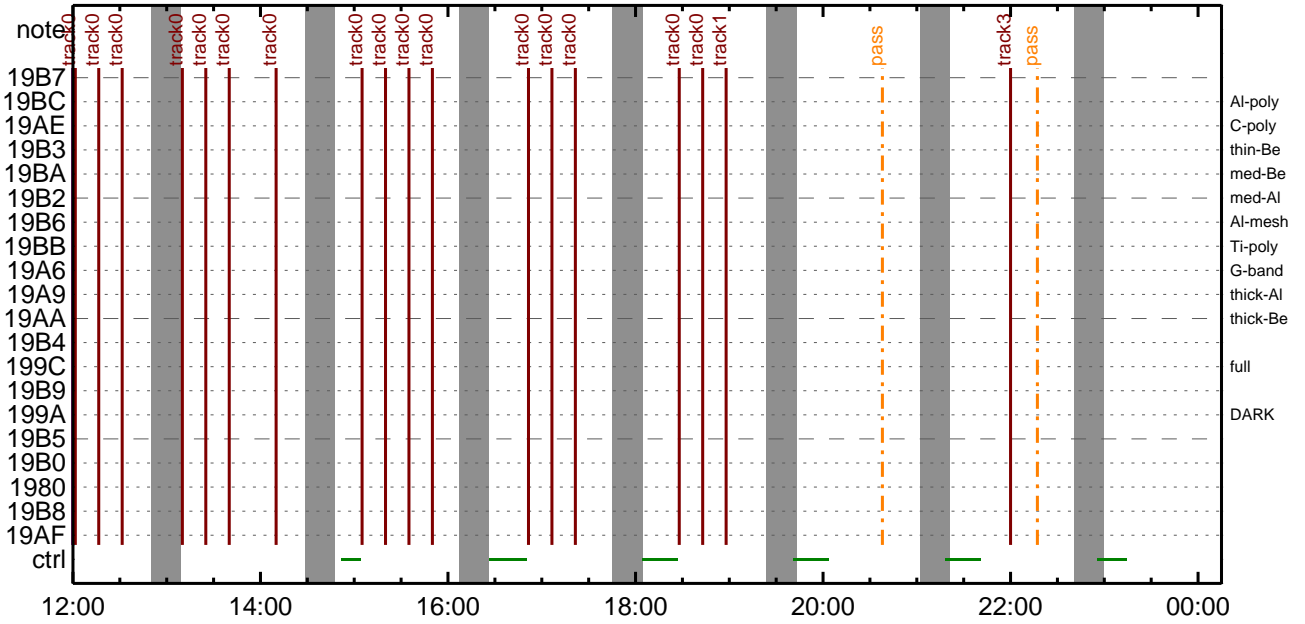
Term	Pointing (x, y)		Comment									
08/24 02:13:00 - 08/24 06:16:24	Track ( -95.0, -419.1) @ 08/24 02:10:00		# AR 11827									
<b>PROG= 03</b>		<b>15-time(s)</b>										
<b>Subr= 1</b>		<b>45-time(s)</b>	<b>10.0sec</b>									
<b>Seqn= 9</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2</b>		<b>1-time(s)</b>	<b>10.0sec</b>									
<b>Seqn= 10</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec



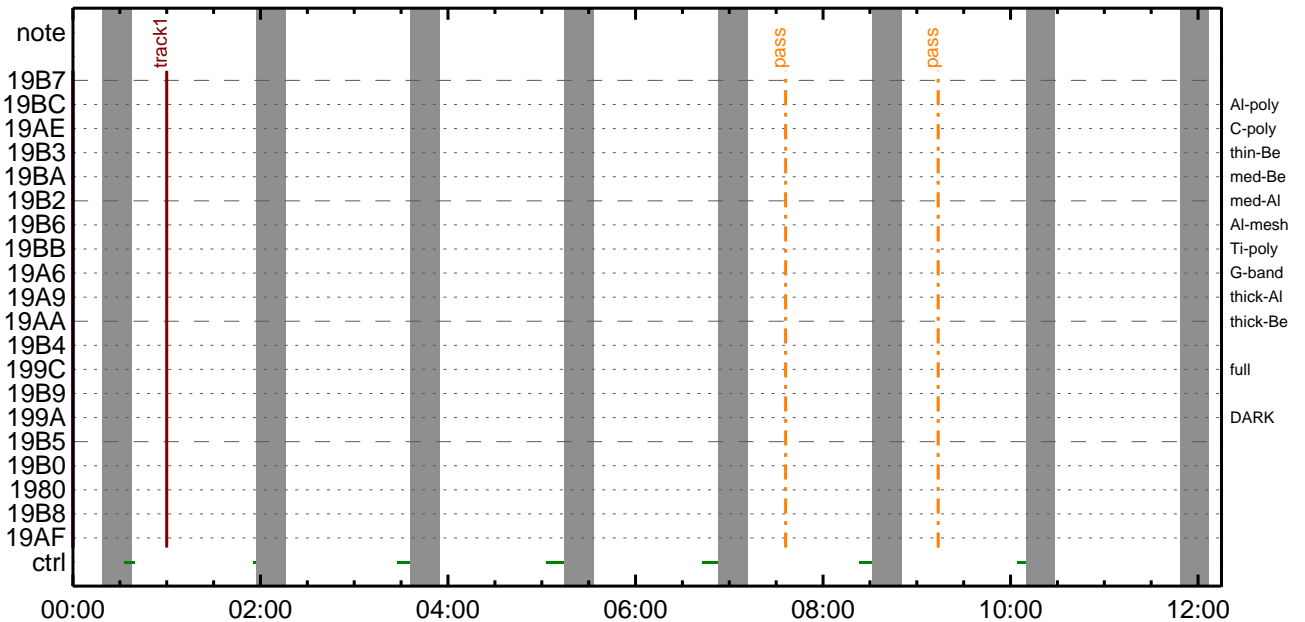
### CMDI #0684 2013/08/22



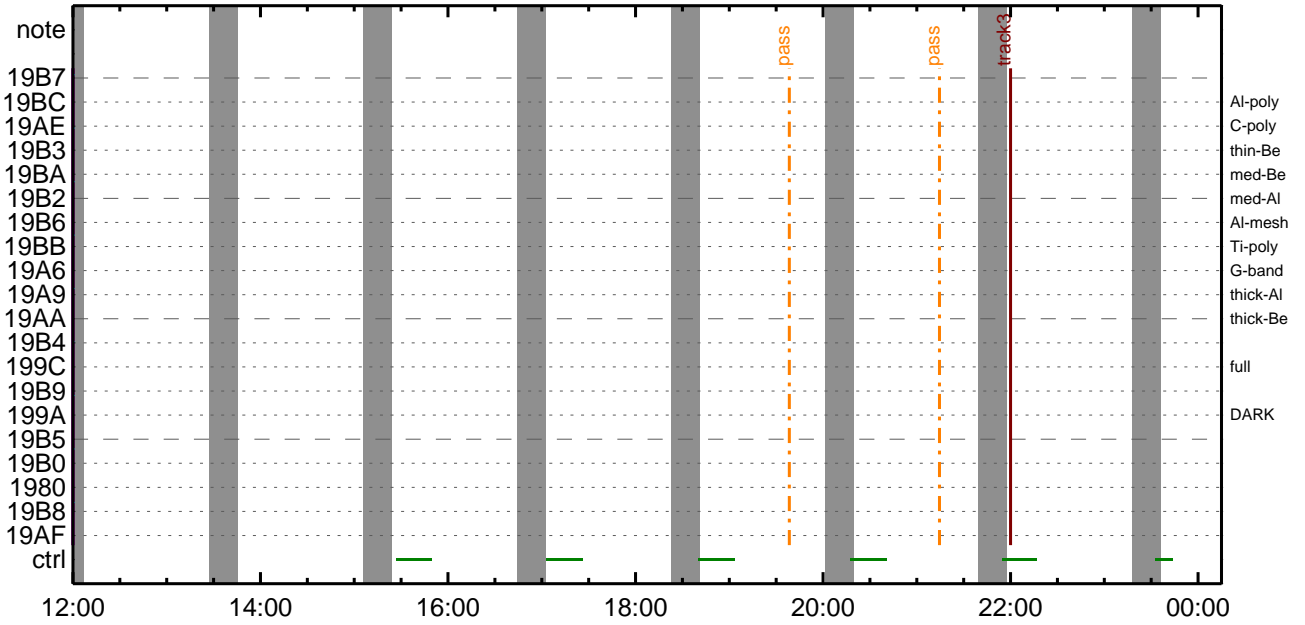
### CMDI #0684 2013/08/22



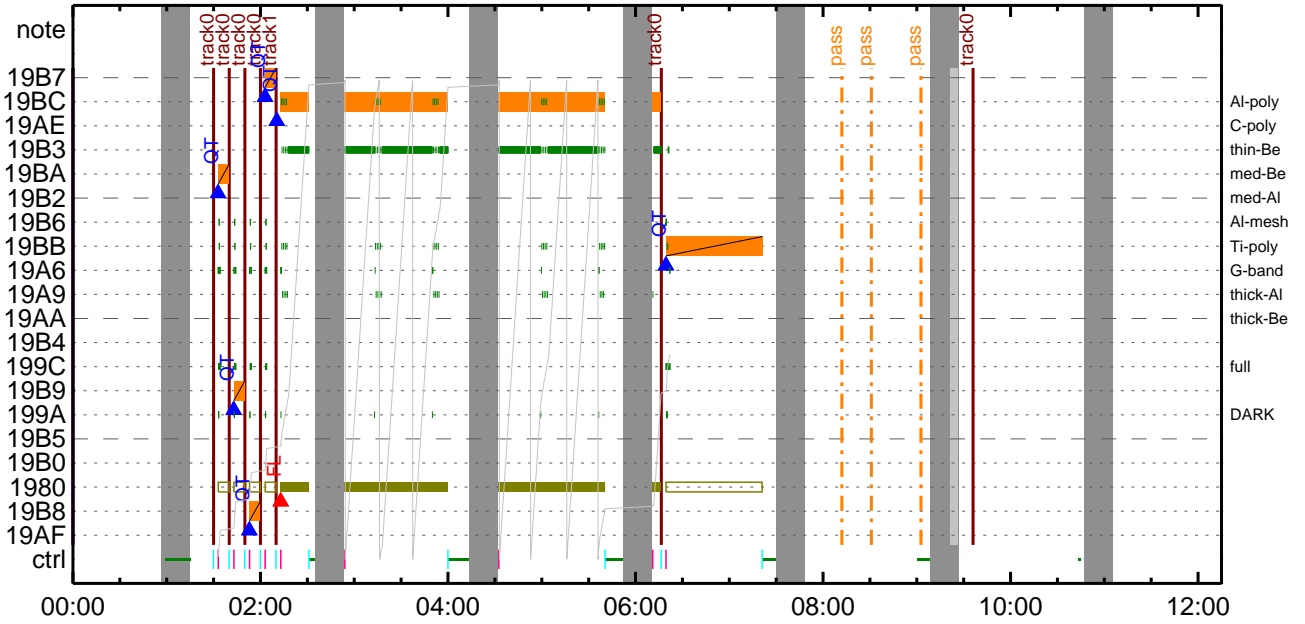
### CMDI #0684 2013/08/23



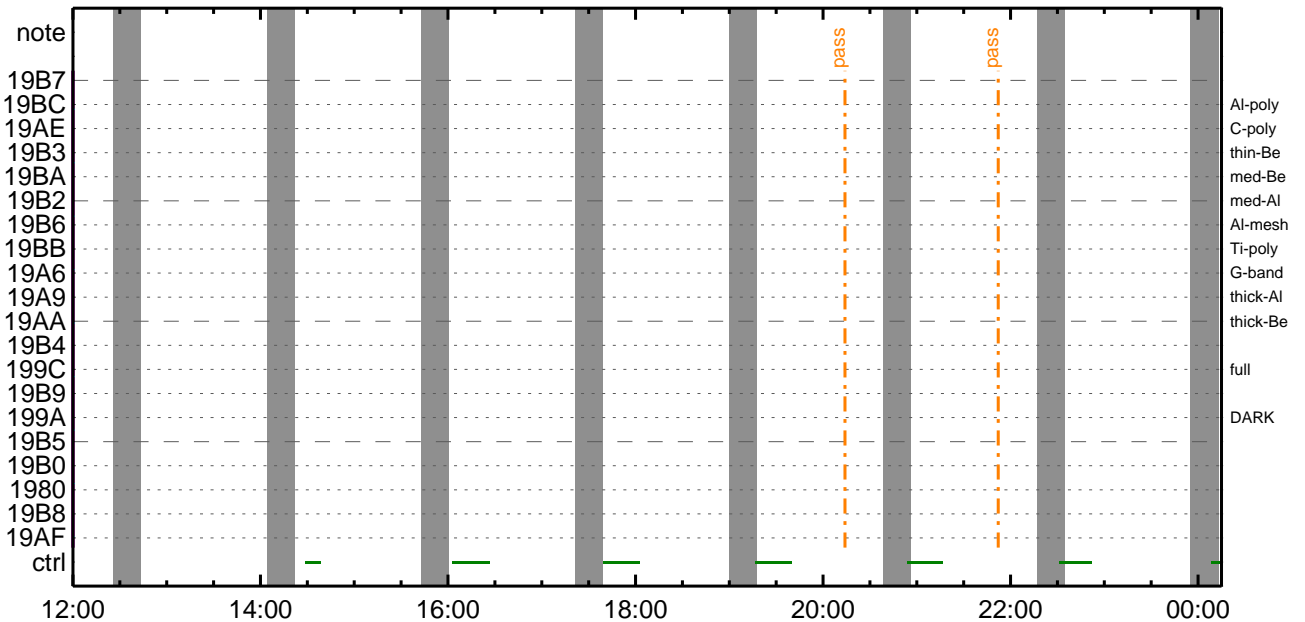
CMDI #0684 2013/08/23



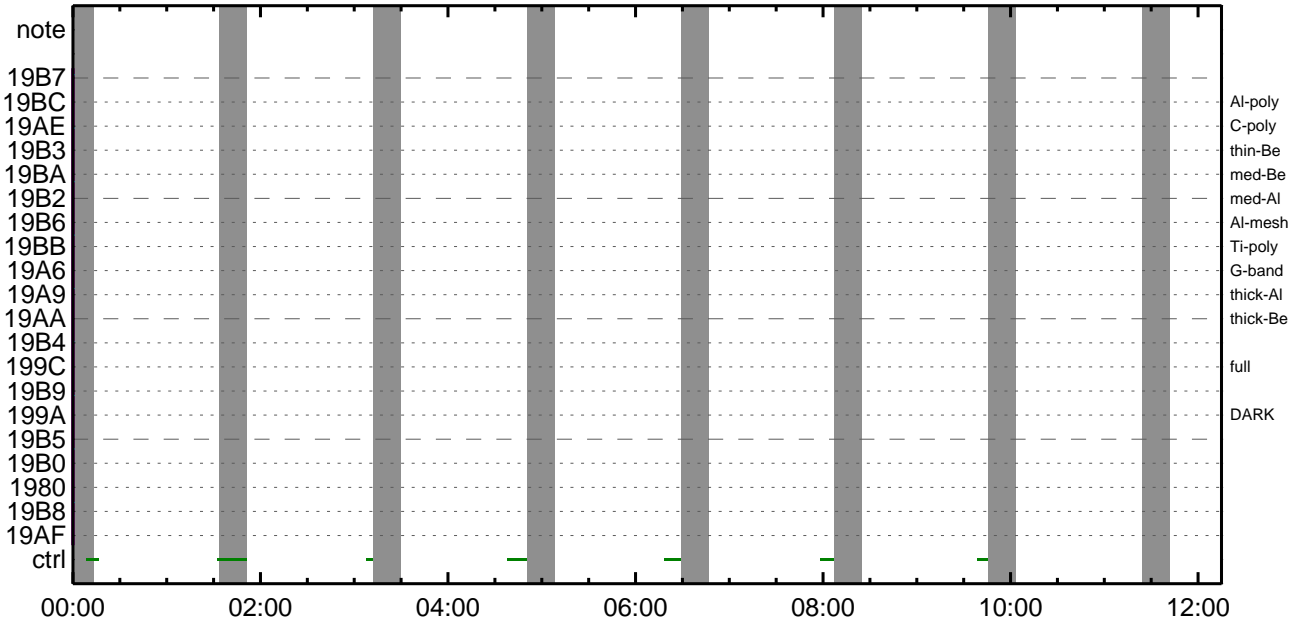
CMDI #0684 2013/08/24



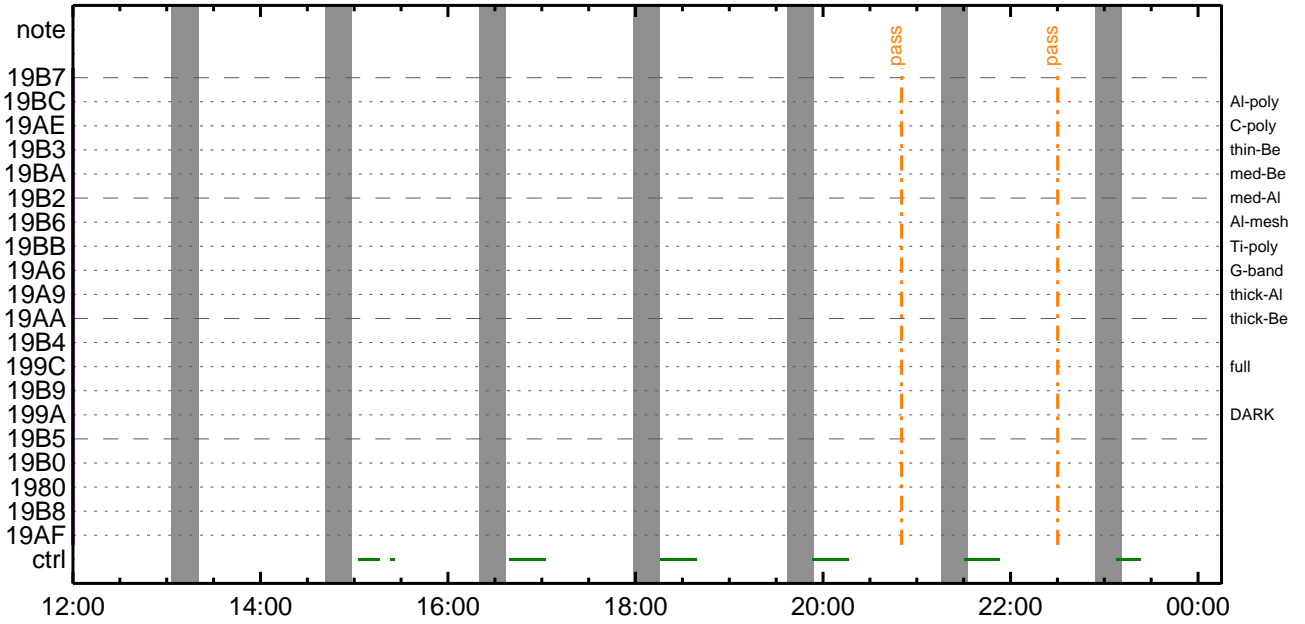
CMDI #0684 2013/08/24



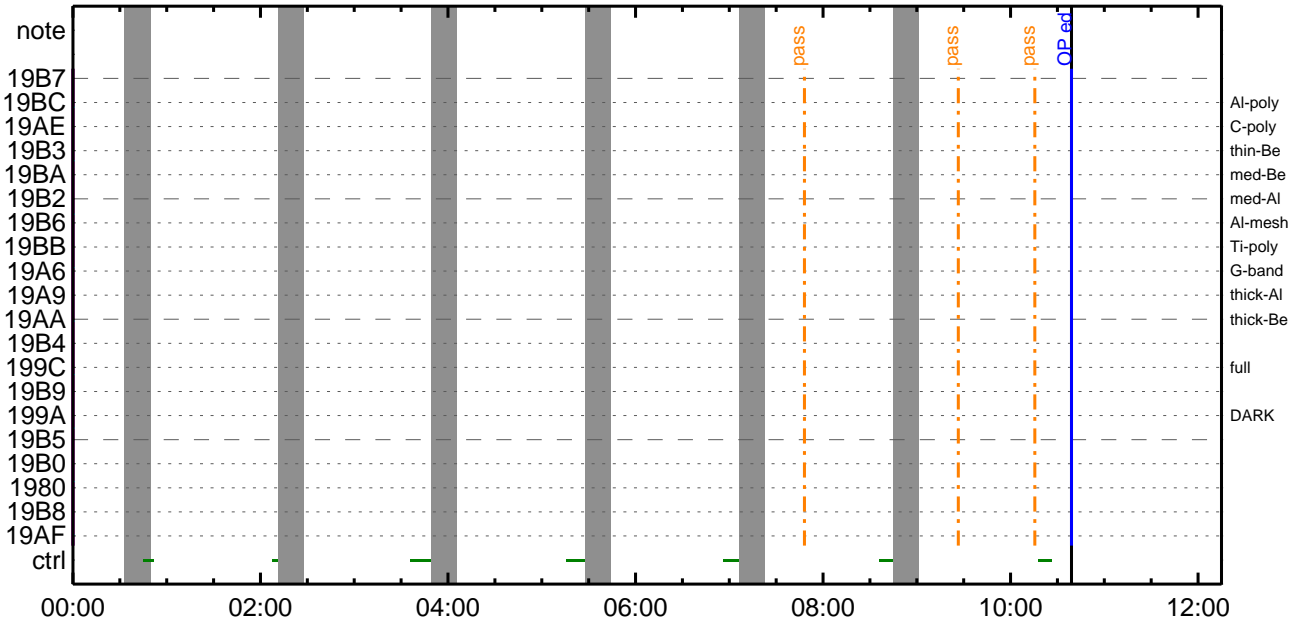
CMDI #0684 2013/08/25

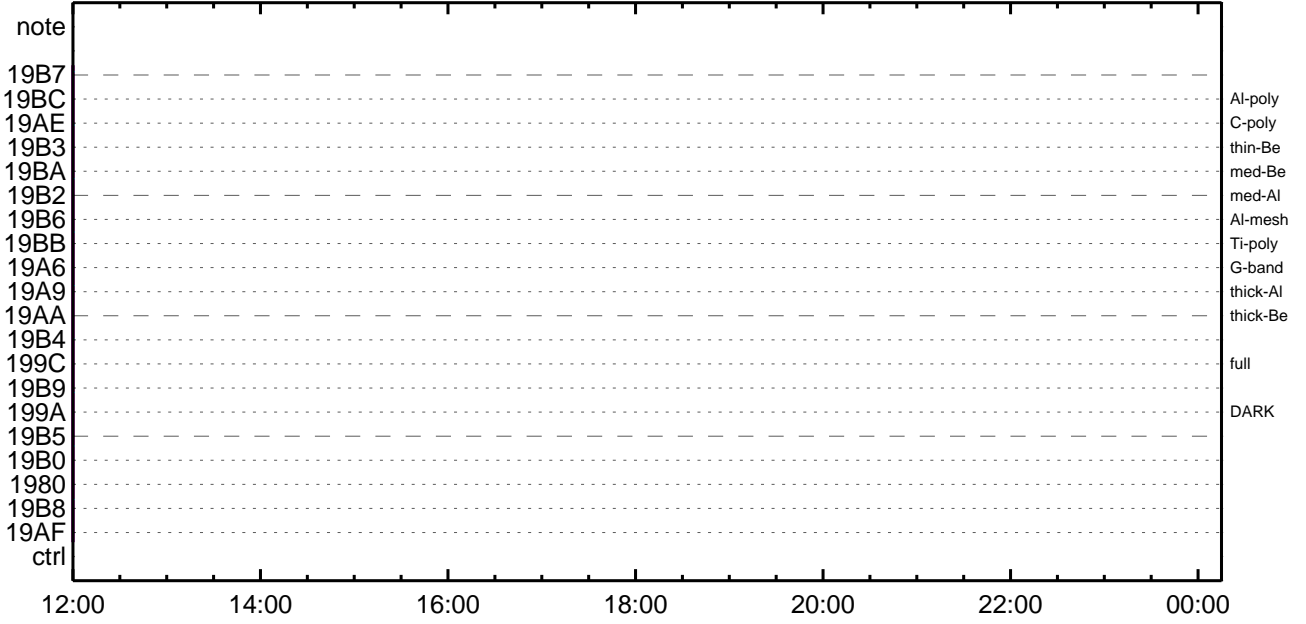


CMDI #0684 2013/08/25



CMDI #0684 2013/08/26









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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;aOP/OGY1;4YE;a
0103 S. OP op-855:OP
0104 ( )
0105 S. OG og-855:OG
0106 ( )
0107 C.
0108 C. ;aNMOG&OPf^°eYAYOX;a
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0120 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½^i»oð³iÇ§
0125 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGqî½E¹ç•ë²iOKoð³iÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0139 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½^i»oð³iÇ§
0144 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGqî½E¹ç•ë²iOKoð³iÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0158 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½^i»oð³iÇ§
0163 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPqî½E¹ç•ë²iOKoð³iÇ§
0165 C.
0166 C. ***** oE²¼oî½A´¶A°oEÉ-°oA÷¿@ (¼âµ-YAYOXx½e½çoðAÓAæoÇ¼^a°°oE¼i¹çoÇoâ) *****
0167 C. DHUYâ;4YE;E½Y½, Y1;4YE;Eoðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0171 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE |§ OPOG UPLOADo-Á÷¿@NGuî½i¹ç;ç°E²¼oîTI-CMDÁ÷¿@oî½A¹Ôo•oEoð³oE;f
0180 C. oEo¿;çSEToEDUMPaîE±°iYNY¹oÇ¹Ôo|o³oE;f
0181 C.
0182 C. TIY³YpYóYEoðAÐî¿(UT)
0183 +. TI 2013-08-22 09:46:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-08-22 09:46:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-08-22 09:46:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2013-08-22 09:50:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄë%îîñαîŷÄŷ§ŷÄŷ-¹àîÛ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]       EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»αò³îÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαîî¾È¹ç•è²îOKαò³îÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÉ;È¼ŷ¼. ŷî;¼ŷÈ;Èαòîãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2013-08-22 09:50:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2013-08-22 09:50:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2013-08-22 09:50:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2013-08-22 09:50:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîαî»ö¼ŷαÈÄα¹αèDCBC•x²è *****
0276 C. (¼á°îŷÖŷÄŷÈŷŷŷÈŷáŷçŷèèÈ¼αα¼Ä»Ûα¹αè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•î Daily±çîñèË'Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 1m3s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ( )
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2013-08-22 09:50:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP 'uÃÎ±Î»ö¼Ý±ñËÃÐ±¹èDCBC·×²è *****
0153 C. (%ã°îÝÓÝÄÝËÝÐÝËÝÄÝ±Ý±Ý±ñËÃÐ±¹è¼±¼Ã»Ü±¹è)
0154 . S. DC-BC dcba-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** ÝÐÝ¹·Ï Daily±¿ÎÑ±Ë´Ø±¹èDCBC·×²è *****
0159 . S. DC-BC dcba-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ;ãLOŠÝÄÝ±ÝÄÝ¹¼Ã»Ü;ã
0164 C.
0165 . C. ***** LOS *****
0166 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-857 2013-08-22 11:53:48 156 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Ä»Ü;ä
0005 C.
0006 C. YÀYB;¼Y³YFYóYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Āí;Ēα¿αΔσ•μ°Ē»Í×ÁÇαíYçYÁY×Yí;¼YÉ;ĒĒ%μ•íĒ;ĒσĒ%°ÇÕα•α¿¼l¹çαÍ;çÁ®, ùα¹αēαβαÇÁ+¿®α•σĒααα³αĒ;Ē
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-269:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Stop SP table >
0036 +. DC 07-F0 MDP_SP_CTRL_MANU
0037 BC (61)
0038 C. -----
0039 C. MDP_SP_CTRL_MODE = MANU [ ]
0040 C. -----
0041 C.
0042 . C. <Upload SP Observation Table>
0043 . S. RAM ram-288:MDP_OBS_S
0044 ( )
0045 C.
0046 . C. < Dump RAMID=MDP_OBS_S >
0047 +. DC 07-F0 MDP_DUMP_SPTBL
0048 BC (83 07 00 00 00 38 b8)
0049 C. -----
0050 C. MDP_OBS_S verify = OK/NG [ ]
0051 C. -----
0052 C.
0053 C. *****
0054 C. SOT TI command set
0055 C. *****
0056 C. Execute, after the success of TBL upload.
0057 +. TI 2013-08-22 09:50:18.0
0058 DC 07-F0 MDP_SOT_MODE_OBSV
0059 BC (40)
0060 . C. -----
0061 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0062 C. -----
0063 C.
0064 C.
0065 C. ***** XRT START *****
0066 C.
0067 +. DC 07-F0 MDP_XRT_CTRL_MANU
0068 BC (c1)
0069 +. DC 07-F0 MDP_XRT_MODE_STBY
0070 BC (c3)
0071 . C. ----- Success Verify ? OK / NG_____
0072 C.
0073 C. XRT Obs. Table Upload
0074 . S. RAM ram-291:MDP_OBS_X
0075 ( )
0076 C.
0077 +. DC 07-F0 MDP_DUMP_XRTTBL
0078 BC (84 07 00 00 00 3a d4)
0079 . C. ----- Comparison Check ? OK / ERR _____
0080 C.
0081 C.
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 01 b1 b1 04 04)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 02 b1 b1 08 08)
0086 +. DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 03 b1 b1 08 08)
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 04 b1 b1 06 06)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 05 85 83 06 06)
0092 +. DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 06 85 83 06 06)
0094 +. DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 07 c0 c0 10 10)
```

```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 08 80 80 20 20)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 09 40 c0 10 10)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 0a 40 40 10 10)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 0b c0 40 10 10)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 0c 85 83 08 08)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0d 80 80 20 08)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 0e 80 80 08 20)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 0f 80 80 06 06)
0112 + DC 07-F0 MDP_XRT_ROI_SET
0113 BC (cd 10 80 80 08 08)
0114 + DC 07-F0 MDP_XRT_FLD_ENA
0115 BC (d8)
0116 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0117 BC (c8)
0118 + DC 07-F0 MDP_XRT_AEC_RESET
0119 BC (d0)
0120 + DC 07-F0 MDP_XRT_ARS_DIS
0121 BC (d5)
0122 + DC 07-F0 MDP_XRT_FLD_RESET
0123 BC (da)
0124 + DC 07-F0 MDP_XRT_QT_PROG_SET
0125 BC (c4 06)
0126 + DC 07-F0 MDP_XRT_FL_PROG_SET
0127 BC (c5 03)
0128 . C. ----- Success Verify ? OK / NG ____
0129 C.
0130 C.
0131 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0132 C.
0133 +. DC 07-F0 MDP_XRT_MODE_OBSV
0134 BC (c2)
0135 +. TI 2013-08-22 09:50:02.0
0136 DC 07-F0 MDP_XRT_MODE_OBSV
0137 BC (c2)
0138 . C. ----- Success Verify ? OK / NG ____
0139 C.
0140 C. ***** XRT END *****
0141 C.
0142 . C. ***** MDP `uAÎaÎ»ô%ÿaÈAð¹aèDCBC•x²è *****
0143 C. (%â°îÿÓÿÁÿÈÿPÿËÿâÿçÿèaÈ%¼aa¼A»Ûa¹aè)
0144 . S. DC-BC dcbc-402:DCBC
0145 (MDP_known_event)
0146 C.
0147 C.
0148 . C. ***** ÿDÿ¹.Ï Daily±;îÑaÈ´Øa¹aèDCBC•x²è *****
0149 . S. DC-BC dcbc-153:DCBC
0150 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0151 C.
0152 C.
0153 . C. ;ãLOSÿÁÿSÿÿÿÿ¼A»Û;ã
0154 C.
0155 . C. ***** LOS *****
0156 C.
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XRT\_OGLIST\_0684.chk

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\*\*\* OP Sequence for XRT \*\*\*

2013/08/22	10:08:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	ec	e6	52	35
2013/08/22	10:23:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	f2	25	43	be
2013/08/22	10:38:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	f4	32	33	e7
2013/08/22	10:53:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	f5	59	23	8d
2013/08/22	11:31:30.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	f6	0d	12	c0
2013/08/22	11:46:30.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	f6	36	01	68
2013/08/22	12:01:30.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	f6	36	01	0e
2013/08/22	12:16:30.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	f6	0d	ef	bf
2013/08/22	12:31:30.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	f5	59	de	f2
2013/08/22	13:10:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	f4	32	ce	98
2013/08/22	13:25:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	f2	25	be	c1
2013/08/22	13:40:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	ec	e6	b0	42
2013/08/22	14:10:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00	02	24	50	e5
2013/08/22	15:05:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00	02	24	3f	33
2013/08/22	15:20:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00	02	24	2d	81
2013/08/22	15:35:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	00	02	24	1b	ce
2013/08/22	15:50:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	00	02	24	0a	24
2013/08/22	16:51:30.0	AOCS_ORe-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	00	02	24	f8	5b
2013/08/22	17:06:30.0	AOCS_ORe-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00	02	24	e6	a8
2013/08/22	17:21:30.0	AOCS_ORe-point_Start_20_OG [0x0aa]							
		AOCU_NM	5	02-76	00	02	24	d4	fe
2013/08/22	18:28:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]							
		AOCU_NM	5	02-76	00	02	24	c3	4c
2013/08/22	18:43:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	00	02	24	b1	9a
2013/08/22	18:58:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	01	00	00	00	00
2013/08/22	22:00:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]							
		AOCU_NM	5	02-76	03	00	00	00	00
2013/08/23	01:00:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	01	00	00	00	00
2013/08/23	20:00:00.0	XRT_TCIB_XRT_S_HTR_A_DIS_437_OG [0x1b5]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2013/08/23	22:00:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]							
		AOCU_NM	5	02-76	03	00	00	00	00
2013/08/24	01:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2013/08/24	01:30:00.0	AOCS_ORe-point_Start_25_OG [0x0af]							
		AOCU_NM	5	02-76	00	2e	f9	2e	f9
2013/08/24	01:32:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2013/08/24	01:32:52.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	10		
2013/08/24	01:32:54.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0		d9			
2013/08/24	01:32:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2013/08/24	01:32:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2013/08/24	01:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2013/08/24	01:39:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2013/08/24	01:40:00.0	AOCS_ORe-point_Start_26_OG [0x0b0]							
		AOCU_NM	5	02-76	00	2e	f9	d1	07
2013/08/24	01:42:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2013/08/24	01:42:52.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	07		
2013/08/24	01:42:54.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0		d9			
2013/08/24	01:42:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2013/08/24	01:42:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2013/08/24	01:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2013/08/24	01:49:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2013/08/24	01:50:00.0	AOCS_ORe-point_Start_27_OG [0x0b1]							
		AOCU_NM	5	02-76	00	d1	07	d1	07
2013/08/24	01:52:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							

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2013/08/24	01:52:52.0	XRT_QT_PROG_SET_416_OG [0x1a0]	4	07-F8	22	ff	aa	00
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	02		
2013/08/24	01:52:54.0	XRT_FLD_DIS_404_OG [0x194]	1	07-F0	d9			
		MDP_XRT_FLD_DIS	1	07-F0	c9			
2013/08/24	01:52:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	1	07-F0	c9			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	d5			
2013/08/24	01:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c1			
2013/08/24	01:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/24	02:00:00.0	AOCS_ORe-point_Start_28_OG [0x0b2]	5	02-76	00	d1	07	2e f9
		AOCU_NM	5	02-76	00	d1	07	2e f9
2013/08/24	02:02:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	4	07-F8	22	ff	aa	00
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2013/08/24	02:02:52.0	XRT_QT_PROG_SET_407_OG [0x197]	2	07-F0	c4	14		
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	14		
2013/08/24	02:02:54.0	XRT_FLD_DIS_404_OG [0x194]	1	07-F0	d9			
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/08/24	02:02:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	1	07-F0	c9			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/08/24	02:02:58.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0	d5			
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/08/24	02:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/24	02:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/24	02:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	4	07-F8	22	fe	97	00
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2013/08/24	02:10:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]	5	02-76	01	00	00	00 00
		AOCU_NM	5	02-76	01	00	00	00 00
2013/08/24	02:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	1	07-F0	d8			
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/08/24	02:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	1	07-F0	c8			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/08/24	02:10:20.0	XRT_AEC_RESET_413_OG [0x19d]	1	07-F0	d0			
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/08/24	02:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0	d5			
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/08/24	02:10:24.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/24	02:10:26.0	XRT_QT_PROG_SET_409_OG [0x199]	2	07-F0	c4	13		
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	13		
2013/08/24	02:12:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	2	07-F0	c5	03		
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2013/08/24	02:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/24	02:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/24	02:31:02.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/24	02:31:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/08/24	02:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/08/24	02:53:00.0	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/24	02:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/24	04:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/24	04:00:02.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/24	04:00:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/08/24	04:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/08/24	04:31:30.0	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/24	04:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/24	05:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/24	05:40:32.0	XRT_FLD_RESET_415_OG [0x19f]	1	07-F0	da			
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/24	05:40:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	1	07-F0	e8			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/08/24	05:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	1	07-F0	e9			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/08/24	06:10:00.0	XRT_Custom_430_OG [0x1ae]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/24	06:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	1	07-F0	c0			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/24	06:16:24.0	XRT_CTRL_MANU_402_OG [0x192]	1	07-F0	c1			
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/24	06:16:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	4	07-F8	22	ff	aa	00
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2013/08/24	06:16:30.0	AOCS_ORe-point_Start_29_OG [0x0b3]	5	02-76	00	00	00	00 00
		AOCU_NM	5	02-76	00	00	00	00 00
2013/08/24	06:16:46.0	XRT_FLD_DIS_434_OG [0x1b2]	1	07-F0	d9			
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/08/24	06:19:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	1	07-F0	c9			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/08/24	06:19:26.0	XRT_ARS_DIS_423_OG [0x1a7]	1	07-F0	d5			



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2013/08/24	06:19:28.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d				
2013/08/24	06:19:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2013/08/24	07:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1					
2013/08/24	07:21:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da					
2013/08/24	07:21:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2013/08/24	07:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2013/08/24	09:36:00.0	AOCS_Orpoint_Start_29_OG [0x0b3]	AOCU_NM	5	02-76	00	00	00	00	00	00