

XRT Timeline to be uploaded on 2013/08/24

Period: 2013/08/24 09:26:00 - 2013/08/29 12:08:00

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Normal mode

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XOB #19B2: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band

Term	Pointing (x, y)	Comment
08/24 09:39:00 - 08/24 18:02:54	Track (-227.0, 145.0) ^{Ⓢ 08/24 09:36:00}	# OP start + 10min AR11828
08/24 18:16:00 - 08/25 05:49:54	Track (-152.9, 143.0) ^{Ⓢ 08/24 18:13:00}	# AR11828
08/25 06:03:00 - 08/25 17:48:24	Track (-50.0, 141.4) ^{Ⓢ 08/25 06:00:00}	# AR11828
08/25 18:40:30 - 08/26 01:59:54	Track (55.4, 141.2) ^{Ⓢ 08/25 17:58:30}	# AR11828
08/26 06:03:00 - 08/26 10:17:00	Fixed (870.0, -300.0)	# EIS spectral atlas S-E off limb

PROG= 15 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec	
Seqn= 8 2-time(s) 2.0sec	
Open/G-band Open/G-band close	Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Subr= 2 2-time(s) 2.0sec	
Seqn= 24 1-time(s) 2.0sec	
Open/Ti-poly Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band Open/G-band open	Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Seqn= 98 4-time(s) 2.0sec	
Al-poly/Open thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly Open/thick-Be close	Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open med-Be/Open close	Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al Open/thick-Al close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Seqn= 21 25-time(s) 120.0sec	
thin-Be/Open Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Al-poly/Open Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
Al-poly/Open Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
thin-Be/Open Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
Al-poly/Open Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
thin-Be/Open Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec
Al-poly/Open Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #19B5: Synoptic Q95 2x2 - Al/mesh(12/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(33/362/1443) + Thi

Term	Pointing (x, y)	Comment
08/24 18:06:00 - 08/24 18:12:54	Fixed (0.0, 0.0)	synoptic, shifted 3.0 min
08/25 05:53:00 - 08/25 05:59:54	Fixed (0.0, 0.0)	synoptic, shifted -10.0 min
08/25 17:51:30 - 08/25 17:58:24	Fixed (0.0, 0.0)	synoptic, shifted -11.5 min
08/26 04:10:00 - 08/26 04:16:54	Fixed (0.0, 0.0)	synoptic - manually shifted

PROG= 05 1-time(s)

Subr= 1 1-time(s) 12.0sec	
Seqn= 82 1-time(s) 4.0sec	
Open/Al-mesh Open/Al-mesh close	Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh Open/Al-mesh close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh Open/Al-mesh close	Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 5 1-time(s) 2.0sec	
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 83 1-time(s) 4.0sec	
Open/Ti-poly Open/Ti-poly close	Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly Open/Ti-poly close	Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly Open/Ti-poly close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 99 1-time(s) 2.0sec	
thin-Be/Open thin-Be/Open close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open thin-Be/Open close	Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open thin-Be/Open close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 66 1-time(s) 2.0sec	
Open/G-band Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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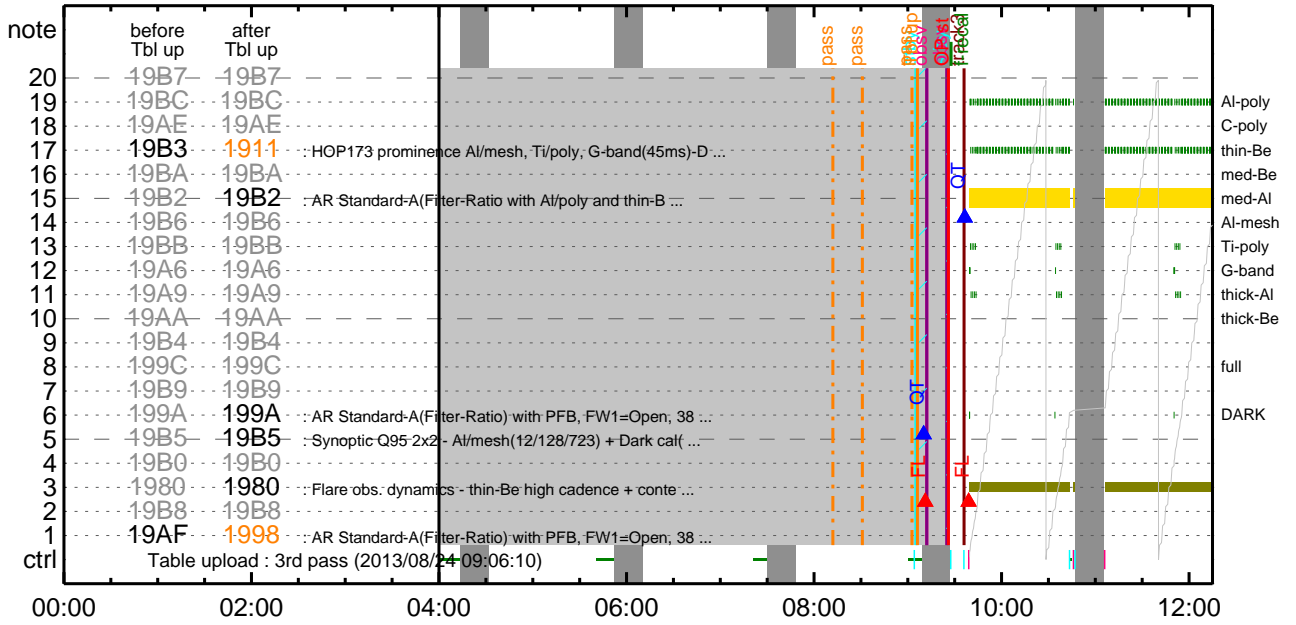
XOB #1911: HOP173 prominence Al/mesh, Ti/poly, G-band(45ms)-DPCM 1x1-512x512-FOV -AEC2-5min cadence with G-band Test

Term	Pointing (x, y)	Comment
08/26 02:03:00 - 08/26 03:36:00	Track (589.3, -409.6) ^{Ⓢ 08/26 02:00:00}	HOP 173 - Filament between AR 11823 and 11827

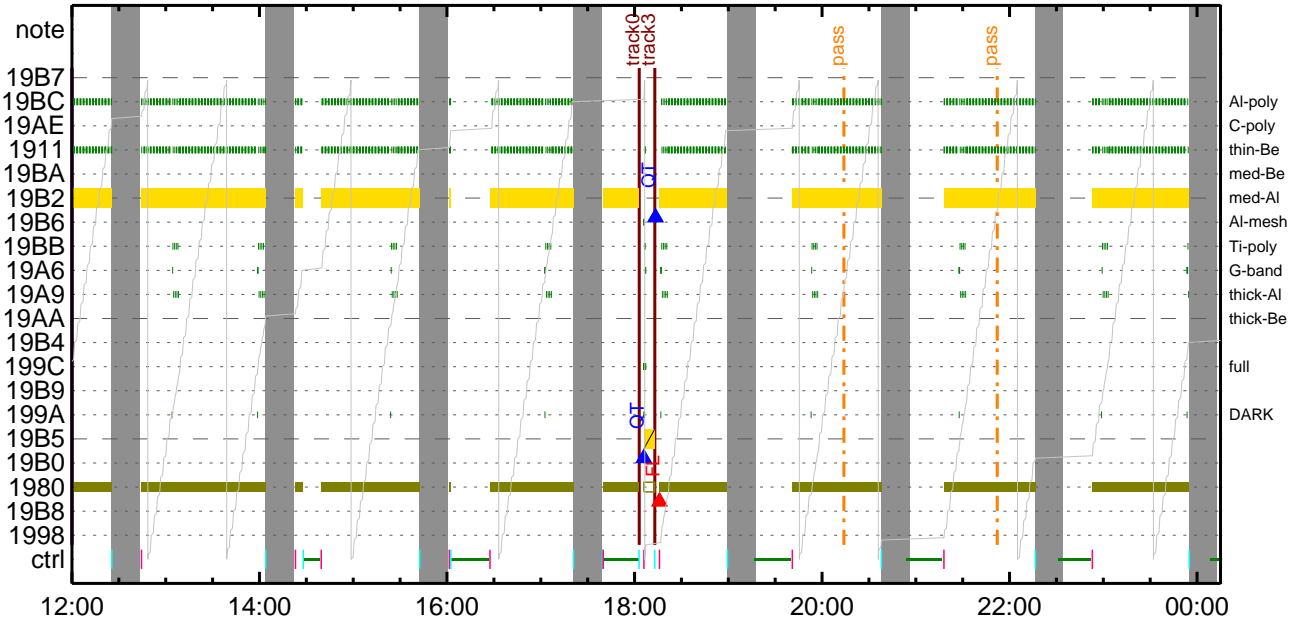
PROG= 17 Inf.-time(s)

Subr= 3 1-time(s) 2.0sec	
Seqn= 49 1-time(s) 2.0sec	
Open/G-band Open/G-band close	Safe Norm 63ms Obs 1x1 512x512 (1024, 1024) DPCM 0 0 2.0sec
Subr= 2 1-time(s) 2.0sec	
Seqn= 48 1-time(s) 2.0sec	
Open/G-band Open/G-band open	Safe Norm 44ms Obs 1x1 512x512 (1024, 1024) Q=95 0 0 2.0sec

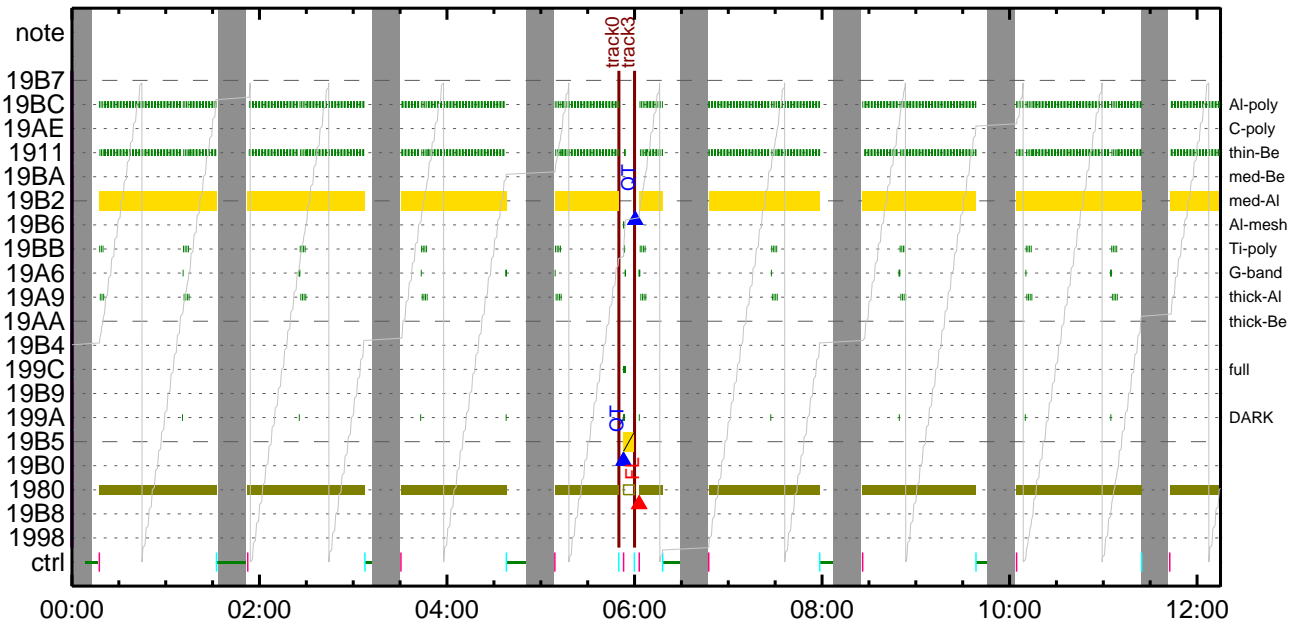
CMDI #0689 2013/08/24



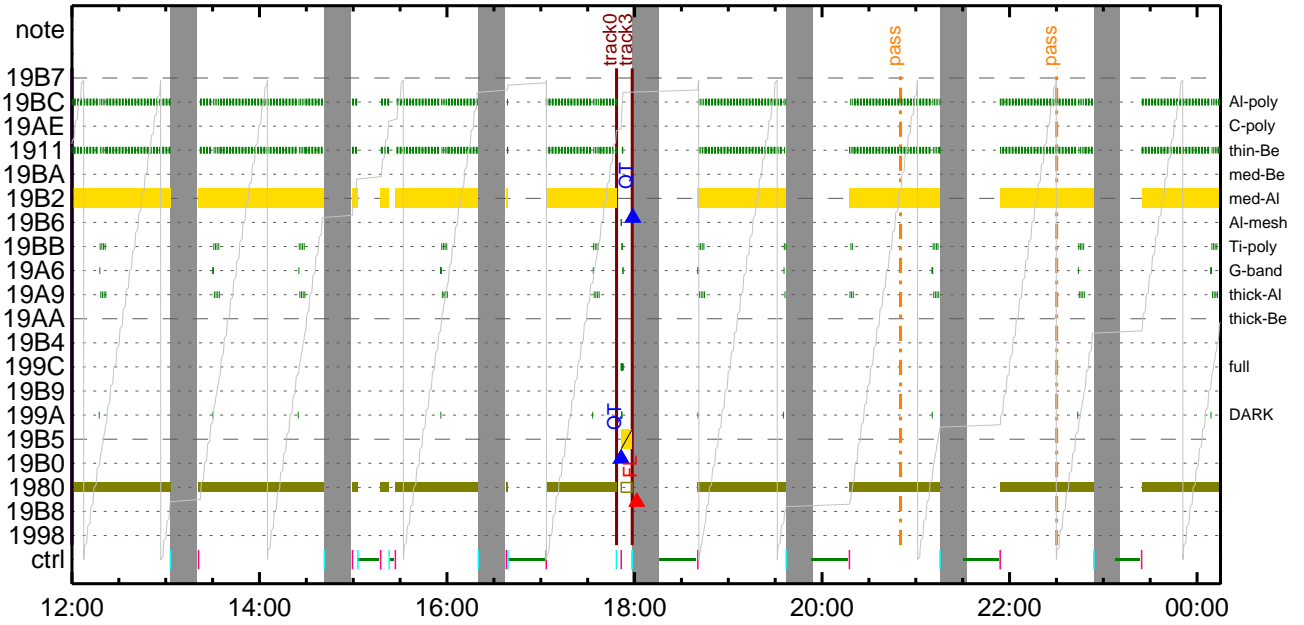
CMDI #0689 2013/08/24



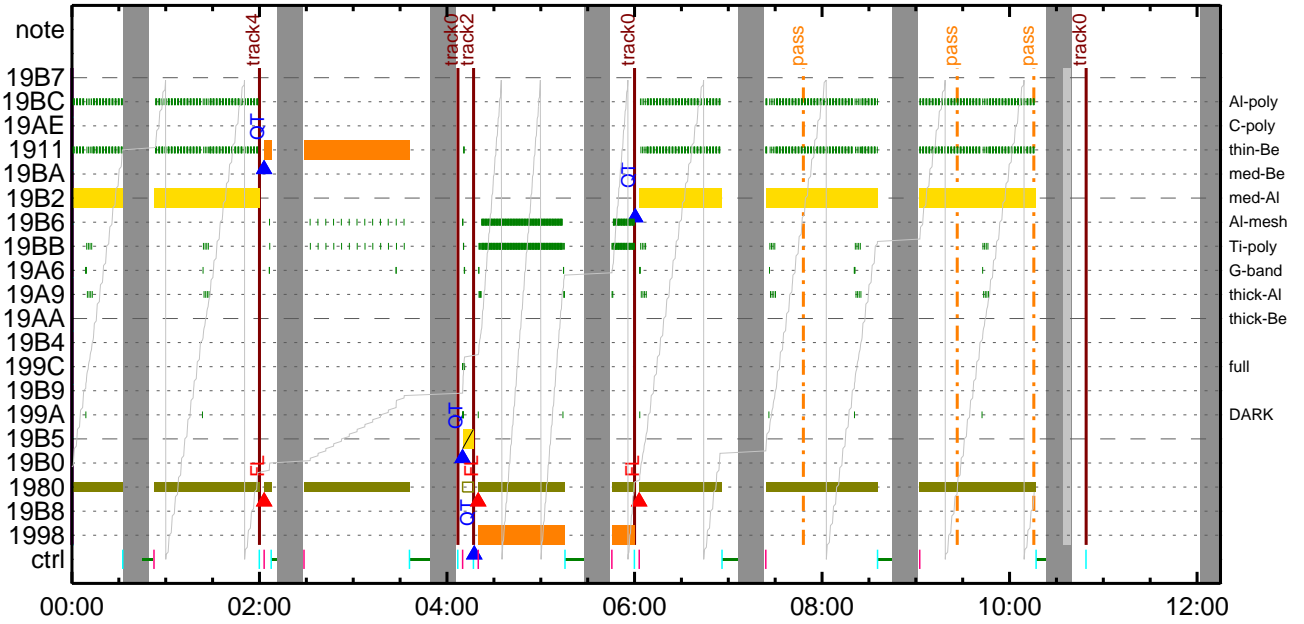
CMDI #0689 2013/08/25



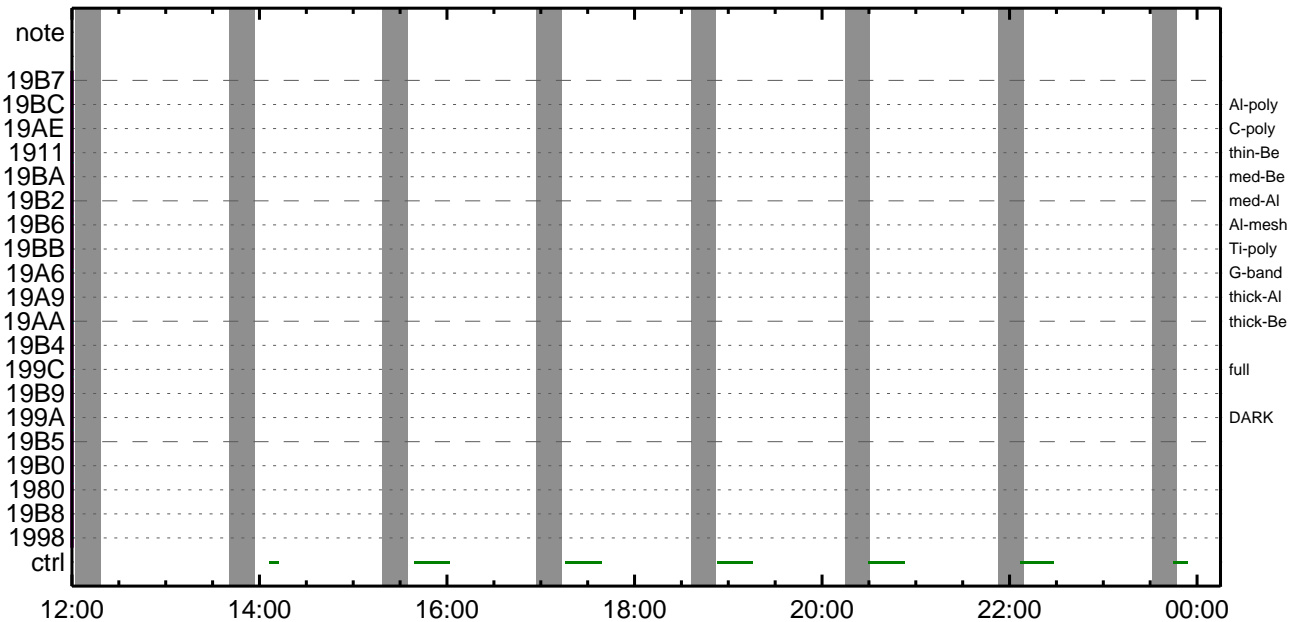
CMDI #0689 2013/08/25



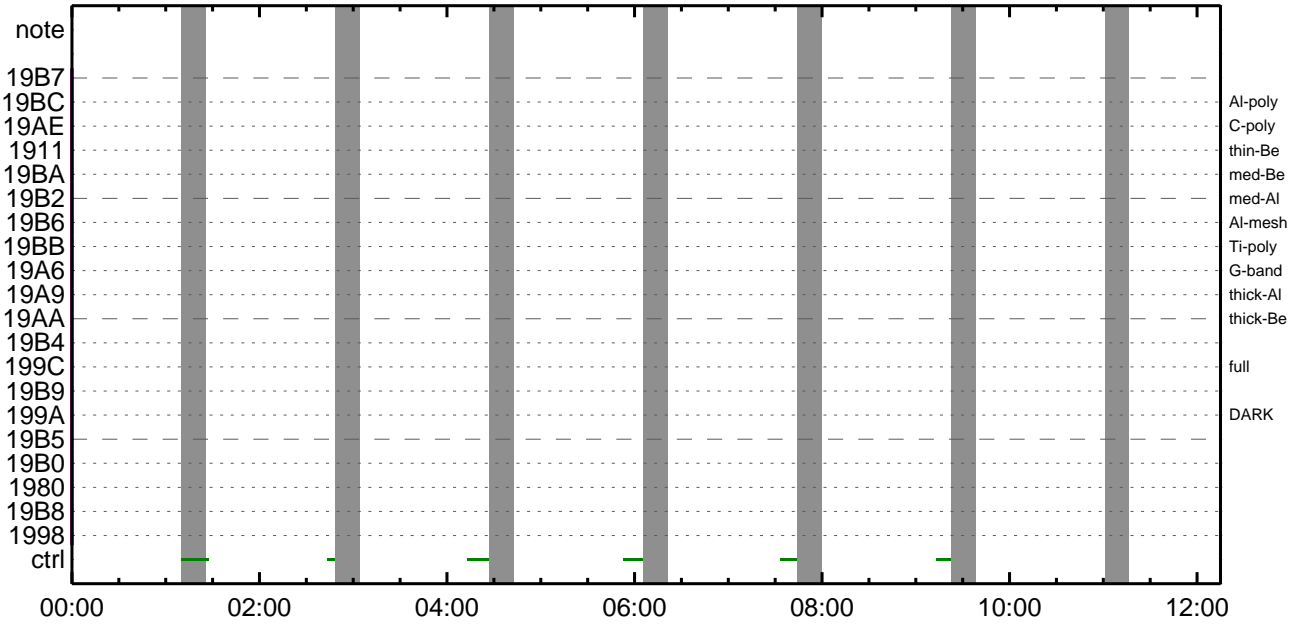
CMDI #0689 2013/08/26



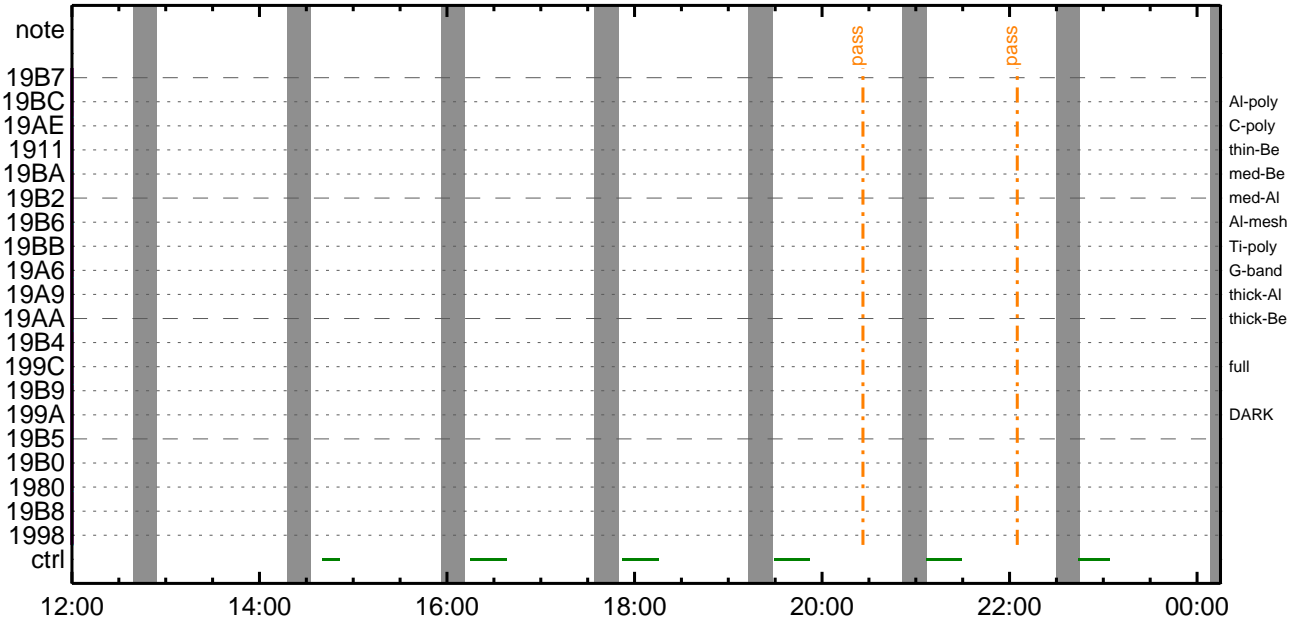
CMDI #0689 2013/08/26



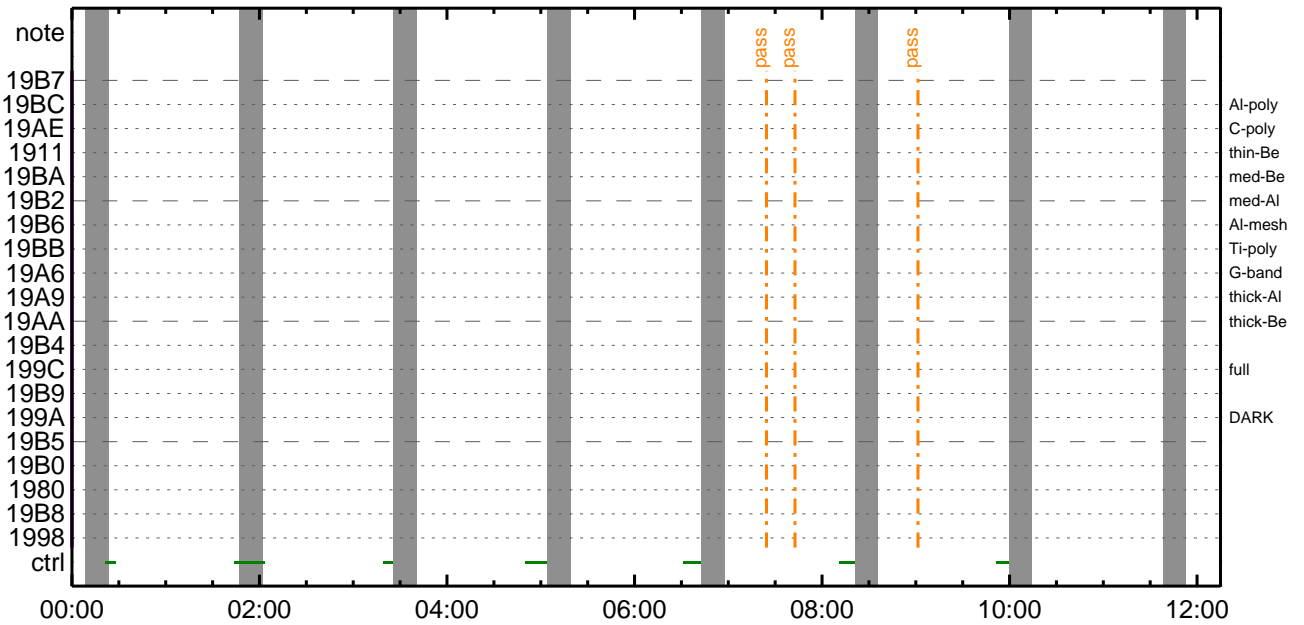
CMDI #0689 2013/08/27



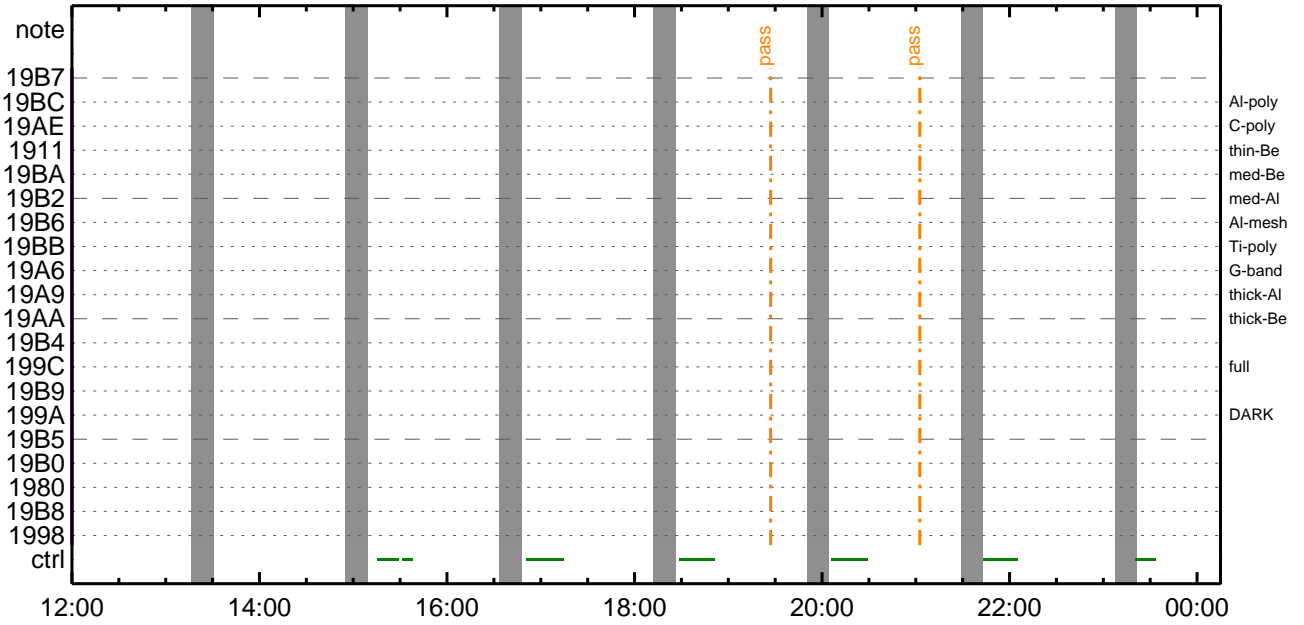
CMDI #0689 2013/08/27



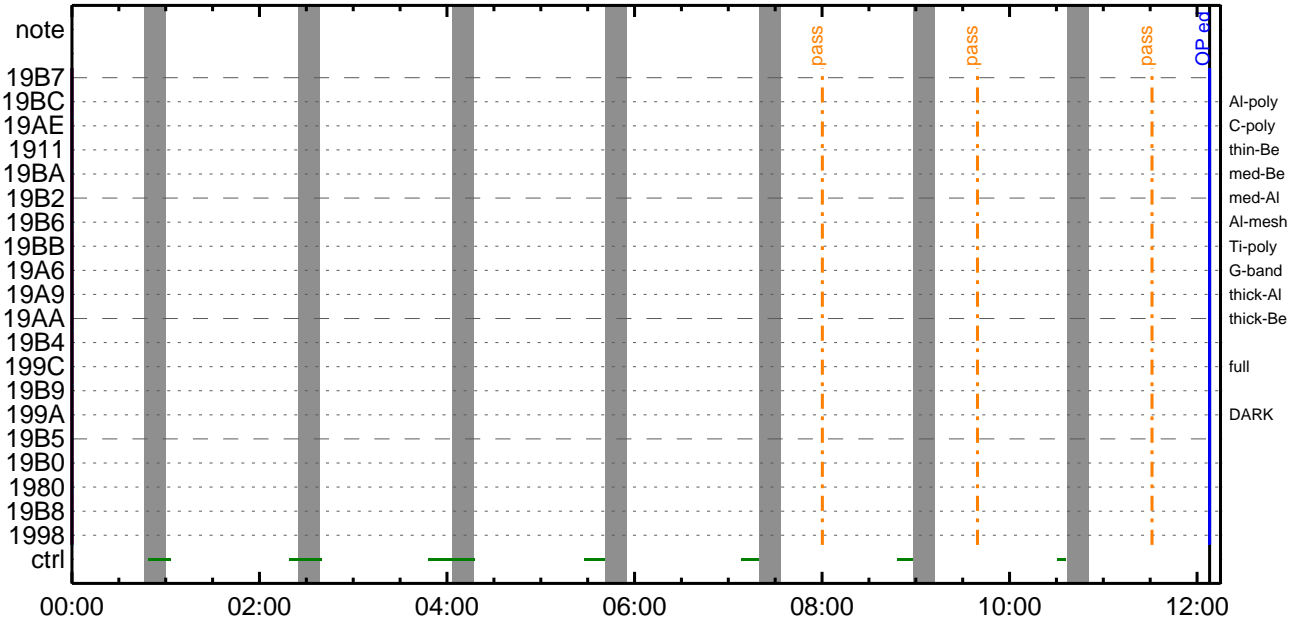
CMDI #0689 2013/08/28



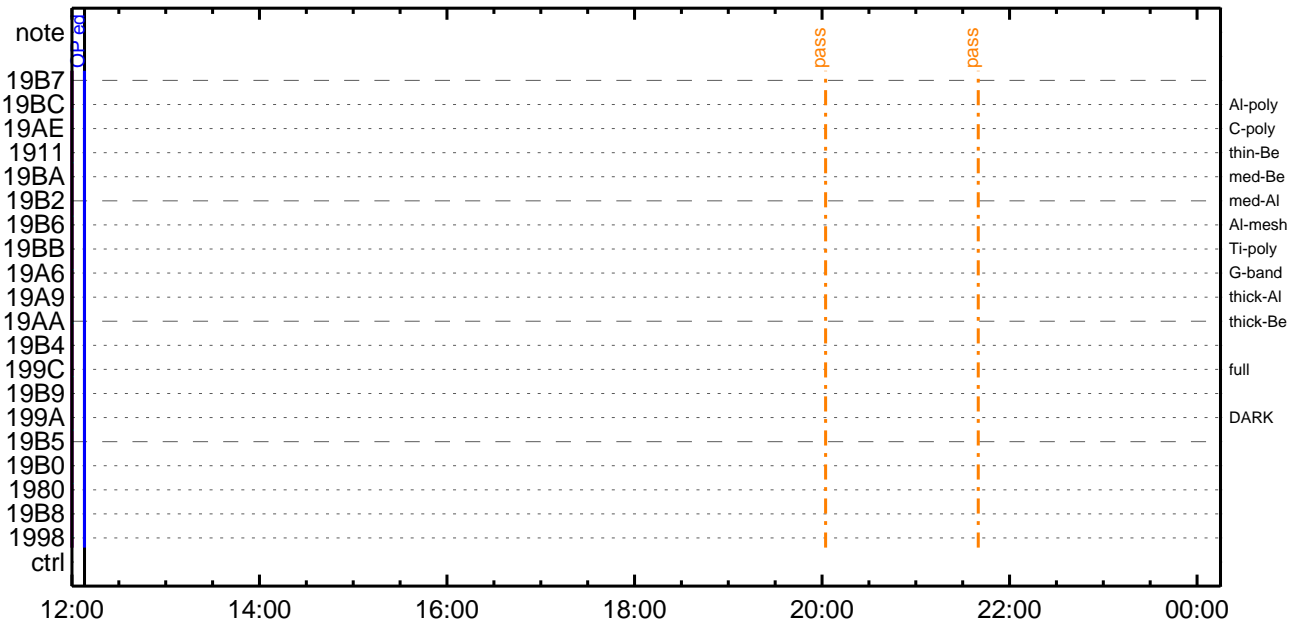
CMDI #0689 2013/08/28



CMDI #0689 2013/08/29



CMDI #0689 2013/08/29




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-860:OP
0104 ( )
0105 S. OG og-860:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYôYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼E¹ç•è²îOK²³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYôYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼E¹ç•è²îOK²³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYôYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼E¹ç•è²îOK²³îÇ§
0165 C.
0166 C. ***** °E²¼²î¼E¹ç•è²îOK²³îÇ§ *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷ç@NG²î¼E¹ç•è²¼²îTI-CMDÁ÷ç²î¼E¹Ô²•²E²³²E;f
0180 C. ²²²ç;çSET²E²DUMP²î¼E¹ç•è²¼²î¼E¹Ô²³²E;f
0181 C.
0182 C. TIY³Y²YôYE²ò²ÁDîç(UT)
0183 +. TI 2013-08-24 09:21:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-08-24 09:21:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-08-24 09:21:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-861 2013-08-24 13:29:27 82 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY~¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È□¿□Å□•µ°Æ»Í×ÁÇ□î¥çYÁY×¥í;¼YÉ;ÈÈ%µ•íÉ;È□È¼°ÇÔ□•□¿¼í¹ç□î;çÀ®, ù□¹□È□È□È□ÇÁ+¿®□•□È□□□³□È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 . C. Verify EIS in MANUAL mode
0049 . C. Estimated OBSTBL upload time is 24s
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-820:EIS_OBSTBL
0054 ( )
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2013-08-24 09:25:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 . C. ***** MDP `ûÁî□î»ô¼Y□ÈÁ□¹□ÈDCBC•x²è *****
0069 C. (¼á°îYÔYÁYÈY¥¥ÈYáYçYÈ□È¼¼□¼Á»Û□¹□È)
0070 . S. DC-BC dcbc-402:DCBC
0071 (MDP_known_event)
0072 C.
0073 C.
0074 . C. ***** YD¥¹.İ Daily±;îÑ□È`Ø□¹□ÈDCBC•x²è *****
0075 . S. DC-BC dcbc-153:DCBC
0076 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077 C.
0078 C.
0079 . C. ;ãLOSÁY$YÁY~¼Á»Û;ã
0080 C.
0081 . C. ***** LOS *****
0082 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-862 2013-08-24 13:29:27 132 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÁø•µ°È»Í×ÁÇøÍYçYÁY×Yí;¼YÉ;ÈÈè%µ•ííÉ;ÈøÈ¼°ÇÓø•ø¿¼í¹çøÍ;çÁ®, ùø¹øèøøøÇÁ+¿®ø•øÈøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-261:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2013-08-24 09:25:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG_____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR _____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 06 06)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 85 83 08 08)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 20)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 20 08)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 80 80 08 20)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0b 80 80 08 08)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0f 80 80 06 06)
0088 + DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 10 80 80 08 08)
0090 + DC 07-F0 MDP_XRT_FLD_ENA
0091 BC (d8)
0092 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0093 BC (c8)
0094 + DC 07-F0 MDP_XRT_AEC_RESET
0095 BC (d0)
```

0096 + DC 07-F0 MDP_XRT_ARS_DIS
0097 BC (d5)
0098 + DC 07-F0 MDP_XRT_FLD_RESET
0099 BC (da)
0100 + DC 07-F0 MDP_XRT_QT_PROG_SET
0101 BC (c4 06)
0102 + DC 07-F0 MDP_XRT_FL_PROG_SET
0103 BC (c5 03)
0104 . C. ----- Success Verify ? OK / NG ____
0105 C.
0106 C.
0107 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0108 C.
0109 +. DC 07-F0 MDP_XRT_MODE_OBSV
0110 BC (c2)
0111 +. TI 2013-08-24 09:25:02.0
0112 DC 07-F0 MDP_XRT_MODE_OBSV
0113 BC (c2)
0114 . C. ----- Success Verify ? OK / NG ____
0115 C.
0116 C. ***** XRT END *****
0117 C.
0118 . C. ***** MDP `uAÎaî»ô%YœEÂÐa¹aèDCBC•x²è *****
0119 C. (%â°îYÓYÂÏEYpYÈYãYçYèaE%¼a¼A»Üa¹aè)
0120 . S. DC-BC dcbc-402:DCBC
0121 (MDP_known_event)
0122 C.
0123 C.
0124 . C. ***** YDY¹·Ï Daily±;îÑaÈ'Øa¹aèDCBC•x²è *****
0125 . S. DC-BC dcbc-153:DCBC
0126 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0127 C.
0128 C.
0129 . C. ;ãLOS¥Á¥S¥Ã¥-¼A»Ü;ã
0130 C.
0131 . C. ***** LOS *****
0132 C.

Aug 24, 13 13:31

XRT_OGLIST_0689.chk

Page 1/7

*** OP Sequence for XRT ***

2013/08/24	09:27:30.0	XRT_CTRL_MANU_448_OG [0x1c0]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/24	09:27:40.0	XRT_FOCUS_RECALIBRATE_433_OG [0x1b1]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2013/08/24	09:31:40.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/08/24	09:35:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/24	09:35:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/08/24	09:36:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2013/08/24	09:36:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/08/24	09:36:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/08/24	09:36:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/08/24	09:36:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/08/24	09:36:24.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/24	09:36:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2013/08/24	09:38:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2013/08/24	09:39:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/24	10:43:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/24	10:43:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/24	10:43:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/24	10:45:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/24	10:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/24	10:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/24	10:47:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/24	10:47:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/24	10:47:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/24	10:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/24	11:05:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/24	11:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/24	12:25:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/24	12:25:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/24	12:25:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/24	12:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/24	12:43:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/24	12:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/24	14:04:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/24	14:04:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/24	14:04:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/24	14:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/24	14:22:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/24	14:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/24	14:28:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/24	14:28:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/24	14:28:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/24	14:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/24	14:38:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/24	14:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/24	15:42:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/24	15:42:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/24	15:42:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/24	15:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9

Saturday August 24, 2013

1/7

2013/08/24	16:00:30.0	XRT_Custom_430_OG [0x1ae]						
2013/08/24	16:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/24	16:02:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/24	16:02:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/24	16:02:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/08/24	16:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/08/24	16:26:30.0	XRT_Custom_430_OG [0x1ae]						
2013/08/24	16:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/24	17:21:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/24	17:21:02.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/24	17:21:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/08/24	17:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/08/24	17:39:00.0	XRT_Custom_430_OG [0x1ae]						
2013/08/24	17:39:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/24	17:39:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/24	17:39:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/08/24	17:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/24	17:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/08/24	18:02:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/24	18:02:56.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2013/08/24	18:03:00.5	AOCS_Ore-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2013/08/24	18:03:16.0	XRT_FLD_DIS_434_OG [0x1b2]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/08/24	18:05:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/08/24	18:05:56.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/08/24	18:05:58.0	XRT_QT_PROG_SET_440_OG [0x1b8]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05			
2013/08/24	18:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/24	18:12:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/24	18:12:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2013/08/24	18:13:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	03 00 00 00 00			
2013/08/24	18:13:16.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/08/24	18:13:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/08/24	18:13:20.0	XRT_AEC_RESET_413_OG [0x19d]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/08/24	18:13:22.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/08/24	18:13:24.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/24	18:13:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f			
2013/08/24	18:15:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03			
2013/08/24	18:16:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/24	18:59:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/24	18:59:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/24	18:59:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/08/24	19:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/08/24	19:40:00.0	XRT_Custom_430_OG [0x1ae]						
2013/08/24	19:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/24	20:38:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/24	20:38:02.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/24	20:38:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/08/24	20:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/08/24	21:17:00.0	XRT_Custom_430_OG [0x1ae]						
2013/08/24	21:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			

Aug 24, 13 13:31

XRT_OGLIST_0689.chk

Page 3/7

2013/08/24	22:16:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/24	22:16:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/24	22:16:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/24	22:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/24	22:52:00.0	XRT_Custom_430_OG [0x1ae]							
2013/08/24	22:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/24	23:55:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/24	23:55:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/24	23:55:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/24	23:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/25	00:16:30.0	XRT_Custom_430_OG [0x1ae]							
2013/08/25	00:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/25	01:32:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/25	01:32:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/25	01:32:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/25	01:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/25	01:51:30.0	XRT_Custom_430_OG [0x1ae]							
2013/08/25	01:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/25	03:07:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/25	03:07:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/25	03:07:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/25	03:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/25	03:29:30.0	XRT_Custom_430_OG [0x1ae]							
2013/08/25	03:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/25	04:38:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/25	04:38:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/25	04:38:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/25	04:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/25	05:08:00.0	XRT_Custom_430_OG [0x1ae]							
2013/08/25	05:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/25	05:49:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/25	05:49:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/08/25	05:50:00.5	AOCs_Orе-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/08/25	05:50:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/08/25	05:52:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/08/25	05:52:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/08/25	05:52:58.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2013/08/25	05:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/25	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/25	05:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/08/25	06:00:00.0	AOCs_Orе-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2013/08/25	06:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/08/25	06:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/08/25	06:00:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/08/25	06:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/08/25	06:00:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/25	06:00:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2013/08/25	06:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2013/08/25	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

Aug 24, 13 13:31

XRT_OGLIST_0689.chk

Page 4/7

2013/08/25	06:18:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/25	06:18:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/25	06:18:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/25	06:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/25	06:46:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/25	06:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/25	07:58:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/25	07:58:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/25	07:58:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/25	08:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/25	08:25:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/25	08:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/25	09:38:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/25	09:38:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/25	09:38:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/25	09:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/25	10:03:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/25	10:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/25	11:24:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/25	11:24:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/25	11:24:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/25	11:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/25	11:41:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/25	11:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/25	13:03:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/25	13:03:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/25	13:03:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/25	13:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/25	13:20:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/25	13:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/25	14:41:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/25	14:41:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/25	14:41:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/25	14:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/25	14:58:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/25	14:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/25	15:03:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/25	15:03:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/25	15:03:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/25	15:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/25	15:16:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/25	15:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/25	15:23:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/25	15:23:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/25	15:23:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/25	15:26:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/25	15:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/25	15:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/25	16:20:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/25	16:20:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/25	16:20:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			

Aug 24, 13 13:31

XRT_OGLIST_0689.chk

Page 5/7

2013/08/25	16:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/25	16:37:00.0	XRT_Custom_430_OG [0x1ae]					
2013/08/25	16:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/25	16:39:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/25	16:39:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/25	16:39:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/25	16:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/25	17:02:30.0	XRT_Custom_430_OG [0x1ae]					
2013/08/25	17:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/25	17:48:24.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/25	17:48:26.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/08/25	17:48:30.0	AOCS_OrE-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2013/08/25	17:48:46.0	XRT_FLD_DIS_434_OG [0x1b2]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/08/25	17:51:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/08/25	17:51:26.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/08/25	17:51:28.0	XRT_QT_PROG_SET_440_OG [0x1b8]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 05	
2013/08/25	17:51:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/25	17:58:24.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/25	17:58:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/08/25	17:58:30.0	AOCS_OrE-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	03 00 00 00 00	
2013/08/25	17:58:30.5	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/25	17:58:32.5	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/25	17:58:34.5	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/25	17:58:46.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/08/25	17:58:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/08/25	17:58:50.0	XRT_AEC_RESET_413_OG [0x19d]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/08/25	17:58:52.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/08/25	17:58:54.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/25	17:58:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f	
2013/08/25	18:01:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2013/08/25	18:01:44.5	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/25	18:39:30.0	XRT_Custom_430_OG [0x1ae]					
2013/08/25	18:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/25	19:37:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/25	19:37:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/25	19:37:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/25	19:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/25	20:16:30.0	XRT_Custom_430_OG [0x1ae]					
2013/08/25	20:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/25	21:15:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/25	21:15:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/25	21:15:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/25	21:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/25	21:53:00.0	XRT_Custom_430_OG [0x1ae]					
2013/08/25	21:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/25	22:54:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/25	22:54:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/25	22:54:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/25	22:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					

Saturday August 24, 2013

5/7

2013/08/25	23:23:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/08/25	23:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
2013/08/26	00:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/26	00:32:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/26	00:32:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/26	00:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/08/26	00:51:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/08/26	00:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
2013/08/26	01:59:54.0	XRT_CTRL_MANU_436_OG [0x1b4]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/26	02:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/26	02:02:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	AOCU_NM	5	02-76	04 00 00 00 00			
2013/08/26	02:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2013/08/26	02:02:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/08/26	02:02:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/08/26	02:02:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/08/26	02:02:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/08/26	02:02:56.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/26	02:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11			
2013/08/26	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03			
2013/08/26	02:07:30.0	XRT_CTRL_MANU_400_OG [0x190]							
2013/08/26	02:07:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/26	02:07:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/26	02:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/26	02:27:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/08/26	02:28:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/08/26	03:36:00.0	XRT_CTRL_MANU_400_OG [0x190]							
2013/08/26	03:36:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/26	03:36:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/26	03:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/08/26	04:06:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/08/26	04:06:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
2013/08/26	04:07:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2013/08/26	04:07:16.0	XRT_FLD_DIS_434_OG [0x1b2]	AOCU_NM	5	02-76	00 00 00 00 00			
2013/08/26	04:09:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/08/26	04:09:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/08/26	04:09:58.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/08/26	04:10:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05			
2013/08/26	04:16:54.0	XRT_CTRL_MANU_402_OG [0x192]							
2013/08/26	04:16:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/26	04:17:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/26	04:17:16.0	XRT_FLD_ENA_411_OG [0x19b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2013/08/26	04:17:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	AOCU_NM	5	02-76	02 00 00 00 00			
2013/08/26	04:17:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/08/26	04:17:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/08/26	04:17:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/08/26	04:17:26.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/08/26	04:19:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/26	04:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01			
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 03			

2013/08/26	05:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/26	05:15:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/26	05:15:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/26	05:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/26	05:44:30.0	XRT_Custom_430_OG [0x1ae]					
2013/08/26	05:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/26	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/26	05:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/08/26	06:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 1a a7 b2 a8	
2013/08/26	06:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/08/26	06:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/08/26	06:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/08/26	06:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/08/26	06:00:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/26	06:00:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f	
2013/08/26	06:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03	
2013/08/26	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/26	06:56:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/26	06:56:02.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/26	06:56:04.5	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/26	06:59:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/26	07:23:00.0	XRT_Custom_430_OG [0x1ae]					
2013/08/26	07:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/26	08:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/26	08:35:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/26	08:35:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/26	08:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/26	09:01:30.0	XRT_Custom_430_OG [0x1ae]					
2013/08/26	09:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/26	10:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/26	10:17:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/26	10:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/26	10:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/26	10:48:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/26	10:49:00.5	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	