

XRT Timeline to be uploaded on 2013/08/29

Period: 2013/08/29 12:08:00 - 2013/09/03 10:40:00

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Normal mode

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XOB #19B4: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-bar

Term	Pointing (x, y)	Comment
08/29 12:30:00 - 08/29 17:27:30	Track (-705.6, 118.3) [Ⓢ] 08/29 12:18:00	# OP start + 10min HOP236 - AR11836 plage
08/30 04:13:00 - 08/30 05:59:54	Track (-220.8, -307.7) [Ⓢ] 08/30 04:10:00	# HOP173 - AR11835
08/30 06:03:00 - 08/30 17:38:54	Track (-592.4, 104.4) [Ⓢ] 08/30 06:00:00	# HOP236 - AR11836 plage
08/30 18:01:30 - 08/31 01:56:00	Track (-527.4, 62.0) [Ⓢ] 08/30 17:49:00	# AR11836
08/31 02:17:00 - 08/31 03:59:54	Track (-464.5, 57.0) [Ⓢ] 08/31 02:00:00	# HOP173 - AR11836
08/31 04:13:00 - 08/31 05:59:54	Track (-7.0, -311.4) [Ⓢ] 08/31 04:10:00	# HOP173 - AR11835
08/31 06:03:00 - 08/31 10:00:00	Track (-411.8, 89.6) [Ⓢ] 08/31 06:00:00	# HOP236 - AR11836 plage

PROG= 09 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 8	2-time(s)	2.0sec										
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2	2-time(s)	2.0sec										
Seqn= 24	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 98	4-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 21	12-time(s)	240.0sec										
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19BD: Synoptic Q95 2x2 - Al/mesh(12/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(33/362/1443) + Thi

Term	Pointing (x, y)	Comment
08/29 17:56:00 - 08/29 18:49:30	Fixed (0.0, 0.0)	Extended synoptic for EIS pointing cal, shifted -7.0 min
08/29 19:03:30 - 08/30 01:59:54	Track (-689.5, 79.6) [Ⓢ] 08/29 18:08:00	# AR11836
08/30 04:03:00 - 08/30 04:09:54	Fixed (0.0, 0.0)	synoptic, shifted manually
08/30 17:42:00 - 08/30 17:48:54	Fixed (0.0, 0.0)	synoptic, shifted -21.0 min
08/31 04:03:00 - 08/31 04:09:54	Fixed (0.0, 0.0)	synoptic, shifted manually

PROG= 18 1-time(s)

Subr= 1	1-time(s)	12.0sec										
Seqn= 82	1-time(s)	4.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 52	1-time(s)	4.0sec										
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 99	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 66	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19BE: CME watch - C/poly - 384x384- 120s - Be/thin - 2x2 - 1024x1024 - 20min - AEC2

Term	Pointing (x, y)	Comment
08/30 02:03:00 - 08/30 03:59:54	Fixed (-805.0, -515.0)	# HOP173 - Prominence at SE limb (Joint Obs. with IRIS)

PROG= 04 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 53	1-time(s)	30.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	1024x1024 (1024, 1024)	DPCM	2	0	2.0sec

Subr= 2 10-time(s) 2.0sec
 Seqn= 54 1-time(s) 120.0sec
 C-poly/Open C-poly/Open close Safe Norm 4.00s Obs 1x1 384x384 (1024, 1024) DPCM 2 0 2.0sec
 Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Flare mode

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XOB #1980: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops-2

Term	Pointing (x, y)	Comment
08/29 12:30:00 - 08/29 17:27:30	Track (-705.6, 118.3) ^{Ⓢ 08/29 12:18:00}	# OP start + 10min HOP236 - AR11836 plage
08/30 02:03:00 - 08/30 03:59:54	Fixed (-805.0, -515.0)	# HOP173 - Prominence at SE limb (Joint Obs. with IRIS)
08/30 04:13:00 - 08/30 05:59:54	Track (-220.8, -307.7) ^{Ⓢ 08/30 04:10:00}	# HOP173 - AR11835
08/30 06:03:00 - 08/30 17:38:54	Track (-592.4, 104.4) ^{Ⓢ 08/30 06:00:00}	# HOP236 - AR11836 plage
08/30 18:01:30 - 08/31 01:56:00	Track (-527.4, 62.0) ^{Ⓢ 08/30 17:49:00}	# AR11836
08/31 02:17:00 - 08/31 03:59:54	Track (-464.5, 57.0) ^{Ⓢ 08/31 02:00:00}	# HOP173 - AR11836
08/31 04:13:00 - 08/31 05:59:54	Track (-7.0, -311.4) ^{Ⓢ 08/31 04:10:00}	# HOP173 - AR11835
08/31 06:03:00 - 08/31 10:00:00	Track (-411.8, 89.6) ^{Ⓢ 08/31 06:00:00}	# HOP236 - AR11836 plage

PROG= 03 15-time(s)

Subr= 1 45-time(s) 10.0sec																			
Seqn= 9 1-time(s) 2.0sec	thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec						
Subr= 2 1-time(s) 10.0sec																			
Seqn= 10 1-time(s) 2.0sec	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec						
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec						
Seqn= 11 1-time(s) 2.0sec	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec						
Seqn= 15 1-time(s) 2.0sec	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec						
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec						
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec						
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval							

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Active Region Search

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NOT USED

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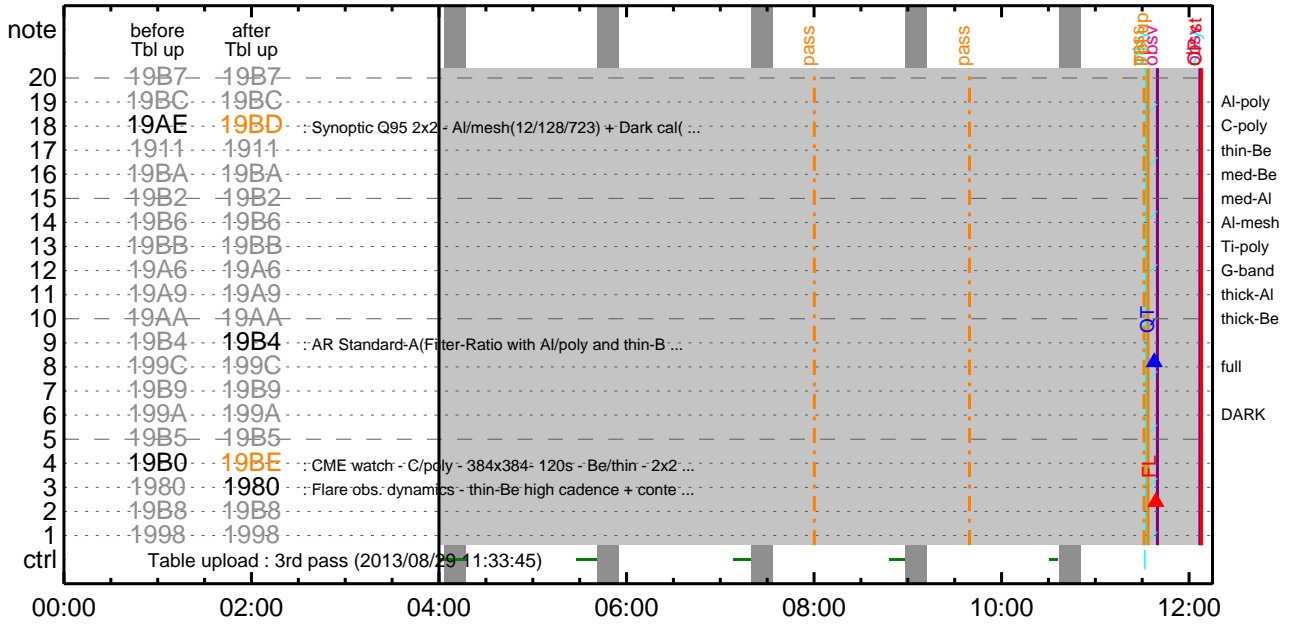
Flare Detection

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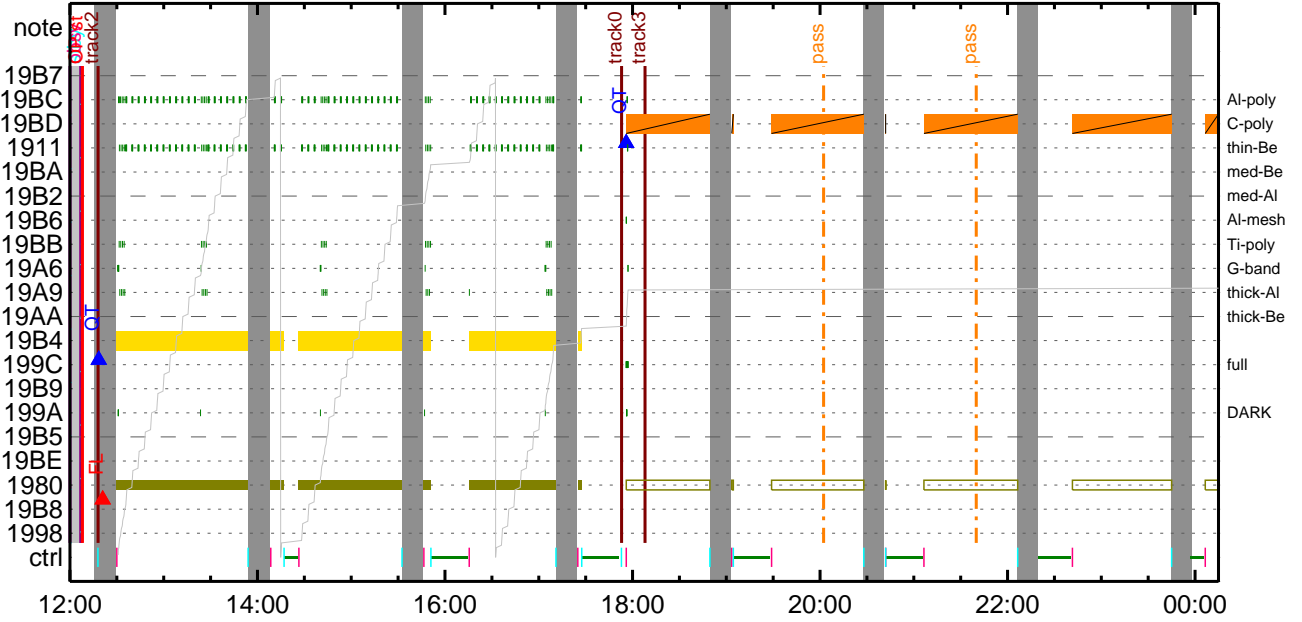
FLD Patrol

Term	Pointing (x, y)	Comment
08/30 02:00:16 - 08/30 04:00:16	Fixed (-805.0, -515.0)	# HOP173 - Prominence at SE limb (Joint Obs. with IRIS)
08/30 04:10:16 - 08/30 17:39:16	Track (-220.8, -307.7) ^{Ⓢ 08/30 04:10:00}	# HOP173 - AR11835
08/30 17:49:16 - 08/31 04:00:16	Track (-527.4, 62.0) ^{Ⓢ 08/30 17:49:00}	# AR11836
08/31 04:10:16 - 09/03 10:40:00	Track (-7.0, -311.4) ^{Ⓢ 08/31 04:10:00}	# HOP173 - AR11835
Open/Ti-poly	Open/thick-Al	close Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

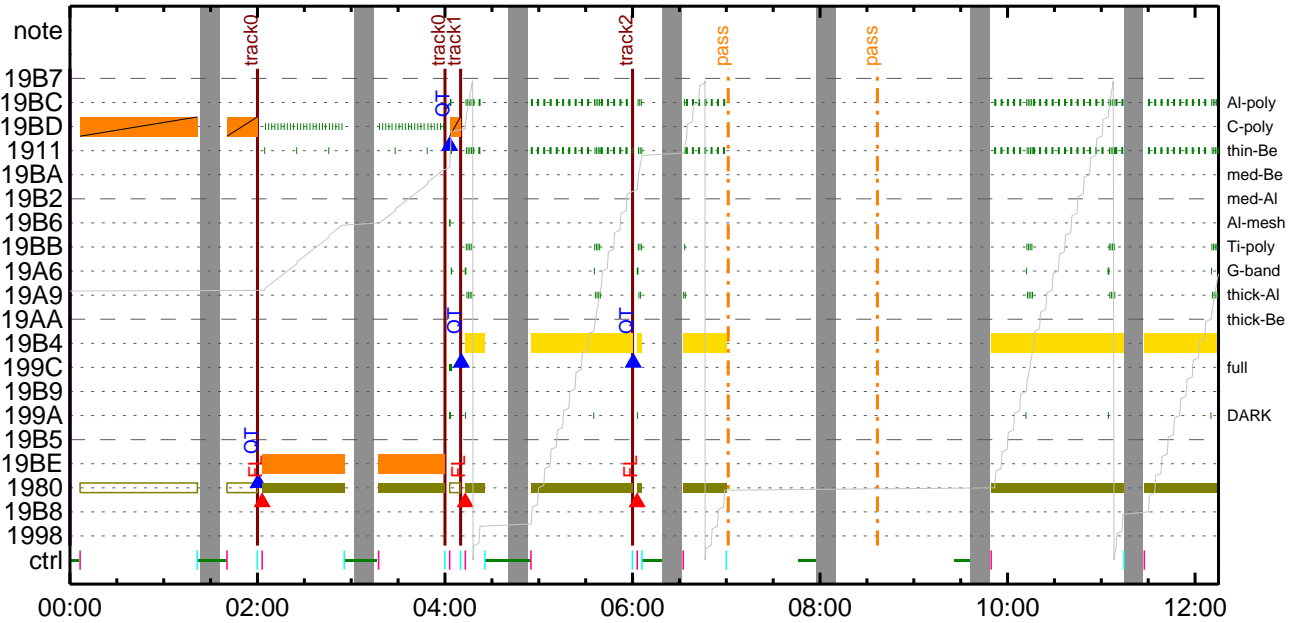
CMDI #0701 2013/08/29



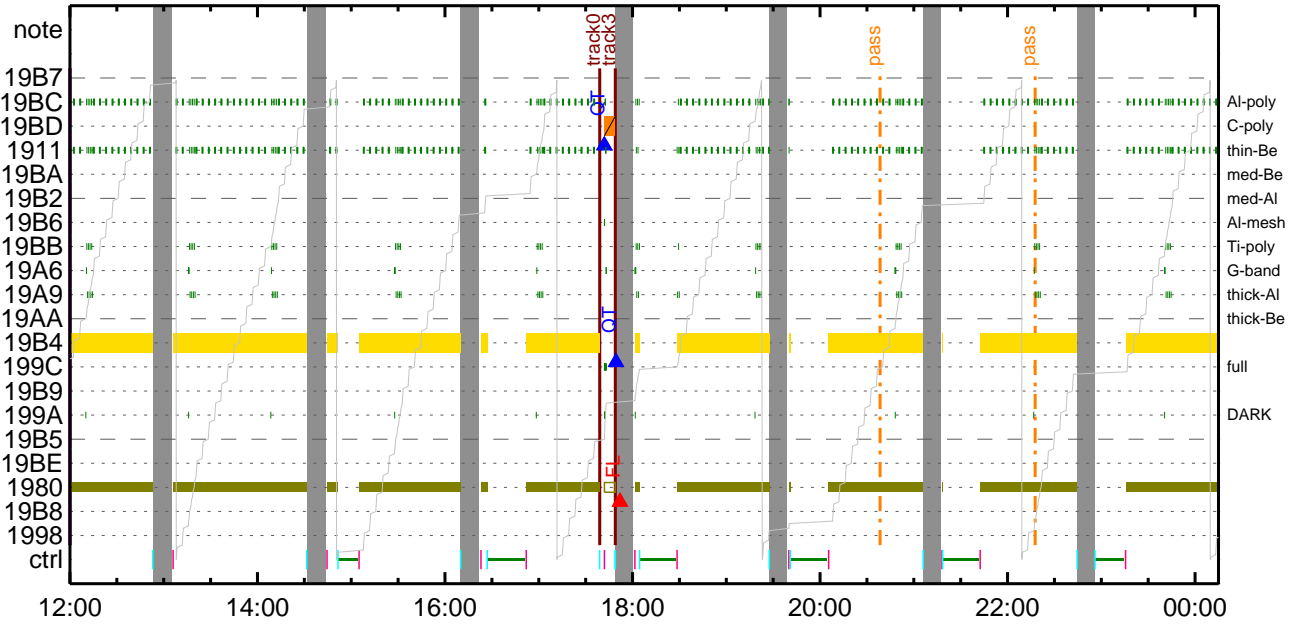
CMDI #0701 2013/08/29



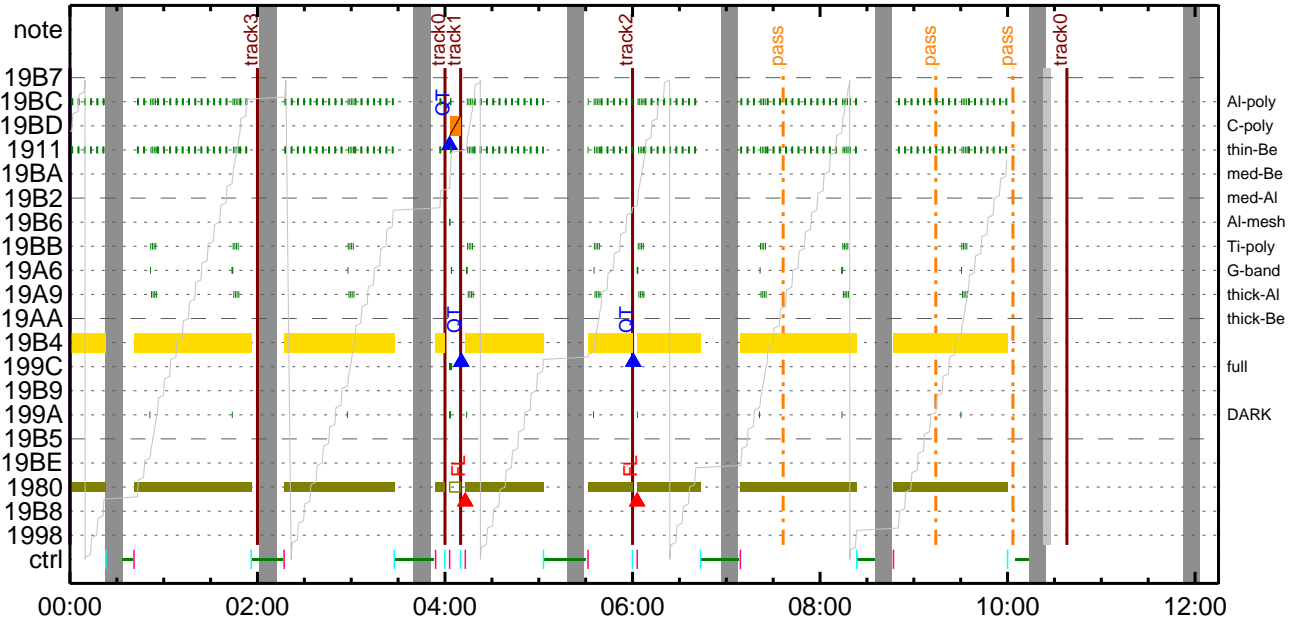
CMDI #0701 2013/08/30



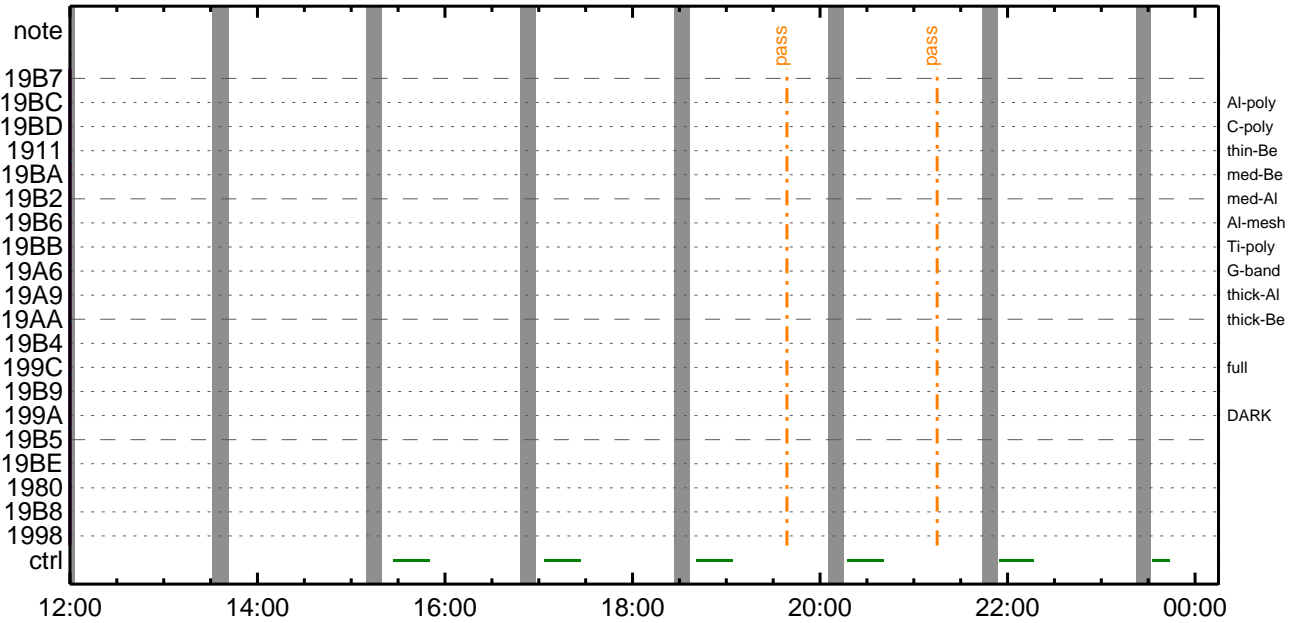
CMDI #0701 2013/08/30



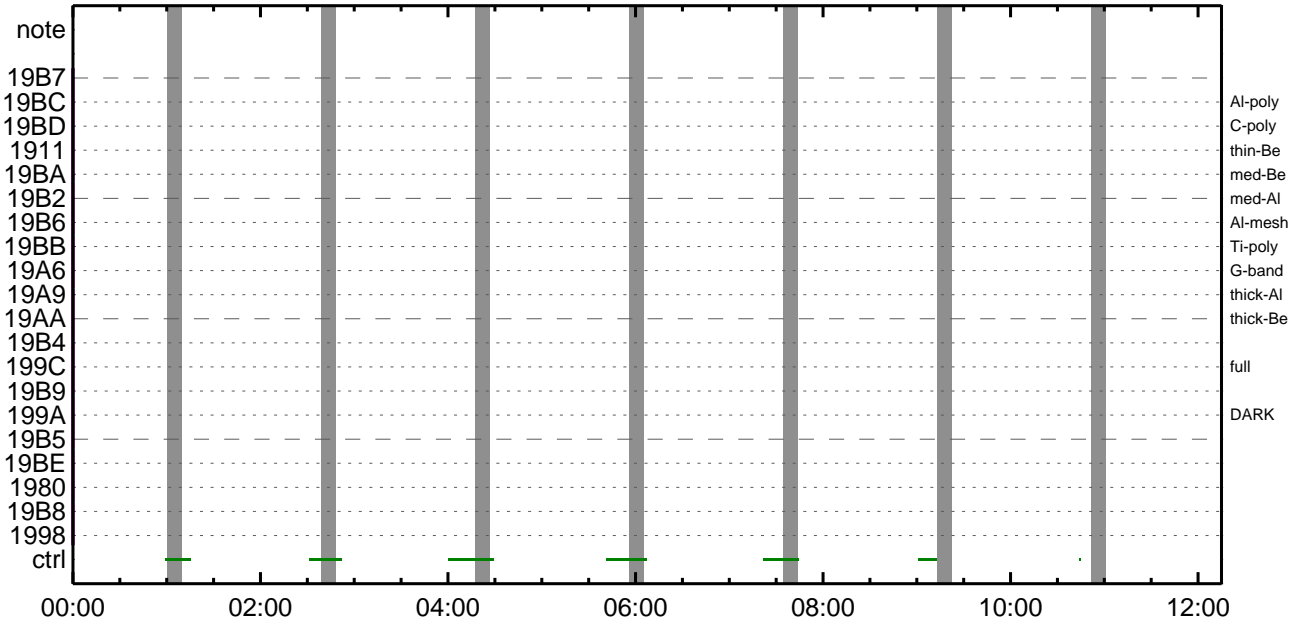
CMDI #0701 2013/08/31



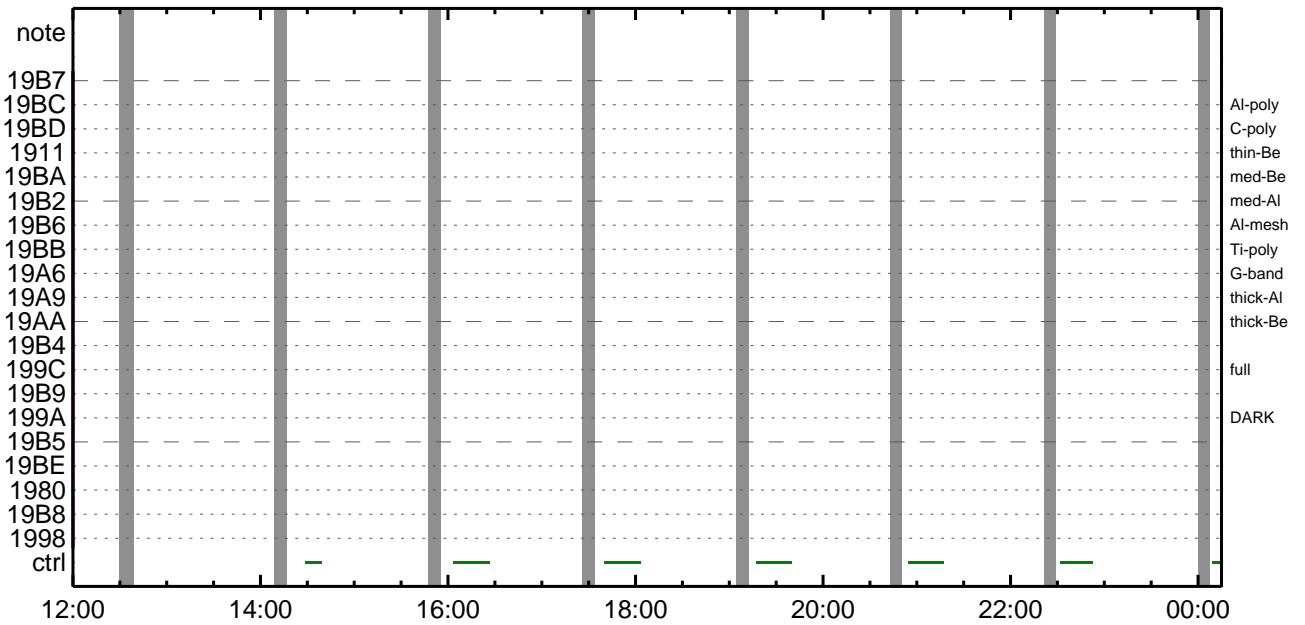
CMDI #0701 2013/08/31



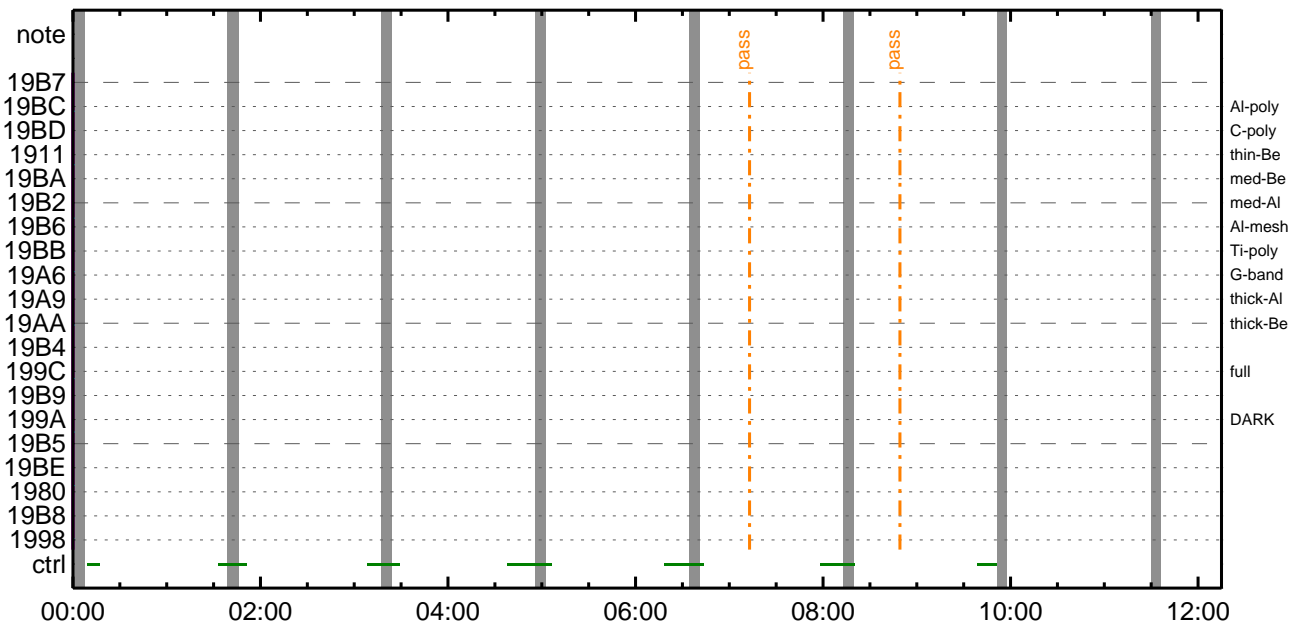
CMDI #0701 2013/09/01



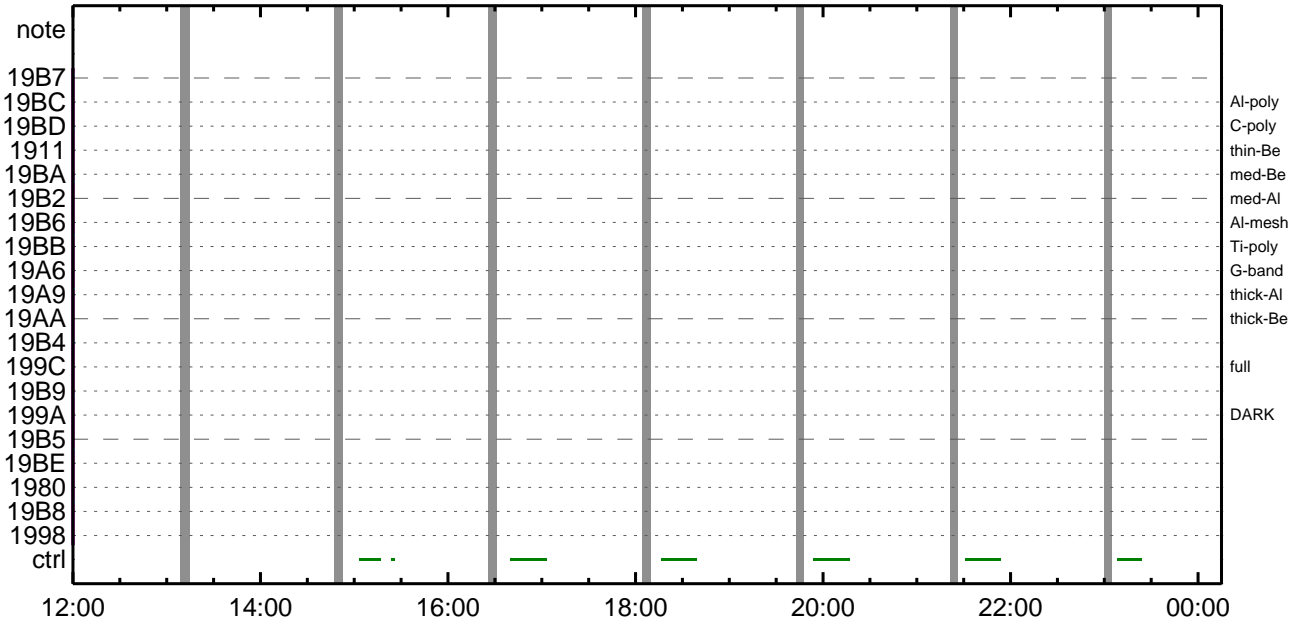
CMDI #0701 2013/09/01



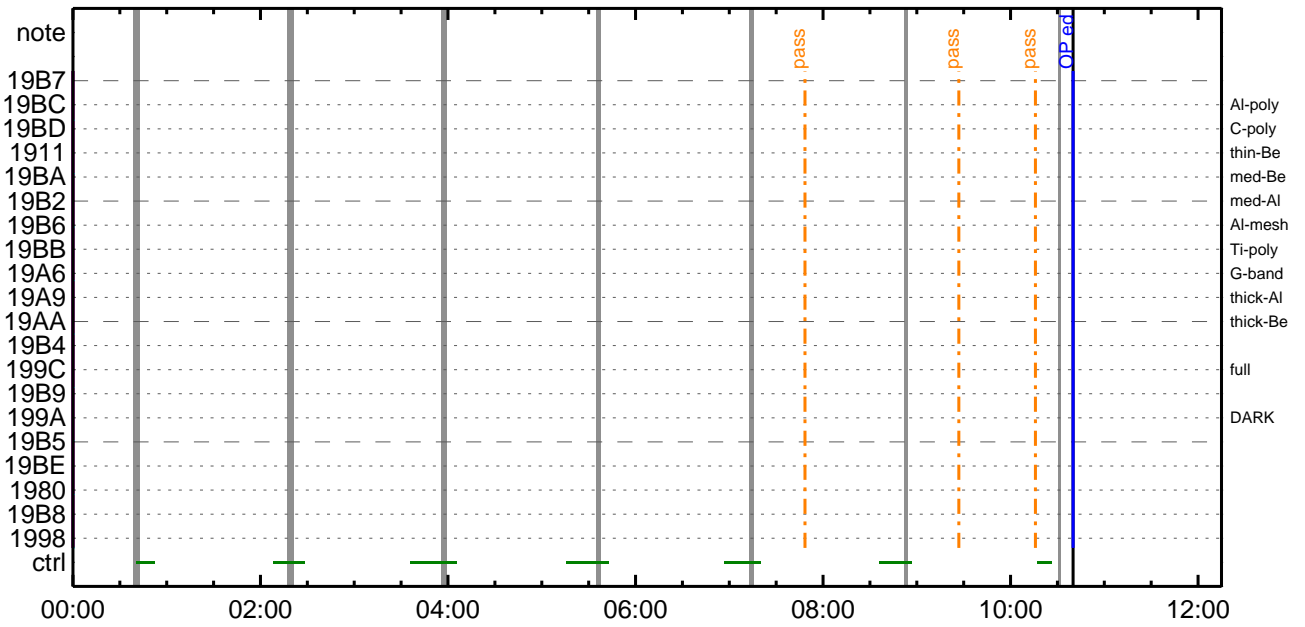
CMDI #0701 2013/09/02



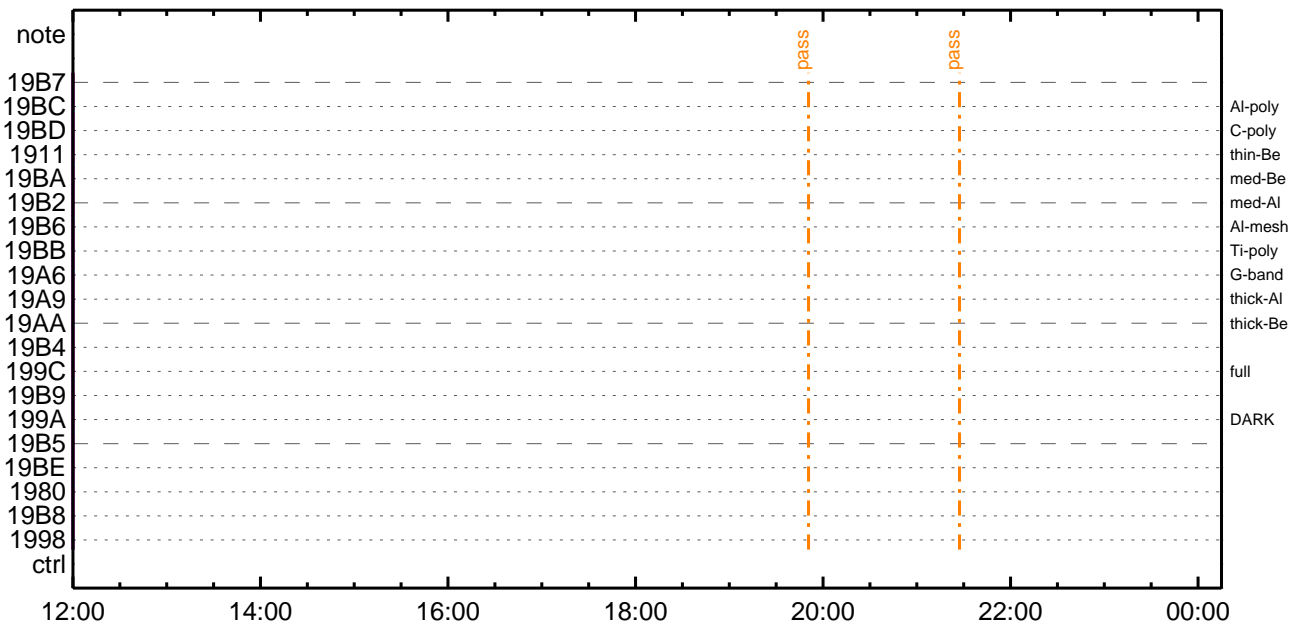
CMDI #0701 2013/09/02



CMDI #0701 2013/09/03



CMDI #0701 2013/09/03




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;aOP/OGY1;4YE;a
0103 S. OP op-868:OP
0104 ( )
0105 S. OG og-868:OG
0106 ( )
0107 C.
0108 C. ;aNMOG&OPf^°eYAYOX;a
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0120 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½^aÎ»oð³ÎÇ§
0125 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGqî½E¹Ç•ë²İOKoð³ÎÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0139 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½^aÎ»oð³ÎÇ§
0144 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGqî½E¹Ç•ë²İOKoð³ÎÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0158 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½^aÎ»oð³ÎÇ§
0163 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPqî½E¹Ç•ë²İOKoð³ÎÇ§
0165 C.
0166 C. ***** oE²¼oİ½Ä´¶A°EĒ¬oĀ÷ĵ® (¼âµ-YAYOXx½e½ÇoðĀŌĀæoÇ¼^a¬°oE¼i¹ÇoÇoâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eoðİâ¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0171 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADo¬Ā÷ĵ®NGuİ½i¹Ç;ĉ°E²¼oİTI-CMDĀ÷ĵ®oİ½Ā¹Ōo•oEoð³oE;f
0180 C. oEoĵ;ĉSEToEDUMPoİĒ±°iYNY¹oÇ¹Ōo|o³oE;f
0181 C.
0182 C. TIY³YpYóYĒoðĀDİĵ(UT)
0183 +. TI 2013-08-29 12:03:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-08-29 12:03:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-08-29 12:03:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP

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(a) Spacecraft Operation Procedure (real-commands)

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main-869 2013-08-29 12:56:07 175 33 SOLAR-B MAIN //
0001  C.
0002  . C. ***** AOS *****
0003  C.
0004  . C. ;ãAOS¥Á¥S¥Á¥~¼Á»Û;ã
0005  C.
0006  C. ¥À¥£;¼¥³¥P¥Ó¥ÉÁ+ç®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008  C.
0009  . C. ***** AOCs : Reload orbital element (send every contact) *****
0010  C.  Áí;ÈçµÄµ°µ»Í×ÁÇçÍ¥ç¥Á¥×¥í;¼¥É;ÈÈÈµ°ííÉ;ÈÈÈ¼°ÇÓµ°µç¼í¹çµí;çÀ®, ù¹µóµçµçÁ+ç®µ°µÈµµµ³µÈ;ç
0011  +. DC 02-8E AOCU_ORB_UPD
0012  C.
0013  C.
0014  . C. *****
0015  C. XÁ+çµ;ON
0016  C. *****
0017  C.  °ÈÀ, í×ÈÝµáLOSPµççÍ»¹õµð¹íí, µ°; çÉóÍ×µÈXÁÓONµí¹õµÈµíµÈµµµ³µÈ;ç
0018  C.
0019  +. DC 03-B4 TCIA_XPA_ON/HI
0020  M. WAIT_SEC 1
0021  + DC 03-84 TCIA_XMOD_ON
0022  M. WAIT_SEC 1
0023  + DC 03-95 TCIA_XMOD_QPSK
0024  C.           çç[HK1_XPA_ON/OFF]           EQ       ON
0025  C.           çç[HK1_XPA_PWR_HI/LO]       EQ       HI
0026  C.           çç[HK1_XMOD_ON/OFF]         EQ       ON
0027  C.           çç[HK1_XMOD_QPSK/PM]        EQ       QPSK
0028  C.
0029  . C. X¥D¥Ó¥É¥í¥Á¥³¼ÔÁõµ°µçµç; ç°È²¼µí°ÈÀ, ¼Èçµçµð¼Á¹õµ¹µÈ;ç
0030  C.
0031  . C. *****
0032  C. DR PT1 Áí¼í°ÈÀ,
0033  C. *****
0034  C.  ° RESTART;ÈPT1;Èµ°µçµç¼í¹çµí; ç°È²¼µí°ÈÀ¹õµ»µ°; çDCBC-150µççÈµç;ç
0035  C.
0036  . C. ;ãPT1°ÈÀ, ³«»Í;ã
0037  +. DC 01-29 DHU_S/X_VC4_OFF
0038  + DC 06-C8 DR_PT1_REP_SEL
0039  BC          (01 00)
0040  + DC 06-B3 DR_REP_START
0041  + DC 01-32 DHU_X_VC4_ON
0042  C.           çç[HK1_REP_PT_1/2]           EQ       PT1   (¼Á¹õ, ;¼Ú)
0043  C.           çç[HK1_REP_STA/STP]         EQ       START (¼Á¹õ, ;¼Ú)
0044  C.           çç[HK1_X_VC4_ON/OFF]        EQ       ON    (¼Á¹õ, ;¼Ú)
0045  C.
0046  . C. ;ã¥ç¥ó¥É¥ÈÁÙÁÏ;ÈÁ°Á²óÈð;È, áµí°ÈÀ, °È³«;ã
0047  +. DC 06-B3 DR_REP_START
0048  + DC 01-32 DHU_X_VC4_ON
0049  C.           çç[HK1_REP_PT_1/2]           EQ       PT1   (¼Á¹õ, ;¼Ú)
0050  C.           çç[HK1_REP_STA/STP]         EQ       START (¼Á¹õ, ;¼Ú)
0051  C.           çç[HK1_X_VC4_ON/OFF]        EQ       ON    (¼Á¹õ, ;¼Ú)
0052  C.
0053  C.
0054  . C. PT1°ÈÀ, µ¼«È°Áá»µµµçµç; ç°È²¼µçµç¼Á¹õµ¹µÈ;ç
0055  C.  ¥ç¥ó¥É¥ÈÁÙÁÏµçµçÁ°Á²óÈðµçµç¼í¹çµí°ÈÁµ»µ°µçµççÁÓµç;ç
0056  C.
0057  . C. *****
0058  C. DR PT2 Áí¼í°ÈÀ,
0059  C. *****
0060  C.  ° RESTART;ÈPT2;Èµ°µçµç¼í¹çµí; ç°È²¼µí°ÈÀ¹õµ»µ°; çDCBC-151µççÈµç;ç
0061  C.
0062  . C. ;ãPT2°ÈÀ, ³«»Í;ã
0063  +. DC 01-29 DHU_S/X_VC4_OFF
0064  + DC 06-C8 DR_PT2_REP_SEL
0065  BC          (02 00)
0066  + DC 06-B3 DR_REP_START
0067  + DC 01-32 DHU_X_VC4_ON
0068  C.           çç[HK1_REP_PT_1/2]           EQ       PT2   (¼Á¹õ, ;¼Ú)
0069  C.           çç[HK1_REP_STA/STP]         EQ       START (¼Á¹õ, ;¼Ú)
0070  C.           çç[HK1_X_VC4_ON/OFF]        EQ       ON    (¼Á¹õ, ;¼Ú)
0071  C.
0072  . C. ;ã¥ç¥ó¥É¥ÈÁÙÁÏ;ÈÁ°Á²óÈð;È, áµí°ÈÀ, °È³«;ã
0073  +. DC 06-B3 DR_REP_START
0074  + DC 01-32 DHU_X_VC4_ON
0075  C.           çç[HK1_REP_PT_1/2]           EQ       PT2   (¼Á¹õ, ;¼Ú)
0076  C.           çç[HK1_REP_STA/STP]         EQ       START (¼Á¹õ, ;¼Ú)
0077  C.           çç[HK1_X_VC4_ON/OFF]        EQ       ON    (¼Á¹õ, ;¼Ú)
0078  C.
0079  . C. *****
0080  C. DR°ÈÀ, Áá»µ;çXÁ+çµ;OFF
0081  C. *****
0082  C.
0083  . C. ;ãDR°ÈÀ, Áá»µ;ã
0084  +. DC 06-B4 DR_REP_STOP
0085  + DC 01-29 DHU_S/X_VC4_OFF
0086  C.           çç[HK1_REP_STA/STP]         EQ       STOP
0087  C.           çç[HK1_S_VC4_ON/OFF]         EQ       OFF
0088  C.           çç[HK1_X_VC4_ON/OFF]         EQ       OFF
0089  C.
0090  . C. ;ãXÁ+çµ;OFF;ã
0091  +. DC 03-85 TCIA_XMOD_OFF
0092  M. WAIT_SEC 1
0093  + DC 03-B5 TCIA_XPA_OFF
0094  C.           çç[HK1_XMOD_ON/OFF]          EQ       OFF
0095  C.           çç[HK1_XPA_ON/OFF]           EQ       OFF

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-870 2013-08-29 12:56:07 176 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Ü;ã
0005 C.
0006 C. YÀYB;¼Y³YFYOYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;ÈçðÁß•µ°Æ»Í×ÁÇçÁíYçYÁY×Yí;¼YÉ;ÈÈè%µ•ííÉ;ÈÈÈ¼°ÇÒç•ç¼i¹ççí;çÀ®,ùç¹çèçßçÁ+ç®ç•çÈçççç³çÈ;ç
0011 +. DC 02-8E AOCC_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-263:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Stop SP table >
0036 +. DC 07-F0 MDP_SP_CTRL_MANU
0037 BC (61)
0038 C. -----
0039 C. MDP_SP_CTRL_MODE = MANU [ ]
0040 C. -----
0041 C.
0042 . C. <Upload SP Observation Table>
0043 . S. RAM ram-289:MDP_OBS_S
0044 ( )
0045 C.
0046 . C. < Dump RAMID=MDP_OBS_S >
0047 +. DC 07-F0 MDP_DUMP_SPTBL
0048 BC (83 07 00 00 00 38 b8)
0049 C. -----
0050 C. MDP_OBS_S verify = OK/NG [ ]
0051 C. -----
0052 C.
0053 . C. < Upload DPL table >
0054 C.
0055 C. YçYÁY×Yí;¼YÉçÍÁ°çÈSTS_CHKçðOFFçÈç¹çè
0056 C.
0057 . S. RAM ram-271:MDP_DPL
0058 ( )
0059 C.
0060 . C. < Dump RAMID=MDP_DPL >
0061 +. DC 07-F0 MDP_DUMP_FGTBL
0062 BC (82 07 00 38 b8 00 40)
0063 C. -----
0064 C. MDP_DPL verify = OK [ ]
0065 C. -----
0066 C.
0067 C. STS_CHKçðONçÈç¹çè
0068 C.
0069 . C. < Update MDP DSC PAR1 >
0070 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0071 BC (4c)
0072 C. MDP_CMD_CODE = F04C0700 [ ]
0073 C. MDP_CMD_CNT (count-up 1) [ ]
0074 C. -----
0075 C.
0076 . C.
0077 C. *****
0078 C. SOT TI command set
0079 C. *****
0080 C. Execute, after the success of TBL upload.
0081 +. TI 2013-08-29 12:07:18.0
0082 DC 07-F0 MDP_SOT_MODE_OBSV
0083 BC (40)
0084 . C. -----
0085 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0086 C. -----
0087 C.
0088 C.
0089 C. ***** XRT START *****
0090 C.
0091 +. DC 07-F0 MDP_XRT_CTRL_MANU
0092 BC (c1)
0093 +. DC 07-F0 MDP_XRT_MODE_STBY
0094 BC (c3)
0095 . C. ----- Success Verify ?
```

OK / NG_____

```

0096 C.
0097 C. XRT Obs. Table Upload
0098 . S. RAM ram-291:MDP_OBS_X
0099 ( )
0100 C.
0101 +. DC 07-F0 MDP_DUMP_XRTTBL
0102 BC (84 07 00 00 00 3a d4)
0103 . C. ----- Comparison Check ? OK / ERR ____
0104 C.
0105 C.
0106 +. DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 01 b1 b1 04 04)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 02 b1 b1 08 08)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 03 b1 b1 08 08)
0112 + DC 07-F0 MDP_XRT_ROI_SET
0113 BC (cd 04 b1 b1 06 06)
0114 + DC 07-F0 MDP_XRT_ROI_SET
0115 BC (cd 05 85 83 06 06)
0116 + DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 06 85 83 06 06)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 07 85 83 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 08 80 80 20 20)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 09 80 80 20 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 0a 80 80 08 20)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 0b 80 80 10 10)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 0c 80 80 06 06)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 0f 80 80 06 06)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 10 80 80 08 08)
0134 + DC 07-F0 MDP_XRT_FLD_ENA
0135 BC (d8)
0136 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0137 BC (c8)
0138 + DC 07-F0 MDP_XRT_AEC_RESET
0139 BC (d0)
0140 + DC 07-F0 MDP_XRT_ARS_DIS
0141 BC (d5)
0142 + DC 07-F0 MDP_XRT_FLD_RESET
0143 BC (da)
0144 + DC 07-F0 MDP_XRT_QT_PROG_SET
0145 BC (c4 09)
0146 + DC 07-F0 MDP_XRT_FL_PROG_SET
0147 BC (c5 03)
0148 . C. ----- Success Verify ? OK / NG ____
0149 C.
0150 C.
0151 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0152 C.
0153 +. DC 07-F0 MDP_XRT_MODE_OBSV
0154 BC (c2)
0155 +. TI 2013-08-29 12:07:02.0
0156 DC 07-F0 MDP_XRT_MODE_OBSV
0157 BC (c2)
0158 . C. ----- Success Verify ? OK / NG ____
0159 C.
0160 C. ***** XRT END *****
0161 C.
0162 . C. ***** MDP `úÃîï»ö%ÝðĒĀð¹ñēDCBC•x²è *****
0163 C. (%ã°îÿÓYĀYĒYÿYĒYĀYçYèè%¼σσ%Ā»Ûñ¹è)
0164 . S. DC-BC dcbc-402:DCBC
0165 (MDP_known_event)
0166 C.
0167 C.
0168 . C. ***** YDÿ¹•İ Daily±¿İÑñĒ´Øñ¹ñēDCBC•x²è *****
0169 . S. DC-BC dcbc-153:DCBC
0170 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0171 C.
0172 C.
0173 . C. ;ãLOSŸÁŸ$ŸĀŸ-¼Ā»Û;ã
0174 C.
0175 . C. ***** LOS *****
0176 C.

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*** OP Sequence for XRT ***

2013/08/29	12:17:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/29	12:17:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/08/29	12:18:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2013/08/29	12:18:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/08/29	12:18:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/08/29	12:18:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/08/29	12:18:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/08/29	12:18:24.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/29	12:18:26.0	XRT_QT_PROG_SET_429_OG [0x1ad]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2013/08/29	12:20:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2013/08/29	12:29:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/29	12:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/29	13:54:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/29	13:54:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/29	13:54:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/29	13:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/29	14:07:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/29	14:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/29	14:17:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/29	14:17:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/29	14:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/29	14:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/29	14:25:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/29	14:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/29	15:32:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/29	15:32:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/29	15:32:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/29	15:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/29	15:45:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/29	15:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/29	15:51:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/29	15:51:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/29	15:51:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/29	15:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/29	16:14:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/29	16:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/29	17:11:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/29	17:11:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/29	17:11:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/29	17:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/29	17:24:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/29	17:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/29	17:27:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/29	17:27:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/29	17:27:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/29	17:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/29	17:52:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/29	17:52:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/08/29	17:53:00.0	AOCS_OrE-point_Start_2_OG [0x098]			

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2013/08/29	17:53:16.0	XRT_FLD_DIS_434_OG [0x1b2]	AOCU_NM	5	02-76	00	00	00	00	00
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/08/29	17:55:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]		1	07-F0	c9				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/08/29	17:55:56.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/08/29	17:55:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]		2	07-F0	c4	12			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2013/08/29	17:56:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/29	18:08:00.0	AOCs_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	03	00	00	00	00
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/29	18:49:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/29	18:49:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/29	18:49:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/29	18:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/29	19:02:30.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c0				
2013/08/29	19:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/29	19:04:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/29	19:04:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/29	19:04:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/29	19:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/29	19:28:00.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c0				
2013/08/29	19:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/29	20:28:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/29	20:28:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/29	20:28:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/29	20:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/29	20:41:00.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c0				
2013/08/29	20:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/29	20:42:00.5	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/29	20:42:02.5	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/29	20:42:04.5	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/29	20:45:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/29	21:05:30.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c0				
2013/08/29	21:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/29	22:06:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/29	22:06:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/29	22:06:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/29	22:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/29	22:40:30.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c0				
2013/08/29	22:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/29	23:45:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/29	23:45:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/29	23:45:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/29	23:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/30	00:05:30.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c0				
2013/08/30	00:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/30	01:21:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/30	01:21:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2013/08/30	01:21:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/08/30	01:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/08/30	01:39:30.0	XRT_Custom_430_OG [0x1ae]		1	07-F0	c0				
2013/08/30	01:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/08/30	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/08/30	01:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]		1	07-F0	c1				

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2013/08/30	02:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
		AOCU_NM		5	02-76	00	2d	ca	47 8d
2013/08/30	02:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/08/30	02:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/08/30	02:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/08/30	02:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/08/30	02:00:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/30	02:00:26.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04		
2013/08/30	02:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2013/08/30	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/30	02:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/30	02:55:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/30	02:55:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/08/30	02:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/08/30	03:16:30.0	XRT_Custom_430_OG [0x1ae]							
2013/08/30	03:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/30	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/30	03:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
2013/08/30	04:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		AOCU_NM		5	02-76	00	00	00	00 00
2013/08/30	04:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/08/30	04:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/08/30	04:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/08/30	04:02:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12		
2013/08/30	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/30	04:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/30	04:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
2013/08/30	04:10:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
		AOCU_NM		5	02-76	01	00	00	00 00
2013/08/30	04:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/08/30	04:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/08/30	04:10:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/08/30	04:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/08/30	04:10:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/30	04:10:26.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09		
2013/08/30	04:12:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2013/08/30	04:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/30	04:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/30	04:25:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/08/30	04:25:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/08/30	04:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/08/30	04:54:00.0	XRT_Custom_430_OG [0x1ae]							
2013/08/30	04:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/08/30	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/08/30	05:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
2013/08/30	06:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
		AOCU_NM		5	02-76	02	00	00	00 00
2013/08/30	06:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/08/30	06:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/08/30	06:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/08/30	06:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			

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2013/08/30	06:00:24.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/30	06:00:26.0	XRT_QT_PROG_SET_429_OG [0x1ad]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2013/08/30	06:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2013/08/30	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/30	06:06:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/30	06:06:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/30	06:06:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/30	06:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/30	06:31:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/30	06:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/30	07:00:00.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/30	07:00:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/30	09:48:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/30	09:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/30	11:14:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/30	11:14:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/30	11:14:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/30	11:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/30	11:26:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/30	11:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/30	12:53:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/30	12:53:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/30	12:53:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/30	12:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/30	13:05:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/30	13:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/30	14:31:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/30	14:31:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/30	14:31:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/30	14:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/30	14:43:30.0	XRT_Custom_430_OG [0x1ae]			
2013/08/30	14:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/30	14:51:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/30	14:51:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/30	14:51:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/30	14:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/30	15:04:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/30	15:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/30	16:10:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/30	16:10:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/30	16:10:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/30	16:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/30	16:22:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/30	16:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/30	16:27:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/30	16:27:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/30	16:27:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/30	16:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/30	16:51:00.0	XRT_Custom_430_OG [0x1ae]			
2013/08/30	16:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/30	17:38:54.0	XRT_CTRL_MANU_402_OG [0x192]			

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2013/08/30	17:38:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_FOCUS_POSITION		4	07-F8	22 ff aa 00
2013/08/30	17:39:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2013/08/30	17:39:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9
2013/08/30	17:41:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/08/30	17:41:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/08/30	17:41:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2013/08/30	17:42:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/30	17:48:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/30	17:48:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/08/30	17:49:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	03 00 00 00 00
2013/08/30	17:49:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/30	17:49:02.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/30	17:49:04.5	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/30	17:49:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/08/30	17:49:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/08/30	17:49:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/08/30	17:49:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/08/30	17:49:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/30	17:49:26.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2013/08/30	17:51:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2013/08/30	17:52:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/30	18:00:30.0	XRT_Custom_430_OG [0x1ae]				
2013/08/30	18:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/30	18:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/30	18:04:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/30	18:04:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/30	18:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/30	18:27:30.0	XRT_Custom_430_OG [0x1ae]				
2013/08/30	18:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/30	19:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/30	19:27:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/30	19:27:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/30	19:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/30	19:39:00.0	XRT_Custom_430_OG [0x1ae]				
2013/08/30	19:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/30	19:41:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/30	19:41:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/30	19:41:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/30	19:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/30	20:04:30.0	XRT_Custom_430_OG [0x1ae]				
2013/08/30	20:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/30	21:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/30	21:06:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/08/30	21:06:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/08/30	21:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/08/30	21:17:00.0	XRT_Custom_430_OG [0x1ae]				
2013/08/30	21:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/08/30	21:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/08/30	21:18:32.0	XRT_FLD_RESET_415_OG [0x19f]				

2013/08/30	21:18:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/30	21:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/30	21:41:30.0	XRT_Custom_430_OG [0x1ae]					
2013/08/30	21:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/30	22:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/30	22:44:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/30	22:44:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/30	22:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/30	22:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/30	22:56:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/30	22:56:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/30	22:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/30	23:14:30.0	XRT_Custom_430_OG [0x1ae]					
2013/08/30	23:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/31	00:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/31	00:23:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/31	00:23:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/31	00:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/31	00:40:00.0	XRT_Custom_430_OG [0x1ae]					
2013/08/31	00:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/31	01:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/31	01:56:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/31	01:56:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/31	01:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/31	02:00:00.0	AOCs_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	03 00 00 00 00	
2013/08/31	02:16:00.0	XRT_Custom_430_OG [0x1ae]					
2013/08/31	02:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/31	03:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/31	03:27:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/31	03:27:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/31	03:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/31	03:53:00.0	XRT_Custom_430_OG [0x1ae]					
2013/08/31	03:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/31	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/31	03:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/08/31	04:00:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2013/08/31	04:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/08/31	04:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/08/31	04:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/08/31	04:02:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2013/08/31	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/31	04:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/31	04:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/08/31	04:10:00.0	AOCs_OrE-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	01 00 00 00 00	
2013/08/31	04:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/08/31	04:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/08/31	04:10:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/08/31	04:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/08/31	04:10:24.0	XRT_FLD_RESET_415_OG [0x19f]					

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2013/08/31	04:10:26.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/31	04:12:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09
2013/08/31	04:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2013/08/31	05:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/31	05:03:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/31	05:03:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/31	05:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/31	05:30:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/31	05:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2013/08/31	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/31	05:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/31	06:00:00.0	AOCs_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2013/08/31	06:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCs_OrE-point_Start_1_OG [0x097]				
2013/08/31	06:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	AOCU_NM	5	02-76	02	00 00 00 00
2013/08/31	06:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/08/31	06:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/08/31	06:00:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/08/31	06:00:26.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/08/31	06:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/31	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09
2013/08/31	06:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03
2013/08/31	06:43:32.0	XRT_FLD_RESET_415_OG [0x19f]	XRT_CTRL_AUTO_408_OG [0x198]				
2013/08/31	06:43:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/31	06:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/31	07:08:00.0	XRT_Custom_430_OG [0x1ae]	XRT_FLD_RESET_415_OG [0x19f]				
2013/08/31	07:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/08/31	08:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/08/31	08:23:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/08/31	08:23:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	XRT_Custom_430_OG [0x1ae]				
2013/08/31	08:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2013/08/31	08:46:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/08/31	08:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/31	10:00:00.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_FLD_RESET_415_OG [0x19f]				
2013/08/31	10:00:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/08/31	10:38:00.0	AOCs_OrE-point_Start_2_OG [0x098]	MDP_XRT_FLD_RESET	1	07-F0	da	
			AOCU_NM	5	02-76	00	00 00 00 00