

# XRT Timeline to be uploaded on 2013/09/03

Period: 2013/09/03 10:40:00 - 2013/09/07 09:51:00

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Normal mode

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## XOB #19B2: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band

Term	Pointing (x, y)	Comment
09/03 10:53:00 - 09/03 18:39:54	Track ( 240.0, 73.1) @ 09/03 10:50:00	# OP start + 10min HOP236 - AR11836(untill 12:30)
09/04 06:42:30 - 09/04 09:34:54	Track ( 407.4, 80.8) @ 09/04 06:39:30	# HOP236 - AR11836 plage with IRIS-SST
09/04 15:38:00 - 09/04 17:52:30	Track ( 492.6, 55.4) @ 09/04 15:35:00	# HOP236 - AR11836 sunspot
09/04 18:28:30 - 09/05 06:01:24	Track ( 514.4, 57.1) @ 09/04 18:25:30	# AR11836 sunspot
09/05 06:14:30 - 09/05 09:52:00	Track ( 586.4, 94.9) @ 09/05 06:11:30	# HOP236 - AR11836 plage
<b>PROG= 15 Inf.-time(s)</b>		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 8 2-time(s) 2.0sec		
Open/G-band	Open/G-band close	Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
└─ Subr= 2 2-time(s) 2.0sec		
└─ Seqn= 24 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Seqn= 98 4-time(s) 2.0sec		
Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/Ti-poly	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open close	Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Al close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Seqn= 21 25-time(s) 120.0sec		
thin-Be/Open	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Al-poly/Open	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
Al-poly/Open	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
thin-Be/Open	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
Al-poly/Open	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
thin-Be/Open	Open/thick-Be close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec
Al-poly/Open	Open/thick-Al close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #19BD: Synoptic Q95 2x2 - Al/mesh(12/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(33/362/1443) + Thin

Term	Pointing (x, y)	Comment
09/03 18:43:00 - 09/03 18:52:30	Fixed ( 0.0, 0.0)	synoptic, shifted manually
09/04 06:32:30 - 09/04 06:39:24	Fixed ( 0.0, 0.0)	synoptic, shifted 29.5 min
09/04 18:18:30 - 09/04 18:25:24	Fixed ( 0.0, 0.0)	synoptic, shifted 15.5 min
09/05 06:04:30 - 09/05 06:11:24	Fixed ( 0.0, 0.0)	synoptic, shifted 1.5 min
<b>PROG= 18 1-time(s)</b>		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 82 1-time(s) 4.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 52 1-time(s) 4.0sec		
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Al-poly/Open	Al-poly/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 99 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 66 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #199F: HOP130-Synoptic 3 Filter- 2x2 Q95 (Almesh 12/128/723ms; Tipoly 24/362/1443ms; Althick 65536ms) with G-band

Term	Pointing (x, y)	Comment
09/03 19:57:00 - 09/03 20:30:00	Fixed ( 0.0, 0.0)	3/15
<b>PROG= 10 2-time(s)</b>		
└─ Subr= 1 1-time(s) 180.0sec		
└─ Seqn= 85 1-time(s) 25.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 86 1-time(s) 25.0sec		

	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	<b>Seqn= 75</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	<b>Seqn= 66</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #19B0: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band**

Term	Pointing (x, y)		Comment										
09/04 00:33:00 - 09/04 06:29:24	Track ( 370.9, 47.6) @ 09/04 00:30:00		# AR11836 sunspot										
<b>PROG= 12 Inf.-time(s)</b>													
	<b>Subr= 1</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	<b>Seqn= 8</b>		<b>2-time(s)</b>			<b>2.0sec</b>							
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
	<b>Subr= 2</b>		<b>2-time(s)</b>			<b>2.0sec</b>							
	<b>Seqn= 24</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	<b>Seqn= 98</b>		<b>4-time(s)</b>			<b>2.0sec</b>							
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	<b>Seqn= 21</b>		<b>45-time(s)</b>			<b>60.0sec</b>							
	thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
	Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #196F: HOP81/206 2-filter - Ti/poly 4s, Al/mesh 4s 60s cadence, G-band - 384x384 45ms**

Term	Pointing (x, y)		Comment										
09/04 09:38:00 - 09/04 15:34:54	Fixed ( -14.0, 876.0)		# HOP206 - N-pole										
<b>PROG= 08 Inf.-time(s)</b>													
	<b>Subr= 1</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	<b>Seqn= 13</b>		<b>2-time(s)</b>			<b>2.0sec</b>							
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
	<b>Subr= 2</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	<b>Seqn= 30</b>		<b>1-time(s)</b>			<b>30.0sec</b>							
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	<b>Subr= 3</b>		<b>30-time(s)</b>			<b>2.0sec</b>							
	<b>Seqn= 25</b>		<b>1-time(s)</b>			<b>60.0sec</b>							
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Flare mode**

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**XOB #1980: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops-2**

Term	Pointing (x, y)		Comment										
09/03 10:53:00 - 09/03 18:39:54	Track ( 240.0, 73.1) @ 09/03 10:50:00		# OP start + 10min HOP236 - AR11836(until 12:30)										
09/04 00:33:00 - 09/04 06:29:24	Track ( 370.9, 47.6) @ 09/04 00:30:00		# AR11836 sunspot										
09/04 06:42:30 - 09/04 09:34:54	Track ( 407.4, 80.8) @ 09/04 06:39:30		# HOP236 - AR11836 plage with IRIS-SST										
09/04 09:38:00 - 09/04 15:34:54	Fixed ( -14.0, 876.0)		# HOP206 - N-pole										
09/04 15:38:00 - 09/04 17:52:30	Track ( 492.6, 55.4) @ 09/04 15:35:00		# HOP236 - AR11836 sunspot										
09/04 18:28:30 - 09/05 06:01:24	Track ( 514.4, 57.1) @ 09/04 18:25:30		# AR11836 sunspot										
09/05 06:14:30 - 09/05 09:52:00	Track ( 586.4, 94.9) @ 09/05 06:11:30		# HOP236 - AR11836 plage										
<b>PROG= 03 15-time(s)</b>													
	<b>Subr= 1</b>		<b>45-time(s)</b>			<b>10.0sec</b>							
	<b>Seqn= 9</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	<b>Subr= 2</b>		<b>1-time(s)</b>			<b>10.0sec</b>							
	<b>Seqn= 10</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	<b>Seqn= 11</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
	<b>Seqn= 15</b>		<b>1-time(s)</b>			<b>2.0sec</b>							
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

L Open/thick-AI Open/thick-AI close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec  
 Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Active Region Search**

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NOT USED

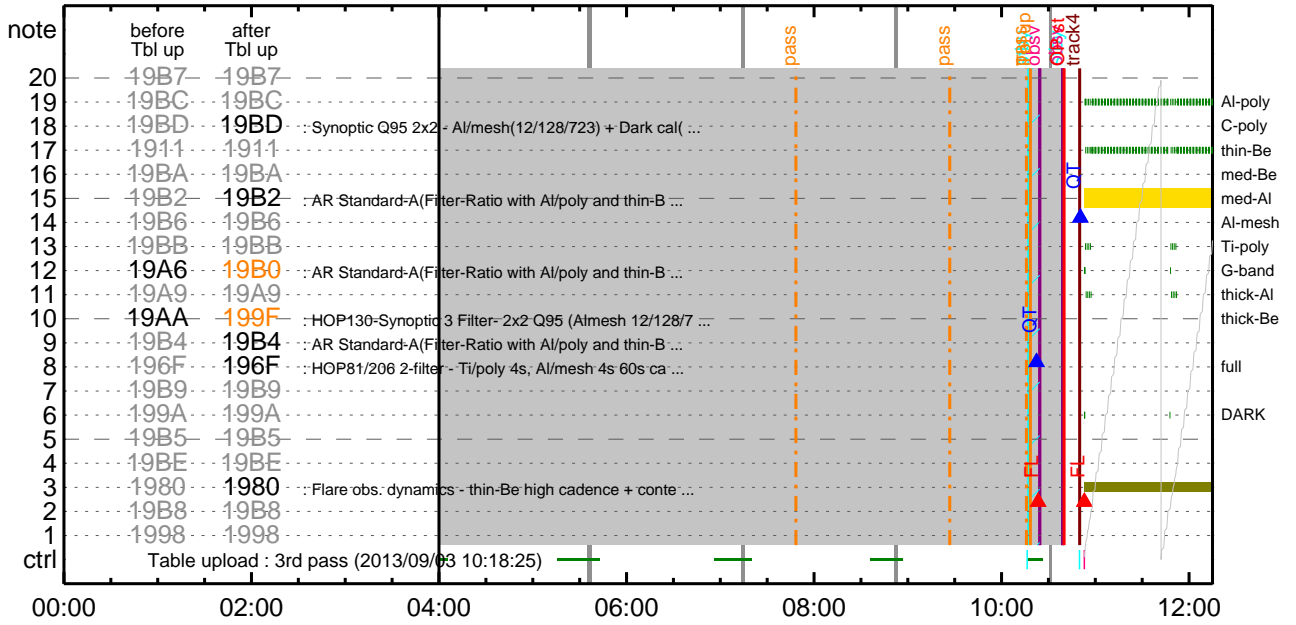
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**Flare Detection**

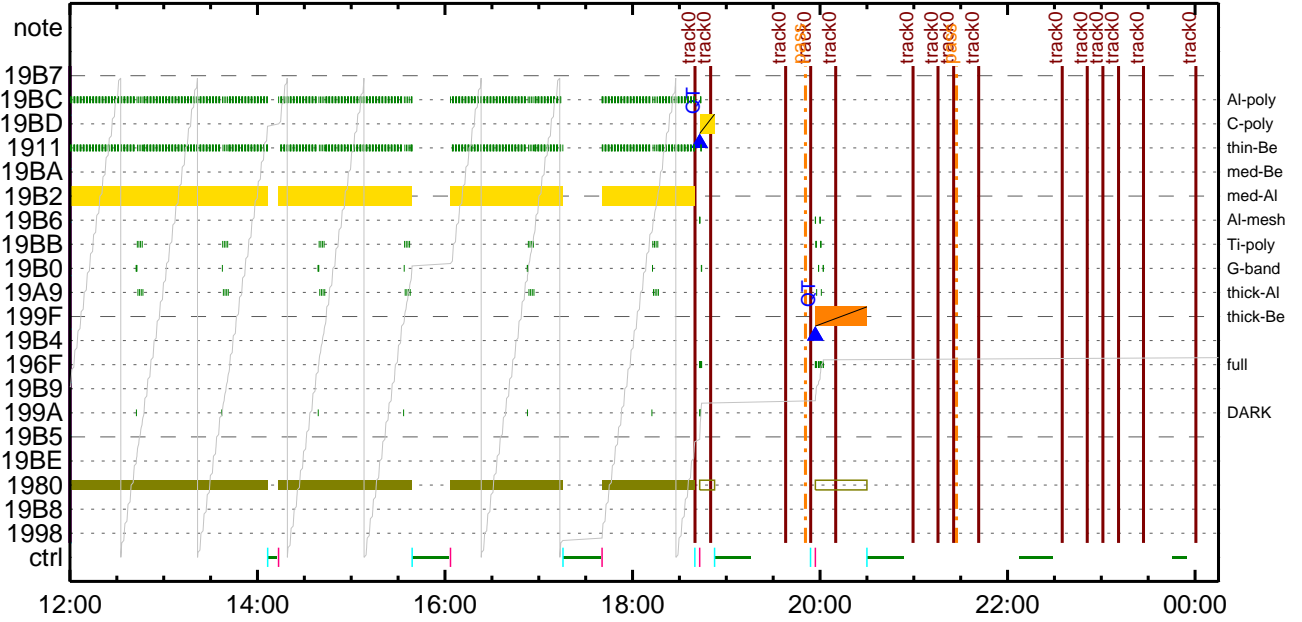
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<b>FLD Patrol</b>										
Term	Pointing (x, y)		Comment							
09/04 00:30:16 - 09/04 06:29:46	Track ( 370.9,	47.6)	<small>@ 09/04 00:30:00</small>	# AR11836 sunspot						
09/04 06:39:46 - 09/04 18:15:46	Track ( 407.4,	80.8)	<small>@ 09/04 06:39:30</small>	# HOP236 - AR11836 plage with IRIS-SST						
09/04 18:25:46 - 09/05 06:01:46	Track ( 514.4,	57.1)	<small>@ 09/04 18:25:30</small>	# AR11836 sunspot						
09/05 06:11:46 - 09/07 09:51:00	Track ( 586.4,	94.9)	<small>@ 09/05 06:11:30</small>	# HOP236 - AR11836 plage						
Open/Ti-poly	Open/thick-AI	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

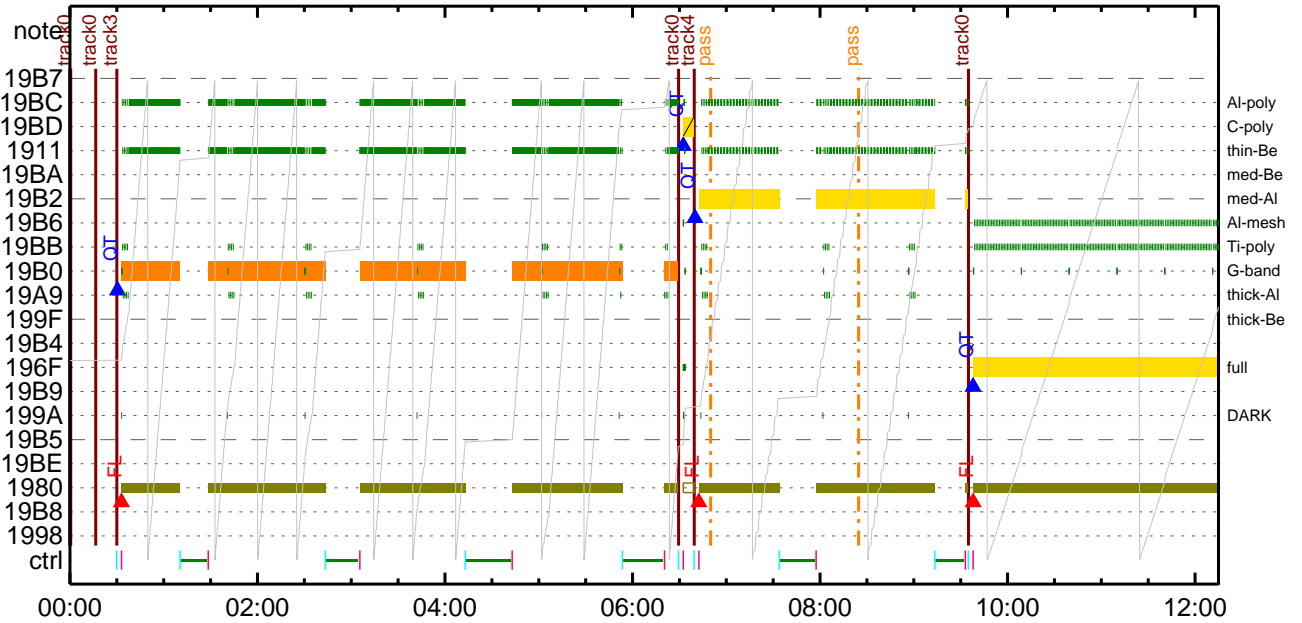
### CMDI #0716 2013/09/03



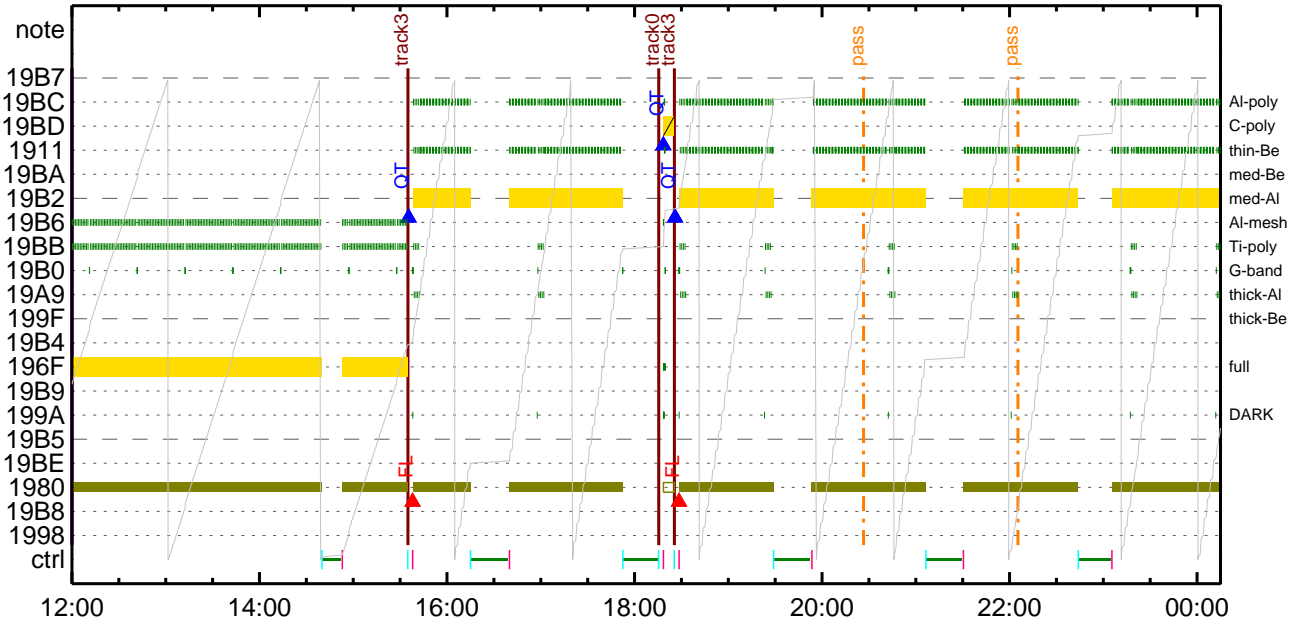
### CMDI #0716 2013/09/03



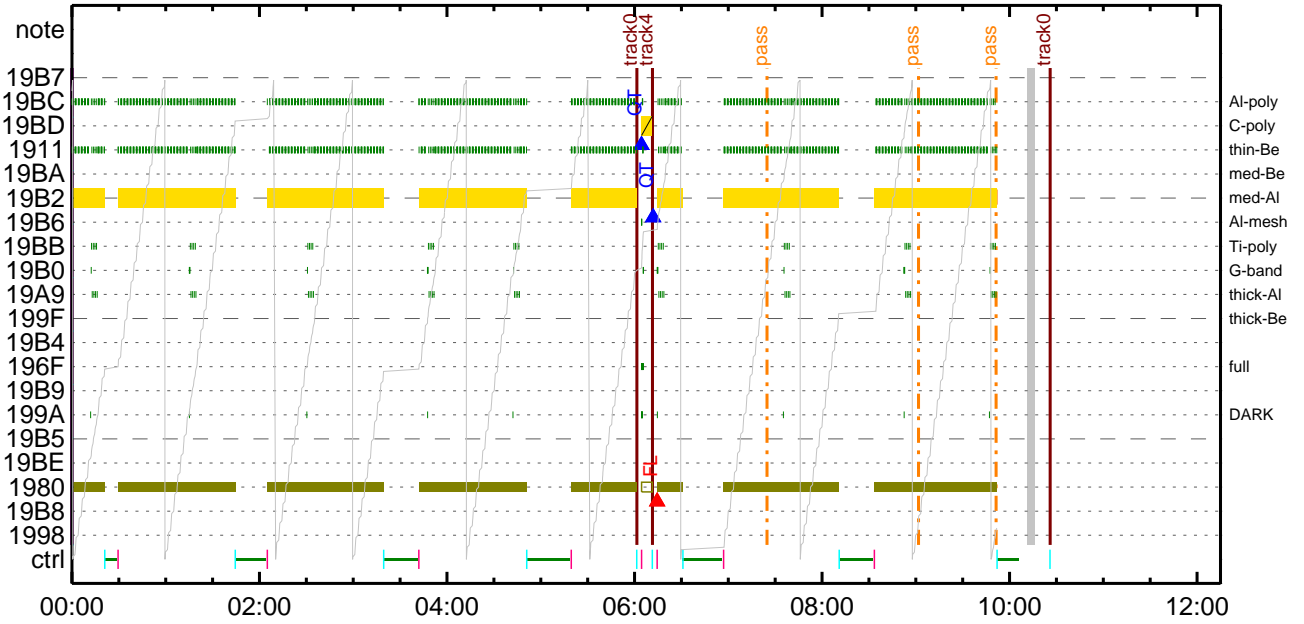
### CMDI #0716 2013/09/04



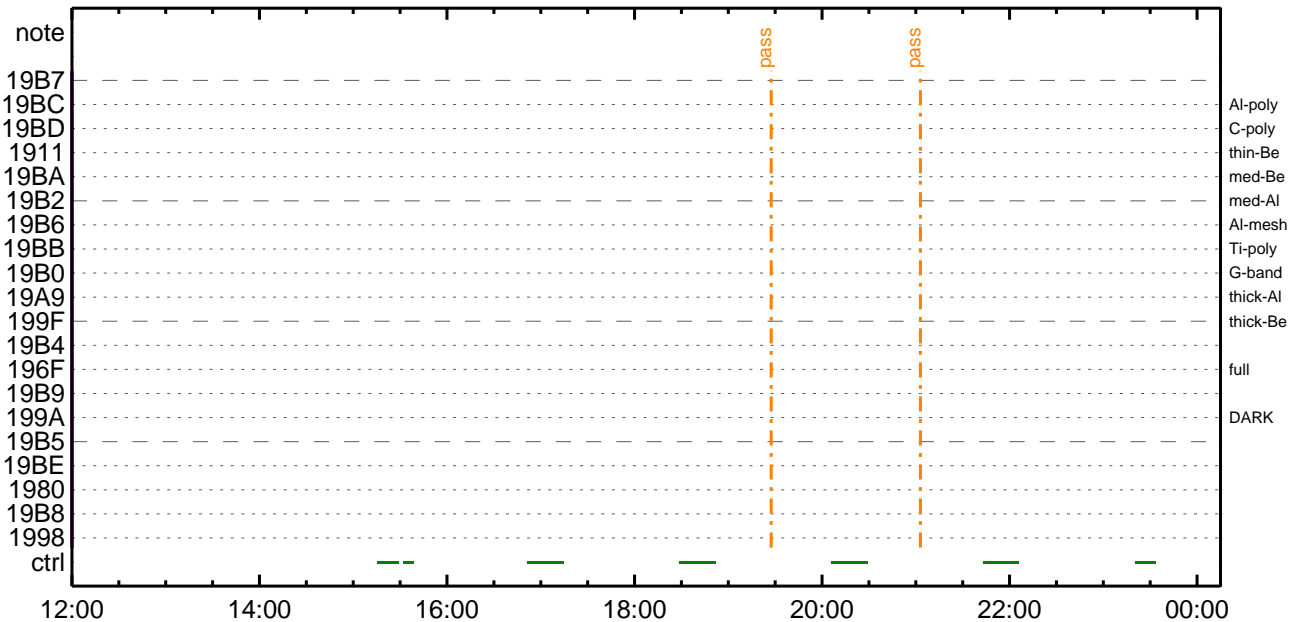
CMDI #0716 2013/09/04



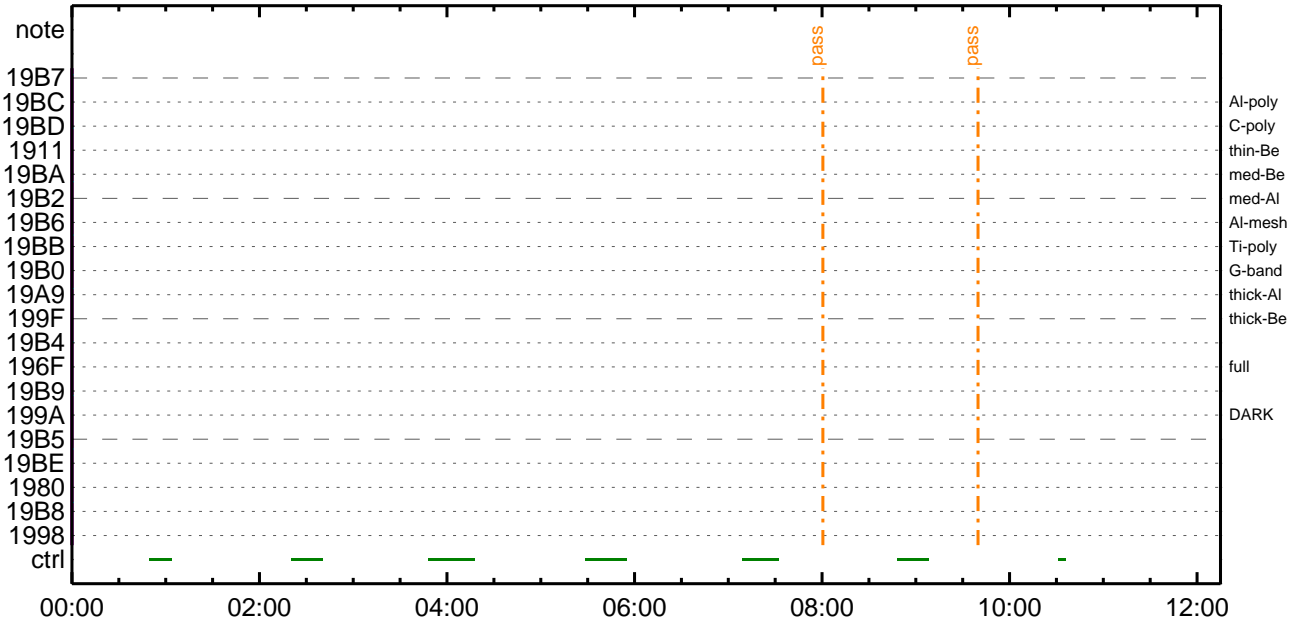
CMDI #0716 2013/09/05



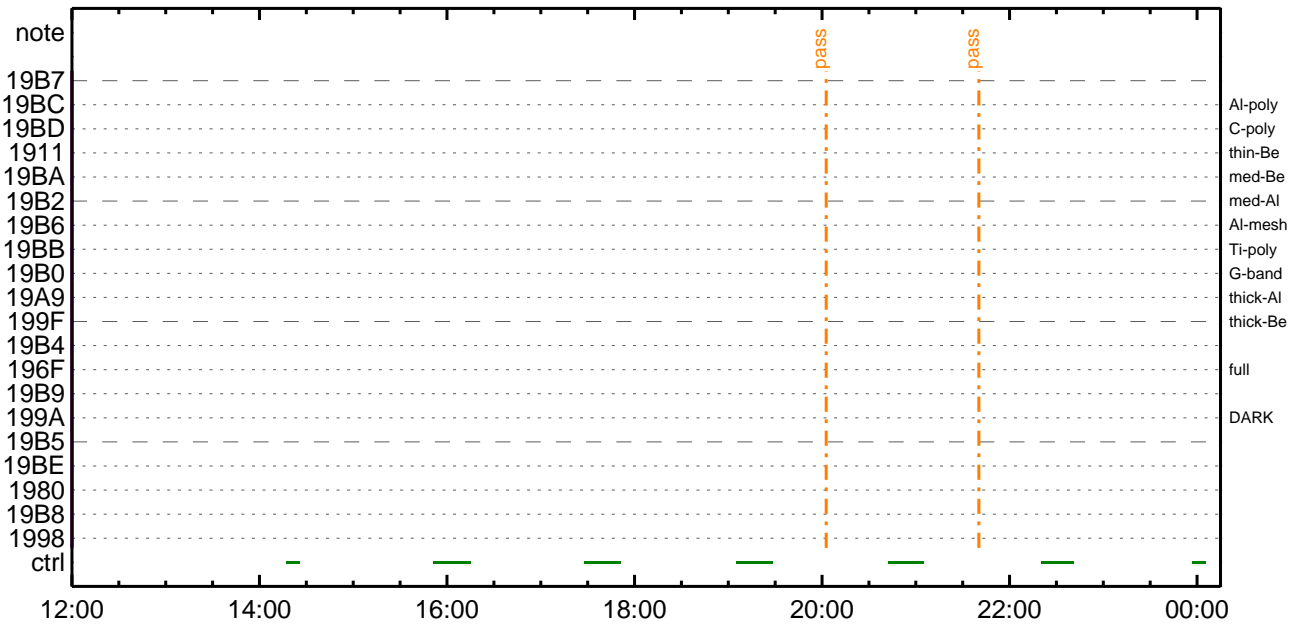
CMDI #0716 2013/09/05



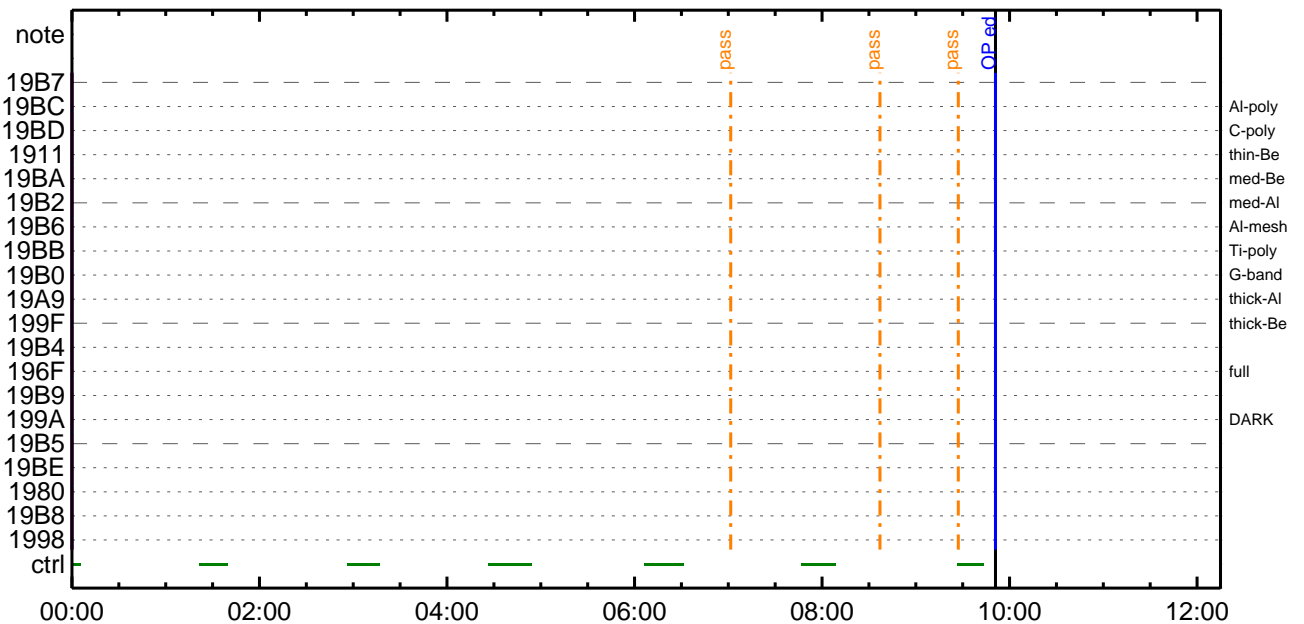
CMDI #0716 2013/09/06



CMDI #0716 2013/09/06



CMDI #0716 2013/09/07









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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-880:OP
0104 ( )
0105 S. OG og-880:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYôYx½ªî»ð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOK²³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYôYx½ªî»ð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOK²³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYôYx½ªî»ð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOK²³îÇ§
0165 C.
0166 C. ***** °E²¼²î½E¹ç•è²îOK²³îÇ§ *****
0167 C. DHUYâ;4YE;E½Y½, Y;½YE;E²ðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷ç@NG²î½E¹ç•è²îOK²³îÇ§
0180 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0181 C.
0182 C. TIY³Y½YôYx½ªî»ð³îÇ§ (UT)
0183 +. TI 2013-09-03 10:35:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-09-03 10:35:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-09-03 10:35:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0096 C.
0097 C. XRT Obs. Table Upload
0098 . S. RAM ram-291:MDP_OBS_X
0099 ( )
0100 C.
0101 +. DC 07-F0 MDP_DUMP_XRTTBL
0102 BC (84 07 00 00 00 3a d4)
0103 . C. ----- Comparison Check ? OK / ERR ____
0104 C.
0105 C.
0106 +. DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 01 b1 b1 04 04)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 02 b1 b1 08 08)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 03 b1 b1 08 08)
0112 + DC 07-F0 MDP_XRT_ROI_SET
0113 BC (cd 04 b1 b1 06 06)
0114 + DC 07-F0 MDP_XRT_ROI_SET
0115 BC (cd 05 85 83 06 06)
0116 + DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 06 85 83 06 06)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 07 85 83 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 08 80 80 20 20)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 09 80 80 20 08)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 0a 80 80 08 20)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 0f 80 80 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 10 80 80 08 08)
0130 + DC 07-F0 MDP_XRT_FLD_ENA
0131 BC (d8)
0132 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0133 BC (c8)
0134 + DC 07-F0 MDP_XRT_AEC_RESET
0135 BC (d0)
0136 + DC 07-F0 MDP_XRT_ARS_DIS
0137 BC (d5)
0138 + DC 07-F0 MDP_XRT_FLD_RESET
0139 BC (da)
0140 + DC 07-F0 MDP_XRT_QT_PROG_SET
0141 BC (c4 09)
0142 + DC 07-F0 MDP_XRT_FL_PROG_SET
0143 BC (c5 03)
0144 . C. ----- Success Verify ? OK / NG ____
0145 C.
0146 C.
0147 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0148 C.
0149 +. DC 07-F0 MDP_XRT_MODE_OBSV
0150 BC (c2)
0151 +. TI 2013-09-03 10:39:02.0
0152 DC 07-F0 MDP_XRT_MODE_OBSV
0153 BC (c2)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C. ***** XRT END *****
0157 C.
0158 . C. ***** MDP (ũãîñî»ö¼ÿñÊÄðñ¹ñèDCBC•x²è *****
0159 C. (¼ã°îÿÓÿÄÿËÿÏÿËÿÄÿÇÿËÿÈÿ¼ñ¼Ä»Ûñ¹ñè)
0160 . S. DC-BC dcbc-402:DCBC
0161 (MDP_known_event)
0162 C.
0163 C.
0164 . C. ***** ÿDÿ¹•Ï Daily±¿ÎññÉ´Øñ¹ñèDCBC•x²è *****
0165 . S. DC-BC dcbc-153:DCBC
0166 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0167 C.
0168 C.
0169 . C. ;ãLOSÿÁÿSÿÄÿÿ-¼Ä»Û;ã
0170 C.
0171 . C. ***** LOS *****
0172 C.

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\*\*\* OP Sequence for XRT \*\*\*

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2013/09/03 10:49:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2013/09/03 10:49:56.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2013/09/03 10:50:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 04 00 00 00 00
2013/09/03 10:50:16.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2013/09/03 10:50:18.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2013/09/03 10:50:20.0 XRT_AEC_RESET_413_OG [0x19d]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2013/09/03 10:50:22.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2013/09/03 10:50:24.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2013/09/03 10:50:26.0 XRT_QT_PROG_SET_444_OG [0x1bc]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0f
2013/09/03 10:52:58.0 XRT_FL_PROG_SET_428_OG [0x1ac]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 03
2013/09/03 10:53:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2013/09/03 14:06:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2013/09/03 14:06:32.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2013/09/03 14:06:34.0 XRT_PREFLR_STRT_418_OG [0x1a2]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2013/09/03 14:09:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2013/09/03 14:12:30.0 XRT_Custom_430_OG [0x1ae]
2013/09/03 14:13:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2013/09/03 15:39:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2013/09/03 15:39:02.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2013/09/03 15:39:04.0 XRT_PREFLR_STRT_418_OG [0x1a2]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2013/09/03 15:42:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2013/09/03 16:02:30.0 XRT_Custom_430_OG [0x1ae]
2013/09/03 16:03:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2013/09/03 17:15:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2013/09/03 17:15:32.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2013/09/03 17:15:34.0 XRT_PREFLR_STRT_418_OG [0x1a2]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2013/09/03 17:18:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2013/09/03 17:39:30.0 XRT_Custom_430_OG [0x1ae]
2013/09/03 17:40:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2013/09/03 18:39:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2013/09/03 18:39:56.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2013/09/03 18:40:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 00 00 00 00
2013/09/03 18:40:16.0 XRT_FLD_DIS_434_OG [0x1b2]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2013/09/03 18:42:54.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2013/09/03 18:42:56.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2013/09/03 18:42:58.0 XRT_QT_PROG_SET_447_OG [0x1bf]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 12
2013/09/03 18:43:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2013/09/03 18:50:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 00 00 00 ac cd
2013/09/03 18:52:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2013/09/03 18:52:32.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2013/09/03 18:52:34.0 XRT_PREFLR_STRT_418_OG [0x1a2]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2013/09/03 18:55:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2013/09/03 19:38:00.0 AOCs_OrE-point_Start_4_OG [0x09a]
                        AOCU_NM 5 02-76 00 00 00 d6 67
2013/09/03 19:53:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2013/09/03 19:53:56.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2013/09/03 19:54:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 00 00 00 00 00
2013/09/03 19:54:16.0 XRT_FLD_DIS_434_OG [0x1b2]
                        MDP_XRT_FLD_DIS 1 07-F0 d9

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2013/09/03	19:56:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/09/03	19:56:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/09/03	19:56:58.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a		
2013/09/03	19:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/03	20:10:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	00	00	29 99
2013/09/03	20:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/03	20:30:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/09/03	20:30:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/09/03	20:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/09/03	20:59:30.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00	00	00	53 33
2013/09/03	21:15:30.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00	d6	36	b7 8e
2013/09/03	21:25:30.0	AOCS_Ore-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00	b4	b5	db 75
2013/09/03	21:41:30.0	AOCS_Ore-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00	ac	5b	00 00
2013/09/03	22:35:00.0	AOCS_Ore-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00	b4	b5	24 8b
2013/09/03	22:51:00.0	AOCS_Ore-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	00	d6	36	48 72
2013/09/03	23:01:00.0	AOCS_Ore-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00	29	ca	b7 8e
2013/09/03	23:11:00.0	AOCS_Ore-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00	4b	4b	db 75
2013/09/03	23:27:00.0	AOCS_Ore-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00	53	a5	00 00
2013/09/04	00:00:30.0	AOCS_Ore-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00	4b	4b	24 8b
2013/09/04	00:16:30.0	AOCS_Ore-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	00	29	db	48 72
2013/09/04	00:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/04	00:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2013/09/04	00:30:00.0	AOCS_Ore-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	03	00	00	00 00
2013/09/04	00:30:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/09/04	00:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/09/04	00:30:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/09/04	00:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/09/04	00:30:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/09/04	00:30:26.0	XRT_QT_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2013/09/04	00:32:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2013/09/04	00:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/04	01:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/04	01:10:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/09/04	01:10:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/09/04	01:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/09/04	01:27:30.0	XRT_Custom_430_OG [0x1ae]							
2013/09/04	01:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/04	02:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/04	02:43:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/09/04	02:43:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/09/04	02:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/09/04	03:04:30.0	XRT_Custom_430_OG [0x1ae]							
2013/09/04	03:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/04	04:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/04	04:13:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/09/04	04:13:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/09/04	04:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/09/04	04:42:00.0	XRT_Custom_430_OG [0x1ae]							



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2013/09/04	04:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/09/04	05:53:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/04	05:53:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/09/04	05:53:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/09/04	05:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/09/04	06:19:30.0	XRT_Custom_430_OG [0x1ae]							
2013/09/04	06:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/09/04	06:29:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/04	06:29:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/09/04	06:29:30.5	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/09/04	06:29:46.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/09/04	06:32:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/09/04	06:32:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/09/04	06:32:28.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2013/09/04	06:32:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/09/04	06:39:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/04	06:39:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/09/04	06:39:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2013/09/04	06:39:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/09/04	06:39:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/09/04	06:39:50.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/09/04	06:39:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/09/04	06:39:54.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/09/04	06:39:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2013/09/04	06:42:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2013/09/04	06:42:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/09/04	07:34:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/04	07:34:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/09/04	07:34:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/09/04	07:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/09/04	07:56:30.0	XRT_Custom_430_OG [0x1ae]							
2013/09/04	07:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/09/04	09:13:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/04	09:13:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/09/04	09:13:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/09/04	09:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/09/04	09:32:00.0	XRT_Custom_430_OG [0x1ae]							
2013/09/04	09:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/09/04	09:34:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/04	09:34:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/09/04	09:35:00.0	AOCS_Ore-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	00 b2 25 01 3f				
2013/09/04	09:35:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/09/04	09:35:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/09/04	09:35:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/09/04	09:35:22.0	XRT_ARS_DIS_417_OG [0x1a1]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/09/04	09:37:54.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/09/04	09:37:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2013/09/04	09:37:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				



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2013/09/04	19:29:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/04	19:29:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/04	19:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/04	19:52:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/04	19:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/04	21:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/04	21:06:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/04	21:06:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/04	21:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/04	21:29:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/04	21:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/04	22:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/04	22:44:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/04	22:44:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/04	22:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/04	23:04:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/04	23:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/05	00:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/05	00:21:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/05	00:21:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/05	00:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/05	00:28:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/05	00:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/05	01:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/05	01:44:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/05	01:44:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/05	01:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/05	02:04:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/05	02:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/05	03:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/05	03:19:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/05	03:19:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/05	03:19:34.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/05	03:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/05	03:41:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/05	03:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/05	04:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/05	04:51:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/05	04:51:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/05	04:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/05	05:18:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/05	05:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/05	06:01:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/05	06:01:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/05	06:01:30.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/09/05	06:01:46.0	XRT_FLD_DIS_434_OG [0x1b2]	AOCU_NM	5	02-76	00 00 00 00 00
2013/09/05	06:04:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2013/09/05	06:04:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/09/05	06:04:28.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/09/05	06:04:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2013/09/05	06:11:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2013/09/05	06:11:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/09/05	06:11:30.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00
2013/09/05	06:11:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/09/05	06:11:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/09/05	06:11:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/09/05	06:11:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/09/05	06:11:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/05	06:11:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2013/09/05	06:14:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2013/09/05	06:14:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/05	06:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/05	06:31:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/05	06:31:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/05	06:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/05	06:56:00.0	XRT_Custom_430_OG [0x1ae]				
2013/09/05	06:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/05	08:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/05	08:11:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/05	08:11:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/05	08:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/05	08:32:30.0	XRT_Custom_430_OG [0x1ae]				
2013/09/05	08:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/05	09:52:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/05	09:52:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/05	09:52:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/05	09:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/05	10:25:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/05	10:25:56.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/05	10:26:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00