

XRT Timeline to be uploaded on 2013/09/21

Period: 2013/09/21 10:33:00 - 2013/09/26 10:36:00

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Normal mode

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XOB #19CB: Quiet Sun - 2-filter - Al/poly 6s, Al/mesh 4s - AEC 2 - 240s cadence, G-band - 384x384 45ms												
Term	Pointing (x, y)		Comment									
09/21 10:46:00 - 09/21 11:59:54	Track (376.1, 274.7)	^{09/21 10:43:00}	# OP start + 10min, HOP 236 (IRIS coordination) until 12UT.									
09/22 07:34:00 - 09/22 10:44:54	Track (524.3, 285.9)	^{09/22 07:30:00}	# HOP 236 (IRIS coordination)									
09/23 08:10:30 - 09/23 11:59:54	Track (7.7, 0.0)	^{09/23 08:00:00}	# HOP 236 (IRIS coordination)									
09/24 08:03:00 - 09/24 10:05:30	Fixed (0.0, 0.0)		# HOP 236 (IRIS coordination)									
PROG= 20 Inf.-time(s)												
Subr= 1	1-time(s)	2.0sec										
└─ Seqn= 68	2-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
└─ Seqn= 30	1-time(s)	30.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Subr= 3	15-time(s)	2.0sec										
└─ Seqn= 70	1-time(s)	240.0sec										
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	2	0	2.0sec
└─ Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19CC: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band												
Term	Pointing (x, y)		Comment									
09/21 12:03:00 - 09/21 17:59:54	Track (647.9, -91.4)	^{09/21 12:00:00}	# AR 11843 observations (with IRIS)									
09/21 22:13:00 - 09/22 06:05:24	Fixed (-922.0, -248.0)		# Returning AR on South East Limb for EIS.									
09/22 06:18:30 - 09/22 07:09:30	Track (-712.7, 27.1)	^{09/22 06:15:30}	# AR on North East limb.									
09/22 17:03:00 - 09/22 17:28:30	Track (-643.5, 18.6)	^{09/22 17:00:00}	# Return to new East limb AR.									
09/22 18:07:00 - 09/23 05:38:54	Track (-636.2, 17.8)	^{09/22 18:04:00}	# AR obs.									
09/23 05:52:00 - 09/23 07:47:00	Track (-551.8, 9.8)	^{09/23 05:49:00}	# AR obs.									
09/23 12:03:00 - 09/23 17:41:24	Track (-504.4, 6.0)	^{09/23 12:00:00}	# AR obs.									
09/23 17:54:30 - 09/24 05:59:54	Track (-457.8, 2.8)	^{09/23 17:51:30}	# AR obs.									
09/24 06:13:00 - 09/24 07:59:54	Track (-355.1, -2.9)	^{09/24 06:10:00}	# AR obs.									
PROG= 06 Inf.-time(s)												
Subr= 1	1-time(s)	2.0sec										
└─ Seqn= 8	2-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2	2-time(s)	2.0sec										
└─ Seqn= 24	1-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 98	4-time(s)	2.0sec										
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 21	11-time(s)	270.0sec										
└─ thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─ Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─ thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
└─ Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19CD: Synoptic Q95 2x2 - Ti/Poly(33/512/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(33/512/2048) + Thin-Be												
Term	Pointing (x, y)		Comment									
09/21 18:03:00 - 09/21 18:09:54	Fixed (0.0, 0.0)		synoptic									
09/22 06:08:30 - 09/22 06:15:24	Fixed (0.0, 0.0)		synoptic, shifted 5.5 min									
09/22 17:57:00 - 09/22 18:03:54	Fixed (0.0, 0.0)		synoptic, shifted -6.0 min									
09/23 05:46:00 - 09/23 05:48:54	Fixed (0.0, 0.0)		synoptic, shifted -21.0 min									
09/23 17:44:30 - 09/23 17:51:24	Fixed (0.0, 0.0)		synoptic, shifted -18.5 min									
09/24 06:03:00 - 09/24 06:09:54	Fixed (0.0, 0.0)		synoptic									
PROG= 09 1-time(s)												
Subr= 1	1-time(s)	12.0sec										
└─ Seqn= 74	1-time(s)	4.0sec										
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5	1-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 76 1-time(s) 4.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 55 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 66 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19A9: G-Band Alignment with North Pole Q90 2x2(G-band only) - 8msec - 5min cadence - Partial Sun-wNGT-2												
Term		Pointing (x, y)						Comment				
09/21 18:25:00 - 09/21 20:09:54		Fixed (0.0, 945.0)						# Coalignment at North Pole				
PROG= 11 1-time(s)												
└─ Subr= 1 1-time(s) 360.0sec												
└─ Seqn= 93 24-time(s) 300.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19AA: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8msec - 8 min cadence-wNGT-2												
Term		Pointing (x, y)						Comment				
09/21 20:25:00 - 09/21 22:09:54		Fixed (-945.0, 0.0)						# Coalignment at East Limb				
PROG= 13 1-time(s)												
└─ Subr= 1 1-time(s) 360.0sec												
└─ Seqn= 94 15-time(s) 480.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19C3: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 45ms-2												
Term		Pointing (x, y)						Comment				
09/22 10:48:00 - 09/22 16:59:54		Fixed (-14.0, 876.0)						# HOP 206 (Polar Panorama Map)				
PROG= 01 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 13 2-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 30 1-time(s) 30.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└─ Subr= 3 30-time(s) 2.0sec												
└─ Seqn= 57 1-time(s) 60.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * * **Flare mode** * * * * *

XOB #19C8: Flare obs. dynamics - thin-Be high cadence long/short pairs + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loc												
Term		Pointing (x, y)						Comment				
09/21 10:46:00 - 09/21 11:59:54		Track (376.1, 274.7) @ 09/21 10:43:00						# OP start + 10min, HOP 236 (IRIS coordination) until 12UT.				
09/22 07:34:00 - 09/22 10:44:54		Track (524.3, 285.9) @ 09/22 07:30:00						# HOP 236 (IRIS coordination)				
09/22 10:48:00 - 09/22 16:59:54		Fixed (-14.0, 876.0)						# HOP 206 (Polar Panorama Map)				
09/23 08:10:30 - 09/23 11:59:54		Track (7.7, 0.0) @ 09/23 08:00:00						# HOP 236 (IRIS coordination)				
09/24 08:03:00 - 09/24 10:05:30		Fixed (0.0, 0.0)						# HOP 236 (IRIS coordination)				
PROG= 12 15-time(s)												
└─ Subr= 1 45-time(s) 2.0sec												
└─ Seqn= 69 1-time(s) 8.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Seqn= 15 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1980: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops-2

Term	Pointing (x, y)	Comment
09/21 12:03:00 - 09/21 17:59:54	Track (647.9, -91.4) @ 09/21 12:00:00	# AR 11843 observations (with IRIS)
09/21 22:13:00 - 09/22 06:05:24	Fixed (-922.0, -248.0)	# Returning AR on South East Limb for EIS.
09/22 06:18:30 - 09/22 07:09:30	Track (-712.7, 27.1) @ 09/22 06:15:30	# AR on North East limb.
09/22 17:03:00 - 09/22 17:28:30	Track (-643.5, 18.6) @ 09/22 17:00:00	# Return to new East limb AR.
09/22 18:07:00 - 09/23 05:38:54	Track (-636.2, 17.8) @ 09/22 18:04:00	# AR obs.
09/23 05:52:00 - 09/23 07:47:00	Track (-551.8, 9.8) @ 09/23 05:49:00	# AR obs.
09/23 12:03:00 - 09/23 17:41:24	Track (-504.4, 6.0) @ 09/23 12:00:00	# AR obs.
09/23 17:54:30 - 09/24 05:59:54	Track (-457.8, 2.8) @ 09/23 17:51:30	# AR obs.
09/24 06:13:00 - 09/24 07:59:54	Track (-355.1, -2.9) @ 09/24 06:10:00	# AR obs.

PROG= 03 15-time(s)

Subr= 1 45-time(s) 10.0sec																				
└ Seqn= 9 1-time(s) 2.0sec																				
└ thin-Be/Open med-Be/Open close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec										
Subr= 2 1-time(s) 10.0sec																				
└ Seqn= 10 1-time(s) 2.0sec																				
└ med-Al/Open med-Al/thick-Al close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec										
└ Open/thick-Be Open/thick-Be close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec										
└ Seqn= 11 1-time(s) 2.0sec																				
└ Al-poly/Open Al-poly/thick-Al close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec										
└ Seqn= 15 1-time(s) 2.0sec																				
└ Open/G-band Open/G-band open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec										
└ Open/thick-Al Open/thick-Al close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec										
└ Open/thick-Al Open/thick-Al close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec										
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval										

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Active Region Search

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NOT USED

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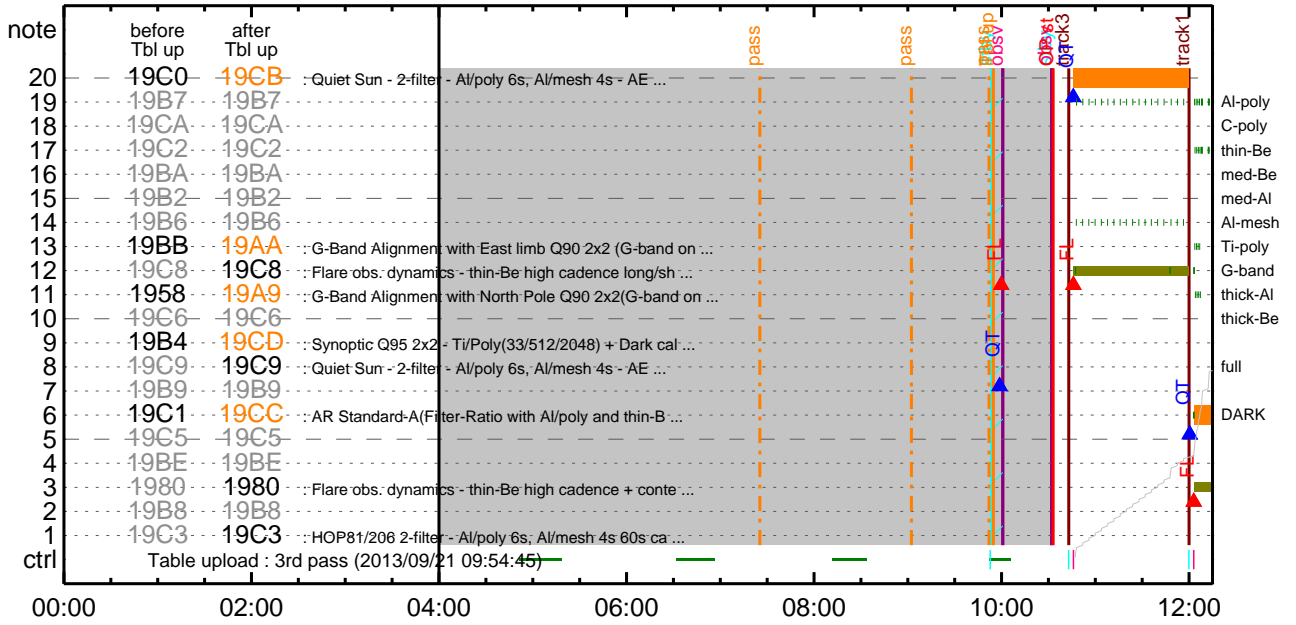
Flare Detection

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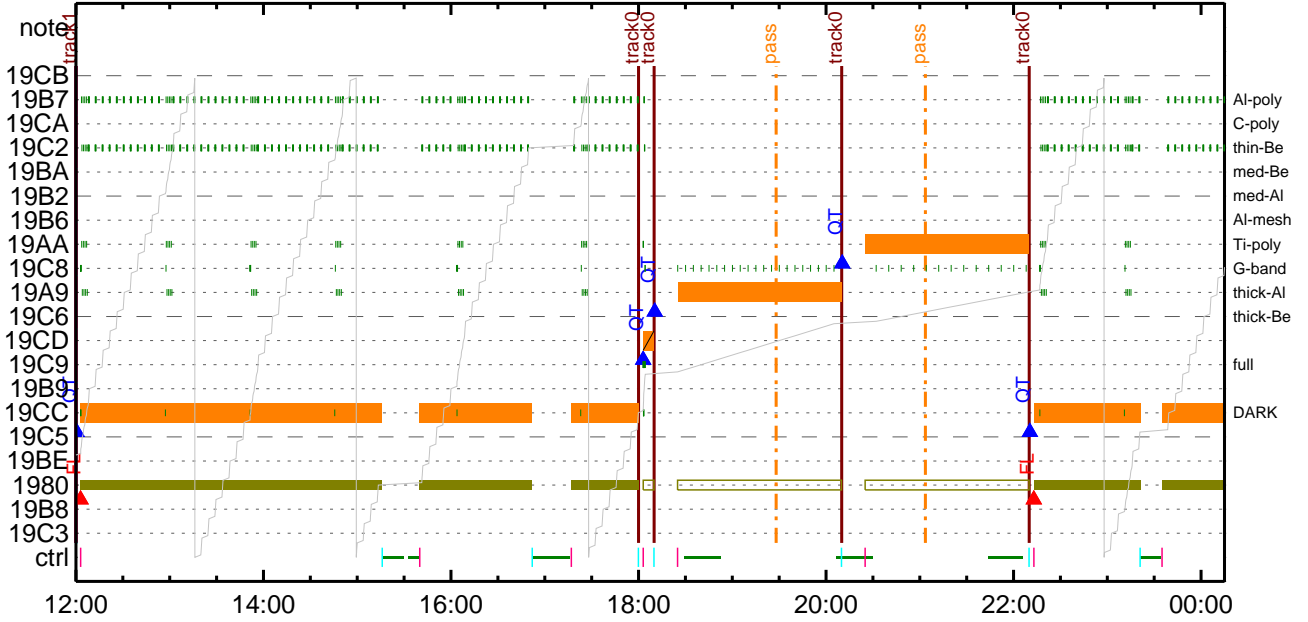
FLD Patrol

Term	Pointing (x, y)	Comment
09/21 22:10:16 - 09/22 06:05:46	Fixed (-922.0, -248.0)	# Returning AR on South East Limb for EIS.
09/22 06:15:46 - 09/22 17:54:16	Track (-712.7, 27.1) @ 09/22 06:15:30	# AR on North East limb.
09/22 18:04:16 - 09/23 05:43:16	Track (-636.2, 17.8) @ 09/22 18:04:00	# AR obs.
09/23 05:49:16 - 09/23 17:41:46	Track (-551.8, 9.8) @ 09/23 05:49:00	# AR obs.
09/23 17:51:46 - 09/24 06:00:16	Track (-457.8, 2.8) @ 09/23 17:51:30	# AR obs.
Open/Ti-poly	Open/thick-Al close	Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

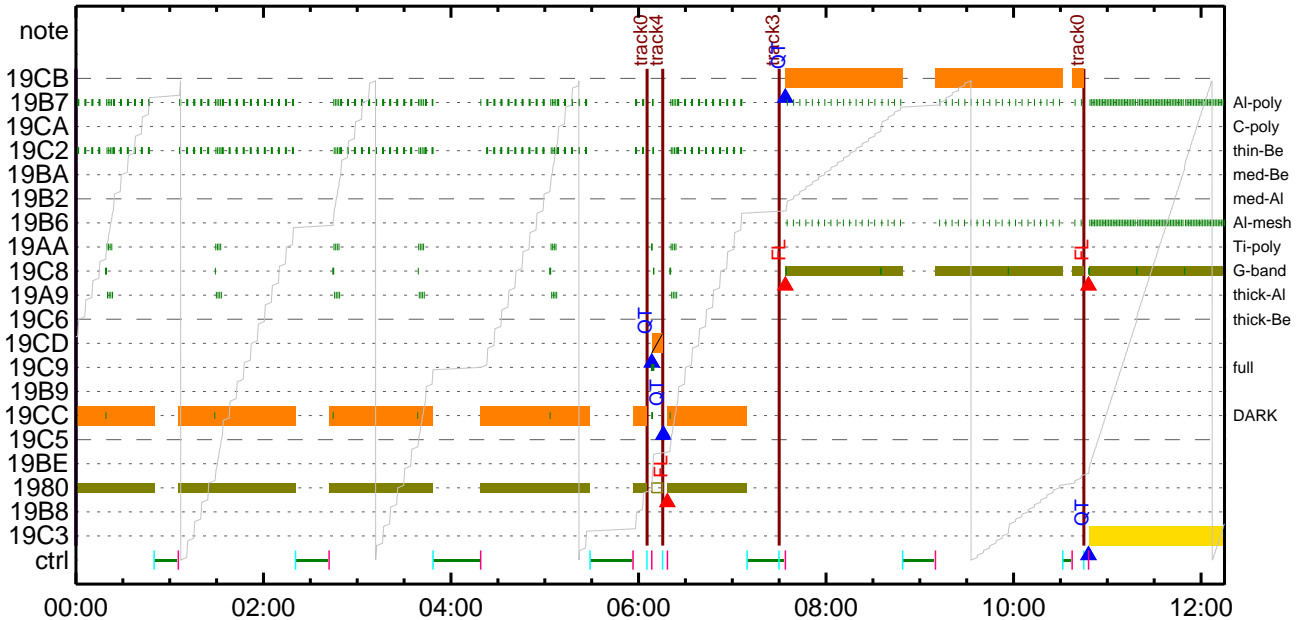
CMDI #0772 2013/09/21



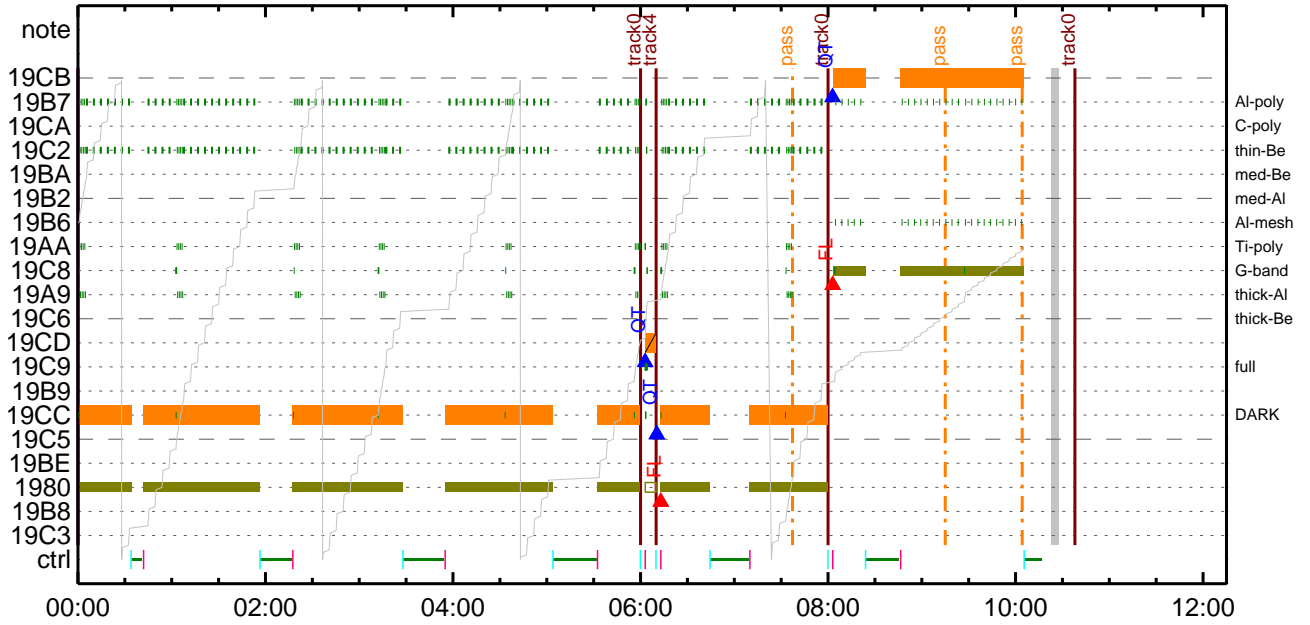
CMDI #0772 2013/09/21



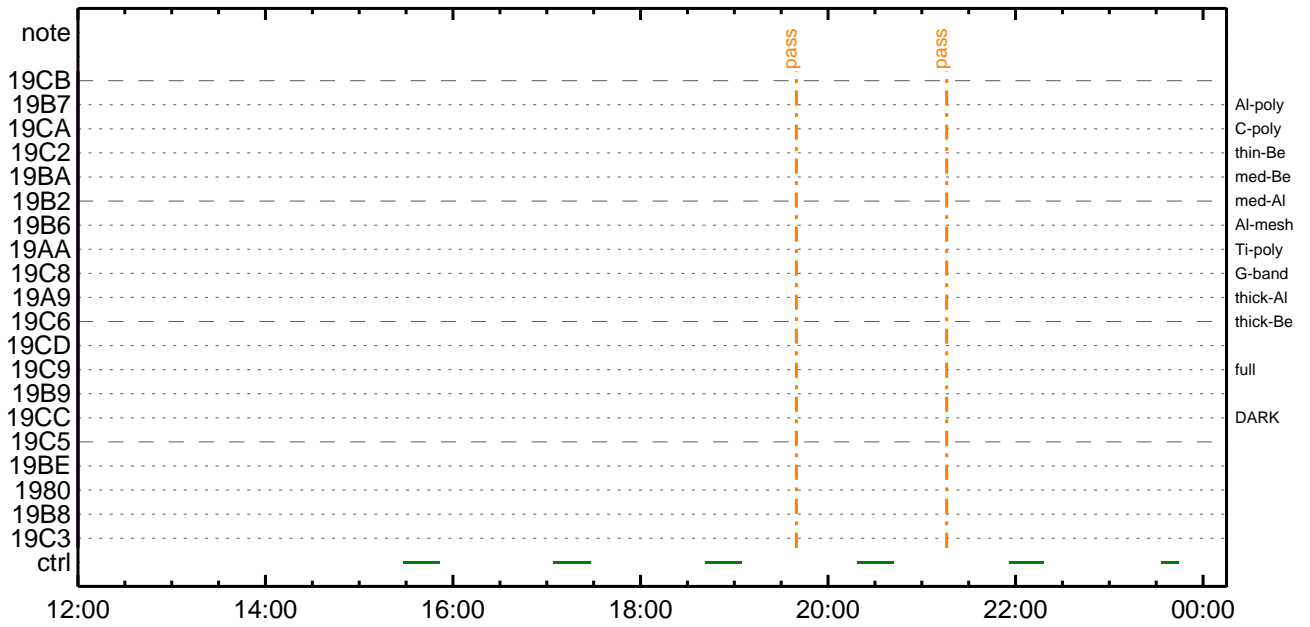
CMDI #0772 2013/09/22



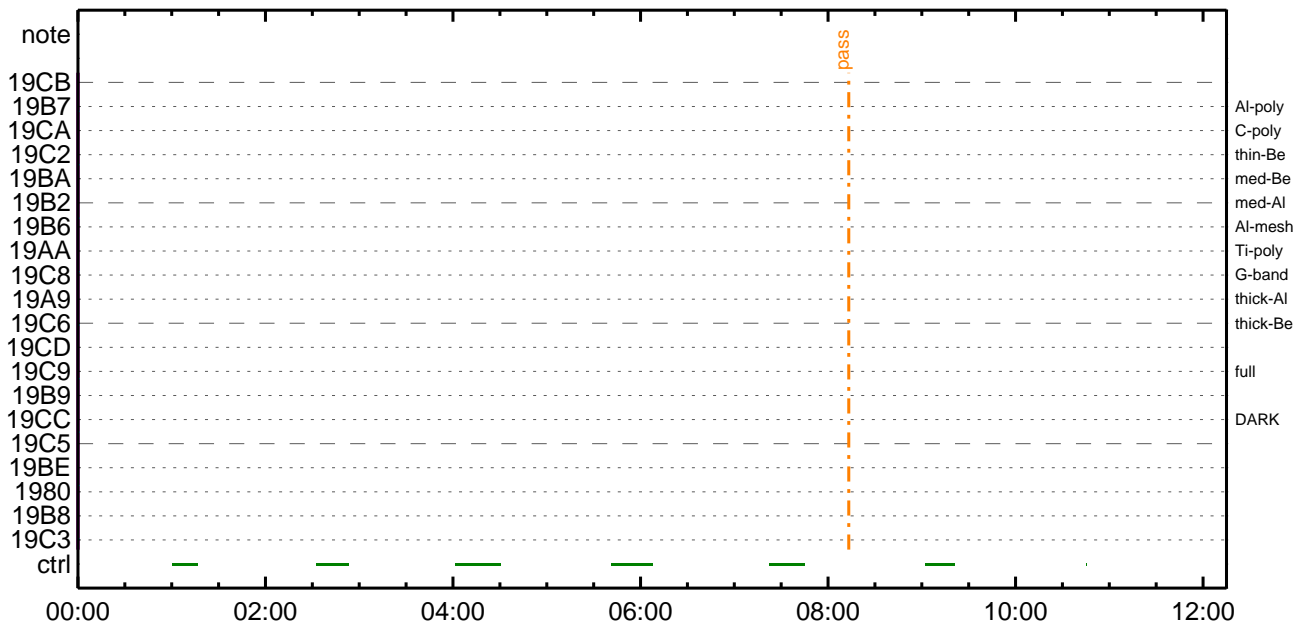
CMDI #0772 2013/09/24



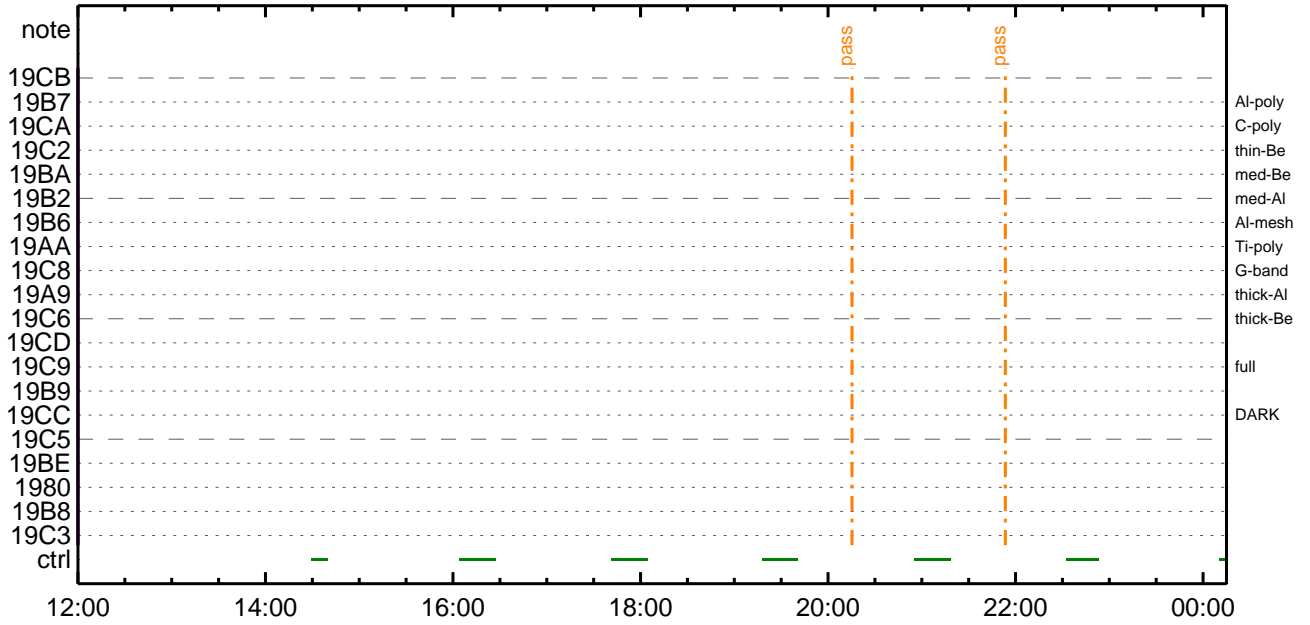
CMDI #0772 2013/09/24



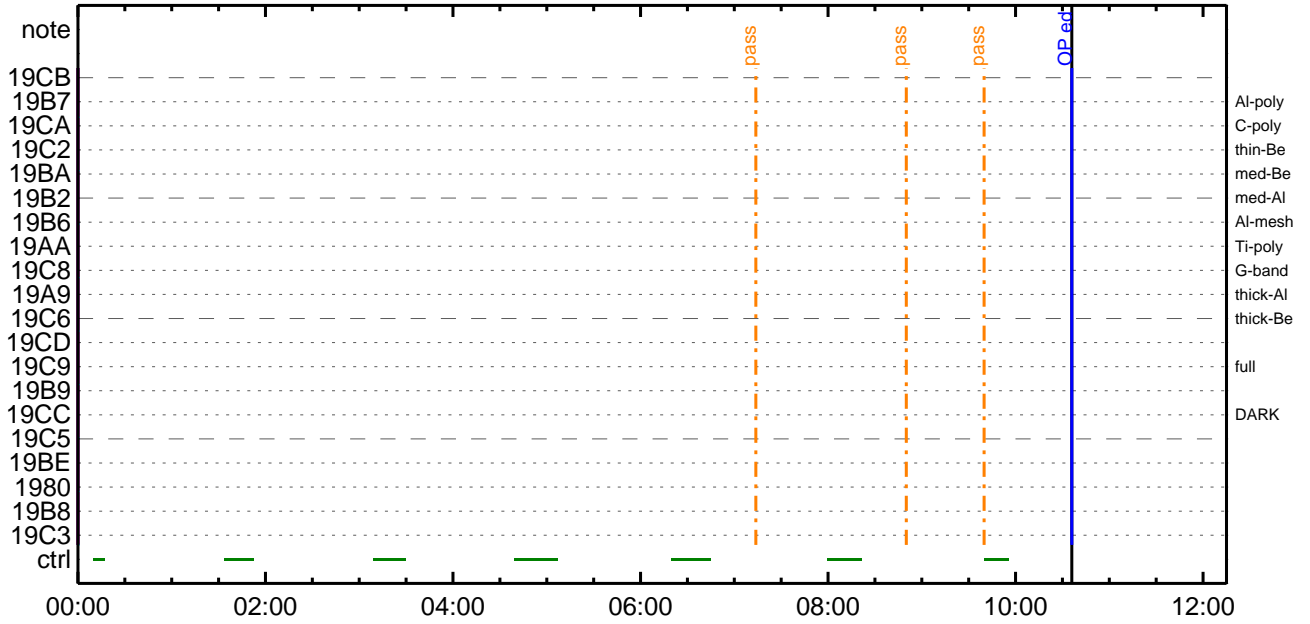
CMDI #0772 2013/09/25



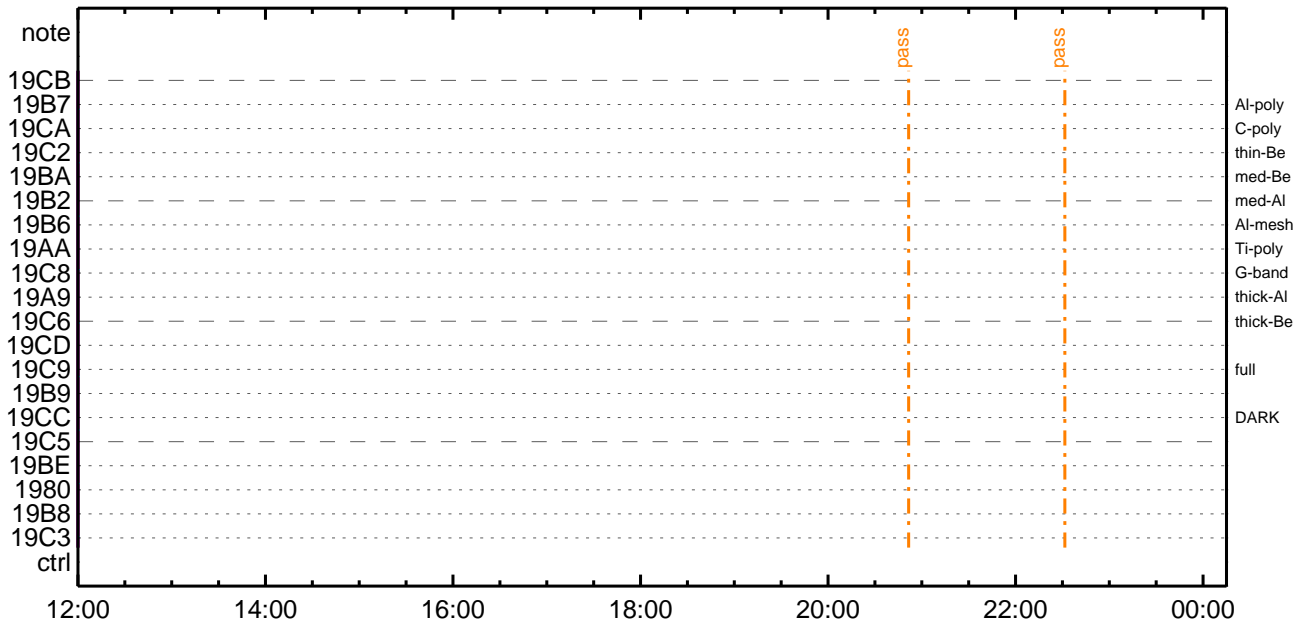
CMDI #0772 2013/09/25



CMDI #0772 2013/09/26



CMDI #0772 2013/09/26




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-917:OP
0104 ( )
0105 S. OG og-917:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ä´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½, Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òî½TI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²²ò¿;çSET²EEDUMP²î½±°î½Y¹²ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²YóYEòðÁDî¿(UT)
0183 +. TI 2013-09-21 10:28:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-09-21 10:28:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-09-21 10:28:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 08 80 80 20 20)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 09 80 80 20 08)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 0a 80 80 08 20)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 0b 80 60 20 18)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 0c a0 80 18 20)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0f 80 80 06 06)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 10 80 80 08 08)
0110 + DC 07-F0 MDP_XRT_FLD_ENA
0111 BC (d8)
0112 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0113 BC (c8)
0114 + DC 07-F0 MDP_XRT_AEC_RESET
0115 BC (d0)
0116 + DC 07-F0 MDP_XRT_ARS_DIS
0117 BC (d5)
0118 + DC 07-F0 MDP_XRT_FLD_RESET
0119 BC (da)
0120 + DC 07-F0 MDP_XRT_QT_PROG_SET
0121 BC (c4 08)
0122 + DC 07-F0 MDP_XRT_FL_PROG_SET
0123 BC (c5 0c)
0124 . C. ----- Success Verify ? OK / NG ____
0125 C.
0126 C.
0127 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0128 C.
0129 +. DC 07-F0 MDP_XRT_MODE_OBSV
0130 BC (c2)
0131 +. TI 2013-09-21 10:32:02.0
0132 DC 07-F0 MDP_XRT_MODE_OBSV
0133 BC (c2)
0134 . C. ----- Success Verify ? OK / NG ____
0135 C.
0136 C. ***** XRT END *****
0137 C.
0138 . C. ***** MDP `úÃîñî»ö¼ÝñÊÃÐñ¹ñèDCBC•x²è *****
0139 C. (%á°îÿÓÿÃÿÊÿPÿËÿáÿçÿèñÊ¼ññ¼Ã»Ûñ¹ñè)
0140 . S. DC-BC dcbc-402:DCBC
0141 (MDP_known_event)
0142 C.
0143 C.
0144 . C. ***** ÿDÿ¹•Ï Daily±¿ÎññÊ´Øñ¹ñèDCBC•x²è *****
0145 . S. DC-BC dcbc-153:DCBC
0146 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0147 C.
0148 C.
0149 . C. ;ãLOSÿÁÿ§ÿËÿ-¼Ã»Û;ã
0150 C.
0151 . C. ***** LOS *****
0152 C.
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Sep 21, 13 13:46

XRT_OGLIST_0772.chk

Page 1/9

*** OP Sequence for XRT ***

2013/09/21	10:42:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/09/21	10:42:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/09/21	10:43:00.0	AOCS_Orе-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	03 00 00 00 00		
2013/09/21	10:43:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/09/21	10:43:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/09/21	10:43:20.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/09/21	10:43:22.0	XRT_ARS_DIS_417_OG [0x1a1]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/09/21	10:45:54.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/09/21	10:45:56.0	XRT_QT_PROG_SET_407_OG [0x197]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14		
2013/09/21	10:45:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c		
2013/09/21	10:46:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/09/21	11:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/09/21	11:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/09/21	12:00:00.0	AOCS_Orе-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	01 00 00 00 00		
2013/09/21	12:00:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/09/21	12:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/09/21	12:00:20.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/09/21	12:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/09/21	12:00:24.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/09/21	12:00:26.0	XRT_QT_PROG_SET_401_OG [0x191]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2013/09/21	12:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03		
2013/09/21	12:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/09/21	15:16:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/09/21	15:16:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/09/21	15:16:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/09/21	15:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/09/21	15:39:00.0	XRT_Custom_430_OG [0x1ae]					
2013/09/21	15:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/09/21	16:52:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/09/21	16:52:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/09/21	16:52:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/09/21	16:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/09/21	17:16:00.0	XRT_Custom_430_OG [0x1ae]					
2013/09/21	17:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/09/21	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/09/21	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/09/21	18:00:00.0	AOCS_Orе-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2013/09/21	18:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/09/21	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/09/21	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/09/21	18:02:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09		
2013/09/21	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/09/21	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/09/21	18:09:56.0	XRT_FOCUS_POSITION_421_OG [0x1a5]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/09/21	18:10:00.0	AOCS_Orе-point_Start_4_OG [0x09a]					
		AOCU_NM	5	02-76	00 ac 00 00 00		
2013/09/21	18:10:16.0	XRT_FLD_DIS_406_OG [0x196]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/09/21	18:10:18.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]					

Sep 21, 13 13:46

XRT_OGLIST_0772.chk

Page 2/9

2013/09/21	18:10:20.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/09/21	18:10:22.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b		
2013/09/21	18:25:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/21	20:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/21	20:09:56.0	XRT_FOCUS_POSITION_421_OG [0x1a5]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2013/09/21	20:10:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	00	00	54 00
2013/09/21	20:10:16.0	XRT_FLD_DIS_406_OG [0x196]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/09/21	20:10:18.0	XRT_FLRCTRL_DIS_449_OG [0x1c1]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/09/21	20:10:20.0	XRT_ARS_DIS_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/09/21	20:10:22.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d		
2013/09/21	20:25:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/21	22:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/21	22:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2013/09/21	22:10:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00	16	0c	51 f3
2013/09/21	22:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/09/21	22:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/09/21	22:10:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/09/21	22:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/09/21	22:10:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/09/21	22:10:26.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06		
2013/09/21	22:12:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	03		
2013/09/21	22:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/21	23:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/21	23:21:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/09/21	23:21:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/09/21	23:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/09/21	23:34:00.0	XRT_Custom_430_OG [0x1ae]							
2013/09/21	23:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/22	00:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/22	00:50:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/09/22	00:50:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/09/22	00:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/09/22	01:04:30.0	XRT_Custom_430_OG [0x1ae]							
2013/09/22	01:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/22	02:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/22	02:20:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/09/22	02:20:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/09/22	02:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/09/22	02:41:00.0	XRT_Custom_430_OG [0x1ae]							
2013/09/22	02:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/22	03:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/22	03:48:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/09/22	03:48:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/09/22	03:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/09/22	04:18:00.0	XRT_Custom_430_OG [0x1ae]							
2013/09/22	04:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/22	05:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/22	05:29:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			

Sep 21, 13 13:46

XRT_OGLIST_0772.chk

Page 3/9

2013/09/22	05:29:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/09/22	05:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/09/22	05:55:30.0	XRT_Custom_430_OG [0x1ae]							
2013/09/22	05:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/09/22	06:05:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/22	06:05:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/09/22	06:05:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/09/22	06:05:46.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/09/22	06:08:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/09/22	06:08:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/09/22	06:08:28.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2013/09/22	06:08:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/09/22	06:15:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/22	06:15:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/09/22	06:15:30.0	AOCS_Ore-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2013/09/22	06:15:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/09/22	06:15:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/09/22	06:15:50.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/09/22	06:15:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/09/22	06:15:54.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/09/22	06:15:56.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2013/09/22	06:18:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2013/09/22	06:18:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/09/22	07:09:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/22	07:09:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/09/22	07:09:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/09/22	07:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/09/22	07:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/22	07:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/09/22	07:30:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2013/09/22	07:30:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/09/22	07:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/09/22	07:30:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/09/22	07:30:22.0	XRT_ARS_DIS_448_OG [0x1c0]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/09/22	07:33:54.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/09/22	07:33:56.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2013/09/22	07:33:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c				
2013/09/22	07:34:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/09/22	08:49:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/22	08:49:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/09/22	08:49:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/09/22	08:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/09/22	09:09:00.0	XRT_Custom_430_OG [0x1ae]							
2013/09/22	09:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/09/22	10:31:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/22	10:31:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/09/22	10:31:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/09/22	10:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							

Sep 21, 13 13:46

XRT_OGLIST_0772.chk

Page 4/9

2013/09/22	10:36:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/09/22	10:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/09/22	10:44:54.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/09/22	10:44:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/09/22	10:45:00.0	AOCS_ORe-point_Start_8_OG [0x09e]						
			AOCU_NM	5	02-76	00 b2 25 01 3f		
2013/09/22	10:45:16.0	XRT_FLD_ENA_411_OG [0x19b]						
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/09/22	10:45:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/09/22	10:45:20.0	XRT_AEC_RESET_413_OG [0x19d]						
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/09/22	10:45:22.0	XRT_ARS_DIS_417_OG [0x1a1]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/09/22	10:47:54.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/09/22	10:47:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2013/09/22	10:47:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]						
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c		
2013/09/22	10:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/09/22	14:18:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/09/22	14:18:02.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/09/22	14:18:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/09/22	14:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/09/22	14:26:30.0	XRT_Custom_430_OG [0x1ae]						
2013/09/22	14:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/09/22	15:52:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/09/22	15:52:02.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/09/22	15:52:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/09/22	15:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/09/22	16:15:30.0	XRT_Custom_430_OG [0x1ae]						
2013/09/22	16:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/09/22	16:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/09/22	16:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/09/22	17:00:00.0	AOCS_ORe-point_Start_7_OG [0x09d]						
			AOCU_NM	5	02-76	04 00 00 00 00		
2013/09/22	17:00:16.0	XRT_FLD_ENA_411_OG [0x19b]						
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/09/22	17:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/09/22	17:00:20.0	XRT_AEC_RESET_413_OG [0x19d]						
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/09/22	17:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/09/22	17:00:24.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/09/22	17:00:26.0	XRT_QT_PROG_SET_401_OG [0x191]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2013/09/22	17:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]						
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 03		
2013/09/22	17:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/09/22	17:28:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/09/22	17:28:32.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/09/22	17:28:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/09/22	17:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/09/22	17:53:54.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/09/22	17:53:56.0	XRT_FOCUS_POSITION_403_OG [0x193]						
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/09/22	17:54:00.0	AOCS_ORe-point_Start_3_OG [0x099]						
			AOCU_NM	5	02-76	00 00 00 00 00		
2013/09/22	17:54:16.0	XRT_FLD_DIS_434_OG [0x1b2]						
			MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/09/22	17:56:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/09/22	17:56:56.0	XRT_ARS_DIS_423_OG [0x1a7]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/09/22	17:56:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 09		
2013/09/22	17:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]						

Sep 21, 13 13:46

XRT_OGLIST_0772.chk

Page 5/9

2013/09/22	18:03:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/22	18:03:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/09/22	18:04:00.0	AOCs_OrE-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	04 00 00 00 00
2013/09/22	18:04:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/09/22	18:04:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/09/22	18:04:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/09/22	18:04:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/09/22	18:04:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/22	18:04:26.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2013/09/22	18:06:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 03
2013/09/22	18:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/22	19:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/22	19:05:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/22	19:05:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/22	19:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/22	19:29:00.0	XRT_Custom_430_OG [0x1ae]				
2013/09/22	19:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/22	20:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/22	20:43:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/22	20:43:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/22	20:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/22	21:06:30.0	XRT_Custom_430_OG [0x1ae]				
2013/09/22	21:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/22	22:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/22	22:20:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/22	22:20:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/22	22:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/22	22:41:30.0	XRT_Custom_430_OG [0x1ae]				
2013/09/22	22:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/22	23:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/22	23:57:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/22	23:57:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/23	00:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/23	00:06:00.0	XRT_Custom_430_OG [0x1ae]				
2013/09/23	00:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/23	01:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/23	01:22:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/23	01:22:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/23	01:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/23	01:40:30.0	XRT_Custom_430_OG [0x1ae]				
2013/09/23	01:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/23	02:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/23	02:56:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/23	02:56:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/23	02:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/23	03:17:30.0	XRT_Custom_430_OG [0x1ae]				
2013/09/23	03:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/23	04:26:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/23	04:26:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da

Sep 21, 13 13:46

XRT_OGLIST_0772.chk

Page 6/9

2013/09/23	04:26:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/09/23	04:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/09/23	04:55:00.0	XRT_Custom_430_OG [0x1ae]							
2013/09/23	04:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/09/23	05:38:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/23	05:38:56.0	XRT_FOCUS_RECALIBRATE_433_OG [0x1b1]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2013/09/23	05:39:00.5	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/09/23	05:42:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/09/23	05:43:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/09/23	05:45:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/09/23	05:45:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/09/23	05:45:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2013/09/23	05:46:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/09/23	05:48:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/23	05:48:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/09/23	05:49:00.0	AOCS_Ore-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2013/09/23	05:49:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/09/23	05:49:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/09/23	05:49:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/09/23	05:49:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/09/23	05:49:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/09/23	05:49:26.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2013/09/23	05:51:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2013/09/23	05:52:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/09/23	06:07:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/23	06:07:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/09/23	06:07:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/09/23	06:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/09/23	06:32:30.0	XRT_Custom_430_OG [0x1ae]							
2013/09/23	06:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/09/23	07:47:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/23	07:47:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/09/23	07:47:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/09/23	07:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/09/23	07:54:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/23	07:54:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/09/23	07:55:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/09/23	07:55:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/09/23	07:55:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/09/23	07:55:22.0	XRT_ARS_DIS_436_OG [0x1b4]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/09/23	08:00:00.0	AOCS_Ore-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2013/09/23	08:10:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/09/23	08:10:26.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2013/09/23	08:10:28.0	XRT_FL_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c				
2013/09/23	08:10:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/09/23	09:27:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/23	09:27:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/09/23	09:27:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							

Sep 21, 13 13:46

XRT_OGLIST_0772.chk

Page 7/9

2013/09/23	09:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/09/23	09:44:00.0	XRT_Custom_430_OG [0x1ae]						
2013/09/23	09:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/09/23	11:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/09/23	11:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/09/23	12:00:00.0	AOCS_ORe-point_Start_7_OG [0x09d]						
			AOCU_NM	5	02-76	04 00 00 00 00		
2013/09/23	12:00:16.0	XRT_FLD_ENA_411_OG [0x19b]						
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/09/23	12:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/09/23	12:00:20.0	XRT_AEC_RESET_413_OG [0x19d]						
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/09/23	12:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/09/23	12:00:24.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/09/23	12:00:26.0	XRT_QT_PROG_SET_401_OG [0x191]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2013/09/23	12:02:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]						
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 03		
2013/09/23	12:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/09/23	14:52:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/09/23	14:52:32.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/09/23	14:52:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/09/23	14:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/09/23	15:05:00.0	XRT_Custom_430_OG [0x1ae]						
2013/09/23	15:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/09/23	16:28:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/09/23	16:28:02.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/09/23	16:28:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/09/23	16:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/09/23	16:51:30.0	XRT_Custom_430_OG [0x1ae]						
2013/09/23	16:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/09/23	17:41:24.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/09/23	17:41:26.0	XRT_FOCUS_POSITION_403_OG [0x193]						
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/09/23	17:41:30.0	AOCS_ORe-point_Start_3_OG [0x099]						
			AOCU_NM	5	02-76	00 00 00 00 00		
2013/09/23	17:41:46.0	XRT_FLD_DIS_434_OG [0x1b2]						
			MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/09/23	17:44:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/09/23	17:44:26.0	XRT_ARS_DIS_423_OG [0x1a7]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/09/23	17:44:28.0	XRT_QT_PROG_SET_429_OG [0x1ad]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 09		
2013/09/23	17:44:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/09/23	17:51:24.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/09/23	17:51:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/09/23	17:51:30.0	AOCS_ORe-point_Start_7_OG [0x09d]						
			AOCU_NM	5	02-76	04 00 00 00 00		
2013/09/23	17:51:46.0	XRT_FLD_ENA_411_OG [0x19b]						
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/09/23	17:51:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/09/23	17:51:50.0	XRT_AEC_RESET_413_OG [0x19d]						
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/09/23	17:51:52.0	XRT_ARS_DIS_423_OG [0x1a7]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/09/23	17:51:54.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/09/23	17:51:56.0	XRT_QT_PROG_SET_401_OG [0x191]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2013/09/23	17:54:28.0	XRT_FL_PROG_SET_428_OG [0x1ac]						
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 03		
2013/09/23	17:54:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/09/23	18:05:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/09/23	18:05:02.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/09/23	18:05:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						

2013/09/23	18:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/23	18:28:30.0	XRT_Custom_430_OG [0x1ae]				
2013/09/23	18:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/23	19:42:00.5	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/23	19:42:03.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/23	19:42:05.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/23	19:45:15.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/23	20:05:30.0	XRT_Custom_430_OG [0x1ae]				
2013/09/23	20:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/23	21:19:30.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/23	21:19:32.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/23	21:19:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/23	21:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/23	21:42:30.0	XRT_Custom_430_OG [0x1ae]				
2013/09/23	21:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/23	22:57:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/23	22:57:02.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/23	22:57:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/23	23:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/23	23:15:00.0	XRT_Custom_430_OG [0x1ae]				
2013/09/23	23:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/24	00:34:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/24	00:34:02.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/24	00:34:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/24	00:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/24	00:41:00.0	XRT_Custom_430_OG [0x1ae]				
2013/09/24	00:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/24	01:56:30.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/24	01:56:32.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/24	01:56:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/24	01:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/24	02:16:30.0	XRT_Custom_430_OG [0x1ae]				
2013/09/24	02:17:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/24	03:28:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/24	03:28:02.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/24	03:28:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/24	03:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/24	03:54:00.0	XRT_Custom_430_OG [0x1ae]				
2013/09/24	03:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/24	05:04:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/24	05:04:02.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/24	05:04:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/24	05:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/24	05:31:30.0	XRT_Custom_430_OG [0x1ae]				
2013/09/24	05:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/24	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/24	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]				
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/09/24	06:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]				
			AOCU_NM	5	02-76	00 00 00 00 00
2013/09/24	06:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]				
			MDP_XRT_FLD_DIS	1	07-F0	d9
2013/09/24	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9

Sep 21, 13 13:46

XRT_OGLIST_0772.chk

Page 9/9

2013/09/24	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/09/24	06:02:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2013/09/24	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/09/24	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/24	06:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/09/24	06:10:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2013/09/24	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/09/24	06:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/09/24	06:10:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/09/24	06:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/09/24	06:10:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/09/24	06:10:26.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2013/09/24	06:12:58.0	XRT_FL_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 03				
2013/09/24	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/09/24	06:44:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/24	06:44:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/09/24	06:44:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/09/24	06:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/09/24	07:09:00.0	XRT_Custom_430_OG [0x1ae]							
2013/09/24	07:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/09/24	07:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/24	07:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/09/24	08:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/09/24	08:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/09/24	08:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/09/24	08:00:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/09/24	08:00:22.0	XRT_ARS_DIS_417_OG [0x1a1]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/09/24	08:02:54.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/09/24	08:02:56.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2013/09/24	08:02:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c				
2013/09/24	08:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/09/24	08:24:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/24	08:24:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/09/24	08:24:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/09/24	08:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/09/24	08:45:30.0	XRT_Custom_430_OG [0x1ae]							
2013/09/24	08:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/09/24	10:05:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/09/24	10:05:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/09/24	10:05:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/09/24	10:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/09/24	10:38:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				