

XRT Timeline to be uploaded on 2013/09/26

Period: 2013/09/26 10:36:00 - 2013/10/01 09:52:00

* * * * *

Normal mode

* * * * *

XOB #19B4: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-bar

Term	Pointing (x, y)	Comment
09/26 10:49:00 - 09/26 11:59:54	Track (186.9, 26.0) @ 09/26 10:46:00	# OP start + 10min, HOP236
09/26 16:03:00 - 09/26 17:53:54	Track (233.8, 27.4) @ 09/26 16:00:00	AR11850 obs
09/26 18:07:00 - 09/27 05:59:54	Track (252.1, 28.1) @ 09/26 18:04:00	AR11850 obs
09/27 06:13:00 - 09/27 17:57:24	Track (357.4, 32.7) @ 09/27 06:10:00	AR11850 obs, HOP236
09/27 18:10:30 - 09/28 06:30:24	Track (456.6, 38.6) @ 09/27 18:07:30	AR11850 obs
09/28 06:43:30 - 09/28 07:34:30	Track (554.1, 46.2) @ 09/28 06:40:30	AR11850

PROG= 02 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec												
Seqn= 8 2-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec												
Seqn= 24 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 98 4-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 21 12-time(s) 240.0sec												
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #19C5: CME watch - C/poly - 384x384- 60s - Be/thin - 2x2 - 1064x1048 - 10min - AEC2 -w/leak test

Term	Pointing (x, y)	Comment
09/26 12:03:00 - 09/26 15:59:54	Fixed (731.0, 551.0)	prominence obs HOP219

PROG= 05 Inf.-time(s)

Subr= 3 1-time(s) 2.0sec												
Seqn= 8 1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Seqn= 24 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 1 6-time(s) 2.0sec												
Seqn= 28 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	1024x1024 (1064, 1048)	DPCM	2	0	2.0sec
Seqn= 31 10-time(s) 60.0sec												
C-poly/Open	C-poly/Open	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	DPCM	2	0	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #19C9: Synoptic Q95 2x2 - Ti/Poly(33/512/2048) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Al-poly(33/512/2048) + Thi

Term	Pointing (x, y)	Comment
09/26 17:57:00 - 09/26 18:03:54	Fixed (0.0, 0.0)	synoptic, shifted -6.0 min
09/27 06:03:00 - 09/27 06:09:54	Fixed (0.0, 0.0)	synoptic
09/27 18:00:30 - 09/27 18:07:24	Fixed (0.0, 0.0)	synoptic, shifted -2.5 min
09/28 06:33:30 - 09/28 06:40:24	Fixed (0.0, 0.0)	synoptic, shifted 30.5 min

PROG= 09 1-time(s)

Subr= 1 1-time(s) 12.0sec												
Seqn= 74 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 76 1-time(s) 4.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 55 1-time(s) 2.0sec												

thin-Be/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	5.66s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 66 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19C3: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 45ms-2

Term	Pointing (x, y)	Comment
09/28 08:03:00 - 09/28 09:48:30	Fixed (60.0, 908.0)	HOP236 @ N-pole
PROG= 01 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 13 2-time(s) 2.0sec		
Open/G-band	Open/G-band	close Safe Norm 63ms Obs 1x1 512x512 (1064, 1048) DPCM 0 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 30 1-time(s) 30.0sec		
Open/G-band	Open/G-band	open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
Subr= 3 30-time(s) 2.0sec		
Seqn= 57 1-time(s) 60.0sec		
Open/Al-mesh	Open/Al-mesh	close Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
Al-poly/Open	Al-poly/Open	close Safe Norm 5.66s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

* * * * *

Flare mode

* * * * *

XOB #19C8: Flare obs. dynamics - thin-Be high cadence long/short pairs + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loc

Term	Pointing (x, y)	Comment
09/26 10:49:00 - 09/26 11:59:54	Track (186.9, 26.0) @ 09/26 10:46:00	# OP start + 10min, HOP236
09/26 12:03:00 - 09/26 15:59:54	Fixed (731.0, 551.0)	prominence obs HOP219
09/26 16:03:00 - 09/26 17:53:54	Track (233.8, 27.4) @ 09/26 16:00:00	AR11850 obs
09/26 18:07:00 - 09/27 05:59:54	Track (252.1, 28.1) @ 09/26 18:04:00	AR11850 obs
09/27 06:13:00 - 09/27 17:57:24	Track (357.4, 32.7) @ 09/27 06:10:00	AR11850 obs, HOP236
09/27 18:10:30 - 09/28 06:30:24	Track (456.6, 38.6) @ 09/27 18:07:30	AR11850 obs
09/28 06:43:30 - 09/28 07:34:30	Track (554.1, 46.2) @ 09/28 06:40:30	AR11850
09/28 08:03:00 - 09/28 09:48:30	Fixed (60.0, 908.0)	HOP236 @ N-pole
PROG= 12 15-time(s)		
Subr= 1 45-time(s) 2.0sec		
Seqn= 69 1-time(s) 8.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
thin-Be/Open	med-Be/Open	close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 10 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be	close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 15 1-time(s) 2.0sec		
Open/G-band	Open/G-band	open Safe Norm 44ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

* * * * *

Active Region Search

* * * * *

NOT USED

* * * * *

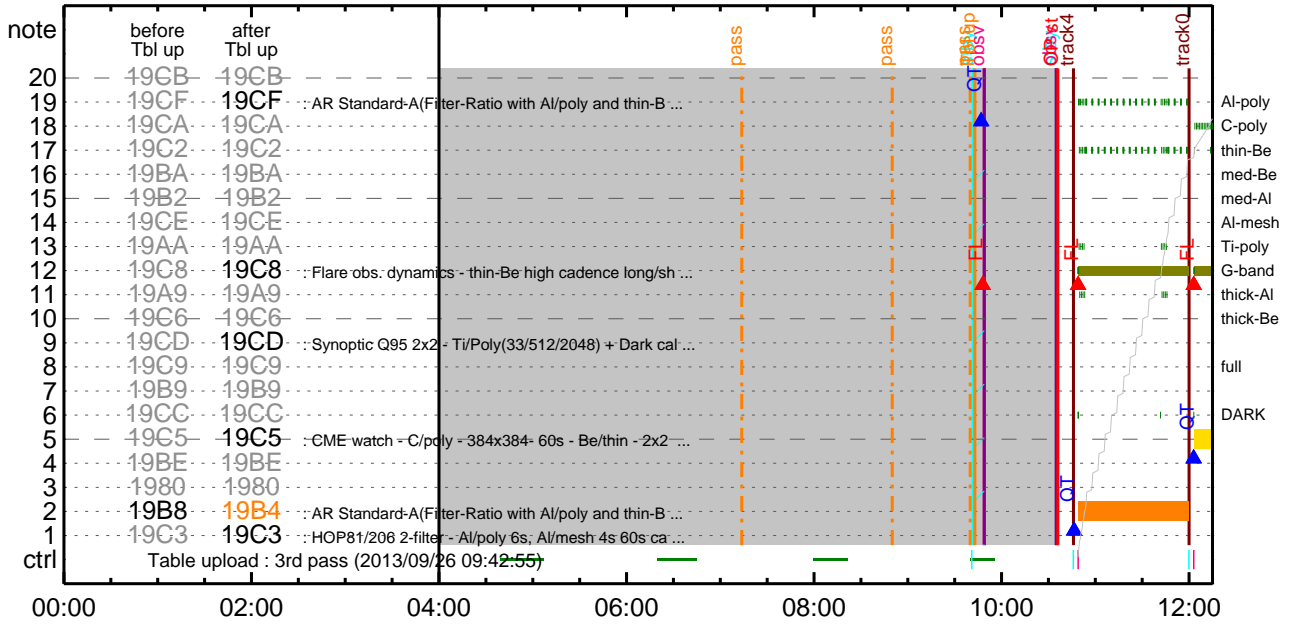
Flare Detection

* * * * *

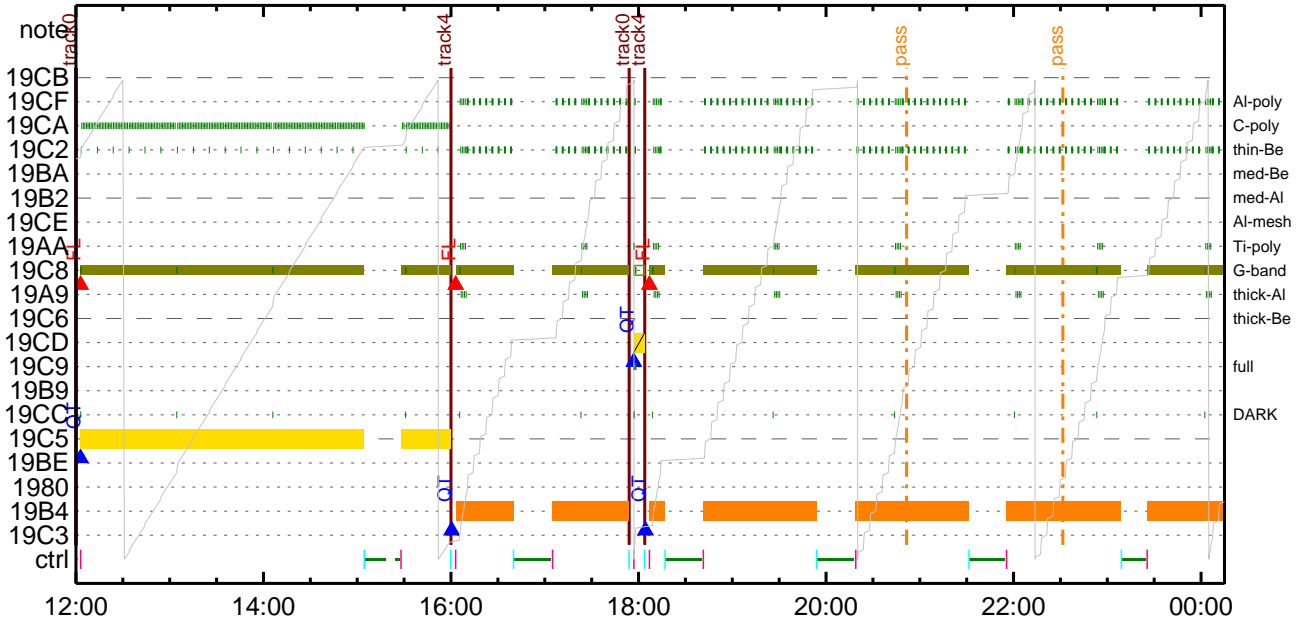
FLD Patrol

Term	Pointing (x, y)	Comment
09/26 18:04:16 - 09/27 06:00:16	Track (252.1, 28.1) @ 09/26 18:04:00	AR11850 obs
09/27 06:10:16 - 09/27 17:57:46	Track (357.4, 32.7) @ 09/27 06:10:00	AR11850 obs, HOP236
09/27 18:07:46 - 09/28 06:30:46	Track (456.6, 38.6) @ 09/27 18:07:30	AR11850 obs
09/28 06:40:46 - 10/01 09:52:00	Track (554.1, 46.2) @ 09/28 06:40:30	AR11850
Open/Ti-poly	Open/thick-Al	close Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

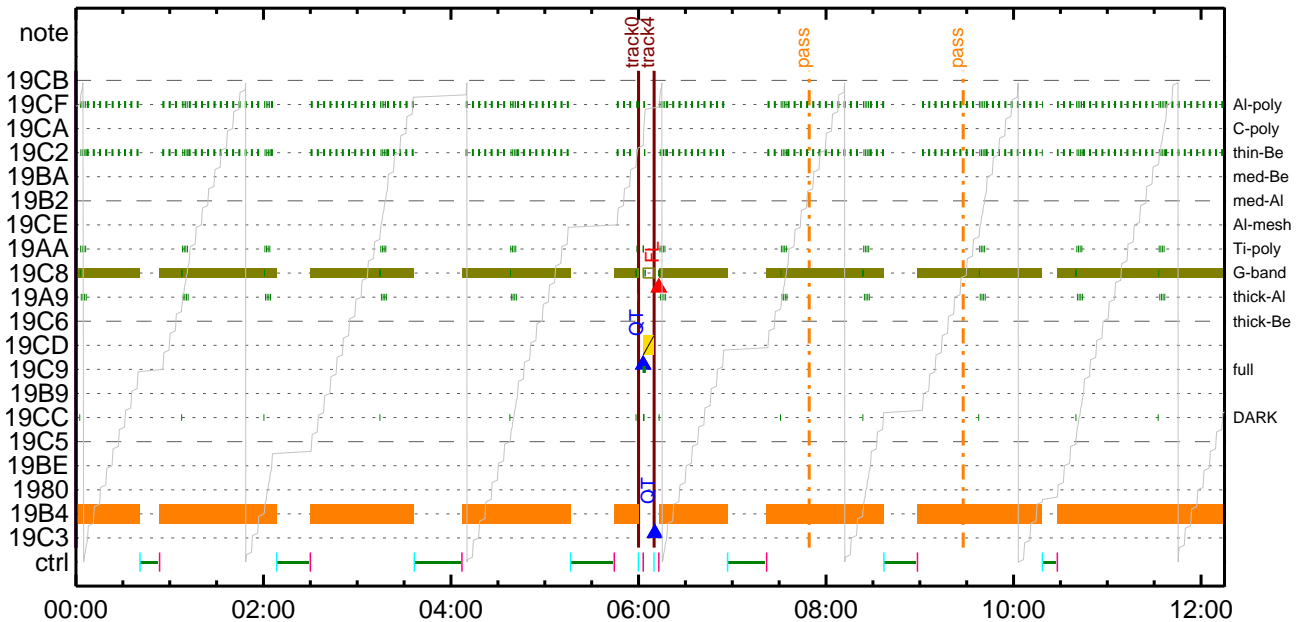
CMDI #0782 2013/09/26



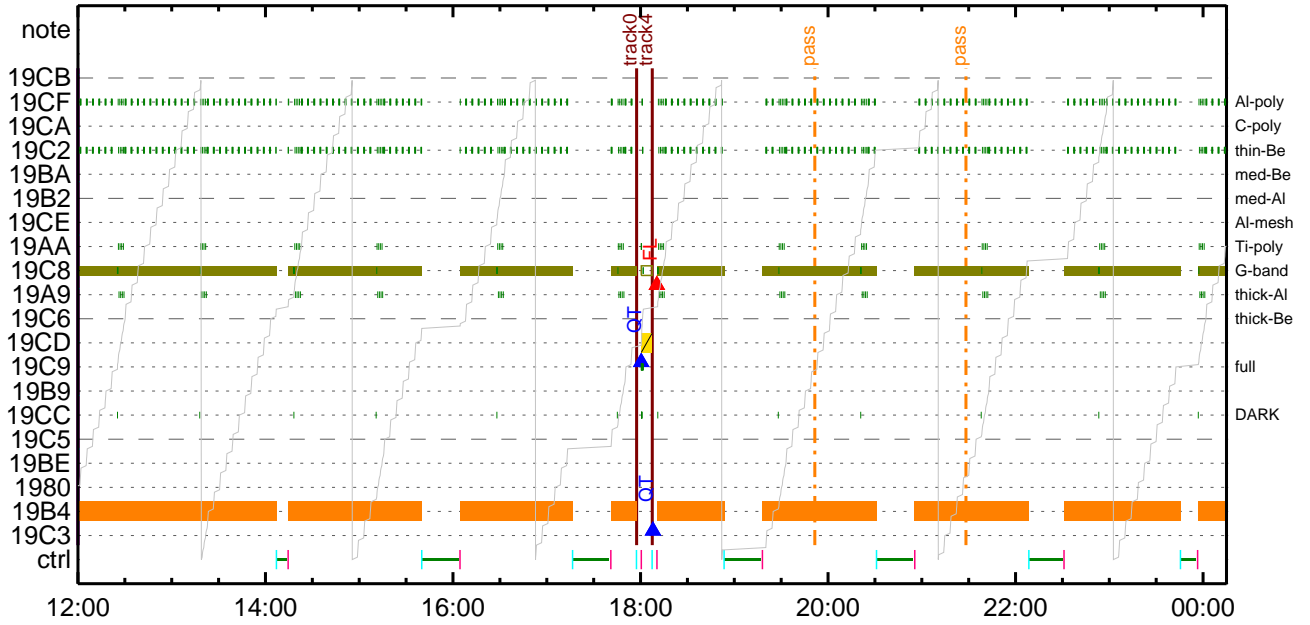
CMDI #0782 2013/09/26



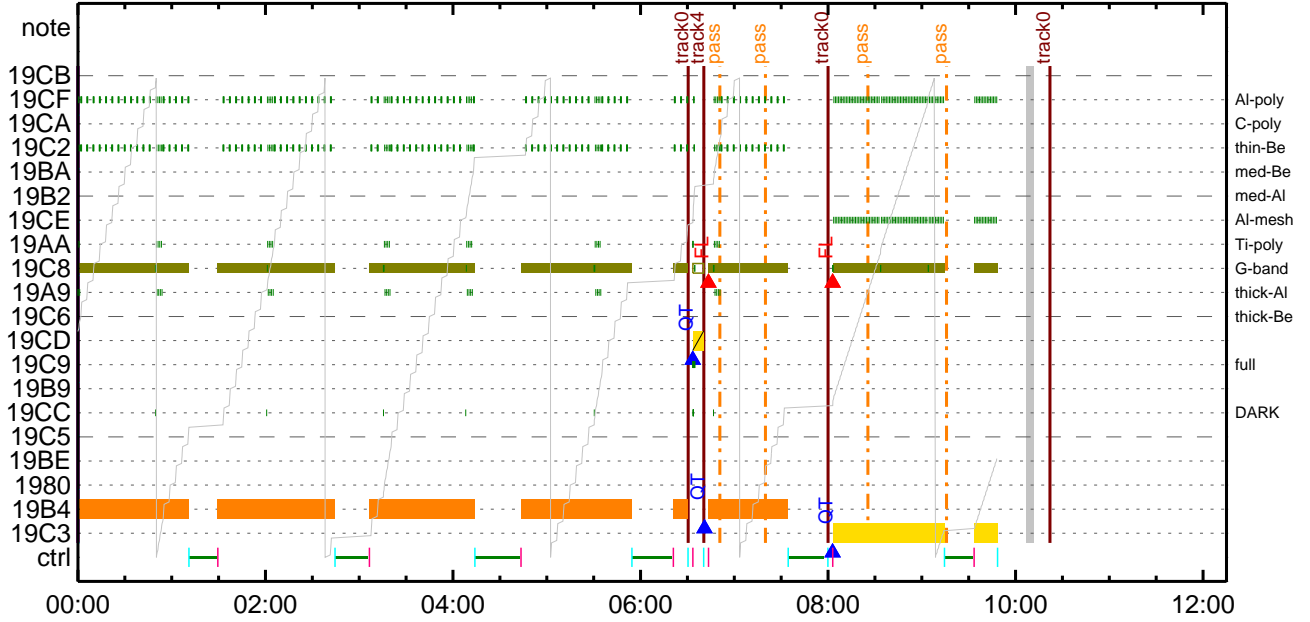
CMDI #0782 2013/09/27



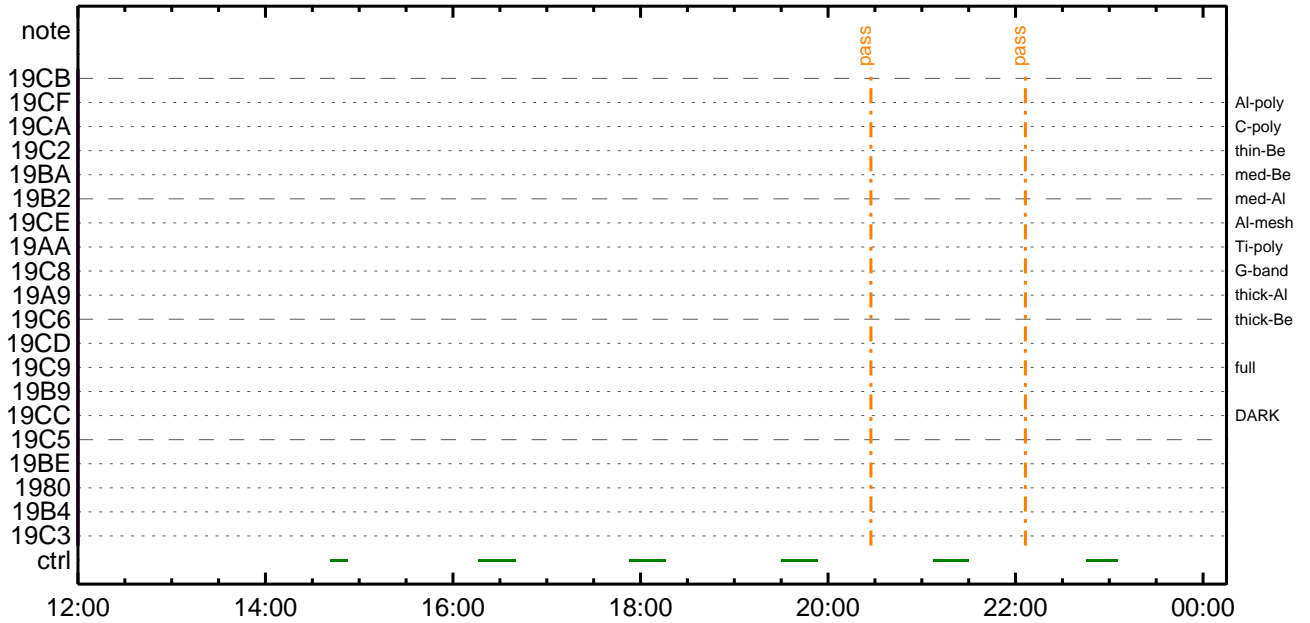
CMDI #0782 2013/09/27



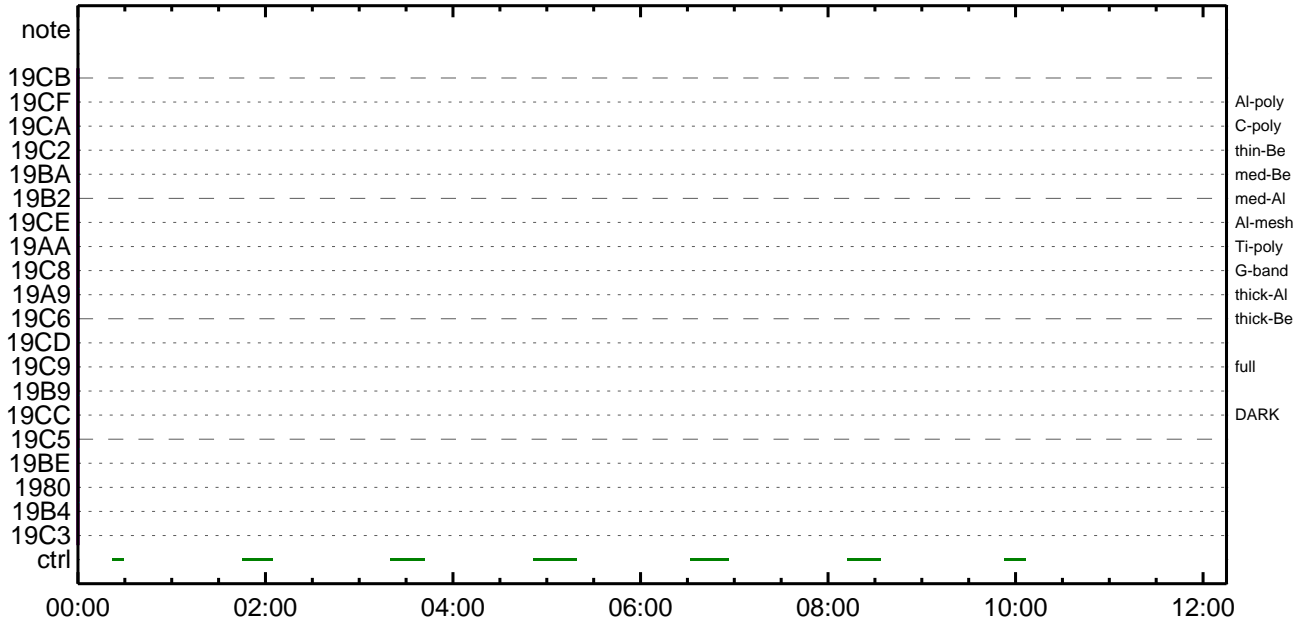
CMDI #0782 2013/09/28



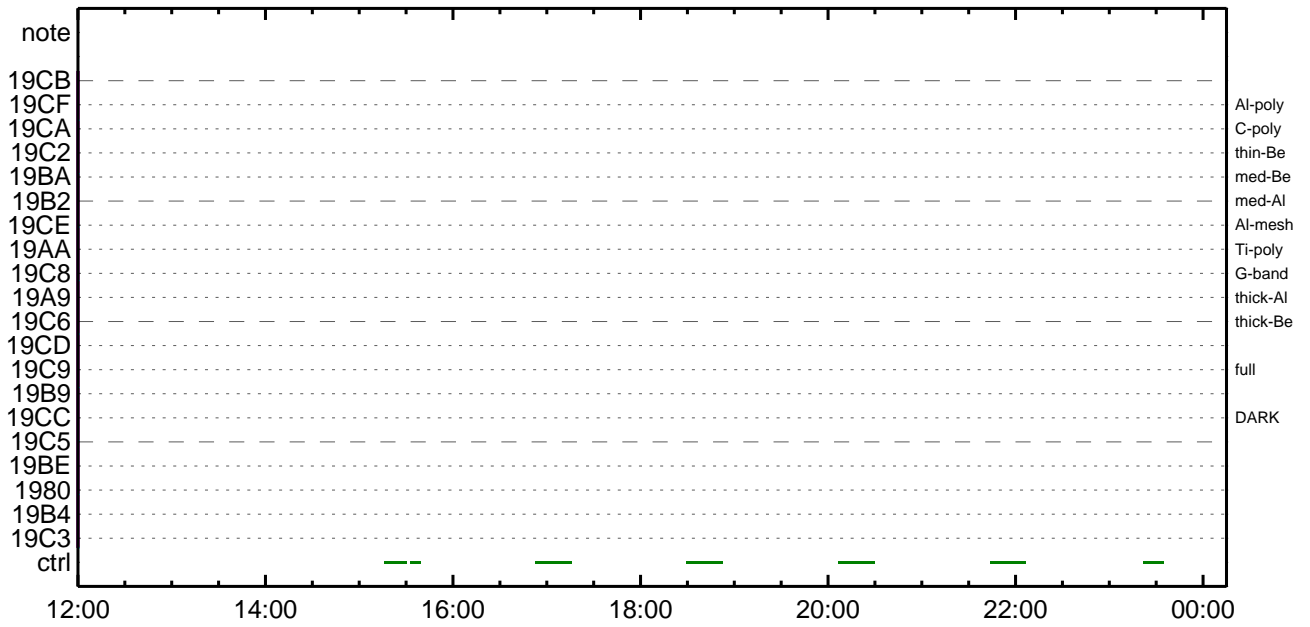
CMDI #0782 2013/09/28



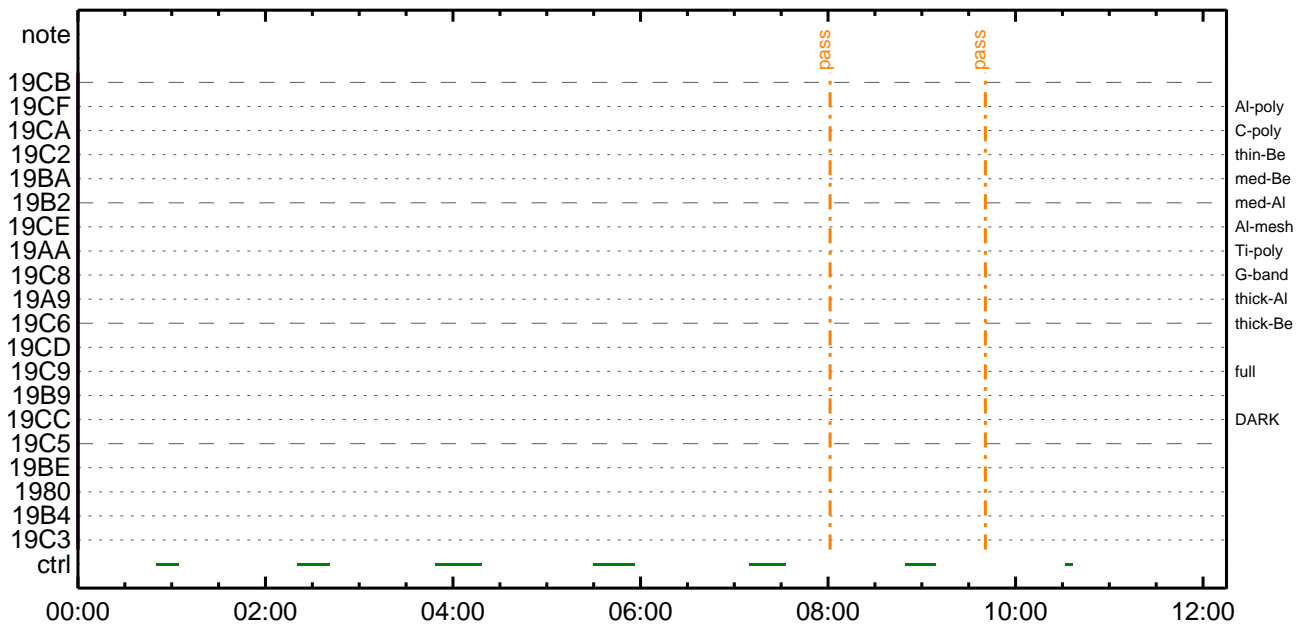
CMDI #0782 2013/09/29



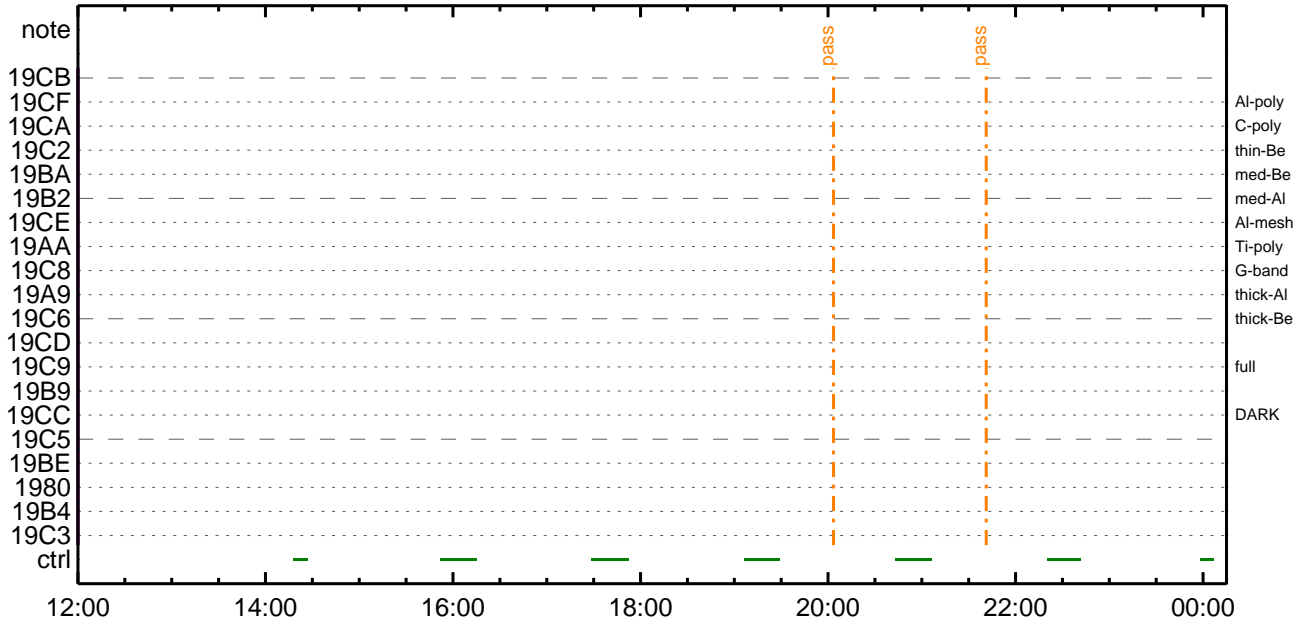
CMDI #0782 2013/09/29



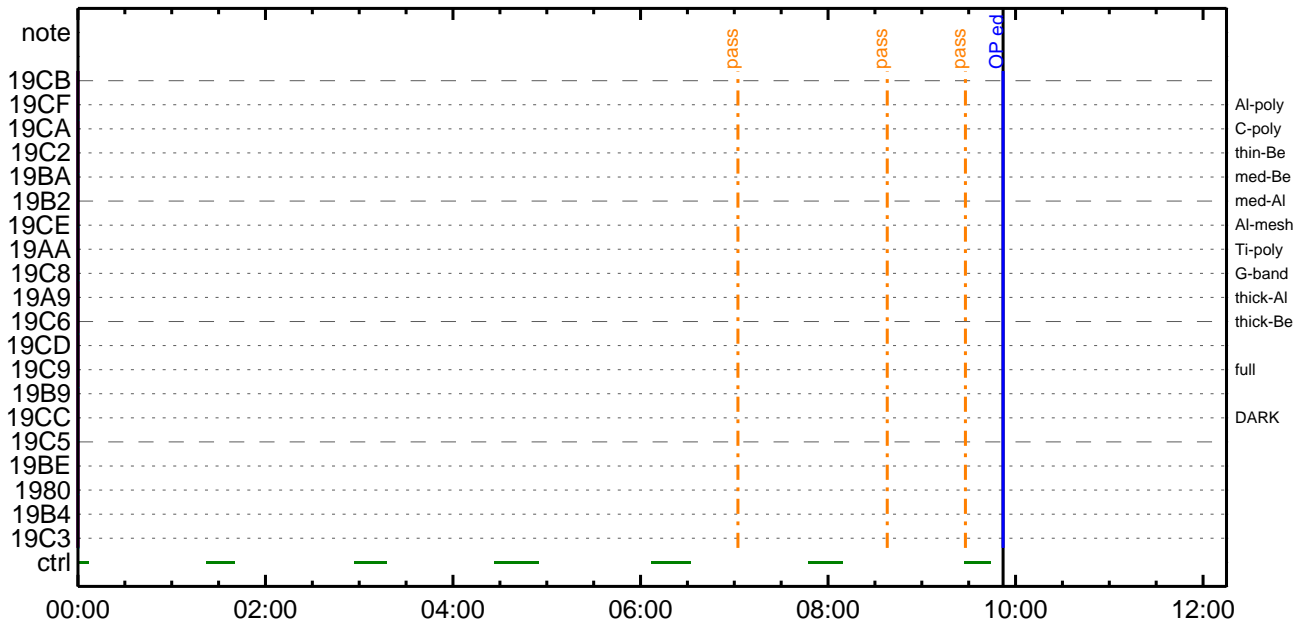
CMDI #0782 2013/09/30



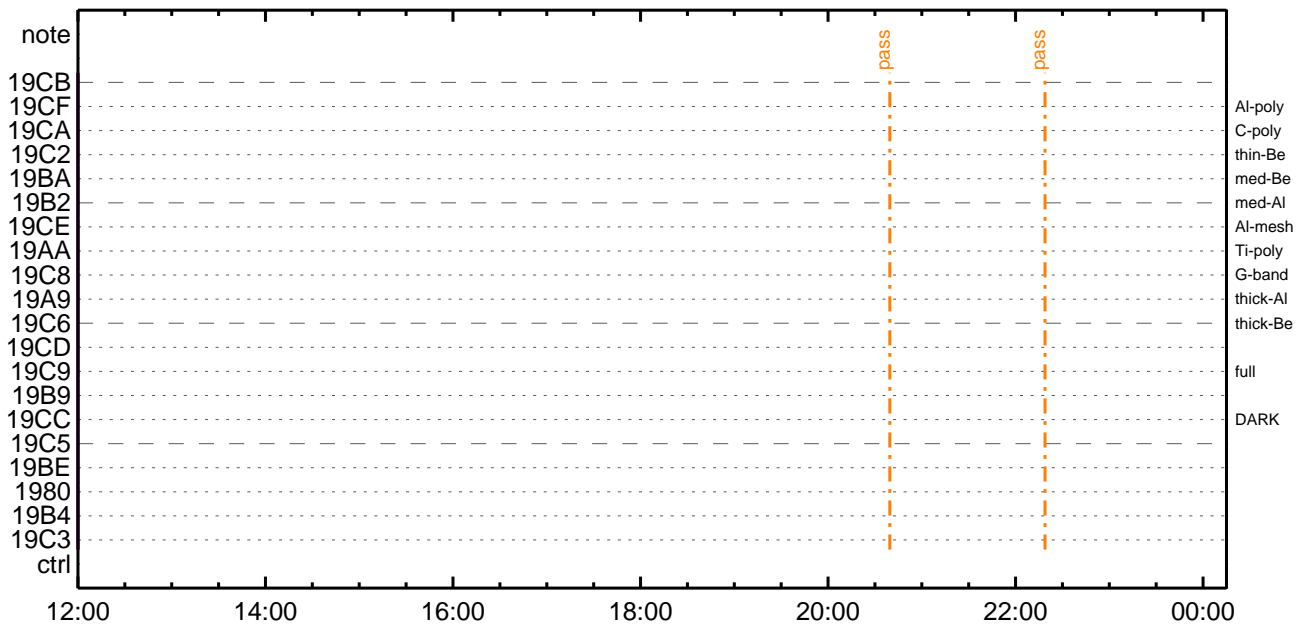
CMDI #0782 2013/09/30



CMDI #0782 2013/10/01



CMDI #0782 2013/10/01




```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-926:OP
0104 ( )
0105 S. OG og-926:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYôYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYôYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYôYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYôYx½ªî»ò³îÇ§²¼òî½A´¶Á°òEÉ¬ò°Á÷¿@) *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²¬Á÷¿@NG²î½E¹ç;ç°E²¼òî½TI-CMDÁ÷¿@²¼òî½A´¶Á°òEÉ¬ò°Á÷¿@;f
0180 C. ²¼òî½;çSET²EEDUMP²î½E¹ç°E²¼òî½A´¶Á°òEÉ¬ò°Á÷¿@;f
0181 C.
0182 C. TIY³Y½YôYÉòòÁDî¿(UT)
0183 +. TI 2013-09-26 10:31:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-09-26 10:31:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-09-26 10:31:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```



```

0194 C.
0195 +. TI 2013-09-26 10:35:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.        çç[HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼îÄë%îíñîîŷÄŷ§ŷÄŷ⁻¹àîÛ
0200 C.        çç[HK1_TI_CMD_ENA/DIS]                    EQ      ENA
0201 C.        çç[HK1_TI_CMD_NUM]                        EQ      4
0202 C.        çç[HK1_NEXT_EXEC_PIM]                     EQ      DHU
0203 C.        çç[HK1_NEXT_EXEC_DC]                       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.        çç[HK1_DMP_TOP_ADRS_1]                    EQ      07
0213 C.        çç[HK1_DMP_TOP_ADRS_0]                    EQ      2B
0214 C.        çç[HK1_DMP_BLOCK_NUM]                      EQ      3
0215 C.        çç[HK1_DMP_REPEAT_NUM]                    EQ      0
0216 C.        çç[HK1_DMA_DMP_PIM]                       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.        çç[HK1_PKT_FORM_NO]                       EQ      7
0220 C.        çç[HK1_PKT_GEN_TIME]                       EQ      0.25 s
0221 C.        çç[HK1_S_TLM_BIT_RATE]                    EQ      32k
0222 C.        çç[HK1_X_TLM_BIT_RATE]                    EQ      4M
0223 C.        çç[HK1_DMP_CHK_FLG]                       EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»ò³îÇ§
0226 C.        çç[HK1_DMP_CHK_FLG]                       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLîî½È¹ç•è²îOKò³îÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ½. ŷî;¼ŷÈ;Èòðîãª¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.        çç[HK1_PKT_FORM_NO]                       EQ      2
0234 C.        çç[HK1_PKT_GEN_TIME]                       EQ      0.5S
0235 C.        çç[HK1_S_TLM_BIT_RATE]                    EQ      32K
0236 C.        çç[HK1_X_TLM_BIT_RATE]                    EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2013-09-26 10:35:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C.      HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2013-09-26 10:35:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2013-09-26 10:35:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.        [ ] [HK1_TI_CMD_NUM]                      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2013-09-26 10:35:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.        [ ] [HK1_TI_CMD_NUM]                      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîãî»ò¼ŷòÈÃðª¹èDCBC•x²è *****
0276 C. (¼ªºîŷÖŷÄŷÈŷŷŷÄŷŷçŷèè¼ªª¼ª»Ûª¹è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•î Daily±çîñîèË'òª¹èDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ⁻¹¼ª»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-928 2013-09-26 14:36:00 132 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èα¿αÁα•μ°È»Í×ÁÇαÍYçYÁY×Yí;¼YÉ;ÈÈ%μ•ííÈ;ÈÈ¼°ÇÔα•α¿¼í¹çαÍ;çÀ®, ùα¹αÈαBαÇÁ+¿®α•αÈααα³αÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-267:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2013-09-26 10:35:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR ____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 06 06)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 85 83 08 08)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 85 83 10 10)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 20 20)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 80 80 20 08)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0b 80 80 08 20)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0f 80 80 06 06)
0088 + DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 10 80 80 08 08)
0090 + DC 07-F0 MDP_XRT_FLD_ENA
0091 BC (d8)
0092 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0093 BC (c8)
0094 + DC 07-F0 MDP_XRT_AEC_RESET
0095 BC (d0)
```

0096 + DC 07-F0 MDP_XRT_ARS_DIS
0097 BC (d5)
0098 + DC 07-F0 MDP_XRT_FLD_RESET
0099 BC (da)
0100 + DC 07-F0 MDP_XRT_QT_PROG_SET
0101 BC (c4 13)
0102 + DC 07-F0 MDP_XRT_FL_PROG_SET
0103 BC (c5 0c)
0104 . C. ----- Success Verify ? OK / NG ____
0105 C.
0106 C.
0107 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0108 C.
0109 +. DC 07-F0 MDP_XRT_MODE_OBSV
0110 BC (c2)
0111 +. TI 2013-09-26 10:35:02.0
0112 DC 07-F0 MDP_XRT_MODE_OBSV
0113 BC (c2)
0114 . C. ----- Success Verify ? OK / NG ____
0115 C.
0116 C. ***** XRT END *****
0117 C.
0118 . C. ***** MDP 'úÃîñî»ö¼ÿñÊÂðñ¹ñèDCBC•x²è *****
0119 C. (¼ã°îÿÓÿÃÿÊÿPÿEÿâÿçÿèñ¼ññ¼Ã»Ûñ¹ñè)
0120 . S. DC-BC dcbc-402:DCBC
0121 (MDP_known_event)
0122 C.
0123 C.
0124 . C. ***** ÿDÿ¹·ÿÿ Daily±;îññè'øñ¹ñèDCBC•x²è *****
0125 . S. DC-BC dcbc-153:DCBC
0126 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0127 C.
0128 C.
0129 . C. ;ãLOSÿÃÿSÿÿÃÿ-¼Ã»Û;ã
0130 C.
0131 . C. ***** LOS *****
0132 C.

Sep 26, 13 14:36

XRT_OGLIST_0782.chk

Page 1/6

*** OP Sequence for XRT ***

2013/09/26	10:45:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/26	10:45:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/09/26	10:46:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	04 00 00 00 00
2013/09/26	10:46:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/09/26	10:46:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/09/26	10:46:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/09/26	10:46:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/09/26	10:46:24.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/26	10:46:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2013/09/26	10:48:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c
2013/09/26	10:49:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/26	11:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/26	11:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/09/26	12:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 cf 03 bf 03
2013/09/26	12:00:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/09/26	12:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/09/26	12:00:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/09/26	12:00:22.0	XRT_ARS_DIS_417_OG [0x1a1]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/09/26	12:02:54.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/26	12:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05
2013/09/26	12:02:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c
2013/09/26	12:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/26	15:04:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/26	15:04:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/26	15:04:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/26	15:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/26	15:27:00.0	XRT_Custom_430_OG [0x1ae]			
2013/09/26	15:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/26	15:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/26	15:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/09/26	16:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	04 00 00 00 00
2013/09/26	16:00:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/09/26	16:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/09/26	16:00:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/09/26	16:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/09/26	16:00:24.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/26	16:00:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2013/09/26	16:02:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c
2013/09/26	16:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/26	16:40:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/26	16:40:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/26	16:40:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/26	16:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/26	17:04:00.0	XRT_Custom_430_OG [0x1ae]			
2013/09/26	17:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/26	17:53:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/26	17:53:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			

Sep 26, 13 14:36

XRT_OGLIST_0782.chk

Page 2/6

2013/09/26	17:54:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		AOCU_NM		5	02-76	00	00	00	00
2013/09/26	17:54:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/09/26	17:56:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/09/26	17:56:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/09/26	17:56:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09		
2013/09/26	17:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/26	18:03:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/26	18:03:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2013/09/26	18:04:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04	00	00	00
2013/09/26	18:04:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/09/26	18:04:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/09/26	18:04:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/09/26	18:04:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/09/26	18:04:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/09/26	18:04:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02		
2013/09/26	18:06:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0c		
2013/09/26	18:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/26	18:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/26	18:17:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/09/26	18:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/09/26	18:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/09/26	18:40:30.0	XRT_Custom_430_OG [0x1ae]							
2013/09/26	18:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/26	19:54:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/26	19:54:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/09/26	19:54:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/09/26	19:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/09/26	20:18:00.0	XRT_Custom_430_OG [0x1ae]							
2013/09/26	20:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/26	21:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/26	21:31:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/09/26	21:31:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/09/26	21:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/09/26	21:54:30.0	XRT_Custom_430_OG [0x1ae]							
2013/09/26	21:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/26	23:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/26	23:09:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/09/26	23:09:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/09/26	23:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/09/26	23:24:30.0	XRT_Custom_430_OG [0x1ae]							
2013/09/26	23:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/27	00:41:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/27	00:41:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/09/27	00:41:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/09/27	00:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/09/27	00:52:30.0	XRT_Custom_430_OG [0x1ae]							
2013/09/27	00:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/27	02:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/27	02:08:32.0	XRT_FLD_RESET_415_OG [0x19f]							

Sep 26, 13 14:36

XRT_OGLIST_0782.chk

Page 3/6

2013/09/27	02:08:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/27	02:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/27	02:29:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/27	02:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/27	03:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/27	03:36:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/27	03:36:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/27	03:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/27	04:06:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/27	04:07:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/27	05:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/27	05:16:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/27	05:16:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/27	05:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/27	05:43:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/27	05:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/09/27	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	AOCS_Ore-point_Start_3_OG [0x099]	5	02-76	00 00 00 00 00
2013/09/27	06:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2013/09/27	06:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9
2013/09/27	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/09/27	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/09/27	06:02:58.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2013/09/27	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/27	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/27	06:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/09/27	06:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00
2013/09/27	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/09/27	06:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/09/27	06:10:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/09/27	06:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/09/27	06:10:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/27	06:10:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2013/09/27	06:12:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c
2013/09/27	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/27	06:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/27	06:57:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/27	06:57:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/27	07:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/27	07:21:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/27	07:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/27	08:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/27	08:37:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/27	08:37:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/27	08:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/27	08:57:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/27	08:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/09/27	10:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	04 00 00 00 00
2013/09/27	10:18:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da

Sep 26, 13 14:36

XRT_OGLIST_0782.chk

Page 4/6

2013/09/27	10:18:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/09/27	10:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/09/27	10:27:00.0	XRT_Custom_430_OG [0x1ae]							
2013/09/27	10:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/27	14:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/27	14:07:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/09/27	14:07:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/09/27	14:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/09/27	14:13:30.0	XRT_Custom_430_OG [0x1ae]							
2013/09/27	14:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/27	15:40:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/27	15:40:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/09/27	15:40:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/09/27	15:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/09/27	16:03:30.0	XRT_Custom_430_OG [0x1ae]							
2013/09/27	16:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/27	17:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/27	17:16:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/09/27	17:16:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/09/27	17:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/09/27	17:40:00.0	XRT_Custom_430_OG [0x1ae]							
2013/09/27	17:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/27	17:57:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/27	17:57:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2013/09/27	17:57:30.0	AOCS_Or-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00			
2013/09/27	17:57:46.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/09/27	18:00:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/09/27	18:00:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/09/27	18:00:28.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09			
2013/09/27	18:00:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/27	18:07:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/27	18:07:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2013/09/27	18:07:30.0	AOCS_Or-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00			
2013/09/27	18:07:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/09/27	18:07:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/09/27	18:07:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/09/27	18:07:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/09/27	18:07:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/09/27	18:07:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02			
2013/09/27	18:10:28.0	XRT_FL_PROG_SET_443_OG [0x1bb]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c			
2013/09/27	18:10:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/27	18:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/27	18:53:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/09/27	18:53:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/09/27	18:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/09/27	19:17:00.0	XRT_Custom_430_OG [0x1ae]							
2013/09/27	19:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/09/27	20:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/09/27	20:31:02.0	XRT_FLD_RESET_415_OG [0x19f]							

Sep 26, 13 14:36

XRT_OGLIST_0782.chk

Page 5/6

2013/09/27	20:31:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/27	20:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/27	20:54:30.0	XRT_Custom_430_OG [0x1ae]				
2013/09/27	20:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/27	22:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/27	22:08:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/27	22:08:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/27	22:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/27	22:30:00.0	XRT_Custom_430_OG [0x1ae]				
2013/09/27	22:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/27	23:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/27	23:45:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/27	23:45:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/27	23:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/27	23:55:30.0	XRT_Custom_430_OG [0x1ae]				
2013/09/27	23:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/28	01:11:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/28	01:11:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/28	01:11:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/28	01:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/28	01:28:30.0	XRT_Custom_430_OG [0x1ae]				
2013/09/28	01:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/28	02:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/28	02:44:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/28	02:44:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/28	02:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/28	03:05:30.0	XRT_Custom_430_OG [0x1ae]				
2013/09/28	03:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/28	04:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/28	04:14:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/28	04:14:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/28	04:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/28	04:42:30.0	XRT_Custom_430_OG [0x1ae]				
2013/09/28	04:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/28	05:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/28	05:54:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/09/28	05:54:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/09/28	05:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/09/28	06:20:00.0	XRT_Custom_430_OG [0x1ae]				
2013/09/28	06:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/28	06:30:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/28	06:30:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/09/28	06:30:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00
2013/09/28	06:30:46.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9
2013/09/28	06:33:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/09/28	06:33:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/09/28	06:33:28.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2013/09/28	06:33:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/09/28	06:40:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/09/28	06:40:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]				

Sep 26, 13 14:36

XRT_OGLIST_0782.chk

Page 6/6

2013/09/28	06:40:30.0	AOCS_Or-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
			AOCU_NM	5	02-76	04	00	00	00
2013/09/28	06:40:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0		d8		
2013/09/28	06:40:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2013/09/28	06:40:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0		d0		
2013/09/28	06:40:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0		d5		
2013/09/28	06:40:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0		da		
2013/09/28	06:40:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0		c4	02	
2013/09/28	06:43:28.0	XRT_FL_PROG_SET_443_OG [0x1bb]	MDP_XRT_FL_PROG_SET	2	07-F0		c5	0c	
2013/09/28	06:43:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2013/09/28	07:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2013/09/28	07:34:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0		da		
2013/09/28	07:34:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2013/09/28	07:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2013/09/28	07:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2013/09/28	07:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2013/09/28	08:00:00.0	AOCS_Or-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	af	4c	fa
2013/09/28	08:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0		d8		
2013/09/28	08:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2013/09/28	08:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0		d0		
2013/09/28	08:00:22.0	XRT_ARS_DIS_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0		d5		
2013/09/28	08:02:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0		da		
2013/09/28	08:02:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0		c4	01	
2013/09/28	08:02:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]	MDP_XRT_FL_PROG_SET	2	07-F0		c5	0c	
2013/09/28	08:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2013/09/28	09:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2013/09/28	09:14:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0		da		
2013/09/28	09:14:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2013/09/28	09:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2013/09/28	09:32:30.0	XRT_Custom_430_OG [0x1ae]							
2013/09/28	09:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2013/09/28	09:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1		
2013/09/28	09:48:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0		da		
2013/09/28	09:48:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2013/09/28	09:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2013/09/28	10:22:00.0	AOCS_Or-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00	00	00	00