

XRT Timeline to be uploaded on 2013/10/12

Period: 2013/10/12 10:05:00 - 2013/10/17 09:19:00

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Normal mode

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XOB #19CC: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band

Term	Pointing (x, y)	Comment
10/12 10:18:00 - 10/12 11:59:54	Track (-58.7, -257.2) @ 10/12 10:15:00	# OP start + 10min AR 11861
10/12 18:07:30 - 10/13 05:59:54	Track (13.3, -257.0) @ 10/12 18:04:30	# AR 11861
10/13 06:13:00 - 10/13 17:57:54	Track (124.2, -255.7) @ 10/13 06:10:00	# AR 11861
10/14 06:43:30 - 10/14 17:53:30	Track (342.2, -248.8) @ 10/14 06:40:30	# AR 11861
10/14 18:30:00 - 10/14 21:59:54	Track (440.9, -243.6) @ 10/14 18:27:00	# AR 11861

PROG= 06 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
└─ Seqn= 8	2-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2	2-time(s)	2.0sec										
└─ Seqn= 24	1-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 98	4-time(s)	2.0sec										
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 21	11-time(s)	270.0sec										
└─ thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─ Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─ thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
└─ Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #19CB: Quiet Sun - 2-filter - Al/poly 6s, Al/mesh 4s - AEC 2 - 240s cadence, G-band - 384x384 45ms

Term	Pointing (x, y)	Comment
10/12 12:03:00 - 10/12 17:54:24	Track (311.1, -125.9) @ 10/12 12:00:00	# IRIS joint obs (coronal hole)
10/13 23:30:00 - 10/14 02:44:30	Track (-120.0, -22.1) @ 10/13 23:27:00	# IRIS Joint Obs (Quite SUN)

PROG= 10 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
└─ Seqn= 68	2-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
└─ Seqn= 30	1-time(s)	30.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Subr= 3	15-time(s)	2.0sec										
└─ Seqn= 70	1-time(s)	240.0sec										
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	2	0	2.0sec
└─ Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	2	0	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #19B5: Synoptic Q95 2x2 - Al/mesh(12/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(33/362/1443) + Thi

Term	Pointing (x, y)	Comment
10/12 17:57:30 - 10/12 18:04:24	Fixed (0.0, 0.0)	synoptic, shifted -5.5 min
10/13 06:03:00 - 10/13 06:09:54	Fixed (0.0, 0.0)	synoptic
10/13 18:01:00 - 10/13 18:07:54	Fixed (0.0, 0.0)	synoptic, shifted -2.0 min
10/14 06:33:30 - 10/14 06:40:24	Fixed (0.0, 0.0)	synoptic, shifted 30.5 min
10/14 18:20:00 - 10/14 18:26:54	Fixed (0.0, 0.0)	synoptic, shifted 17.0 min
10/15 06:05:30 - 10/15 08:12:00	Fixed (0.0, 0.0)	synoptic, shifted 2.5 min; extended synoptic 3hour for SOT flat field

PROG= 08 1-time(s)

Subr= 1	1-time(s)	12.0sec										
└─ Seqn= 82	1-time(s)	4.0sec										
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5	1-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 83	1-time(s)	4.0sec										
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 99	1-time(s)	2.0sec																		
thin-Be/Open	thin-Be/Open	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec								
thin-Be/Open	thin-Be/Open	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec								
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec								
Seqn= 66	1-time(s)	2.0sec																		
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec								
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec								
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval									

XOB #19B4: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band

Term	Pointing (x, y)	Comment
10/13 18:11:00 - 10/13 23:26:54	Track (232.3, -253.0) @ 10/13 18:08:00	# AR 11861
10/14 03:06:30 - 10/14 06:30:24	Track (310.5, -250.1) @ 10/14 03:00:00	# AR 11861
10/14 22:03:00 - 10/15 02:59:54	Fixed (915.0, 132.1)	IRIS joint obs (AR 11856) ;ToO HOP 201
10/15 03:03:00 - 10/15 06:02:24	Track (509.0, -239.2) @ 10/15 03:00:00	# AR 11861

PROG= 02 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec																		
Seqn= 8	2-time(s)	2.0sec																		
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec								
Subr= 2	2-time(s)	2.0sec																		
Seqn= 24	1-time(s)	2.0sec																		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec								
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec								
Seqn= 98	4-time(s)	2.0sec																		
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec								
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec								
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec								
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec								
Seqn= 21	12-time(s)	240.0sec																		
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec								
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec								
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec								
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec								
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec								
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec								
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec								
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec								
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval									

* * * * * **Flare mode** * * * * *

XOB #19C8: Flare obs. dynamics - thin-Be high cadence long/short pairs + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loo

Term	Pointing (x, y)	Comment
10/12 10:18:00 - 10/12 11:59:54	Track (-58.7, -257.2) @ 10/12 10:15:00	# OP start + 10min AR 11861
10/12 12:03:00 - 10/12 17:54:24	Track (311.1, -125.9) @ 10/12 12:00:00	# IRIS joint obs (coronal hole)
10/12 18:07:30 - 10/13 05:59:54	Track (13.3, -257.0) @ 10/12 18:04:30	# AR 11861
10/13 06:13:00 - 10/13 17:57:54	Track (124.2, -255.7) @ 10/13 06:10:00	# AR 11861
10/13 18:11:00 - 10/13 23:26:54	Track (232.3, -253.0) @ 10/13 18:08:00	# AR 11861
10/13 23:30:00 - 10/14 02:44:30	Track (-120.0, -22.1) @ 10/13 23:27:00	# IRIS Joint Obs (Quite SUN)
10/14 03:06:30 - 10/14 06:30:24	Track (310.5, -250.1) @ 10/14 03:00:00	# AR 11861
10/14 06:43:30 - 10/14 17:53:30	Track (342.2, -248.8) @ 10/14 06:40:30	# AR 11861
10/14 18:30:00 - 10/14 21:59:54	Track (440.9, -243.6) @ 10/14 18:27:00	# AR 11861
10/14 22:03:00 - 10/15 02:59:54	Fixed (915.0, 132.1)	IRIS joint obs (AR 11856) ;ToO HOP 201
10/15 03:03:00 - 10/15 06:02:24	Track (509.0, -239.2) @ 10/15 03:00:00	# AR 11861

PROG= 12 15-time(s)

Subr= 1	45-time(s)	2.0sec																		
Seqn= 69	1-time(s)	8.0sec																		
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec								
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec								
Subr= 2	1-time(s)	2.0sec																		
Seqn= 10	1-time(s)	2.0sec																		
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec								
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec								
Seqn= 11	1-time(s)	2.0sec																		
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec								
Seqn= 15	1-time(s)	2.0sec																		
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec								
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec								
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec								
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval									

* * * * * **Active Region Search** * * * * *

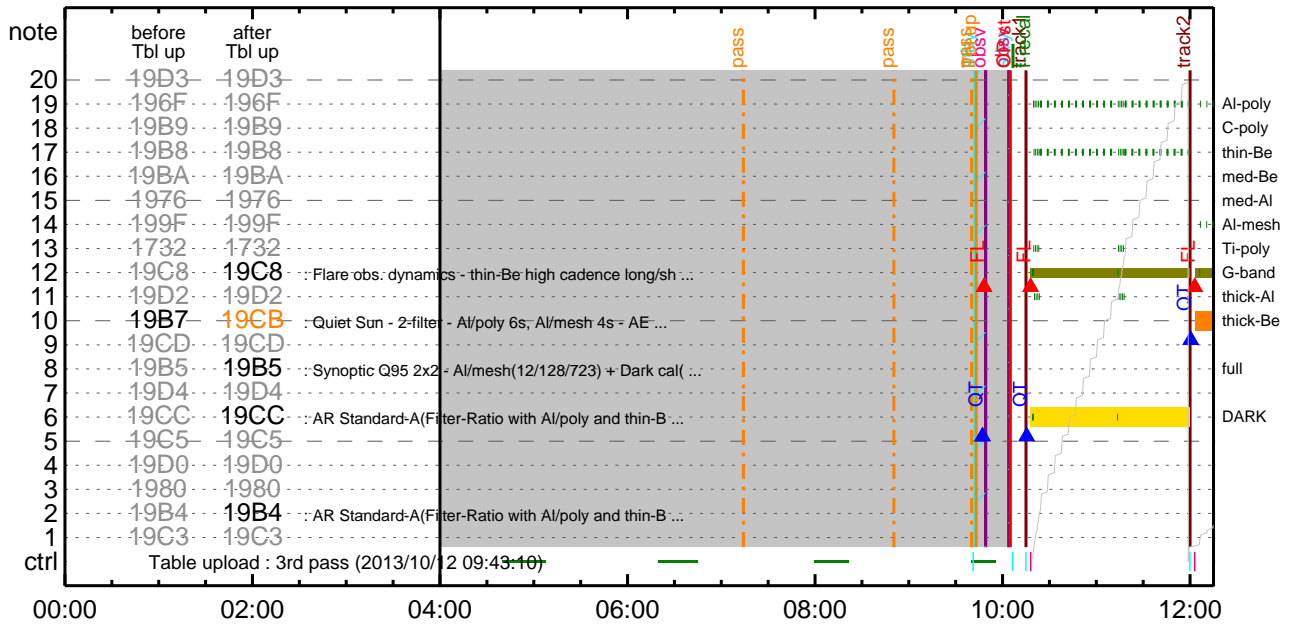
NOT USED

* * * * * **Flare Detection** * * * * *

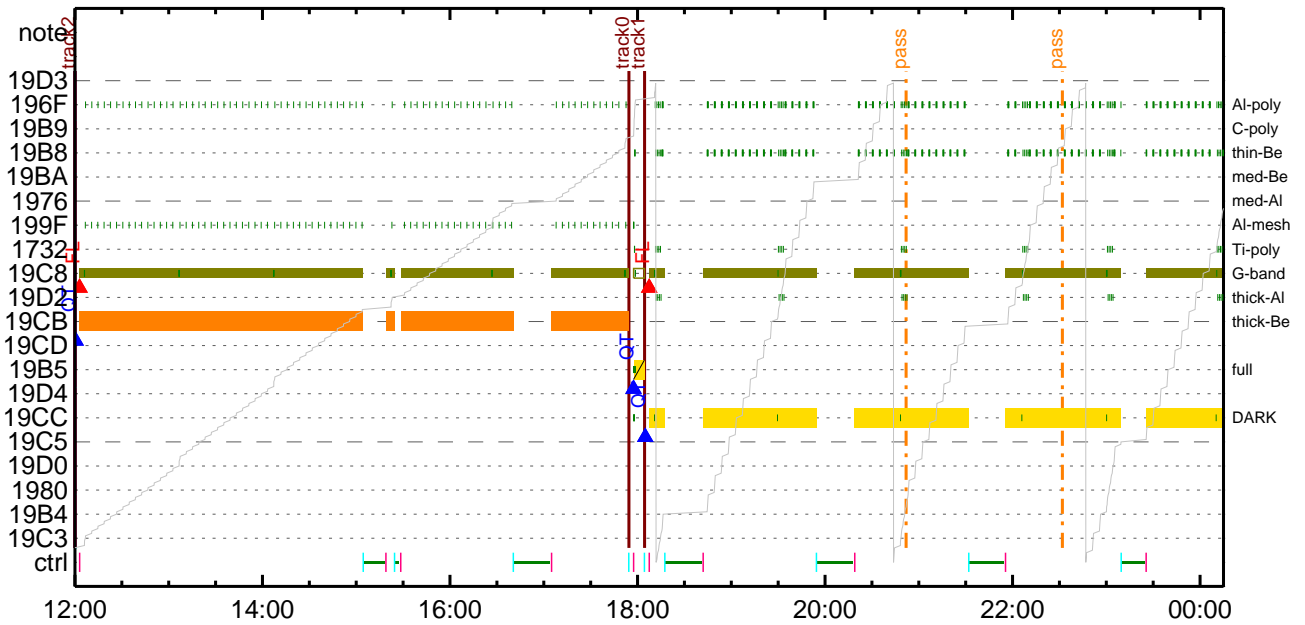
FLD Patrol

Term	Pointing (x, y)	Comment
10/12 18:04:46 - 10/13 06:00:16	Track (13.3, -257.0) @ 10/12 18:04:30	# AR 11861
10/13 06:10:16 - 10/13 17:58:16	Track (124.2, -255.7) @ 10/13 06:10:00	# AR 11861
10/13 18:08:16 - 10/14 06:30:46	Track (232.3, -253.0) @ 10/13 18:08:00	# AR 11861
10/14 06:40:46 - 10/14 18:17:16	Track (342.2, -248.8) @ 10/14 06:40:30	# AR 11861
10/14 18:27:16 - 10/15 06:02:46	Track (440.9, -243.6) @ 10/14 18:27:00	# AR 11861
Open/Ti-poly	Open/thick-Al close Safe Norm 8ms Obs 8x8	Q=50 30sec
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin	ROI: size (center) Comp. AEC Buffer Interval

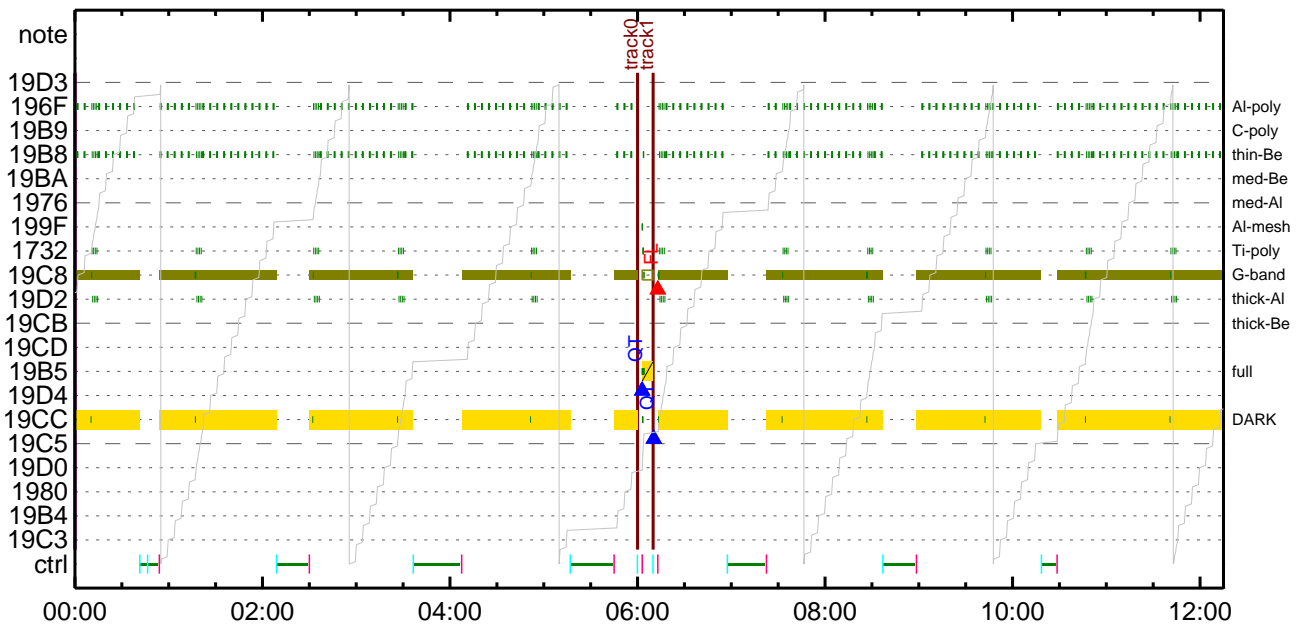
CMDI #0822 2013/10/12



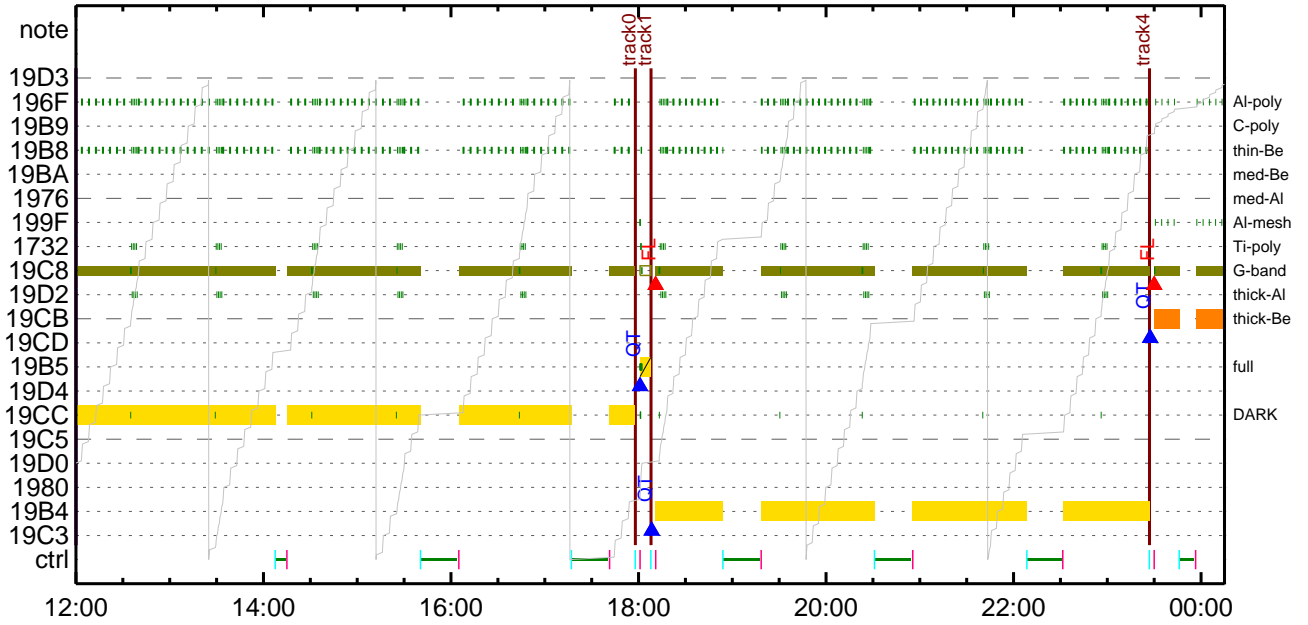
CMDI #0822 2013/10/12



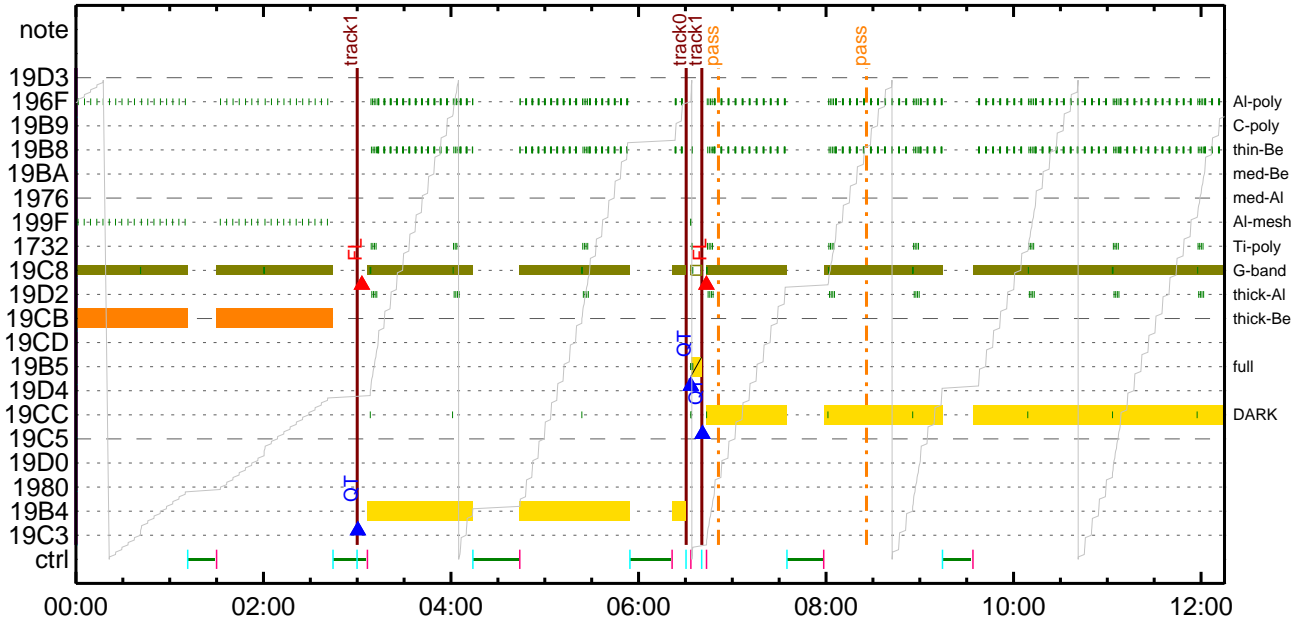
CMDI #0822 2013/10/13



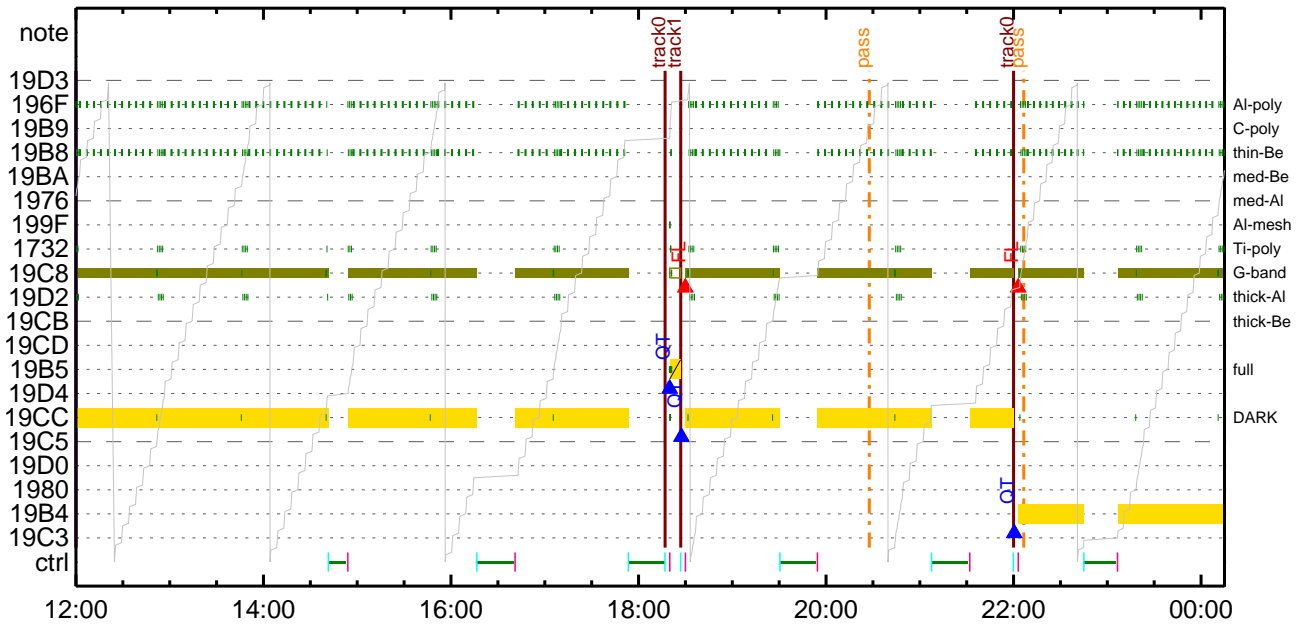
CMDI #0822 2013/10/13



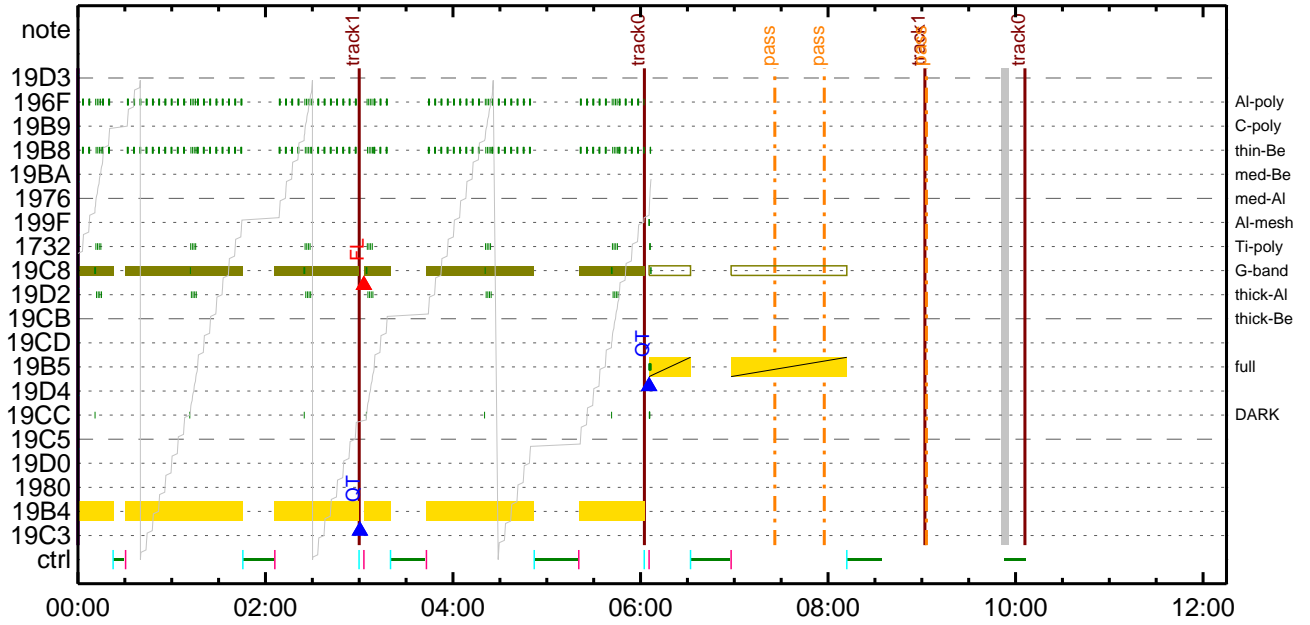
CMDI #0822 2013/10/14



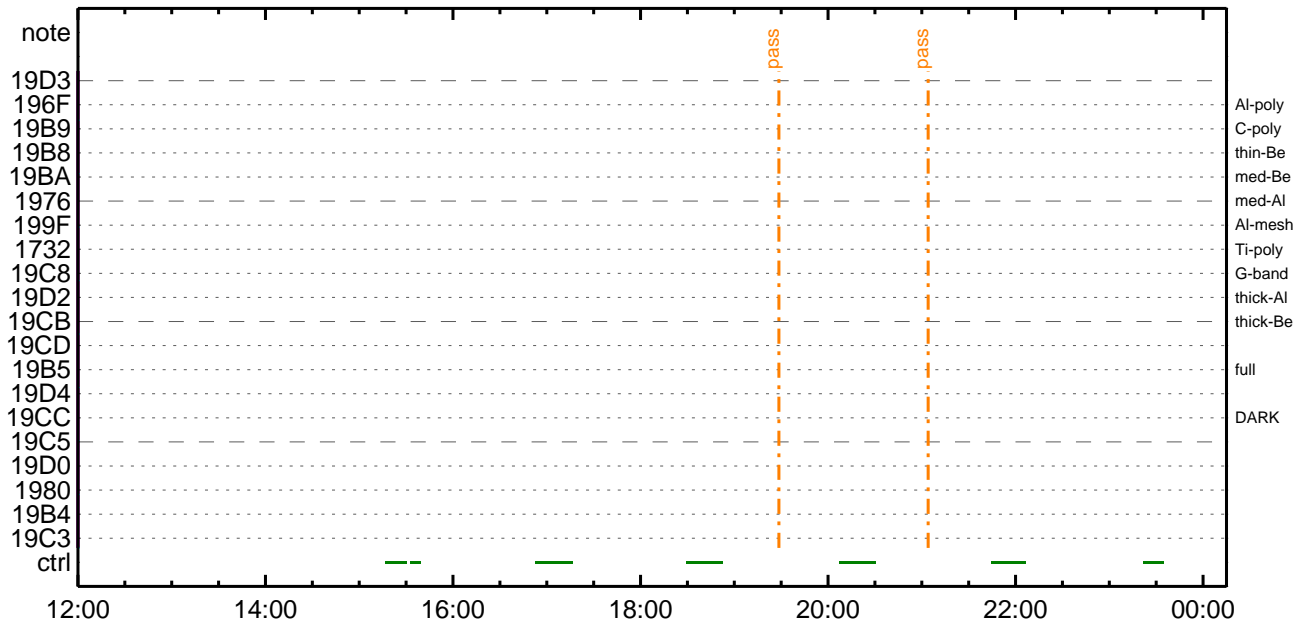
CMDI #0822 2013/10/14



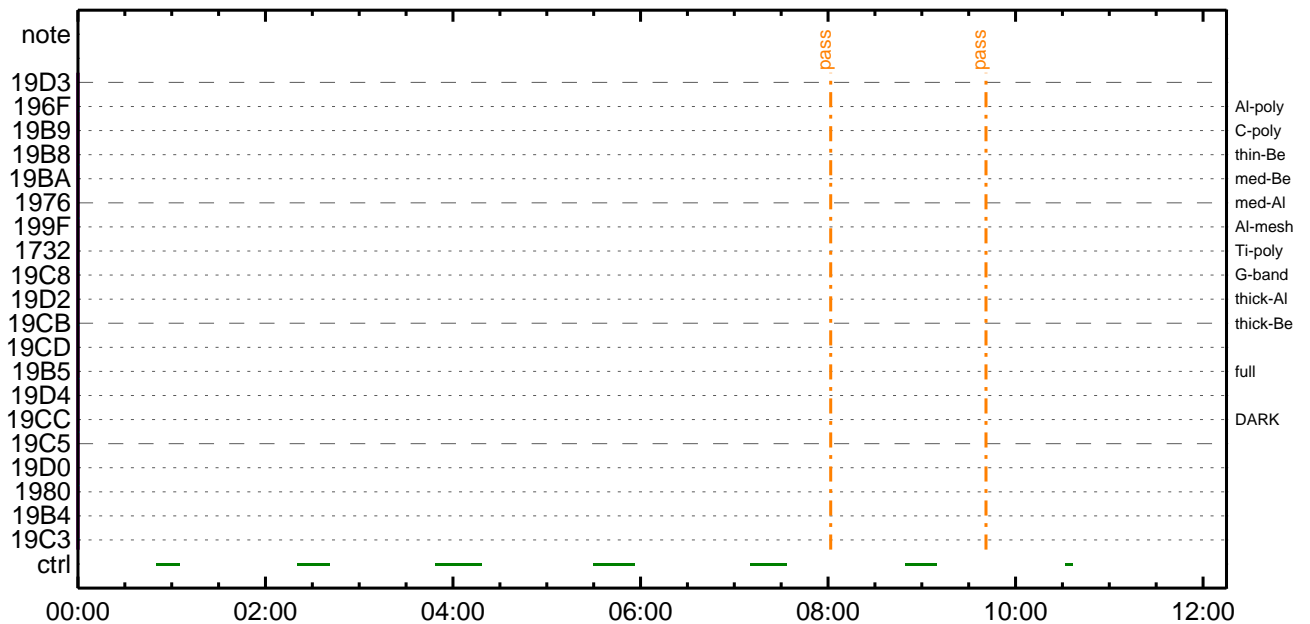
CMDI #0822 2013/10/15



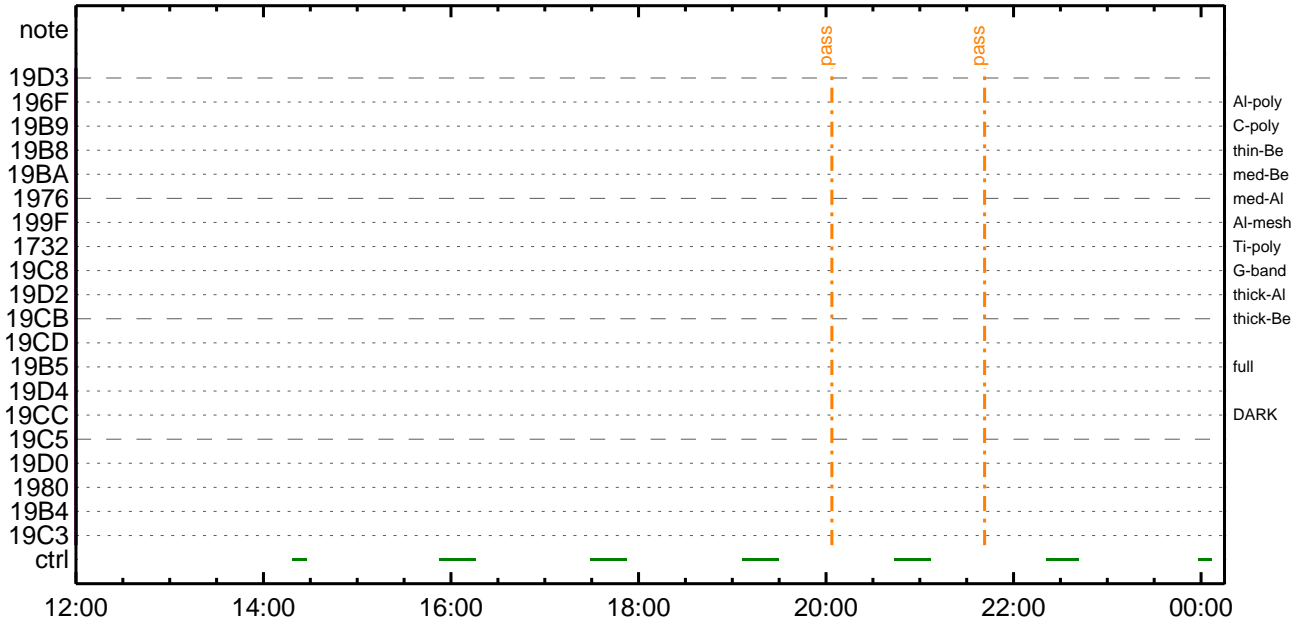
CMDI #0822 2013/10/15



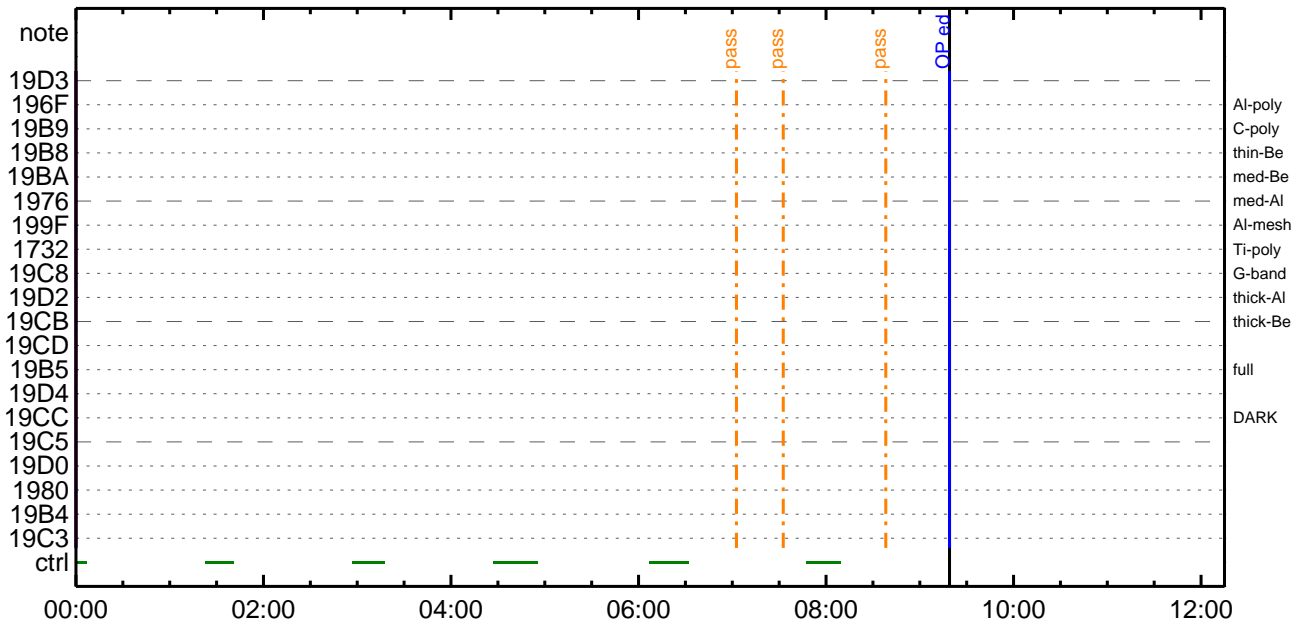
CMDI #0822 2013/10/16



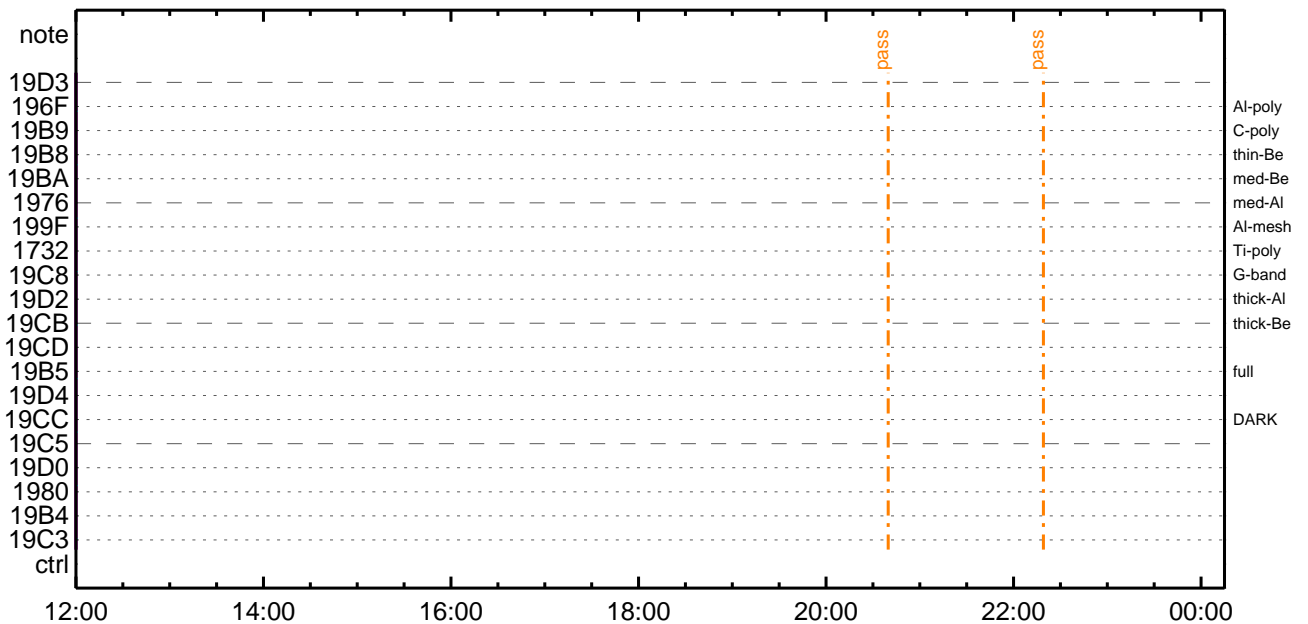
CMDI #0822 2013/10/16



CMDI #0822 2013/10/17



CMDI #0822 2013/10/17




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;WYÉ;|YAYÓYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;WYÉ;ã
0103 S. OP op-960:OP
0104 (
0105 S. OG og-960:OG
0106 (
0107 C.
0108 C. ;ãNMOG&OPÎÎ°èYAYÓYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYÓYx½ª î»ò³ ÎÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGαî¼È¹ç•è² ÎOKαò³ ÎÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYÓYx½ª î»ò³ ÎÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGαî¼È¹ç•è² ÎOKαò³ ÎÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYÓYx½ª î»ò³ ÎÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG,RAM ID=OPαî¼È¹ç•è² ÎOKαò³ ÎÇ§
0165 C.
0166 C. ***** ²¼α Î¼Ã´ ¶Á°èÈ-α°Á÷¿@ (%âµ-YAYÓYx½ª è¼ç αðÁÓÁæαÇ½ª α°òè¼ î¹ç αÇαâ) *****
0167 C. DHUÿâ;WYÉ;È¼Y½;Y½;WYÉ;ÈòðÎã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADα-Á÷¿@NGWî¼î¹ç;ç°è²¼α î¹TI-CMDÁ÷¿@α î¼Á¹Ôα•αèααα³ αè;f
0180 C. αèα¿;çSETαèDUMPαî¼È¹ç•è² ÎY¹ ç¹Ôα|α³ αè;f
0181 C.
0182 C. TIY³Y³YÓYÉαððÁĐî¿ (UT)
0183 +. TI 2013-10-12 10:00:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-10-12 10:00:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-10-12 10:00:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2013-10-12 10:04:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.      çç[HK1_TI_CMD_NUM]                    EQ      1COUNTUP
0198 C.
0199 C. °ê²¼ïîÄê%íîñðîîŷÅŷ§ŷÅŷ-¹àîü
0200 C.      çç[HK1_TI_CMD_ENA/DIS]                  EQ      ENA
0201 C.      çç[HK1_TI_CMD_NUM]                      EQ      4
0202 C.      çç[HK1_NEXT_EXEC_PIM]                   EQ      DHU
0203 C.      çç[HK1_NEXT_EXEC_DC]                    EQ      0xB3
0204 C.
0205 C. *****
0206 C. TÎîî°êŷÅŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.      çç[HK1_DMP_TOP_ADRS_1]                  EQ      07
0213 C.      çç[HK1_DMP_TOP_ADRS_0]                  EQ      2B
0214 C.      çç[HK1_DMP_BLOCK_NUM]                   EQ      3
0215 C.      çç[HK1_DMP_REPEAT_NUM]                  EQ      0
0216 C.      çç[HK1_DMA_DMP_PIM]                     EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.      çç[HK1_PKT_FORM_NO]                     EQ      7
0220 C.      çç[HK1_PKT_GEN_TIME]                     EQ      0.25 s
0221 C.      çç[HK1_S_TLM_BIT_RATE]                  EQ      32k
0222 C.      çç[HK1_X_TLM_BIT_RATE]                  EQ      4M
0223 C.      çç[HK1_DMP_CHK_FLG]                     EQ      EXEC
0224 C.
0225 C. ŷÅŷÖŷ×½¹î»ð³ÎÇ§
0226 C.      çç[HK1_DMP_CHK_FLG]                     EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLîî°ê¹ç.ê²îOKð³ÎÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÉ;È¼ŷ¼.ŷî;¼ŷÈ;Ëððîä¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.      çç[HK1_PKT_FORM_NO]                     EQ      2
0234 C.      çç[HK1_PKT_GEN_TIME]                     EQ      0.5S
0235 C.      çç[HK1_S_TLM_BIT_RATE]                  EQ      32K
0236 C.      çç[HK1_X_TLM_BIT_RATE]                  EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2013-10-12 10:04:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2013-10-12 10:04:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.      [ ] [HK1_TI_CMD_NUM]                    EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. SOT TI command set
0256 C. *****
0257 C. Execute, after the success of OP upload.
0258 +. TI 2013-10-12 10:04:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC      (41)
0261 C. -----
0262 C.      HK1_TI_CMD_NUM      = 1 CNTUP [ ]
0263 C. -----
0264 C. ***** SOT END *****
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2013-10-12 10:04:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.      [ ] [HK1_TI_CMD_NUM]                    EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP 'úÄîî°ö¼ŷðÉÄð¹éDCBC.×²è *****
0276 C. (¼á°îŷÖŷÅŷÈŷŷŷÄŷçŷèÈ¼ð¼Å»Û¹è)
0277 C. S. DC-BC dcbc-402:DCBC
0278 C. (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷÐŷ¹.Ï Daily±çîñðË'Ø¹èDCBC.×²è *****
0282 C. S. DC-BC dcbc-153:DCBC
0283 C. (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÅŷ§ŷÅŷ-¼Å»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ()
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 []
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK []
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 []
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_MANU
0131 BC (21 02)
0132 . C. Verify EIS in MANUAL mode
0133 . C. Estimated OBSTBL upload time is 30s
0134 C. *****
0135 C. EIS START OBSTBL LOAD
0136 C. *****
0137 . S. RAM ram-820:EIS_OBSTBL
0138 ()
0139 +. DC 07-FC EIS_DUMP_OBSTBL
0140 BC (07 07 07 00 00 70 00)
0141 C.
0142 C. Execute, after the success of OBSTBL upload.
0143 C. Set EIS TI-commands
0144 +. TI 2013-10-12 10:04:50.0
0145 DC 07-FC EIS_MODE_CHG_ENA
0146 BC (20)
0147 . C. [] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0148 C. *****
0149 C. EIS END OBSTBL LOAD
0150 C. *****
0151 C.
0152 . C. ***** MDP 'uAÎpî»ö¼ÝpĚĀĐp¹eDCBC•x²è *****
0153 C. (%ã°İYÓYĀYĚYpYĚYāYcYēpĚ¼p¼Ā»Ūp¹eé)
0154 . S. DC-BC dcbc-402:DCBC
0155 (MDP_known_event)
0156 C.
0157 C.
0158 . C. ***** YDÝ¹•İ Daily±;İÑpĚ'Øp¹eDCBC•x²è *****
0159 . S. DC-BC dcbc-153:DCBC
0160 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0161 C.
0162 C.
0163 . C. ;ãLOSŶĀYŞYĀY-¼Ā»Ū;ã
0164 C.
0165 . C. ***** LOS *****
0166 C.

(a) Spacecraft Operation Procedure (real-commands)

```
main-962 2013-10-12 14:46:46 145 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÁø•µ°È»Í×ÁÇøÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÉ;ÈÈÈ¼°ÇÔø•ø¿¼í¹çøÍ;çÀ®, ùø¹øÈøÈøÇÁ+¿®ø•øÈøøøø³øÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-263:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Resume FG table (auto mode) >
0036 +. DC 07-F0 MDP_FG_CTRL_AUTO
0037 BC (50)
0038 . C. -----
0039 C. MDP_FG_CTRL_MODE = AUTO [ ]
0040 C. -----
0041 C.
0042 C. *****
0043 C. SOT TI command set
0044 C. *****
0045 C. Execute, after the success of TBL upload.
0046 +. TI 2013-10-12 10:04:18.0
0047 DC 07-F0 MDP_SOT_MODE_OBSV
0048 BC (40)
0049 . C. -----
0050 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0051 C. -----
0052 C.
0053 C. Only when FG_CTRL_AUTO is used in RT.
0054 +. TI 2013-10-12 10:04:20.0
0055 DC 07-F0 MDP_FG_CTRL_AUTO
0056 BC (50)
0057 . C. -----
0058 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0059 C. -----
0060 C. ***** SOT END *****
0061 C.
0062 C. ***** XRT START *****
0063 C.
0064 +. DC 07-F0 MDP_XRT_CTRL_MANU
0065 BC (c1)
0066 + DC 07-F0 MDP_XRT_MODE_STBY
0067 BC (c3)
0068 . C. ----- Success Verify ? OK / NG_____
0069 C.
0070 C. XRT Obs. Table Upload
0071 . S. RAM ram-291:MDP_OBS_X
0072 ( )
0073 C.
0074 +. DC 07-F0 MDP_DUMP_XRTTBL
0075 BC (84 07 00 00 00 3a d4)
0076 . C. ----- Comparison Check ? OK / ERR _____
0077 C.
0078 C.
0079 +. DC 07-F0 MDP_XRT_ROI_SET
0080 BC (cd 01 b1 b1 04 04)
0081 + DC 07-F0 MDP_XRT_ROI_SET
0082 BC (cd 02 b1 b1 08 08)
0083 + DC 07-F0 MDP_XRT_ROI_SET
0084 BC (cd 03 b1 b1 08 08)
0085 + DC 07-F0 MDP_XRT_ROI_SET
0086 BC (cd 04 b1 b1 06 06)
0087 + DC 07-F0 MDP_XRT_ROI_SET
0088 BC (cd 05 85 83 06 06)
0089 + DC 07-F0 MDP_XRT_ROI_SET
0090 BC (cd 06 85 83 06 06)
0091 + DC 07-F0 MDP_XRT_ROI_SET
0092 BC (cd 07 85 83 08 08)
0093 + DC 07-F0 MDP_XRT_ROI_SET
0094 BC (cd 08 80 80 20 20)
0095 + DC 07-F0 MDP_XRT_ROI_SET
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0096 BC (cd 09 80 80 20 08)
0097 + DC 07-F0 MDP_XRT_ROI_SET
0098 BC (cd 0a 80 80 08 20)
0099 + DC 07-F0 MDP_XRT_ROI_SET
0100 BC (cd 0f 80 80 06 06)
0101 + DC 07-F0 MDP_XRT_ROI_SET
0102 BC (cd 10 80 80 08 08)
0103 + DC 07-F0 MDP_XRT_FLD_ENA
0104 BC (d8)
0105 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0106 BC (c8)
0107 + DC 07-F0 MDP_XRT_AEC_RESET
0108 BC (d0)
0109 + DC 07-F0 MDP_XRT_ARS_DIS
0110 BC (d5)
0111 + DC 07-F0 MDP_XRT_FLD_RESET
0112 BC (da)
0113 + DC 07-F0 MDP_XRT_QT_PROG_SET
0114 BC (c4 06)
0115 + DC 07-F0 MDP_XRT_FL_PROG_SET
0116 BC (c5 0c)
0117 . C. ----- Success Verify ? OK / NG ____
0118 C.
0119 C.
0120 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0121 C.
0122 +. DC 07-F0 MDP_XRT_MODE_OBSV
0123 BC (c2)
0124 +. TI 2013-10-12 10:04:02.0
0125 DC 07-F0 MDP_XRT_MODE_OBSV
0126 BC (c2)
0127 . C. ----- Success Verify ? OK / NG ____
0128 C.
0129 C. ***** XRT END *****
0130 C.
0131 . C. ***** MDP 'úÃîðî»ö¼ÝðËÃðð¹ñèDCBC•x²è *****
0132 C. (%ã°îÿÓÿÄÿËÿPÿËÿÄÿçÿËè%¼ðð¼Ã»Ûð¹ñè)
0133 . S. DC-BC dcbc-402:DCBC
0134 (MDP_known_event)
0135 C.
0136 C.
0137 . C. ***** ÿÐÿ¹•ï Daily±¿îÑñË´Øð¹ñèDCBC•x²è *****
0138 . S. DC-BC dcbc-153:DCBC
0139 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0140 C.
0141 C.
0142 . C. ;ãLOSÿÃÿ$ÿÄÿ-¼Ã»Û;ä
0143 C.
0144 . C. ***** LOS *****
0145 C.

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XRT_OGLIST_0822.chk

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*** OP Sequence for XRT ***

2013/10/12	10:06:30.0	XRT_CTRL_MANU_435_OG [0x1b3]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/12	10:06:40.0	XRT_FOCUS_RECALIBRATE_433_OG [0x1b1]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2013/10/12	10:10:40.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/10/12	10:14:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/12	10:14:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/10/12	10:15:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2013/10/12	10:15:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/10/12	10:15:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/10/12	10:15:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/10/12	10:15:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/10/12	10:15:24.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/12	10:15:26.0	XRT_QT_PROG_SET_401_OG [0x191]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2013/10/12	10:17:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c
2013/10/12	10:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/12	11:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/12	11:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/10/12	12:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	02 00 00 00 00
2013/10/12	12:00:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/10/12	12:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/10/12	12:00:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/10/12	12:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/10/12	12:00:24.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/12	12:00:26.0	XRT_QT_PROG_SET_441_OG [0x1b9]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2013/10/12	12:02:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c
2013/10/12	12:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/12	15:04:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/12	15:04:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/12	15:04:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/12	15:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/12	15:18:00.0	XRT_Custom_430_OG [0x1ae]			
2013/10/12	15:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/12	15:24:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/12	15:24:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/12	15:24:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/12	15:27:30.0	XRT_Custom_430_OG [0x1ae]			
2013/10/12	15:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/12	15:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/12	16:40:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/12	16:40:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/12	16:40:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/12	16:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/12	17:04:00.0	XRT_Custom_430_OG [0x1ae]			
2013/10/12	17:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/12	17:54:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/12	17:54:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/10/12	17:54:30.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/10/12	17:54:46.0	XRT_FLD_DIS_434_OG [0x1b2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9

2013/10/12	17:57:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/10/12	17:57:26.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/10/12	17:57:28.0	XRT_QT_PROG_SET_444_OG [0x1bc]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	08		
2013/10/12	17:57:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/10/12	18:04:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/10/12	18:04:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2013/10/12	18:04:30.0	AOCS_Ore-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	01	00	00	00
2013/10/12	18:04:46.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/10/12	18:04:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/10/12	18:04:50.0	XRT_AEC_RESET_413_OG [0x19d]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/10/12	18:04:52.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/10/12	18:04:54.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/10/12	18:04:56.0	XRT_QT_PROG_SET_401_OG [0x191]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	06		
2013/10/12	18:07:28.0	XRT_FL_PROG_SET_443_OG [0x1bb]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0c		
2013/10/12	18:07:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/10/12	18:17:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/10/12	18:17:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/10/12	18:17:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/10/12	18:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/10/12	18:41:00.0	XRT_Custom_430_OG [0x1ae]						
2013/10/12	18:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/10/12	19:54:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/10/12	19:54:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/10/12	19:54:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/10/12	19:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/10/12	20:18:00.0	XRT_Custom_430_OG [0x1ae]						
2013/10/12	20:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/10/12	21:32:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/10/12	21:32:02.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/10/12	21:32:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/10/12	21:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/10/12	21:54:30.0	XRT_Custom_430_OG [0x1ae]						
2013/10/12	21:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/10/12	23:09:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/10/12	23:09:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/10/12	23:09:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/10/12	23:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/10/12	23:24:30.0	XRT_Custom_430_OG [0x1ae]						
2013/10/12	23:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/10/13	00:41:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/10/13	00:41:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/10/13	00:41:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/10/13	00:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/10/13	00:46:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/10/13	00:46:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/10/13	00:46:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/10/13	00:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/10/13	00:53:00.0	XRT_Custom_430_OG [0x1ae]						
2013/10/13	00:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			

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2013/10/13	02:09:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/10/13	02:09:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/10/13	02:09:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/10/13	02:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/10/13	02:29:00.5	XRT_Custom_430_OG [0x1ae]							
2013/10/13	02:30:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/10/13	03:36:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/10/13	03:36:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/10/13	03:36:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/10/13	03:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/10/13	04:06:30.0	XRT_Custom_430_OG [0x1ae]							
2013/10/13	04:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/10/13	05:17:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/10/13	05:17:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/10/13	05:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/10/13	05:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/10/13	05:44:00.0	XRT_Custom_430_OG [0x1ae]							
2013/10/13	05:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/10/13	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/10/13	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/10/13	06:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/10/13	06:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/10/13	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/10/13	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/10/13	06:02:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2013/10/13	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/10/13	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/10/13	06:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/10/13	06:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2013/10/13	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/10/13	06:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/10/13	06:10:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/10/13	06:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/10/13	06:10:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/10/13	06:10:26.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2013/10/13	06:12:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c				
2013/10/13	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/10/13	06:57:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/10/13	06:57:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/10/13	06:57:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/10/13	07:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/10/13	07:21:30.0	XRT_Custom_430_OG [0x1ae]							
2013/10/13	07:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/10/13	08:37:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/10/13	08:37:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/10/13	08:37:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/10/13	08:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/10/13	08:57:30.0	XRT_Custom_430_OG [0x1ae]							
2013/10/13	08:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

2013/10/13	10:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/10/13	10:18:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/10/13	10:18:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/10/13	10:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/10/13	10:27:30.0	XRT_Custom_430_OG [0x1ae]					
2013/10/13	10:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/10/13	14:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/10/13	14:07:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/10/13	14:07:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/10/13	14:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/10/13	14:14:00.0	XRT_Custom_430_OG [0x1ae]					
2013/10/13	14:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/10/13	15:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/10/13	15:40:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/10/13	15:40:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/10/13	15:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/10/13	16:04:00.0	XRT_Custom_430_OG [0x1ae]					
2013/10/13	16:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/10/13	17:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/10/13	17:17:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/10/13	17:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/10/13	17:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/10/13	17:40:30.0	XRT_Custom_430_OG [0x1ae]					
2013/10/13	17:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/10/13	17:57:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/10/13	17:57:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/10/13	17:58:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	
2013/10/13	17:58:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/10/13	18:00:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/10/13	18:00:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/10/13	18:00:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08	
2013/10/13	18:01:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/10/13	18:07:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/10/13	18:07:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/10/13	18:08:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00	
2013/10/13	18:08:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/10/13	18:08:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/10/13	18:08:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/10/13	18:08:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/10/13	18:08:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/10/13	18:08:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02	
2013/10/13	18:10:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c	
2013/10/13	18:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/10/13	18:54:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/10/13	18:54:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/10/13	18:54:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/10/13	18:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/10/13	19:17:30.0	XRT_Custom_430_OG [0x1ae]					
2013/10/13	19:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2013/10/13	20:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/10/13	20:31:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/10/13	20:31:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/10/13	20:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/10/13	20:54:30.0	XRT_Custom_430_OG [0x1ae]							
2013/10/13	20:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/10/13	22:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/10/13	22:08:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/10/13	22:08:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/10/13	22:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/10/13	22:30:30.5	XRT_Custom_430_OG [0x1ae]							
2013/10/13	22:31:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/10/13	23:26:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/10/13	23:26:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2013/10/13	23:27:00.0	AOCs_Or-e-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	04 00 00 00 00			
2013/10/13	23:27:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/10/13	23:27:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/10/13	23:27:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/10/13	23:27:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/10/13	23:27:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/10/13	23:27:26.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a			
2013/10/13	23:29:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c			
2013/10/13	23:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/10/13	23:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/10/13	23:46:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/10/13	23:46:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/10/13	23:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/10/13	23:55:30.0	XRT_Custom_430_OG [0x1ae]							
2013/10/13	23:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/10/14	01:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/10/14	01:11:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/10/14	01:11:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/10/14	01:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/10/14	01:29:00.0	XRT_Custom_430_OG [0x1ae]							
2013/10/14	01:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/10/14	02:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/10/14	02:44:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/10/14	02:44:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/10/14	02:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/10/14	02:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/10/14	02:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2013/10/14	03:00:00.0	AOCs_Or-e-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00			
2013/10/14	03:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/10/14	03:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/10/14	03:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/10/14	03:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/10/14	03:00:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/10/14	03:00:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02			
2013/10/14	03:02:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]							

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2013/10/14	03:05:30.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0c
2013/10/14	03:06:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
2013/10/14	04:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/10/14	04:14:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/10/14	04:14:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/10/14	04:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/10/14	04:43:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/10/14	04:44:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
2013/10/14	05:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/10/14	05:54:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/10/14	05:54:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/10/14	05:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/10/14	06:20:30.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/10/14	06:21:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
2013/10/14	06:30:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/10/14	06:30:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/10/14	06:30:30.0	AOCS_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2013/10/14	06:30:46.0	XRT_FLD_DIS_434_OG [0x1b2]	AOCU_NM	5	02-76	00 00 00 00	00
2013/10/14	06:33:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/10/14	06:33:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/10/14	06:33:28.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/10/14	06:33:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08
2013/10/14	06:40:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/10/14	06:40:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/10/14	06:40:30.0	AOCS_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00
2013/10/14	06:40:46.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00	00
2013/10/14	06:40:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/10/14	06:40:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/10/14	06:40:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/10/14	06:40:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/10/14	06:40:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/10/14	06:43:28.0	XRT_FL_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06
2013/10/14	06:43:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0c
2013/10/14	07:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/10/14	07:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/10/14	07:35:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/10/14	07:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/10/14	07:57:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/10/14	07:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2013/10/14	09:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/10/14	09:14:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/10/14	09:14:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/10/14	09:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/10/14	09:33:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/10/14	09:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
2013/10/14	14:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/10/14	14:41:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/10/14	14:41:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/10/14	14:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

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2013/10/14	14:53:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/10/14	14:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2013/10/14	16:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/10/14	16:16:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/10/14	16:16:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/10/14	16:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/10/14	16:40:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/10/14	16:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2013/10/14	17:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/10/14	17:53:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/10/14	17:53:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/10/14	17:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/10/14	18:16:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/10/14	18:16:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/10/14	18:17:00.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/10/14	18:17:16.0	XRT_FLD_DIS_434_OG [0x1b2]	AOCU_NM	5	02-76	00 00 00 00 00		
2013/10/14	18:19:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/10/14	18:19:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/10/14	18:19:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/10/14	18:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2013/10/14	18:26:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/10/14	18:26:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/10/14	18:27:00.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/10/14	18:27:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00		
2013/10/14	18:27:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/10/14	18:27:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/10/14	18:27:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/10/14	18:27:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/10/14	18:27:26.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/10/14	18:29:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2013/10/14	18:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c		
2013/10/14	19:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/10/14	19:30:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/10/14	19:30:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/10/14	19:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/10/14	19:53:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/10/14	19:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2013/10/14	21:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/10/14	21:07:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/10/14	21:07:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/10/14	21:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/10/14	21:31:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/10/14	21:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2013/10/14	21:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/10/14	21:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/10/14	22:00:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/10/14	22:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 f4 42 ae a8		
2013/10/14	22:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/10/14			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		

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2013/10/14	22:00:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/10/14	22:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/10/14	22:00:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/10/14	22:00:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2013/10/14	22:02:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0c			
2013/10/14	22:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/10/14	22:45:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/10/14	22:45:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/10/14	22:45:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/10/14	22:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/10/14	23:05:30.0	XRT_Custom_430_OG [0x1ae]							
2013/10/14	23:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/10/15	00:22:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/10/15	00:22:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/10/15	00:22:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/10/15	00:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/10/15	00:29:30.0	XRT_Custom_430_OG [0x1ae]							
2013/10/15	00:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/10/15	01:45:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/10/15	01:45:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/10/15	01:45:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/10/15	01:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/10/15	02:05:00.0	XRT_Custom_430_OG [0x1ae]							
2013/10/15	02:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/10/15	02:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/10/15	02:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/10/15	03:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2013/10/15	03:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/10/15	03:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/10/15	03:00:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/10/15	03:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/10/15	03:00:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/10/15	03:00:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2013/10/15	03:02:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	0c			
2013/10/15	03:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/10/15	03:20:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/10/15	03:20:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/10/15	03:20:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/10/15	03:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/10/15	03:42:00.0	XRT_Custom_430_OG [0x1ae]							
2013/10/15	03:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/10/15	04:52:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/10/15	04:52:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/10/15	04:52:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/10/15	04:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/10/15	05:19:30.0	XRT_Custom_430_OG [0x1ae]							
2013/10/15	05:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/10/15	06:02:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/10/15	06:02:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	

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2013/10/15	06:02:30.0	AOCS_OrE-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00 00
2013/10/15	06:02:46.0	XRT_FLD_DIS_434_OG [0x1b2] MDP_XRT_FLD_DIS	1	07-F0	d9
2013/10/15	06:05:24.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/10/15	06:05:26.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2013/10/15	06:05:28.0	XRT_QT_PROG_SET_444_OG [0x1bc] MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2013/10/15	06:05:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/15	06:32:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/15	06:32:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/15	06:32:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/15	06:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/15	06:57:00.0	XRT_Custom_430_OG [0x1ae]			
2013/10/15	06:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/15	08:12:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/15	08:12:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/15	08:12:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/15	08:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/15	09:02:00.0	AOCS_OrE-point_Start_1_OG [0x097] AOCU_NM	5	02-76	01 00 00 00 00
2013/10/15	10:06:00.0	AOCS_OrE-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00 00