

Period: 2013/10/15 09:56:00 - 2013/10/19 08:55:00

Normal mode

XOB #19B5: Synoptic Q95 2x2 - Al/mesh(12/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(33/362/1443) + Thi																
Term		Pointing (x, y)					Comment									
10/15 18:03:00 - 10/15 18:09:54		Fixed (0.0, 0.0)					synoptic									
10/16 06:09:00 - 10/16 06:15:54		Fixed (0.0, 0.0)					synoptic, shifted 6.0 min									
10/16 17:57:30 - 10/16 19:06:00		Fixed (0.0, 0.0)					synoptic, shifted -5.5 min									
10/16 19:30:30 - 10/17 00:19:54		Track (769.3, -213.0) @ 10/16 18:04:30					# AR 11861 obs									
10/17 05:42:30 - 10/17 06:07:00		Fixed (0.0, 0.0)					synoptic, shifted -20.5 min									
10/17 06:33:30 - 10/17 07:47:30		Track (827.5, -203.5) @ 10/17 05:49:30					# AR 11861 obs									
PROG= 08 1-time(s)																
└ Subr= 1 1-time(s) 12.0sec																
└ Seqn= 82 1-time(s) 4.0sec																
└┐ Open/Al-mesh Open/Al-mesh close Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└┐ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└┐ Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└ Seqn= 5 1-time(s) 2.0sec																
└┐ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec																
└┐ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec																
└┐ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec																
└┐ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec																
└┐ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec																
└ Seqn= 83 1-time(s) 4.0sec																
└┐ Open/Ti-poly Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└┐ Open/Ti-poly Open/Ti-poly close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└┐ Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└ Seqn= 99 1-time(s) 2.0sec																
└┐ thin-Be/Open thin-Be/Open close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└┐ thin-Be/Open thin-Be/Open close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└┐ thin-Be/Open thin-Be/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec																
└ Seqn= 66 1-time(s) 2.0sec																
└┐ Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec																
└┐ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec																
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval																

Flare mode

XOB #19C8: Flare obs. dynamics - thin-Be high cadence long/short pairs + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops			
Term	Pointing (x, y)	Comment	
10/15 10:09:00 - 10/15 17:59:54	Track (-611.2, 253.7) @ 10/15 10:06:00	# OP start + 10min New AR11868 11869 with IRIS	
10/15 18:13:00 - 10/16 06:05:54	Track (247.4, -475.1) @ 10/15 18:10:00	# HOP 180 AR11865	
10/16 06:19:00 - 10/16 10:59:54	Track (-472.0, 244.2) @ 10/16 06:16:00	# AR11868 11869	

10/16 11:03:00 - 10/16 12:59:54 Fixed (-690.0, 480.0) # SOT obs at E-limb
10/16 13:03:00 - 10/16 17:29:00 Track (393.0, -468.5) @ 10/16 13:00:00 # HOP 180 AR 11865 with IRIS
10/17 00:23:00 - 10/17 05:39:24 Track (769.3, -213.0) @ 10/16 18:04:30 # AR 11861 obs

PROG= 12 15-time(s)												
Subr= 1		45-time(s)		2.0sec								
Seqn= 69		1-time(s)		8.0sec								
thin-Be/Open		med-Be/Open		close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2 0 2.0sec
thin-Be/Open		med-Be/Open		close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
Subr= 2		1-time(s)		2.0sec								
Seqn= 10		1-time(s)		2.0sec								
med-Al/Open		med-Al/thick-Al		close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
Open/thick-Be		Open/thick-Be		close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3 0 2.0sec
Seqn= 11		1-time(s)		2.0sec								
Al-poly/Open		Al-poly/thick-Al		close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2 0 2.0sec
Seqn= 15		1-time(s)		2.0sec								
Open/G-band		Open/G-band		open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0 0 2.0sec
Open/thick-Al		Open/thick-Al		close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0 0 2.0sec
Open/thick-Al		Open/thick-Al		close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0 0 2.0sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

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Active Region Search

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NOT USED

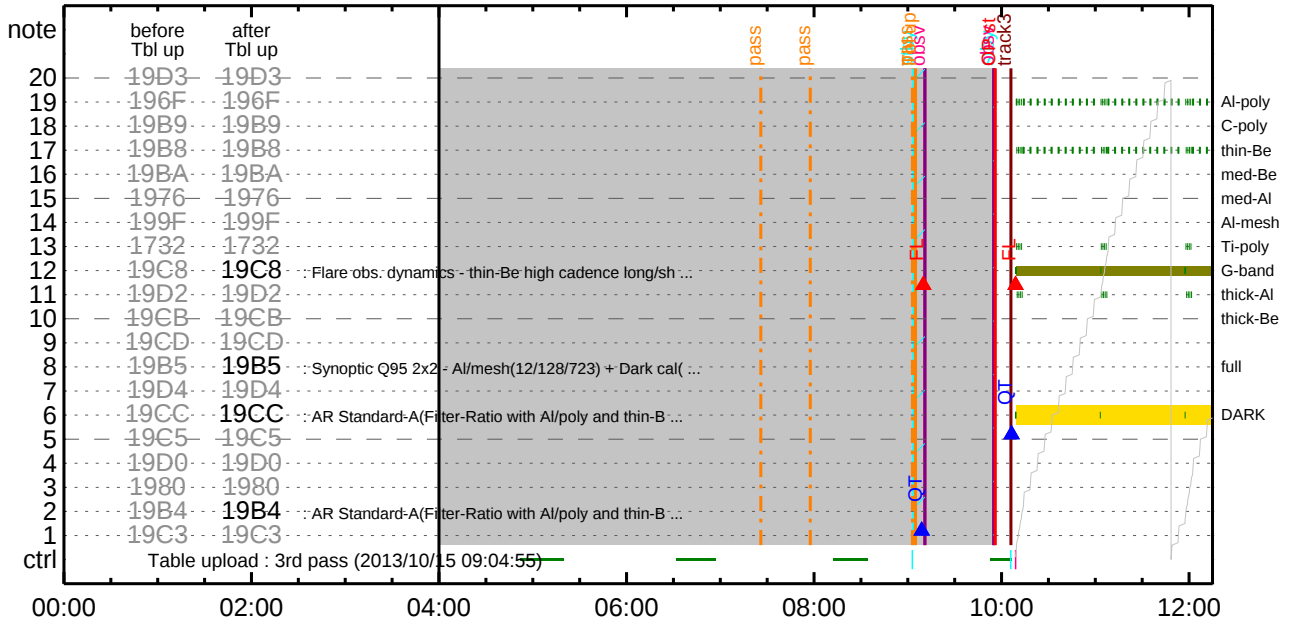
* * * * *

Flare Detection

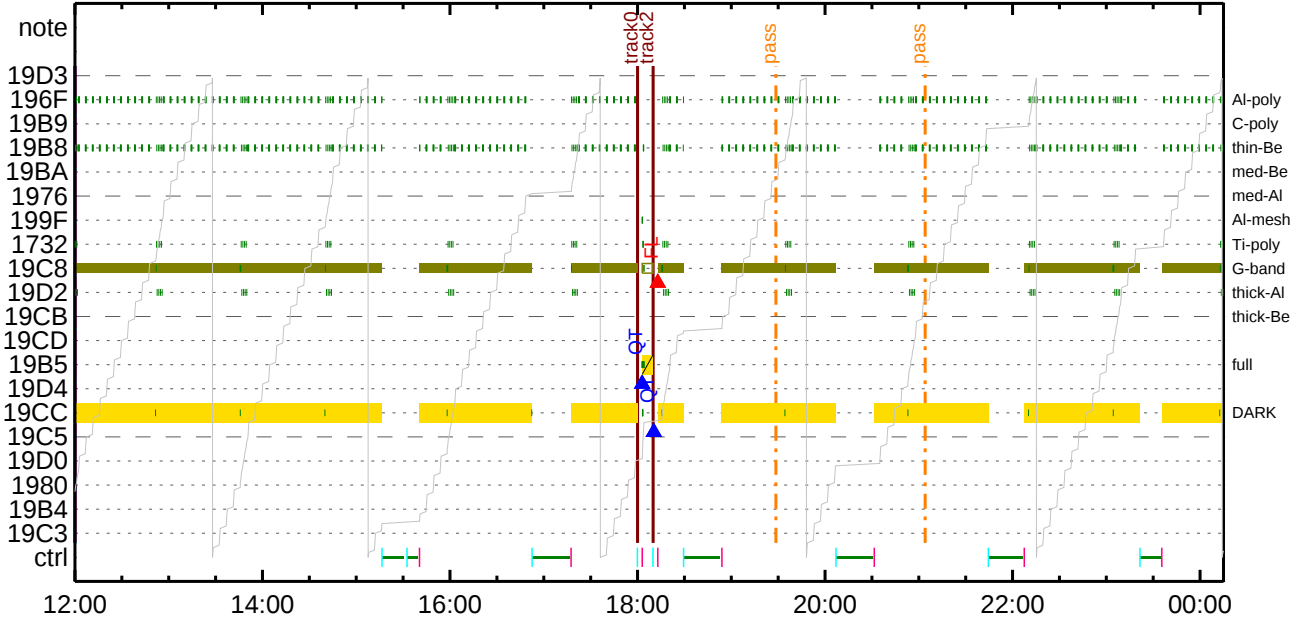
* * * * *

FLD Patrol												
Term		Pointing (x, y)						Comment				
10/15 18:10:16 - 10/16 06:06:16		Track (247.4, -475.1) @ 10/15 18:10:00						# HOP 180 AR11865				
10/16 06:16:16 - 10/16 17:54:46		Track (-472.0, 244.2) @ 10/16 06:16:00						# AR11868 11869				
10/17 00:20:16 - 10/17 05:39:46		Track (769.3, -213.0) @ 10/16 18:04:30						# AR 11861 obs				
Open/Ti-poly		Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50		30sec	
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

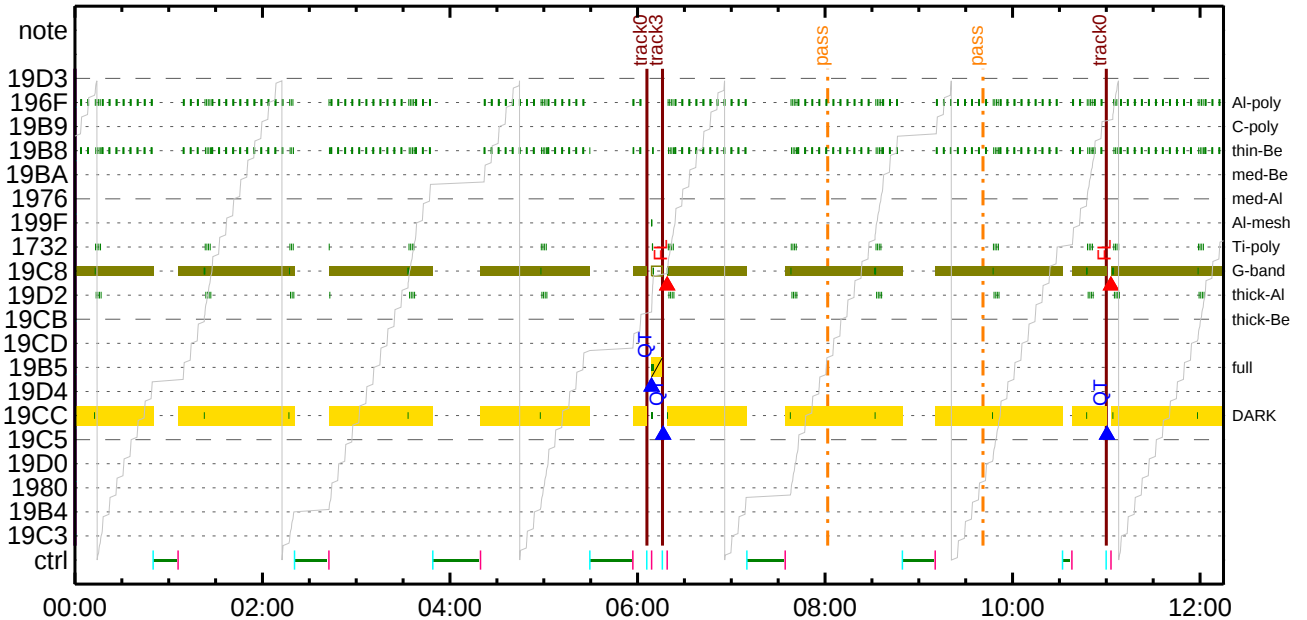
CMDI #0830 2013/10/15



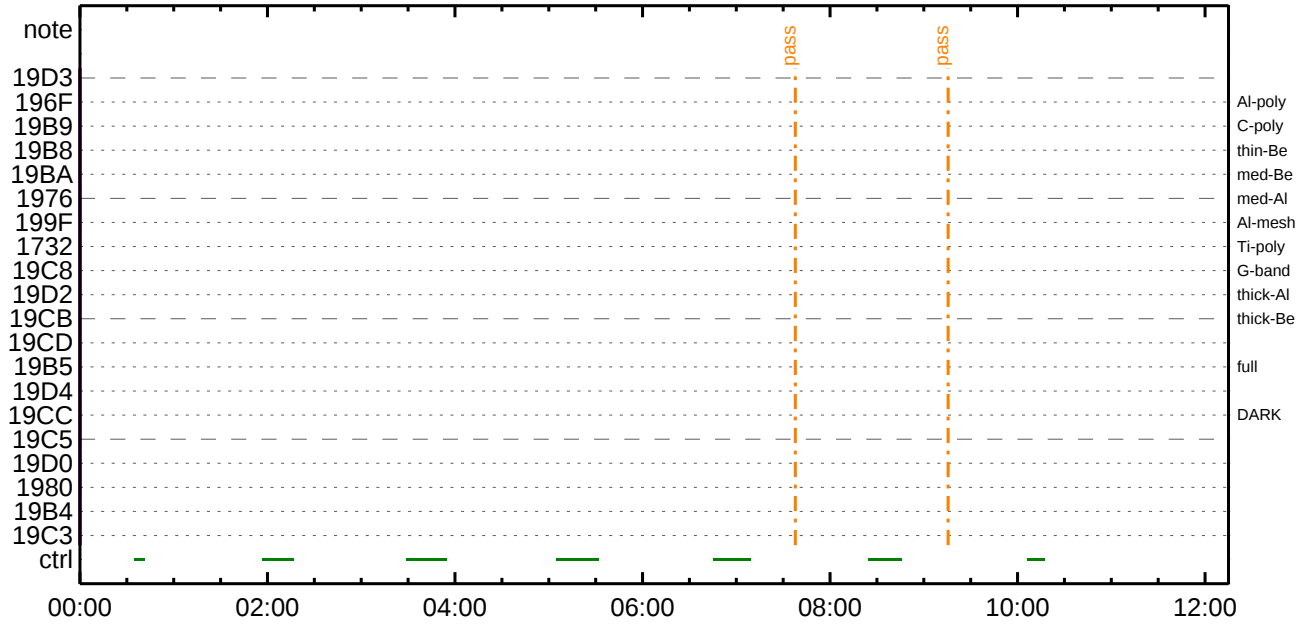
CMDI #0830 2013/10/15



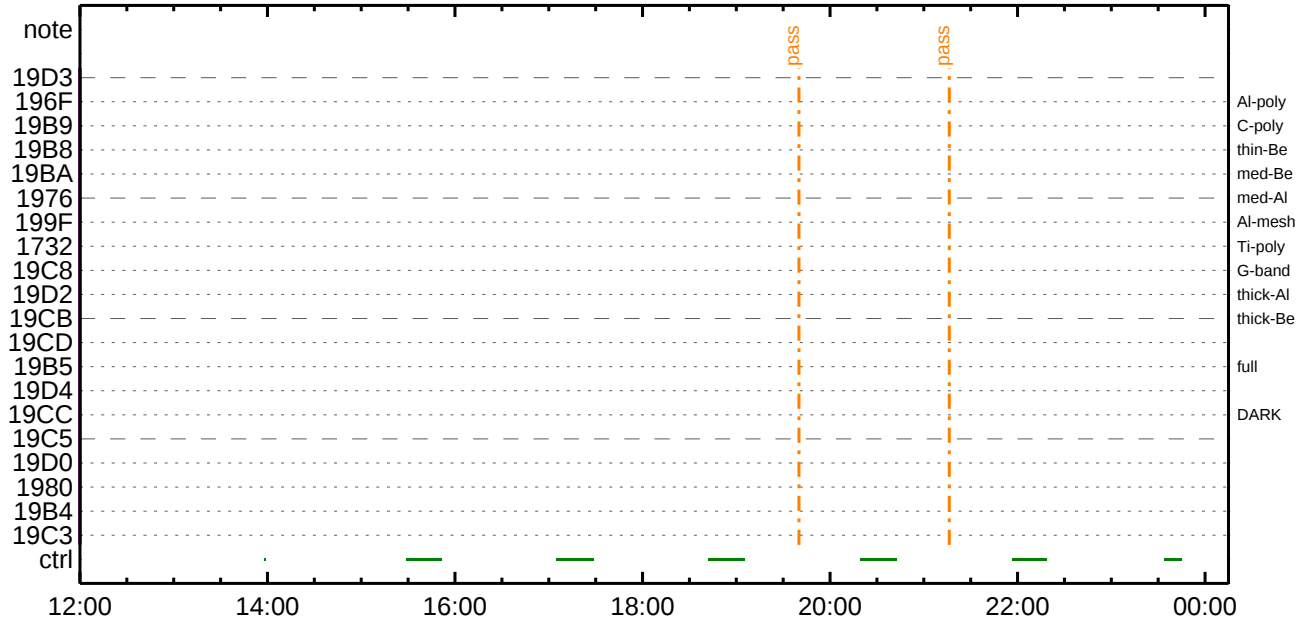
CMDI #0830 2013/10/16



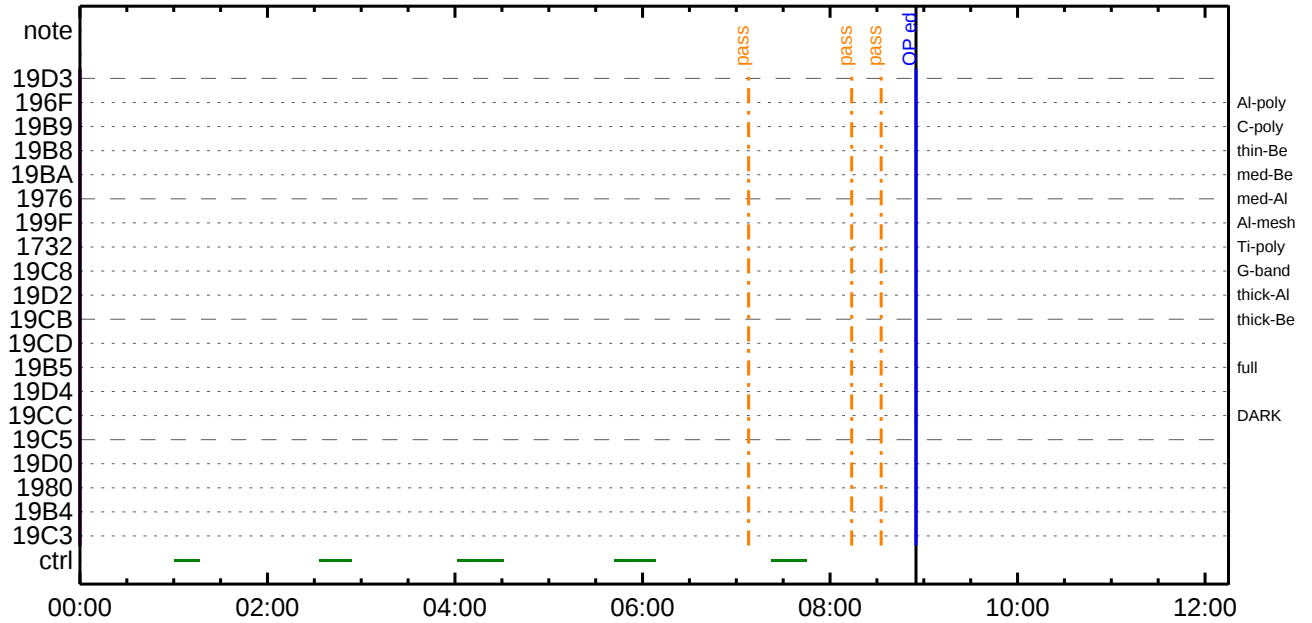
CMDI #0830 2013/10/18



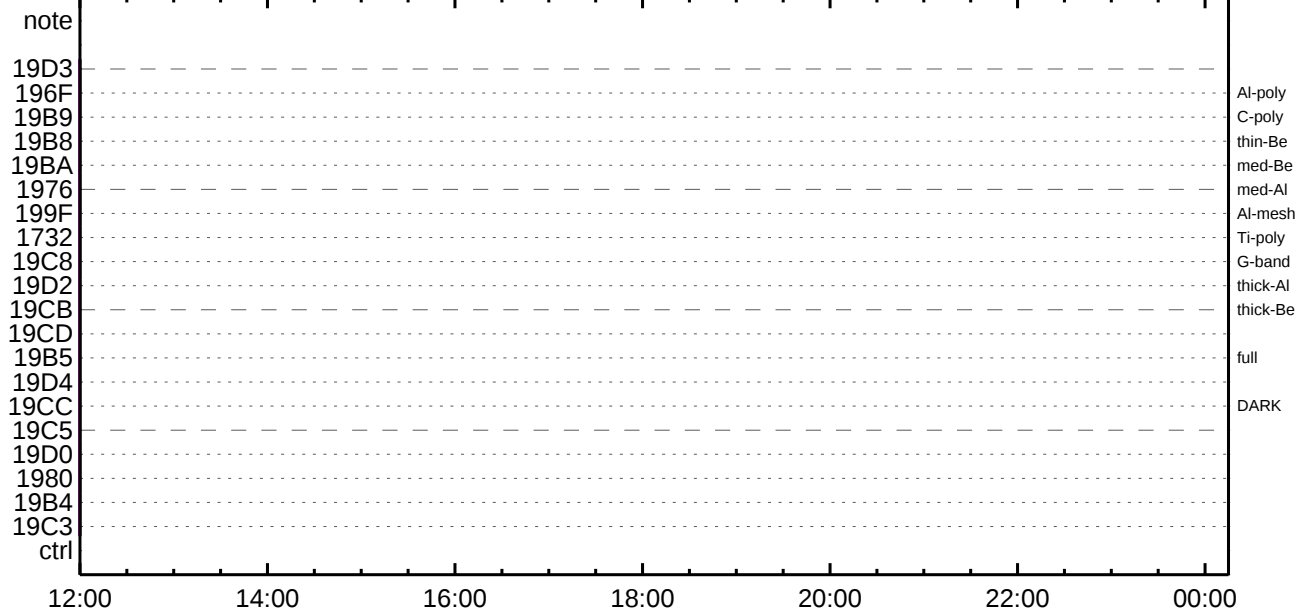
CMDI #0830 2013/10/18



CMDI #0830 2013/10/19



CMDI #0830 2013/10/19



(a) Spacecraft Operation Procedure (real-commands)

```
main-965 2013-10-15 13:05:41 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Á»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āi;Ē¿¿A¤•µ°Æ»İ×AÇ¤İ¥Ç¥Á¥×¥İj¼¥ÉjĒĒ½µ•İĒĒjĒĒ½°Ç¿¤•¤¿¼¹Ç¤İjÇÀ®, Ü¤¹¤Ē¤¤¤ÇÁ÷¿®¤•¤Ē¤¤¤¤ĒjĒ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿®µjON
0016 C. *****
0017 C. ¨ °ĒĒ, İ×ĒY¤¤ŁOS¤¤¤Ç¤İ»p`Œ¤¤¹İİ, ¤•jÇĒŒİ×¤ĒXĒŒŒON¤İ¹Œ¤Ēİ¤Ē¤¤¤¤ĒjĒ
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Đ¥Ó¥É¥İ¥Ã¥¼ŒĒŒ¤-°ĒĒĒ¤•¤¿¤ĒjÇ°Ē²¼¤İ°ĒĒ, ¼Ē½Ç¤¤Œ¼Ē¹Œ¤¹¤ĒjĒ
0030 C.
0031 . C. *****
0032 C. DR PT1 Āİ¼İ°ĒĒ
0033 C. *****
0034 C. ¨ RESTARTjĒPT1jĒ¤•¤¿¤¤¼¹Ç¤İjÇ°Ē²¼¤İ¼Ē¹Œ¤¤¤¤jÇDCBC-150¤Œ¿Ē¤¤¤jĒ
0035 C.
0036 . C. jãPT1°ĒĒ, ³«»İjã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ē¹Œ, j¼Œ)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ē¹Œ, j¼Œ)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ē¹Œ, j¼Œ)
0045 C.
0046 . C. jã¥Ç¥Ó¥Æ¥ĒĒŒŒjĒĒ•Ē°²ŒĒjĒ, Ē¤İ°ĒĒ, °Ē³«jã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ē¹Œ, j¼Œ)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ē¹Œ, j¼Œ)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ē¹Œ, j¼Œ)
0052 C.
0053 C.
0054 . C. PT1°ĒĒ, ¤-¼«Æ°ĒĒ»Œ¤•¤¿, ĒjÇ°Ē²¼¤¤Œ¼Ē¹Œ¤¹¤ĒjĒ
0055 C. ¥Ç¥Ó¥Æ¥ĒĒŒŒ¤ĒĒ•Ē°²ŒĒ¤Œ¤¤¼¹Ç¤İ°İ°İ»¤¹¤Ē¤¤ÇĒŒ¤ĒjĒ
0056 C.
0057 . C. *****
0058 C. DR PT2 Āİ¼İ°ĒĒ
0059 C. *****
0060 C. ¨ RESTARTjĒPT2jĒ¤•¤¿¤¤¼¹Ç¤İjÇ°Ē²¼¤İ¼Ē¹Œ¤¤¤¤jÇDCBC-151¤Œ¿Ē¤¤¤jĒ
0061 C.
0062 . C. jãPT2°ĒĒ, ³«»İjã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ē¹Œ, j¼Œ)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ē¹Œ, j¼Œ)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ē¹Œ, j¼Œ)
0071 C.
0072 . C. jã¥Ç¥Ó¥Æ¥ĒĒŒŒjĒĒ•Ē°²ŒĒjĒ, Ē¤İ°ĒĒ, °Ē³«jã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ē¹Œ, j¼Œ)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ē¹Œ, j¼Œ)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ē¹Œ, j¼Œ)
0078 C.
0079 . C. *****
0080 C. DR°ĒĒ, Ē¤»ŒjÇXÁ÷¿®µjOFF
0081 C. *****
0082 C.
0083 . C. jãDR°ĒĒ, Ē¤»Œjã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. jãXÁ÷¿®µjOFFjã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
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0096 C.
0097 C.
0098 . C. *****
0099 C. OP/OG¥1¡¼¥É¡¼¥À¥ó×
0100 C. *****
0101 C.
0102 C. ¡ãOP/OG¥1¡¼¥É¡¼ä
0103 S. OP op-965:OP
0104 ()
0105 S. OG og-965:OG
0106 ()
0107 C.
0108 C. ¡ãNMOG&OPÎÎ°è¥À¥ó×¡ä
0109 C. NMOG(0x200000-0x207FFF;S 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0120 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. ¥À¥ó×½ªÎ»ð³İÇŞ
0125 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG¤Î%Ê¹Ç•ê²İOK¤ð³İÇŞ
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;S 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0139 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. ¥À¥ó×½ªÎ»ð³İÇŞ
0144 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG¤Î%Ê¹Ç•ê²İOK¤ð³İÇŞ
0146 C.
0147 C. NMOG(0x210000-0x2100FF;S 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0158 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. ¥À¥ó×½ªÎ»ð³İÇŞ
0163 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG,RAM ID=OP¤Î%Ê¹Ç•ê²İOK¤ð³İÇŞ
0165 C.
0166 C. ***** °Ê²¼¤Î%Ã´¶Á¤ðÊË-¤ºÁ÷¿® (%ãµ-¥À¥ó×½ªê½Ç¤ðÄÖæ¤Ç½¤ª¨¤ë%¡¹Ç¤Ç¤â) *****
0167 C. DHU¥â¡¼¥É¡¼¥Ï½, ¥¡¼¼¥É¡¼¥Ï½
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0171 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ¡§ OPOG UPLOAD¤-Á÷¿®NG¤Î%¡¹Ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Î%Ã¹Ô¤ª¤Ê¤¤¤³¤Ê¡£
0180 C. ¤¤¤¿¡¢¡¢SET¤EDUMP¤Î±±°¡¼N¥¹¡¢Ç¹Ô¤ª¤Ê¡£
0181 C.
0182 C. TI¥³¥¤¥ó¥É¤ðÄÖİ¿(UT)
0183 +. TI 2013-10-15 09:51:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-10-15 09:51:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-10-15 09:51:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
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0194 C.
0195 +. TI 2013-10-15 09:55:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.
0198 C.
0199 C. °Ê²¼□İÄê%İİÎñİ¥Á¥$¥Á¥¹àİÜ
0200 C.
0201 C.
0202 C.
0203 C.
0204 C.
0205 C. *****
0206 C. TIIÎ°è¥Á¥Ó¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C.
0213 C.
0214 C.
0215 C.
0216 C.
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C.
0220 C.
0221 C.
0222 C.
0223 C.
0224 C.
0225 C. ¥Á¥Ó¥×½ªİ»ð³İÇ$
0226 C.
0227 C.
0228 C. RAM ID=TI_TBL□İ%Ê¹Ç•è²İOKð³İÇ$
0229 C.
0230 C. DHU¥âı¼¥ÊıÊ¼¥½, ¥ıı¼¥ÊıËð³İä□¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C.
0234 C.
0235 C.
0236 C.
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2013-10-15 09:55:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC (41)
0245 C.
0246 C.
0247 C.
0248 C.
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2013-10-15 09:55:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC (21 02)
0258 +. TI 2013-10-15 09:55:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC (22)
0261 C.
0262 C.
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2013-10-15 09:55:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC (c3)
0271 C.
0272 C.
0273 C.
0274 C.
0275 C. ***** MDP `ÜÄİ□İ»ð¾¥□İÄð□¹ðDCBC•×²è *****
0276 C. (%â°İ¥Ö¥Ä¥İ¥P¥İ¥â¥Ç¥İ¥è½¼□¼A»Ü□¹è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ð¥¹•İ Daily±çİÎñİ`Ø□¹èDCBC•×²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. įäLOS¥Á¥$¥Á¥¼A»Üįä
0287 C.
0288 C. ***** LOS *****
0289 C.

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main-966 2013-10-15 13:05:41 91 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. jãAOS¥Á$¥$¥Ã¥¬¼Ä»Üjã
0005 C.
0006 C. ¥Ä¥£;¼¥³¥Ð¥Ó¥ÉÄ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ÄíjË¿¿À«•µ°Æ»Í×ÅÇ«İ¥¢¥Ä¥×¥ıj¼¥ÉjĚĚ½µ•İĚjËĚ¼ºÇÒ«•«¿%İ¹Ç«İj ÇÀ®, ù«¹«Ě«¿ÇÄ÷¿®«•«Ě««³«Ěj£
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ()
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. ***** AOCS Commands (Orbital Element Update) *****
0046 C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048 BC (16)
0049 +. DC 02-8E AOCU_ORB_UPD
0050 C.
0051 C. <A_ORB>[ORBIT] EPC = 4389239.5 +- 1.0 (s) [ ]
0052 C.
0053 . C.
0054 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055 +. DC 07-FC EIS_MODE_MANU
0056 BC (21 02)
0057 . C. Verify EIS in MANUAL mode
0058 . C. Estimated OBSTBL upload time is 13s
0059 C. *****
0060 C. EIS START OBSTBL LOAD
0061 C. *****
0062 . S. RAM ram-820:EIS_OBSTBL
0063 ()
0064 +. DC 07-FC EIS_DUMP_OBSTBL
0065 BC (07 07 07 00 00 70 00)
0066 C.
0067 C. Execute, after the success of OBSTBL upload.
0068 C. Set EIS TI-commands
0069 +. TI 2013-10-15 09:55:50.0
0070 DC 07-FC EIS_MODE_CHG_ENA
0071 BC (20)
0072 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0073 C. *****
0074 C. EIS END OBSTBL LOAD
0075 C. *****
0076 C.
0077 . C. ***** MDP `ŮÄİ«İ»Ö¼Y«ĔÄ«¹«ĚDCBC•x²è *****
0078 C. (%ä°İ¥Ó¥Ä¥É¥Ð¥É¥â¥¢¥ē«½««¼A»Ü«¹«Ě)
0079 . S. DC-BC dcbc-402:DCBC
0080 (MDP_known_event)
0081 C.
0082 C.
0083 . C. ***** ¥Ð¥¹•İ Daily±¿İÑ«Ĕ'Ø¹«ĚDCBC•x²è *****
0084 . S. DC-BC dcbc-153:DCBC
0085 (SPECIAL-CMD-DAILY_OPERATIN_DCB)
0086 C.
0087 C.
0088 . C. jãLOS¥Á$¥$¥Ã¥¬¼Ä»Üjã
0089 C.
0090 . C. ***** LOS *****
0091 C.
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0096 C.
0097 C.
0098 C. *****
0099 C. SOT table upload
0100 C. *****
0101 C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 C. <Upload FG Observation Table>
0109 S. RAM ram-264:MDP_OBS_F
0110 ( )
0111 C.
0112 C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. < Upload DPL table >
0120 C.
0121 C. ¥¢¥Ã¥×¥í¼¥É°ÎÁ°¤ESTS_CHK¤òOFF¤Ė¤¹¤ĕ
0122 C.
0123 S. RAM ram-271:MDP_DPL
0124 ( )
0125 C.
0126 C. < Dump RAMID=MDP_DPL >
0127 +. DC 07-F0 MDP_DUMP_FGTBL
0128 BC (82 07 00 38 b8 00 40)
0129 C. -----
0130 C. MDP_DPL verify = OK [ ]
0131 C. -----
0132 C.
0133 C. STS_CHK¤òON¤Ė¤¹¤ĕ
0134 C.
0135 C. < Update MDP DSC PAR1 >
0136 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0137 BC (4c)
0138 C. MDP_CMD_CODE = F04C0700 [ ]
0139 C. MDP_CMD_CNT (count-up 1) [ ]
0140 C. -----
0141 C.
0142 C.
0143 C. *****
0144 C. SOT TI command set
0145 C. *****
0146 C. Execute, after the success of TBL upload.
0147 +. TI 2013-10-15 09:55:18.0
0148 DC 07-F0 MDP_SOT_MODE_OBSV
0149 BC (40)
0150 C. -----
0151 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0152 C. -----
0153 C.
0154 C.
0155 C. ***** XRT START *****
0156 C.
0157 +. DC 07-F0 MDP_XRT_CTRL_MANU
0158 BC (c1)
0159 + DC 07-F0 MDP_XRT_MODE_STBY
0160 BC (c3)
0161 C. ----- Success Verify ? OK / NG____
0162 C.
0163 C. XRT Obs. Table Upload
0164 S. RAM ram-291:MDP_OBS_X
0165 ( )
0166 C.
0167 +. DC 07-F0 MDP_DUMP_XRTTBL
0168 BC (84 07 00 00 00 3a d4)
0169 C. ----- Comparison Check ? OK / ERR ____
0170 C.
0171 C.
0172 +. DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 01 b1 b1 04 04)
0174 + DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 02 b1 b1 08 08)
0176 + DC 07-F0 MDP_XRT_ROI_SET
0177 BC (cd 03 b1 b1 08 08)
0178 + DC 07-F0 MDP_XRT_ROI_SET
0179 BC (cd 04 b1 b1 06 06)
0180 + DC 07-F0 MDP_XRT_ROI_SET
0181 BC (cd 05 85 83 06 06)
0182 + DC 07-F0 MDP_XRT_ROI_SET
0183 BC (cd 06 85 83 06 06)
0184 + DC 07-F0 MDP_XRT_ROI_SET
0185 BC (cd 07 85 83 08 08)
0186 + DC 07-F0 MDP_XRT_ROI_SET
0187 BC (cd 08 80 80 20 20)
0188 + DC 07-F0 MDP_XRT_ROI_SET
0189 BC (cd 09 80 80 20 08)
0190 + DC 07-F0 MDP_XRT_ROI_SET
0191 BC (cd 0a 80 80 08 20)
0192 + DC 07-F0 MDP_XRT_ROI_SET
0193 BC (cd 0f 80 80 06 06)

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0194 + DC 07-F0 MDP_XRT_ROI_SET
0195 BC (cd 10 80 80 08 08)
0196 + DC 07-F0 MDP_XRT_FLD_ENA
0197 BC (d8)
0198 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0199 BC (c8)
0200 + DC 07-F0 MDP_XRT_AEC_RESET
0201 BC (d0)
0202 + DC 07-F0 MDP_XRT_ARS_DIS
0203 BC (d5)
0204 + DC 07-F0 MDP_XRT_FLD_RESET
0205 BC (da)
0206 + DC 07-F0 MDP_XRT_QT_PROG_SET
0207 BC (c4 02)
0208 + DC 07-F0 MDP_XRT_FL_PROG_SET
0209 BC (c5 0c)
0210 . C. ----- Success Verify ? OK / NG ____
0211 C.
0212 C.
0213 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0214 C.
0215 +. DC 07-F0 MDP_XRT_MODE_OBSV
0216 BC (c2)
0217 +. TI 2013-10-15 09:55:02.0
0218 DC 07-F0 MDP_XRT_MODE_OBSV
0219 BC (c2)
0220 . C. ----- Success Verify ? OK / NG ____
0221 C.
0222 C. ***** XRT END *****
0223 C.
0224 . C. ***** MDP '0ÃÎ±Î»Ö%Ý±ÊÂð±±êDCBC•x²è *****
0225 C. (%ã°î¥0¥Ã¥Ê¥p¥Ê¥ã¥c¥è±½¼±¼Ã»Û±±è)
0226 . S. DC-BC dcbc-402:DCBC
0227 (MDP_known_event)
0228 C.
0229 C.
0230 . C. ***** ¥Ð¥¹•İ Daily±¿İÑ±Ê´0±±êDCBC•x²è *****
0231 . S. DC-BC dcbc-153:DCBC
0232 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0233 C.
0234 C.
0235 . C. ¡ãLOS¥Á¥S¥Ã¥¬¼Ã»Û¡ã
0236 C.
0237 . C. ***** LOS *****
0238 C.
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*** OP Sequence for XRT ***

2013/10/15	10:05:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/10/15	10:05:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2013/10/15	10:06:00.0	AOCS_ORe-point_Start_1_OG [0x097]		
		AOCU_NM	5	02-76 03 00 00 00 00
2013/10/15	10:06:16.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2013/10/15	10:06:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2013/10/15	10:06:20.0	XRT_AEC_RESET_413_OG [0x19d]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2013/10/15	10:06:22.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2013/10/15	10:06:24.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/10/15	10:06:26.0	XRT_QT_PROG_SET_401_OG [0x191]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 06
2013/10/15	10:08:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 0c
2013/10/15	10:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/10/15	15:16:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/10/15	15:16:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/10/15	15:16:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/10/15	15:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/10/15	15:32:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/10/15	15:32:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/10/15	15:32:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/10/15	15:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/10/15	15:39:30.0	XRT_Custom_430_OG [0x1ae]		
2013/10/15	15:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/10/15	16:52:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/10/15	16:52:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/10/15	16:52:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/10/15	16:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/10/15	17:16:30.0	XRT_Custom_430_OG [0x1ae]		
2013/10/15	17:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/10/15	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/10/15	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2013/10/15	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]		
		AOCU_NM	5	02-76 00 00 00 00 00
2013/10/15	18:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2013/10/15	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2013/10/15	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2013/10/15	18:02:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 08
2013/10/15	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/10/15	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/10/15	18:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2013/10/15	18:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]		
		AOCU_NM	5	02-76 02 00 00 00 00
2013/10/15	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2013/10/15	18:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2013/10/15	18:10:20.0	XRT_AEC_RESET_413_OG [0x19d]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2013/10/15	18:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2013/10/15	18:10:24.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/10/15	18:10:26.0	XRT_QT_PROG_SET_401_OG [0x191]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 06
2013/10/15	18:12:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 0c
2013/10/15	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/10/15	18:29:30.0	XRT_CTRL_MANU_400_OG [0x190]		

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2013/10/15	18:29:32.0	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/15	18:29:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/15	18:32:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/15	18:53:00.0	XRT_Custom_430_0G [0x1ae]			
2013/10/15	18:54:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/15	20:07:00.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/15	20:07:02.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/15	20:07:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/15	20:10:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/15	20:30:30.0	XRT_Custom_430_0G [0x1ae]			
2013/10/15	20:31:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/15	21:44:30.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/15	21:44:32.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/15	21:44:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/15	21:47:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/15	22:06:30.0	XRT_Custom_430_0G [0x1ae]			
2013/10/15	22:07:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/15	23:21:30.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/15	23:21:32.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/15	23:21:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/15	23:24:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/15	23:34:30.0	XRT_Custom_430_0G [0x1ae]			
2013/10/15	23:35:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/16	00:50:00.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/16	00:50:02.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/16	00:50:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/16	00:53:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/16	01:05:00.0	XRT_Custom_430_0G [0x1ae]			
2013/10/16	01:06:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/16	02:20:30.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/16	02:20:32.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/16	02:20:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/16	02:23:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/16	02:41:30.0	XRT_Custom_430_0G [0x1ae]			
2013/10/16	02:42:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/16	03:49:00.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/16	03:49:02.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/16	03:49:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/16	03:52:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/16	04:18:30.0	XRT_Custom_430_0G [0x1ae]			
2013/10/16	04:19:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/16	05:29:30.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/16	05:29:32.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/16	05:29:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/16	05:32:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/16	05:56:00.0	XRT_Custom_430_0G [0x1ae]			
2013/10/16	05:57:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/16	06:05:54.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/16	06:05:56.0	XRT_FOCUS_POSITION_403_0G [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/10/16	06:06:00.0	AOCS_ORe-point_Start_2_0G [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00

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2013/10/16	06:06:16.0	XRT_FLD_DIS_434_OG [0x1b2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/10/16	06:08:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/10/16	06:08:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/10/16	06:08:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2013/10/16	06:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/16	06:15:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/16	06:15:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/10/16	06:16:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2013/10/16	06:16:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/10/16	06:16:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/10/16	06:16:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/10/16	06:16:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/10/16	06:16:24.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/16	06:16:26.0	XRT_QT_PROG_SET_401_OG [0x191]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2013/10/16	06:18:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c
2013/10/16	06:19:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/16	07:10:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/16	07:10:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/16	07:10:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/16	07:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/16	07:33:30.0	XRT_Custom_430_OG [0x1ae]			
2013/10/16	07:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/16	08:49:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/16	08:49:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/16	08:49:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/16	08:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/16	09:09:30.0	XRT_Custom_430_OG [0x1ae]			
2013/10/16	09:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/16	10:32:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/16	10:32:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/16	10:32:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/16	10:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/16	10:37:00.0	XRT_Custom_430_OG [0x1ae]			
2013/10/16	10:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/16	10:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/16	10:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/10/16	11:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 d5 59 3d 58
2013/10/16	11:00:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/10/16	11:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/10/16	11:00:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/10/16	11:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/10/16	11:00:24.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/16	11:00:26.0	XRT_QT_PROG_SET_401_OG [0x191]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2013/10/16	11:02:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c
2013/10/16	11:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/16	12:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/16	12:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/10/16	13:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	02 00 00 00 00

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2013/10/16	13:00:16.0	XRT_FLD_ENA_411_0G [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/10/16	13:00:18.0	XRT_FLRCTRL_ENA_412_0G [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/10/16	13:00:20.0	XRT_AEC_RESET_413_0G [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/10/16	13:00:22.0	XRT_ARS_DIS_423_0G [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/10/16	13:00:24.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/16	13:00:26.0	XRT_QT_PROG_SET_401_0G [0x191]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2013/10/16	13:02:58.0	XRT_FL_PROG_SET_443_0G [0x1bb]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c
2013/10/16	13:03:00.0	XRT_CTRL_AUTO_408_0G [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/16	14:18:30.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/16	14:18:32.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/16	14:18:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/16	14:21:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/16	14:27:30.0	XRT_Custom_430_0G [0x1ae]			
2013/10/16	14:28:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/16	15:52:30.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/16	15:52:32.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/16	15:52:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/16	15:55:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/16	16:16:00.0	XRT_Custom_430_0G [0x1ae]			
2013/10/16	16:17:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/16	17:29:00.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/16	17:29:02.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/16	17:29:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/16	17:32:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/16	17:54:24.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/16	17:54:26.0	XRT_FOCUS_POSITION_403_0G [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/10/16	17:54:30.0	AOCS_Ore-point_Start_2_0G [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/10/16	17:54:46.0	XRT_FLD_DIS_434_0G [0x1b2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/10/16	17:57:24.0	XRT_FLRCTRL_DIS_405_0G [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/10/16	17:57:26.0	XRT_ARS_DIS_423_0G [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/10/16	17:57:28.0	XRT_QT_PROG_SET_444_0G [0x1bc]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2013/10/16	17:57:30.0	XRT_CTRL_AUTO_408_0G [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/16	18:04:30.0	AOCS_Ore-point_Start_5_0G [0x09b]			
		AOCU_NM	5	02-76	01 00 00 00 00
2013/10/16	19:06:00.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/16	19:06:02.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/16	19:06:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/16	19:09:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/16	19:29:30.0	XRT_Custom_430_0G [0x1ae]			
2013/10/16	19:30:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/16	20:43:30.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/16	20:43:32.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/16	20:43:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/16	20:46:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/16	21:07:00.0	XRT_Custom_430_0G [0x1ae]			
2013/10/16	21:08:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/16	22:21:00.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/16	22:21:02.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/16	22:21:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/16	22:24:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]			

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2013/10/16	22:42:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/16	22:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/16	23:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/16	23:58:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/16	23:58:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/17	00:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/17	00:06:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/17	00:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/17	00:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/10/17	00:19:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/10/17	00:20:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/10/17	00:20:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/10/17	00:20:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/10/17	00:20:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/17	00:20:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2013/10/17	00:20:26.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c
2013/10/17	00:22:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/17	00:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/17	01:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/17	01:22:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/17	01:22:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/17	01:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/17	01:41:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/17	01:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/17	02:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/17	02:57:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/17	02:57:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/17	03:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CUSTOM_430_OG [0x1ae]	1	07-F0	e9
2013/10/17	03:18:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/17	03:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/17	04:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/17	04:27:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/17	04:27:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/17	04:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_CUSTOM_430_OG [0x1ae]	1	07-F0	e9
2013/10/17	04:55:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/17	04:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/17	05:39:24.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/10/17	05:39:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	AOCs_Re-point_Start_2_OG [0x098]	5	02-76	00 00 00 00 00
2013/10/17	05:39:30.5	AOCs_Re-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2013/10/17	05:39:46.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9
2013/10/17	05:42:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/10/17	05:42:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/10/17	05:42:28.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2013/10/17	05:42:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/17	05:49:30.0	AOCs_Re-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	01 00 00 00 00
2013/10/17	06:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/17	06:07:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/17	06:07:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/17	06:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				

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2013/10/17	06:32:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/17	06:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/17	07:47:30.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/17	07:47:32.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/17	07:47:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/17	07:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/17	09:29:00.0	AOCS_ORe-point_Start_2_OG [0x098]				
			AOCU_NM	5	02-76	00 00 00 00 00