

# XRT Timeline to be uploaded on 2013/10/29

Period: 2013/10/29 10:41:00 - 2013/11/02 09:19:00

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Normal mode

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## XOB #19B3: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-bar

Term	Pointing (x, y)	Comment
10/29 10:54:00 - 10/29 13:59:54	Fixed ( 930.7, 70.4)	# OP start + 10min, AR11875 limb obs for 2 orbits
10/29 14:03:00 - 10/29 17:57:54	Track ( -244.8, -247.9) <sup>© 10/29 14:00:00</sup>	# Track AR11882

**PROG= 01 Inf.-time(s)**

- Subr= 1 1-time(s) 2.0sec
  - Seqn= 8 2-time(s) 2.0sec
    - Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
- Subr= 2 2-time(s) 2.0sec
  - Seqn= 24 1-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
    - Open/G-band Open/G-band open Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
  - Seqn= 98 4-time(s) 2.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
    - Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
  - Seqn= 21 15-time(s) 180.0sec
    - thin-Be/Open Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - Al-poly/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - thin-Be/Open Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
    - Al-poly/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
    - thin-Be/Open Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
    - Al-poly/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
    - thin-Be/Open Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec
    - Al-poly/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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## XOB #19D5: Synoptic Q95 2x2 - Al/mesh(12/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(33/362/1443) + Thi

Term	Pointing (x, y)	Comment
10/29 18:01:00 - 10/29 18:07:54	Fixed ( 0.0, 0.0)	synoptic, shifted -2.0 min
10/30 06:33:30 - 10/30 06:40:24	Fixed ( 0.0, 0.0)	synoptic, shifted 30.5 min
10/30 18:20:00 - 10/30 18:26:54	Fixed ( 0.0, 0.0)	synoptic, shifted 17.0 min
10/31 06:05:30 - 10/31 06:12:24	Fixed ( 0.0, 0.0)	synoptic, shifted 2.5 min

**PROG= 05 1-time(s)**

- Subr= 1 1-time(s) 12.0sec
  - Seqn= 82 1-time(s) 4.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 5 1-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
  - Seqn= 83 1-time(s) 4.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 99 1-time(s) 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 6 1-time(s) 2.0sec
    - Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
    - Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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## XOB #19B4: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-bar

Term	Pointing (x, y)	Comment
10/29 18:11:00 - 10/30 06:30:24	Track ( -207.9, -248.4) <sup>© 10/29 18:08:00</sup>	# Cont,

**PROG= 02 Inf.-time(s)**

- Subr= 1 1-time(s) 2.0sec
  - Seqn= 8 2-time(s) 2.0sec
    - Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
- Subr= 2 2-time(s) 2.0sec
  - Seqn= 24 1-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
    - Open/G-band Open/G-band open Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
  - Seqn= 98 4-time(s) 2.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec

Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 21 12-time(s) 240.0sec</b>												
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #19CC: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band**

Term	Pointing (x, y)	Comment
10/30 06:43:30 - 10/30 17:53:30	Track ( -94.0, -249.1) @ 10/30 06:40:30	# Cont,
10/30 18:30:00 - 10/31 01:45:30	Track ( 14.3, -248.7) @ 10/30 18:27:00	# Cont,
10/31 06:15:30 - 10/31 09:50:54	Track ( 122.3, -247.3) @ 10/31 06:12:30	# Track AR11882

**PROG= 06 Inf.-time(s)**

<b>Subr= 1 1-time(s) 2.0sec</b>												
<b>Seqn= 8 2-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
<b>Subr= 2 2-time(s) 2.0sec</b>												
<b>Seqn= 24 1-time(s) 2.0sec</b>												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Seqn= 98 4-time(s) 2.0sec</b>												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 21 11-time(s) 270.0sec</b>												
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #19A9: G-Band Alignment with North Pole Q90 2x2(G-band only) - 8msec - 5min cadence - Partial Sun-wNGT-2**

Term	Pointing (x, y)	Comment
10/31 02:15:00 - 10/31 03:59:54	Fixed ( 0.0, 945.0)	# Co-alignment at N-pole

**PROG= 13 1-time(s)**

<b>Subr= 1 1-time(s) 360.0sec</b>												
<b>Seqn= 93 24-time(s) 300.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #19AA: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8msec - 8 min cadence-wNGT-2**

Term	Pointing (x, y)	Comment
10/31 04:15:00 - 10/31 06:02:24	Fixed ( -945.0, 0.0)	# Co-alignment at E-limb

**PROG= 15 1-time(s)**

<b>Subr= 1 1-time(s) 360.0sec</b>												
<b>Seqn= 94 15-time(s) 480.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Flare mode**

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**XOB #19C8: Flare obs. dynamics - thin-Be high cadence long/short pairs + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loo**

Term	Pointing (x, y)	Comment
10/29 10:54:00 - 10/29 13:59:54	Fixed ( 930.7, 70.4)	# OP start + 10min, AR11875 limb obs for 2 orbits
10/29 14:03:00 - 10/29 17:57:54	Track ( -244.8, -247.9) @ 10/29 14:00:00	# Track AR11882
10/29 18:11:00 - 10/30 06:30:24	Track ( -207.9, -248.4) @ 10/29 18:08:00	# Cont,
10/30 06:43:30 - 10/30 17:53:30	Track ( -94.0, -249.1) @ 10/30 06:40:30	# Cont,
10/30 18:30:00 - 10/31 01:45:30	Track ( 14.3, -248.7) @ 10/30 18:27:00	# Cont,
10/31 06:15:30 - 10/31 09:50:54	Track ( 122.3, -247.3) @ 10/31 06:12:30	# Track AR11882

**PROG= 12 15-time(s)**

<b>Subr= 1 45-time(s) 2.0sec</b>												
<b>Seqn= 69 1-time(s) 8.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Subr= 2		1-time(s)	2.0sec										
Seqn= 10		1-time(s)	2.0sec										
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11		1-time(s)	2.0sec										
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 15		1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

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NOT USED

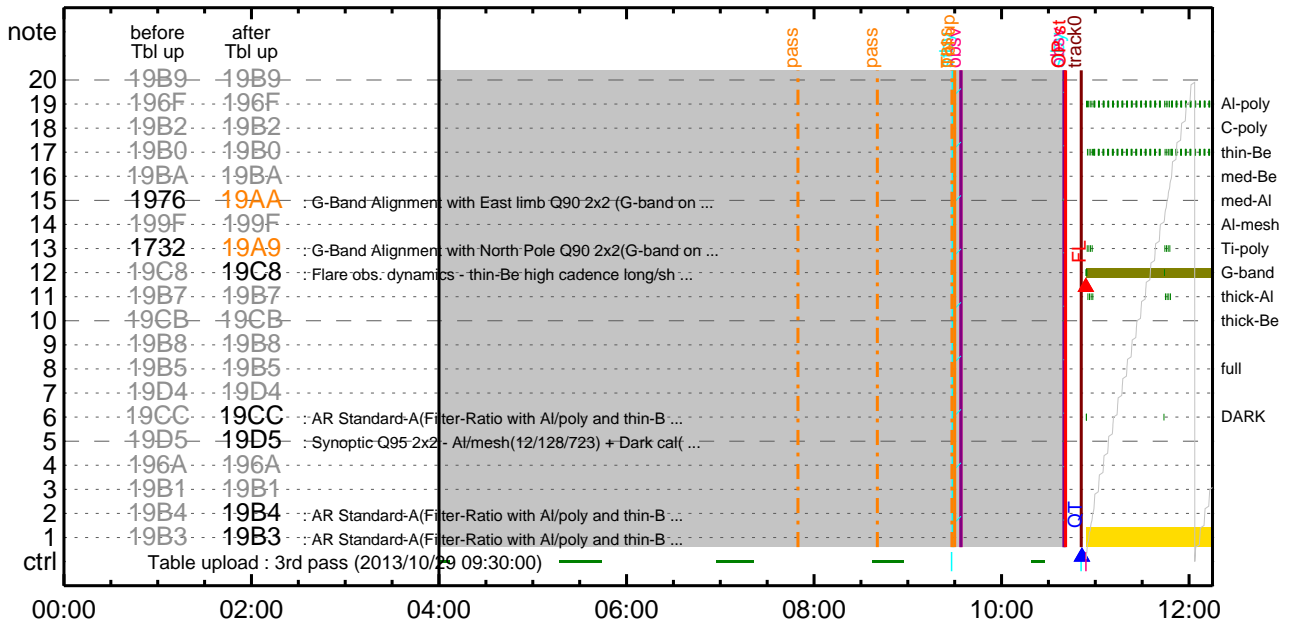
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### Flare Detection

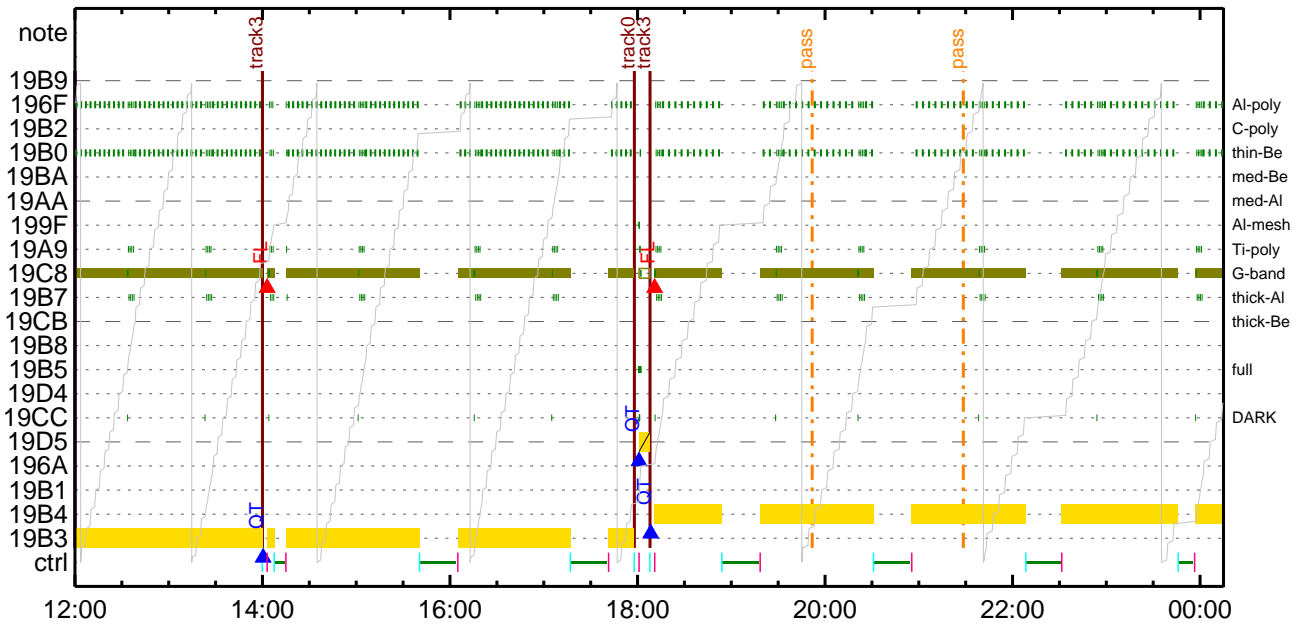
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FLD Patrol													
Term	Pointing (x, y)		Comment										
10/29 18:08:16 - 10/30 06:30:46	Track ( -207.9, -248.4)	<sup>Ⓢ</sup> 10/29 18:08:00	# Cont,										
10/30 06:40:46 - 10/30 18:17:16	Track ( -94.0, -249.1)	<sup>Ⓢ</sup> 10/30 06:40:30	# Cont,										
10/30 18:27:16 - 10/31 02:00:16	Track ( 14.3, -248.7)	<sup>Ⓢ</sup> 10/30 18:27:00	# Cont,										
10/31 06:12:46 - 11/02 09:19:00	Track ( 122.3, -247.3)	<sup>Ⓢ</sup> 10/31 06:12:30	# Track AR11882										
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec			
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

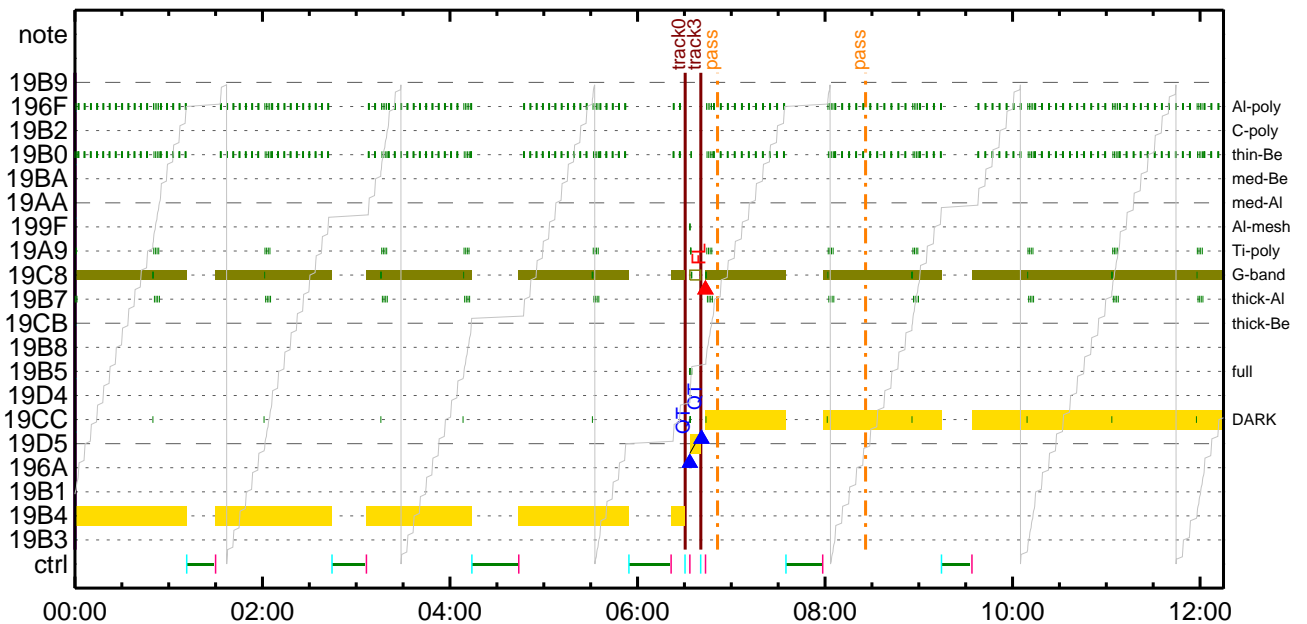
### CMDI #0862 2013/10/29



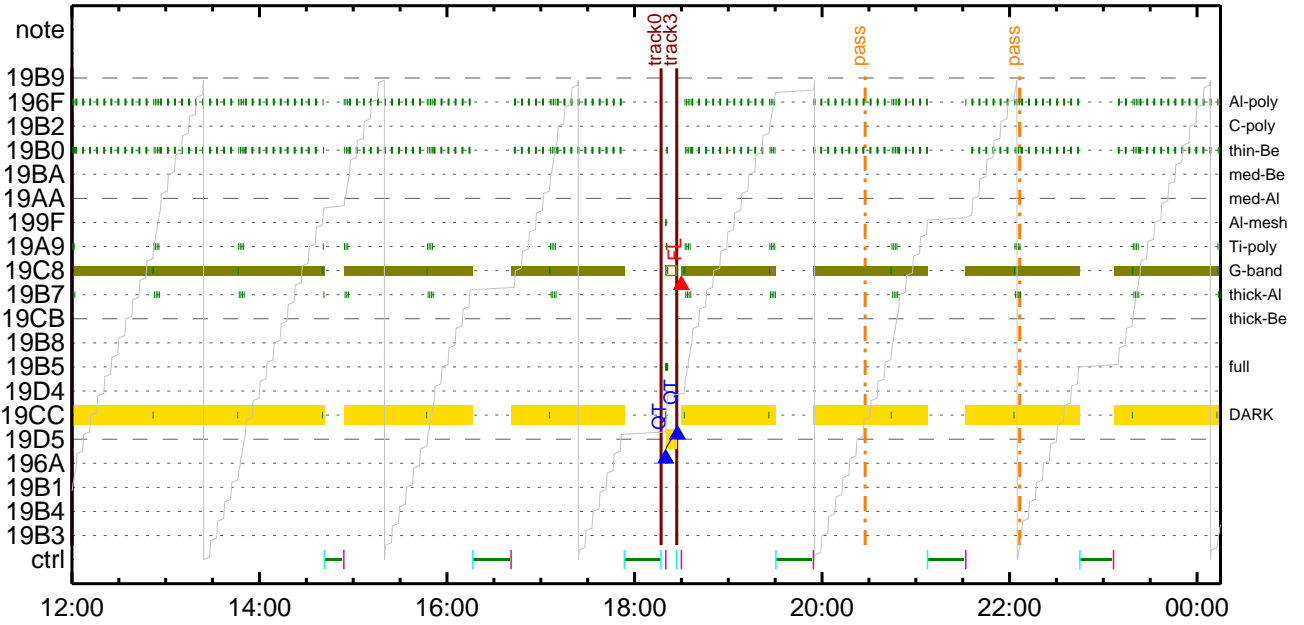
### CMDI #0862 2013/10/29



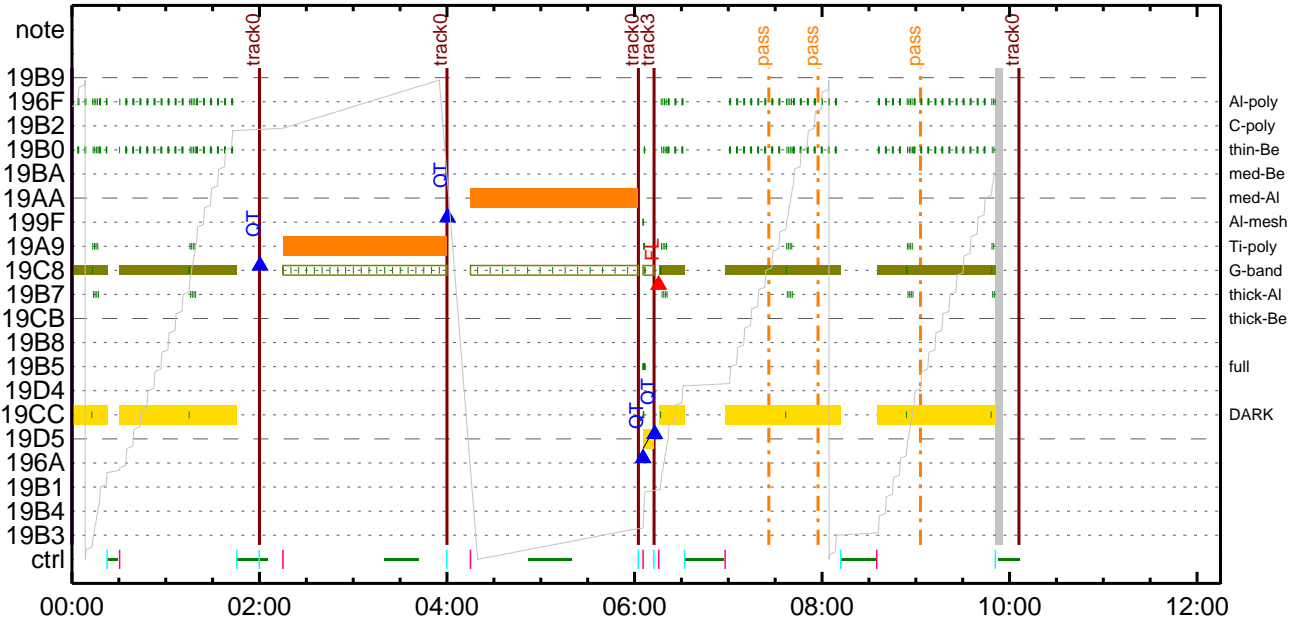
### CMDI #0862 2013/10/30



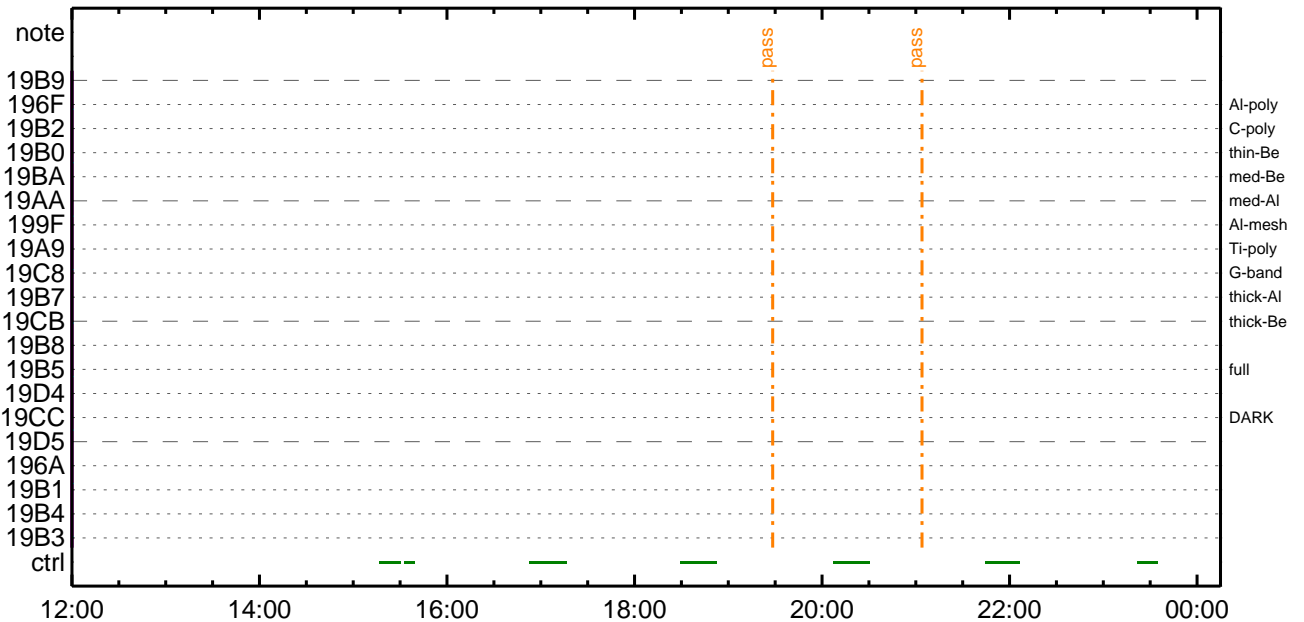
CMDI #0862 2013/10/30



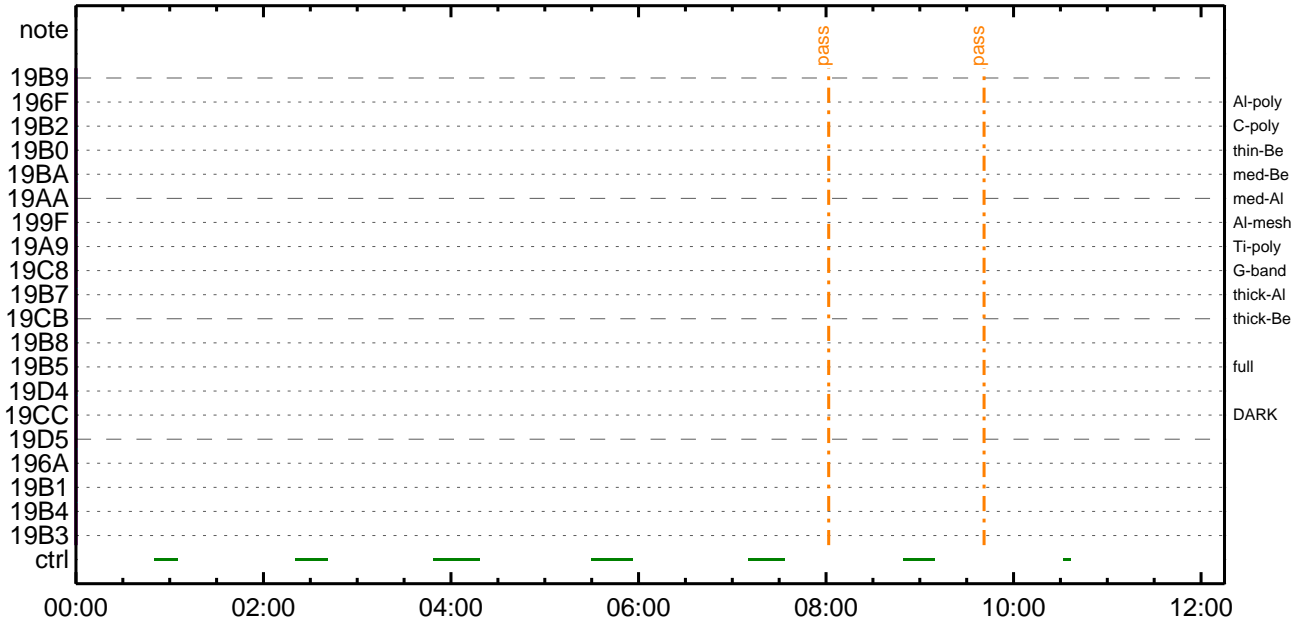
CMDI #0862 2013/10/31



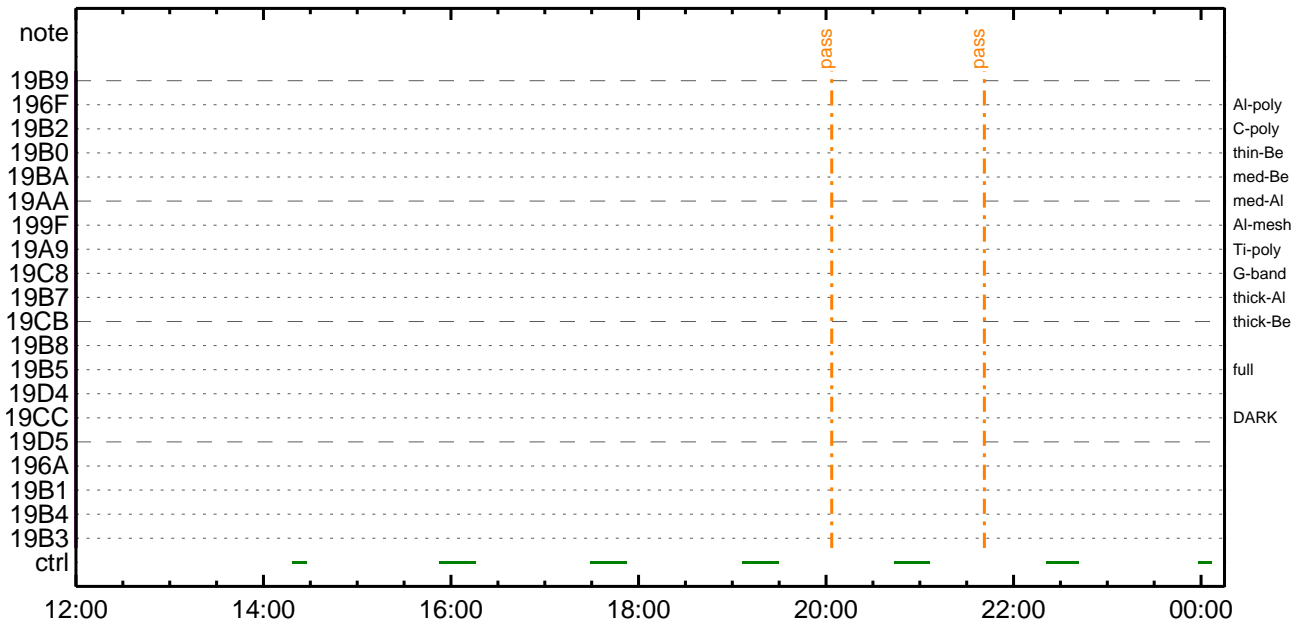
CMDI #0862 2013/10/31



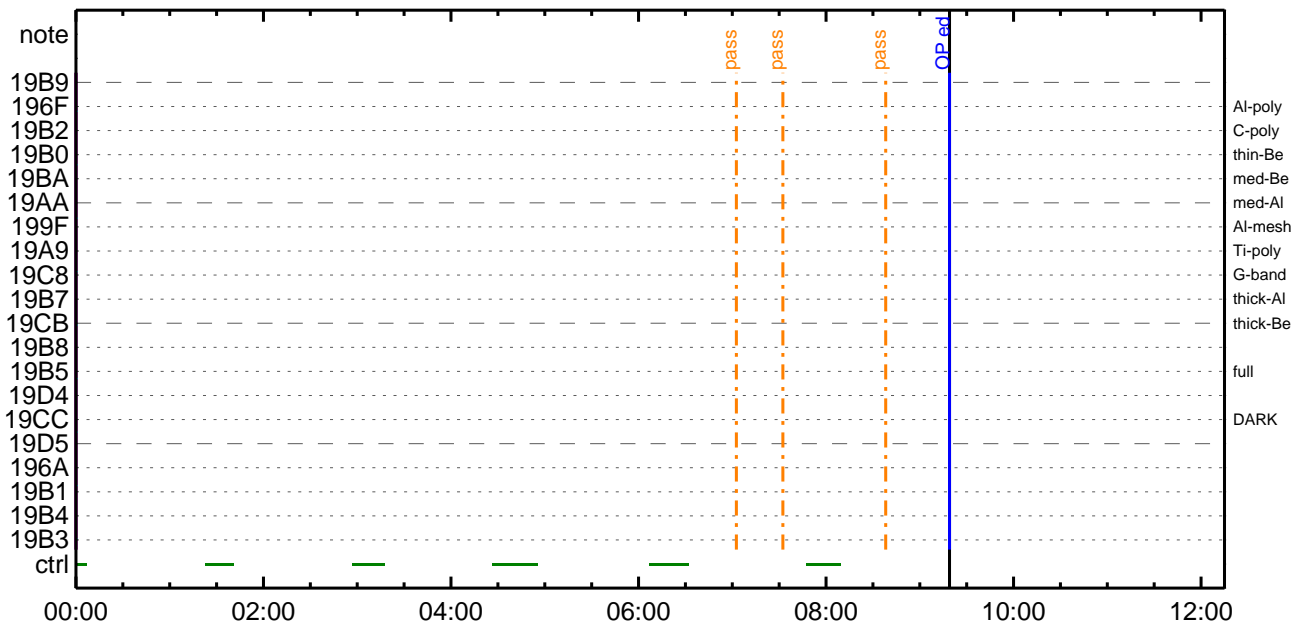
CMDI #0862 2013/11/01



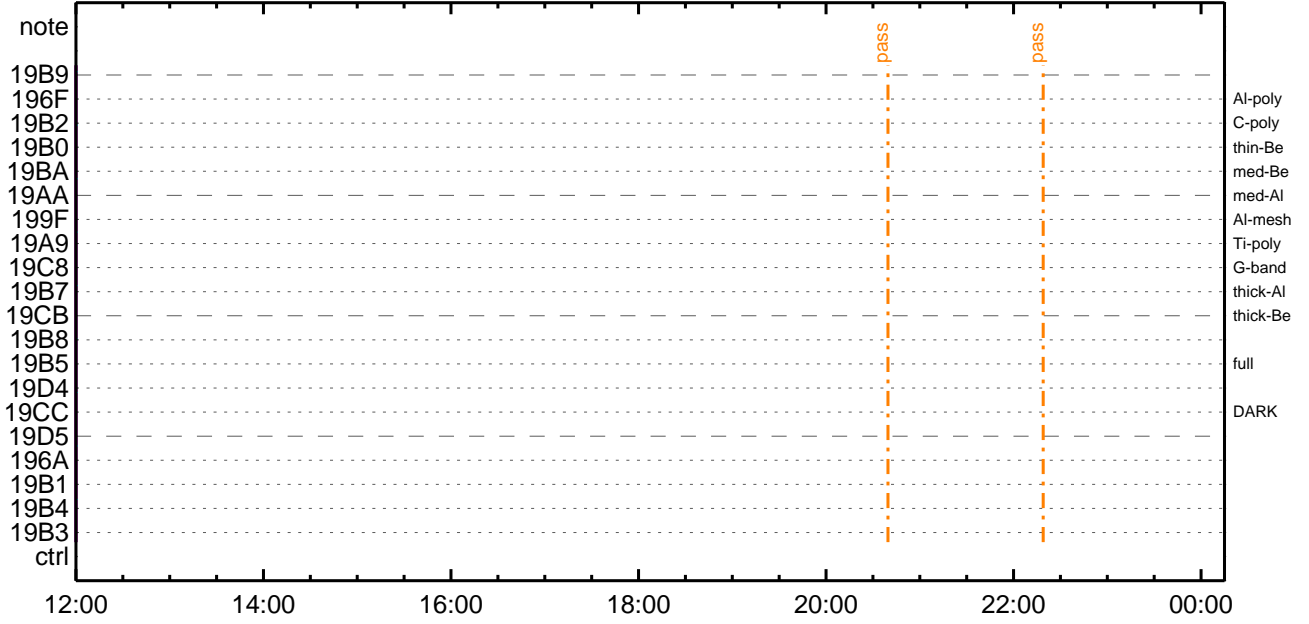
CMDI #0862 2013/11/01



CMDI #0862 2013/11/02



CMDI #0862 2013/11/02







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0096 C.
0097 C.
0098 C.
0099 C. *****
0100 C. *****
0101 C.
0102 C. ;ãOP/OGYí;¼YÉ;ä
0103 S. OP op-993:OP
0104 ( )
0105 S. OG og-993:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYÅYóYx;ä
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YÅYóYx¼ªªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGªî¼E¹ç•è²îOKªò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YÅYóYx¼ªªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGªî¼E¹ç•è²îOKªò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YÅYóYx¼ªªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPªî¼E¹ç•è²îOKªò³îÇ§
0165 C.
0166 C. ***** °E²¼ªî¼A´¶Áª°EÉ¬ª°Á÷¿® (¼ªµ-YÅYóYx¼E½çªòÁÓÆªç¼ª°ª°E¼i¹çªçªâ) *****
0167 C. DHUªâ;¼YÉ;E¼Y½, Yî;¼YÉ;Eªòîãª¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADª¬Á÷¿®NGªî¼i¹ç;ç°E²¼ªîîTI-CMDÁ÷¿®ªî¼Á¹Ôª°ªEªªª³ªE;f
0180 C. ªªª¿;çSETªEEDUMªî¼E±°iYÑY¹ªç¹Ôª|ª³ªE;f
0181 C.
0182 C. TIY³YªYóYÉªòªÁî¿¿(UT)
0183 +. TI 2013-10-29 10:36:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-10-29 10:36:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-10-29 10:36:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

```
0194 C.
0195 +. TI 2013-10-29 10:40:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0198 C.
0199 C. °È²¼αîÄè%îíñαîŷÄŷ§ŷÄŷ-¹àîŰ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]            EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]                EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]            EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]            EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîŷ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]          EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]          EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]           EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]         EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]            EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]             EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]            EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]         EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]         EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]            EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»αò³îÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]            EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαîŷÈ¹ç•è²îOKαò³îÇ§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;È¼ŷ¼. ŷî;¼ŷÈ;Èαòîãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]             EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]            EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]         EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]         EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2013-10-29 10:40:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2013-10-29 10:40:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]           EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2013-10-29 10:40:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.          [ ] [HK1_TI_CMD_NUM]           EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP `ûÄîαî»ò¼ŷαÈÄα¹αèDCBC•×²è *****
0265 C. (¼ª°îŷÖŷÄŷÈŷŷÈŷâŷçŷèè²¼αα¼Ä»Űα¹αè)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŷDŷ¹.İ Daily±;îñαÈ¹Øα¹αèDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. îãLOSŷÄŷŷÄŷŷÄŷ-¼Ä»Ű;ã
0276 C.
0277 C. ***** LOS *****
0278 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-994 2013-10-29 12:31:31 91 33 SOLAR-B MAIN //
0001   C.
0002   . C. ***** AOS *****
0003   C.
0004   . C. ;ãAOSYÁY$YÁY~¼Á»Û;ã
0005   C.
0006   C. YÁY$;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008   C.
0009   . C. ***** AOCs : Reload orbital element (send every contact) *****
0010   C.   Áí;Èº¿ÒÁº•µ°Æ»Í×ÁÇòÍYçYÁY×Yí;¼YÉ;ÈÈ%µ•íÉ;ÈºÉ¼°ÇÔº•¿¼i¹çºÍ;çÁ®, ùº¹ºÈºººçÁ+¿®º•ºÈºººººÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012   C.
0013   C.
0014   . C. ***** AOCs Commands (Tracking Curve Upload) *****
0015   C. Upload the Orbit Element and the Target Attitude
0016   C. RAM-ID:TARGET_ATT
0017   . S. RAM   ram-150:TARGET_ATT
0018   ( )
0019   C.
0020   C.
0021   C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023   BC   (07 00 00 00 18 00)
0024   C.
0025   C.       <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026   C.
0027   C.
0028   C. Change the TLMFormatNo for the AOCs Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030   BC   (04 0b f8)
0031   C.
0032   C. Wait for AOCSDUMP to end
0033   C.
0034   . C. Check the dump memory
0035   C.
0036   C.                                     Result = OK [ ]
0037   C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039   BC   (02 0a f8)
0040   C.
0041   C.                                     <A_***>[TLM STS] FMT = 2 [ ]
0042   C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044   . C.
0045   . C. ***** AOCs Commands (Orbital Element Update) *****
0046   C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048   BC   (16)
0049 +. DC 02-8E AOCU_ORB_UPD
0050   C.
0051   C.       <A_ORB>[ORBIT] EPC = 5602448.5 +- 1.0 (s) [ ]
0052   C.
0053   . C.
0054   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055 +. DC 07-FC EIS_MODE_MANU
0056   BC   (21 02)
0057   . C. Verify EIS in MANUAL mode
0058   . C. Estimated OBSTBL upload time is 13s
0059   C. *****
0060   C. EIS START OBSTBL LOAD
0061   C. *****
0062   . S. RAM   ram-820:EIS_OBSTBL
0063   ( )
0064 +. DC 07-FC EIS_DUMP_OBSTBL
0065   BC   (07 07 07 00 00 70 00)
0066   C.
0067   C. Execute, after the success of OBSTBL upload.
0068   C. Set EIS TI-commands
0069 +. TI 2013-10-29 10:40:50.0
0070   DC 07-FC EIS_MODE_CHG_ENA
0071   BC   (20)
0072   . C.           [ ] [HK1_TI_CMD_NUM]   EQ   1 COUNTUP
0073   C. *****
0074   C. EIS END OBSTBL LOAD
0075   C. *****
0076   C.
0077   . C. ***** MDP `´ÃÍòÎ»ò¼YºÈÁºº¹ºÈDCBC•×²è *****
0078   C. (¾á°íYÓYÁYÉYºYÉYáYçYÈºº¼ºº¼ºº¼Á»Ûºº¹ºº)
0079   . S. DC-BC dcbc-402:DCBC
0080   (MDP_known_event)
0081   C.
0082   C.
0083   . C. ***** YDY¹•İ Daily±¿İÑºÈ´Øº¹ºÈDCBC•×²è *****
0084   . S. DC-BC dcbc-153:DCBC
0085   (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0086   C.
0087   C.
0088   . C. ;ãLOSÁY$YÁY~¼Á»Û;ã
0089   C.
0090   . C. ***** LOS *****
0091   C.
```





```
0194 DC 07-F0 MDP_XRT_MODE_OBSV
0195 BC (c2)
0196 . C. ----- Success Verify ? OK / NG ____
0197 C.
0198 C. ***** XRT END *****
0199 C.
0200 . C. ***** MDP `úÃîñî»ò¼ŷñÊÃĐñ¹ñèDCBC•x²è *****
0201 C. (¼á°îŷÓŷÃŷÈŷŲŷËŷáŷçŷèñÊ¼ñ¼Ã»Ûñ¹ñè)
0202 . S. DC-BC dcbc-402:DCBC
0203 (MDP_known_event)
0204 C.
0205 C.
0206 . C. ***** ŷĐŷ¹•İ Daily±¿İññÊ´Øñ¹ñèDCBC•x²è *****
0207 . S. DC-BC dcbc-153:DCBC
0208 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0209 C.
0210 C.
0211 . C. ;ãLOSŷÁŷŲŷÃŷ¬¼Ã»Ûñ¹ñè
0212 C.
0213 . C. ***** LOS *****
0214 C.
```

\*\*\* OP Sequence for XRT \*\*\*

```

2013/10/29 10:50:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2013/10/29 10:50:56.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2013/10/29 10:51:00.0 AOCS_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 f9 bb ad 48
2013/10/29 10:51:16.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2013/10/29 10:51:18.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2013/10/29 10:51:20.0 XRT_AEC_RESET_413_OG [0x19d]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2013/10/29 10:51:22.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2013/10/29 10:51:24.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2013/10/29 10:51:26.0 XRT_QT_PROG_SET_426_OG [0x1aa]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 01
2013/10/29 10:53:58.0 XRT_FL_PROG_SET_443_OG [0x1bb]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0c
2013/10/29 10:54:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2013/10/29 13:59:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2013/10/29 13:59:56.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2013/10/29 14:00:00.0 AOCS_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 03 00 00 00 00
2013/10/29 14:00:16.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2013/10/29 14:00:18.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2013/10/29 14:00:20.0 XRT_AEC_RESET_413_OG [0x19d]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2013/10/29 14:00:22.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2013/10/29 14:00:24.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2013/10/29 14:00:26.0 XRT_QT_PROG_SET_426_OG [0x1aa]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 01
2013/10/29 14:02:58.0 XRT_FL_PROG_SET_443_OG [0x1bb]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 0c
2013/10/29 14:03:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2013/10/29 14:07:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2013/10/29 14:07:32.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2013/10/29 14:07:34.0 XRT_PREFLR_STRT_418_OG [0x1a2]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2013/10/29 14:10:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2013/10/29 14:14:00.0 XRT_Custom_430_OG [0x1ae]
2013/10/29 14:15:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2013/10/29 15:40:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2013/10/29 15:40:32.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2013/10/29 15:40:34.0 XRT_PREFLR_STRT_418_OG [0x1a2]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2013/10/29 15:43:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2013/10/29 16:04:00.0 XRT_Custom_430_OG [0x1ae]
2013/10/29 16:05:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2013/10/29 17:17:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2013/10/29 17:17:02.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2013/10/29 17:17:04.0 XRT_PREFLR_STRT_418_OG [0x1a2]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2013/10/29 17:20:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2013/10/29 17:40:30.0 XRT_Custom_430_OG [0x1ae]
2013/10/29 17:41:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2013/10/29 17:57:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2013/10/29 17:57:56.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00
2013/10/29 17:58:00.0 AOCS_OrE-point_Start_3_OG [0x099]
                        AOCU_NM 5 02-76 00 00 00 00 00
2013/10/29 17:58:16.0 XRT_FLD_DIS_434_OG [0x1b2]
                        MDP_XRT_FLD_DIS 1 07-F0 d9
2013/10/29 18:00:54.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS 1 07-F0 c9
2013/10/29 18:00:56.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2013/10/29 18:00:58.0 XRT_QT_PROG_SET_440_OG [0x1b8]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 05

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## XRT\_OGLIST\_0862.chk

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2013/10/29	18:01:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/29	18:07:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/29	18:07:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/10/29	18:08:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	03 00 00 00 00
2013/10/29	18:08:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/10/29	18:08:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/10/29	18:08:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/10/29	18:08:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/10/29	18:08:24.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/29	18:08:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2013/10/29	18:10:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c
2013/10/29	18:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/29	18:54:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/29	18:54:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/29	18:54:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/29	18:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/29	19:17:30.0	XRT_Custom_430_OG [0x1ae]			
2013/10/29	19:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/29	20:31:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/29	20:31:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/29	20:31:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/29	20:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/29	20:54:30.0	XRT_Custom_430_OG [0x1ae]			
2013/10/29	20:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/29	22:08:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/29	22:08:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/29	22:08:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/29	22:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/29	22:30:30.0	XRT_Custom_430_OG [0x1ae]			
2013/10/29	22:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/29	23:46:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/29	23:46:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/29	23:46:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/29	23:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/29	23:55:30.0	XRT_Custom_430_OG [0x1ae]			
2013/10/29	23:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/30	01:11:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/30	01:11:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/30	01:11:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/30	01:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/30	01:29:00.0	XRT_Custom_430_OG [0x1ae]			
2013/10/30	01:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/30	02:44:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/30	02:44:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/10/30	02:44:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/10/30	02:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/10/30	03:05:30.0	XRT_Custom_430_OG [0x1ae]			
2013/10/30	03:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/10/30	04:14:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/10/30	04:14:02.0	XRT_FLD_RESET_415_OG [0x19f]			



2013/10/30	04:14:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/10/30	04:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/10/30	04:43:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/10/30	04:44:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/10/30	05:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/10/30	05:54:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/10/30	05:54:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/10/30	05:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/10/30	06:20:30.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/10/30	06:21:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/10/30	06:30:24.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/10/30	06:30:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	AOCU_NM	5	02-76	00 00 00 00 00		
2013/10/30	06:30:30.0	AOCs_OrE-point_Start_3_OG [0x099]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/10/30	06:30:46.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/10/30	06:33:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/10/30	06:33:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/10/30	06:33:28.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2013/10/30	06:33:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/10/30	06:40:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/10/30	06:40:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/10/30	06:40:30.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 00 00 00 00		
2013/10/30	06:40:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/10/30	06:40:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/10/30	06:40:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/10/30	06:40:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/10/30	06:40:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/10/30	06:40:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2013/10/30	06:43:28.0	XRT_FL_PROG_SET_443_OG [0x1bb]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c		
2013/10/30	06:43:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/10/30	07:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/10/30	07:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/10/30	07:35:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/10/30	07:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/10/30	07:57:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/10/30	07:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/10/30	09:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/10/30	09:14:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/10/30	09:14:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/10/30	09:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/10/30	09:33:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/10/30	09:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/10/30	14:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/10/30	14:41:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/10/30	14:41:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/10/30	14:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/10/30	14:53:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/10/30	14:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/10/30	16:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/10/30	16:16:32.0	XRT_FLD_RESET_415_OG [0x19f]						

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2013/10/30	16:16:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/10/30	16:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/10/30	16:40:00.0	XRT_Custom_430_OG [0x1ae]							
2013/10/30	16:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/10/30	17:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/10/30	17:53:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/10/30	17:53:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/10/30	17:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/10/30	18:16:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/10/30	18:16:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2013/10/30	18:17:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00			
2013/10/30	18:17:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/10/30	18:19:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/10/30	18:19:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/10/30	18:19:58.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 05			
2013/10/30	18:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/10/30	18:26:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/10/30	18:26:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2013/10/30	18:27:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 00 00 00 00			
2013/10/30	18:27:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/10/30	18:27:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/10/30	18:27:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/10/30	18:27:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/10/30	18:27:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/10/30	18:27:26.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06			
2013/10/30	18:29:58.0	XRT_FL_PROG_SET_443_OG [0x1bb]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c			
2013/10/30	18:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/10/30	19:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/10/30	19:30:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/10/30	19:30:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/10/30	19:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/10/30	19:53:30.0	XRT_Custom_430_OG [0x1ae]							
2013/10/30	19:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/10/30	21:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/10/30	21:07:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/10/30	21:07:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/10/30	21:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/10/30	21:31:00.0	XRT_Custom_430_OG [0x1ae]							
2013/10/30	21:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/10/30	22:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/10/30	22:45:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/10/30	22:45:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/10/30	22:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/10/30	23:05:30.0	XRT_Custom_430_OG [0x1ae]							
2013/10/30	23:06:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/10/31	00:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/10/31	00:22:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/10/31	00:22:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			

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2013/10/31	00:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/10/31	00:29:30.0	XRT_Custom_430_OG [0x1ae]							
2013/10/31	00:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/10/31	01:45:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/10/31	01:45:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/10/31	01:45:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/10/31	01:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/10/31	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/10/31	01:59:56.0	XRT_FOCUS_POSITION_449_OG [0x1c1]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/10/31	02:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 ac 00 00 00				
2013/10/31	02:00:16.0	XRT_FLD_DIS_438_OG [0x1b6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/10/31	02:00:18.0	XRT_FLRCTRL_DIS_406_OG [0x196]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/10/31	02:00:20.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/10/31	02:00:22.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2013/10/31	02:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/10/31	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/10/31	03:59:56.0	XRT_FOCUS_POSITION_449_OG [0x1c1]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/10/31	04:00:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 00 00 54 00				
2013/10/31	04:00:16.0	XRT_FLD_DIS_438_OG [0x1b6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/10/31	04:00:18.0	XRT_FLRCTRL_DIS_406_OG [0x196]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/10/31	04:00:20.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/10/31	04:00:22.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2013/10/31	04:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/10/31	06:02:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/10/31	06:02:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/10/31	06:02:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/10/31	06:02:46.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/10/31	06:05:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/10/31	06:05:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/10/31	06:05:28.0	XRT_QT_PROG_SET_440_OG [0x1b8]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05				
2013/10/31	06:05:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/10/31	06:12:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/10/31	06:12:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/10/31	06:12:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2013/10/31	06:12:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/10/31	06:12:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/10/31	06:12:50.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/10/31	06:12:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/10/31	06:12:54.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/10/31	06:12:56.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2013/10/31	06:15:28.0	XRT_FL_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0c				
2013/10/31	06:15:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/10/31	06:32:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/10/31	06:32:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/10/31	06:32:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/10/31	06:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/10/31	06:57:00.0	XRT_Custom_430_OG [0x1ae]							
2013/10/31	06:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							

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2013/10/31	08:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/10/31	08:12:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/10/31	08:12:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/10/31	08:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/10/31	08:34:00.0	XRT_Custom_430_OG [0x1ae]					
2013/10/31	08:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/10/31	09:50:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/10/31	10:06:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00	