

# XRT Timeline to be uploaded on 2013/11/09

Period: 2013/11/09 10:33:00 - 2013/11/14 10:41:00

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## Normal mode

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### XOB #19DD: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band

Term	Pointing (x, y)	Comment
11/09 10:46:46 - 11/09 17:54:24	Track ( 55.4, -279.6) @ 11/09 10:43:00	# OP start + 10min, Track AR11890
11/09 18:08:15 - 11/10 05:38:54	Track ( 122.0, -278.6) @ 11/09 18:04:30	# Cont,
11/10 05:52:45 - 11/10 09:44:54	Track ( 227.1, -276.6) @ 11/10 05:49:00	# Cont,

**PROG= 20 1-time(s)**

- Subr= 1 1-time(s) 2.0sec
  - Seqn= 8 2-time(s) 2.0sec
    - Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
- Subr= 2 2-time(s) 2.0sec
  - Seqn= 24 1-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
    - Open/G-band Open/G-band open Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
  - Seqn= 98 4-time(s) 2.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - Open/Ti-poly Open/thick-Be close Safe Norm 1.00s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
    - Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
  - Seqn= 18 45-time(s) 50.0sec
    - thin-Be/Open Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - Al-poly/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - thin-Be/Open Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
    - Al-poly/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
    - thin-Be/Open Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
    - Al-poly/Open Open/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

### XOB #19DC: Synoptic 9 Filter w/ Triplet Exposures - 2x2 Q98 - Gband(8ms)

Term	Pointing (x, y)	Comment
11/09 17:57:30 - 11/09 18:04:24	Fixed ( 0.0, 0.0)	synoptic, shifted -5.5 min

**PROG= 16 1-time(s)**

- Subr= 1 1-time(s) 12.0sec
  - Seqn= 84 1-time(s) 4.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 5 1-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
  - Seqn= 86 1-time(s) 4.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 95 1-time(s) 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 6 1-time(s) 2.0sec
    - Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
    - Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
- Subr= 2 2-time(s) 12.0sec
  - Seqn= 56 1-time(s) 2.0sec
    - Al-poly/Open Al-poly/Open close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Al-poly/Open Al-poly/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
  - Seqn= 22 1-time(s) 2.0sec
    - C-poly/Open C-poly/Open close Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - C-poly/Open C-poly/Open close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
  - Seqn= 43 1-time(s) 2.0sec
    - Al-poly/Ti-poly Al-poly/thick-Al close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
  - Seqn= 38 1-time(s) 4.0sec
    - med-Al/Open med-Al/Open close Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
  - Seqn= 46 1-time(s) 4.0sec
    - Open/thick-Be Open/thick-Be close Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

### XOB #19DA: Synoptic Q95 2x2 - Al/mesh(5/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/362/1443) + Thin

Term	Pointing (x, y)	Comment
11/10 05:46:45 - 11/10 05:48:54	Fixed ( 0.0, 0.0)	synoptic, shifted -21.0 min

**PROG= 17 1-time(s)**

Subr= 1		1-time(s)		12.0sec											
Seqn= 84		1-time(s)		4.0sec											
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 5		1-time(s)		2.0sec											
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec		
Seqn= 86		1-time(s)		4.0sec											
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 95		1-time(s)		2.0sec											
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 6		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

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### Flare mode

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### XOB #19DE: Flare obs. dynamics - thin-Be high cadence long/short pairs + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loc

Term	Pointing (x, y)	Comment
11/09 10:46:46 - 11/09 17:54:24	Track ( 55.4, -279.6) @ 11/09 10:43:00	# OP start + 10min, Track AR11890
11/09 18:08:15 - 11/10 05:38:54	Track ( 122.0, -278.6) @ 11/09 18:04:30	# Cont,
11/10 05:52:45 - 11/10 09:44:54	Track ( 227.1, -276.6) @ 11/10 05:49:00	# Cont,

### PROG= 13 15-time(s)

Subr= 1		45-time(s)		2.0sec											
Seqn= 44		1-time(s)		8.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1024, 1024)	Q=95	2	0	2.0sec		
thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Subr= 2		1-time(s)		2.0sec											
Seqn= 10		1-time(s)		2.0sec											
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Seqn= 11		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512	(1024, 1024)	Q=95	2	0	2.0sec		
Seqn= 15		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512	(1024, 1024)	Q=98	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

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### Active Region Search

\* \* \* \* \*

NOT USED

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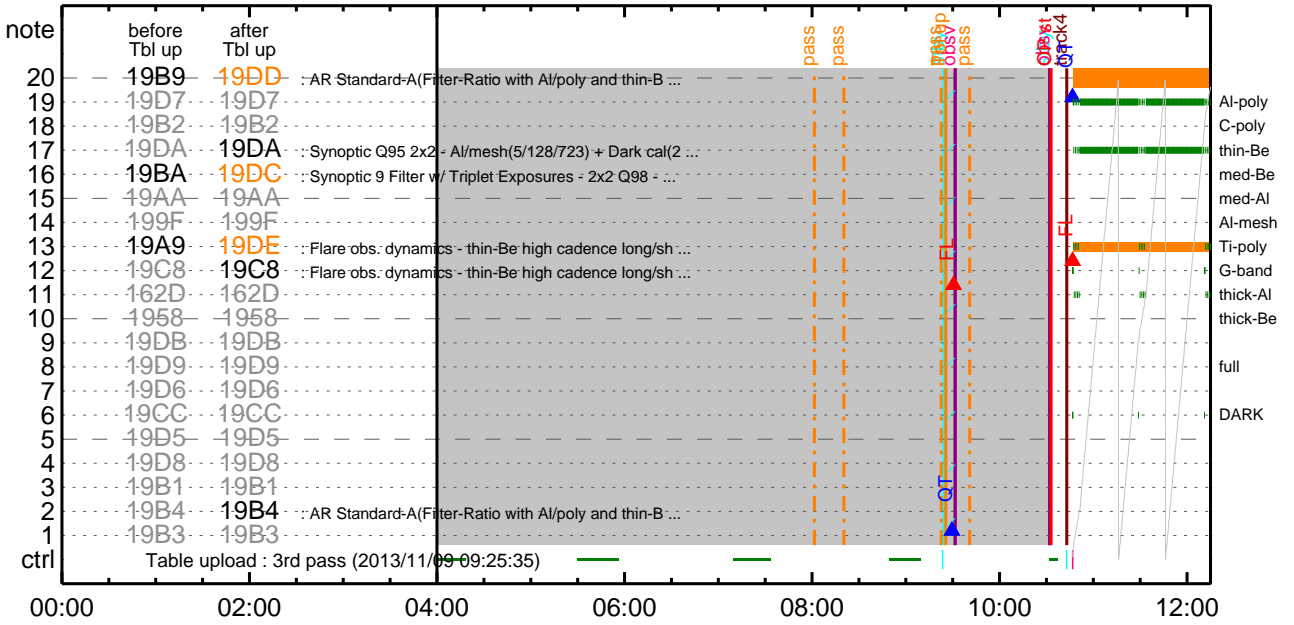
### Flare Detection

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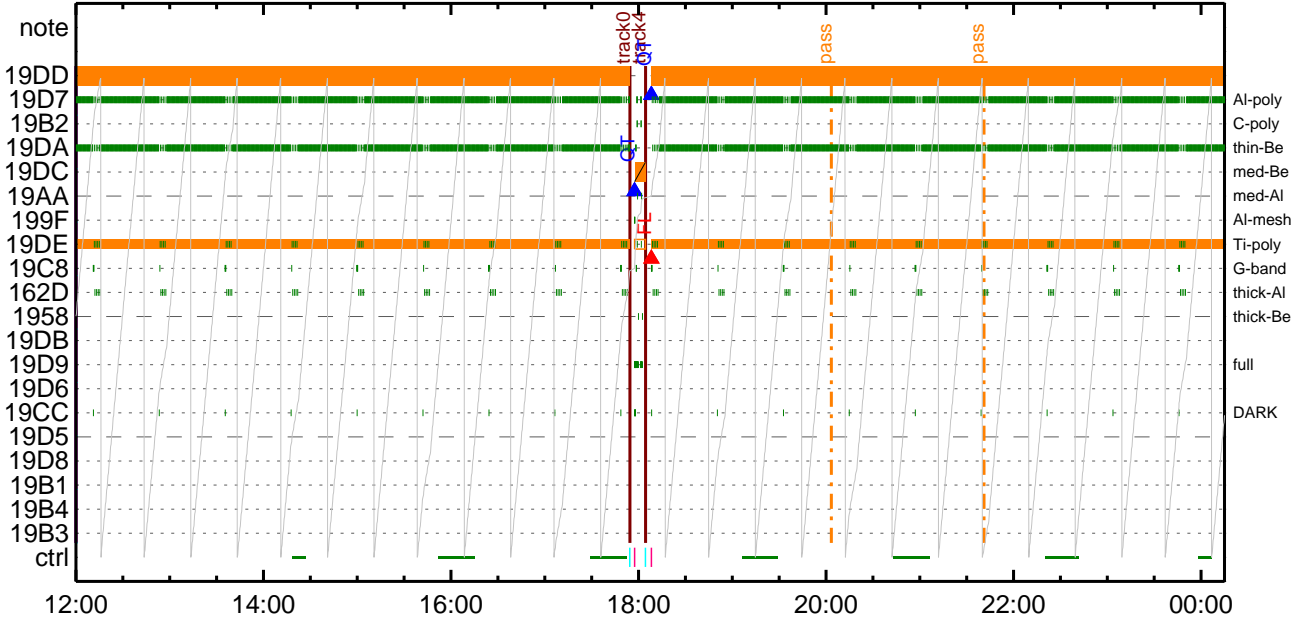
### FLD Patrol

Term	Pointing (x, y)	Comment
11/09 18:08:01 - 11/10 05:46:31	Track ( 122.0, -278.6) @ 11/09 18:04:30	# Cont,
11/10 05:52:31 - 11/14 10:41:00	Track ( 227.1, -276.6) @ 11/10 05:49:00	# Cont,
Open/Ti-poly	Open/thick-Al	close Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

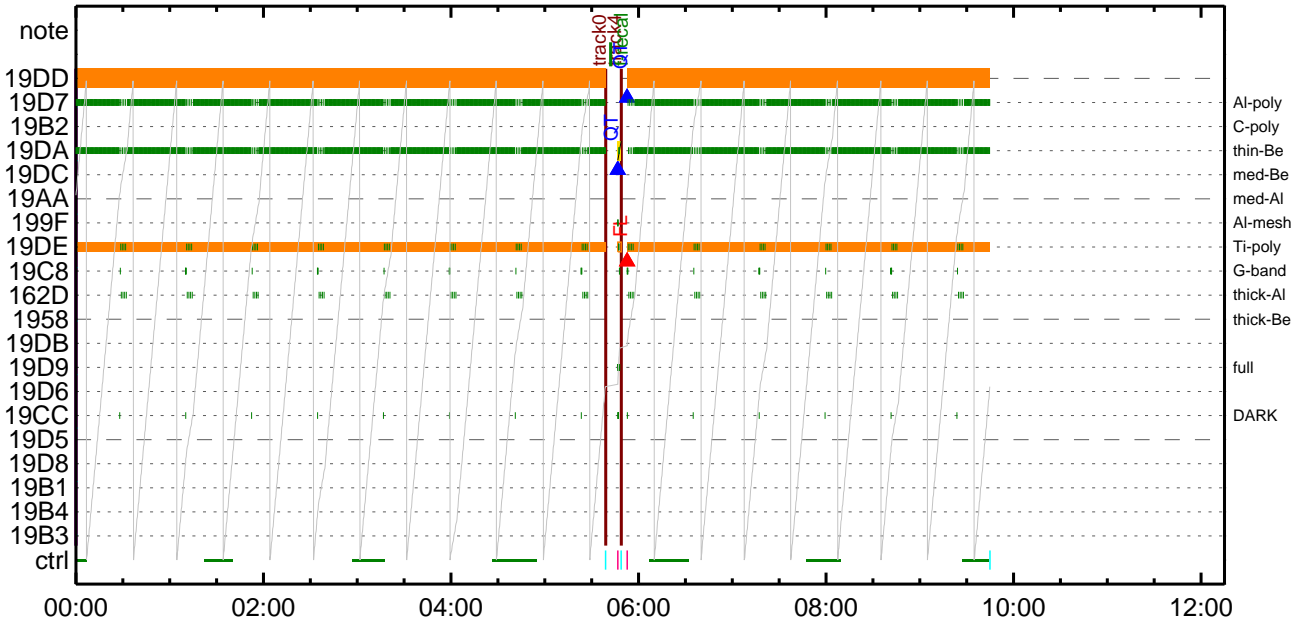
### CMDI #0889 2013/11/09



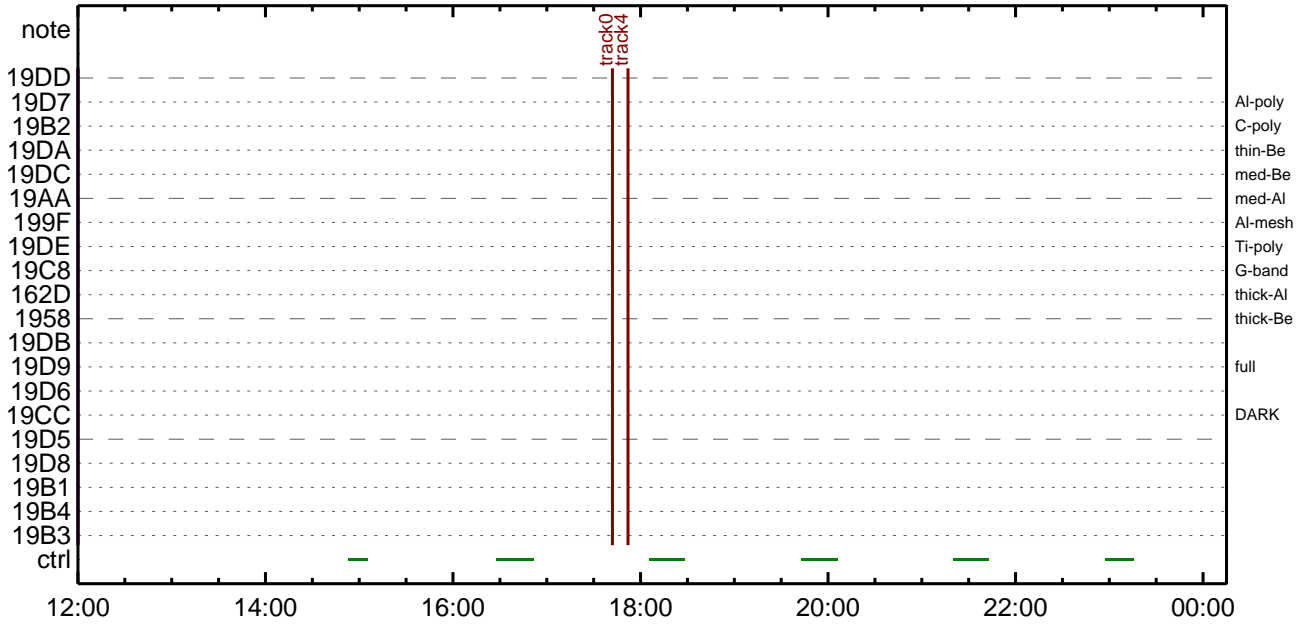
### CMDI #0889 2013/11/09



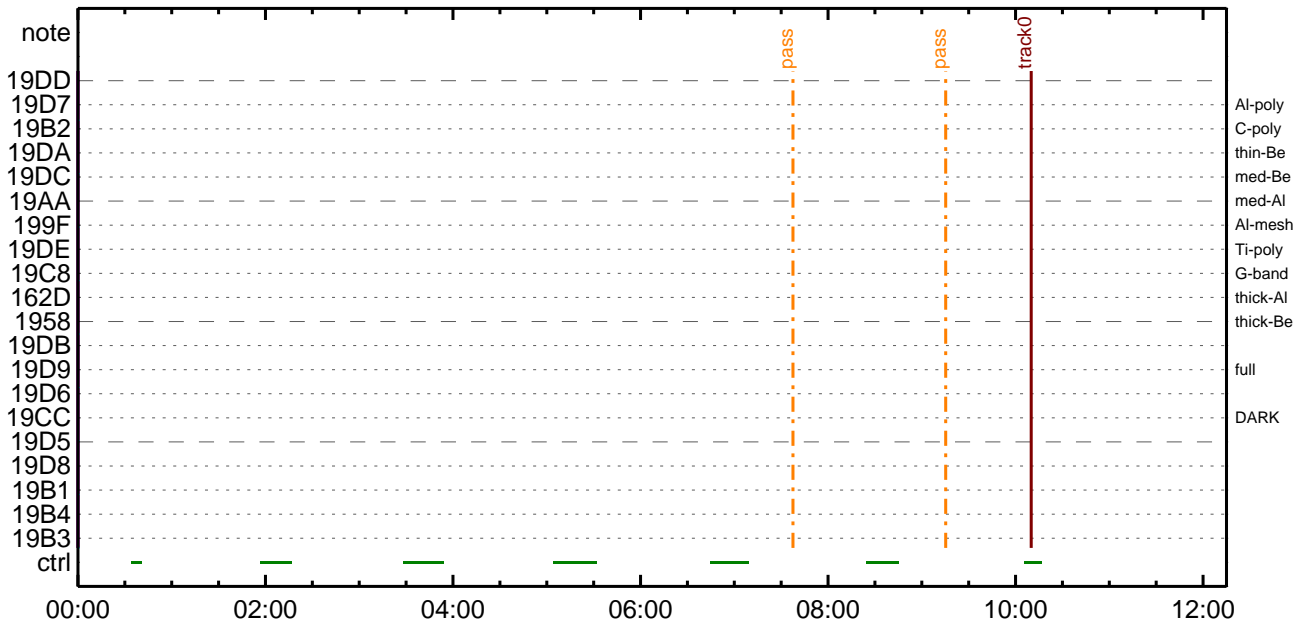
### CMDI #0889 2013/11/10



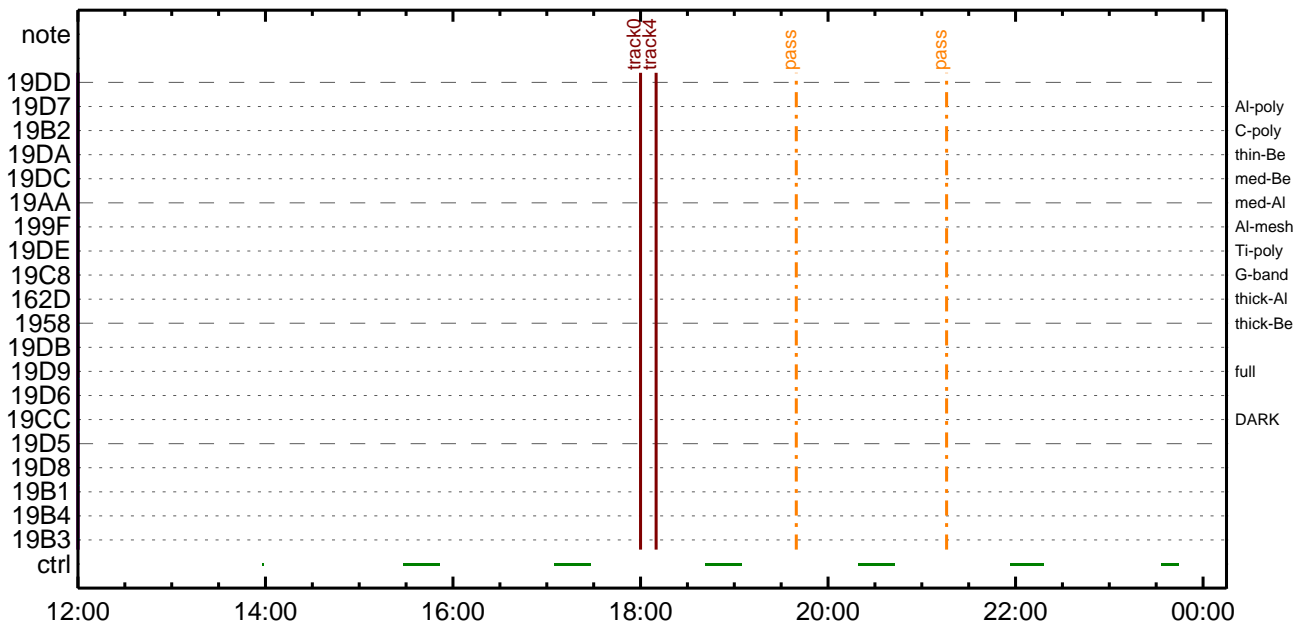
CMDI #0889 2013/11/10



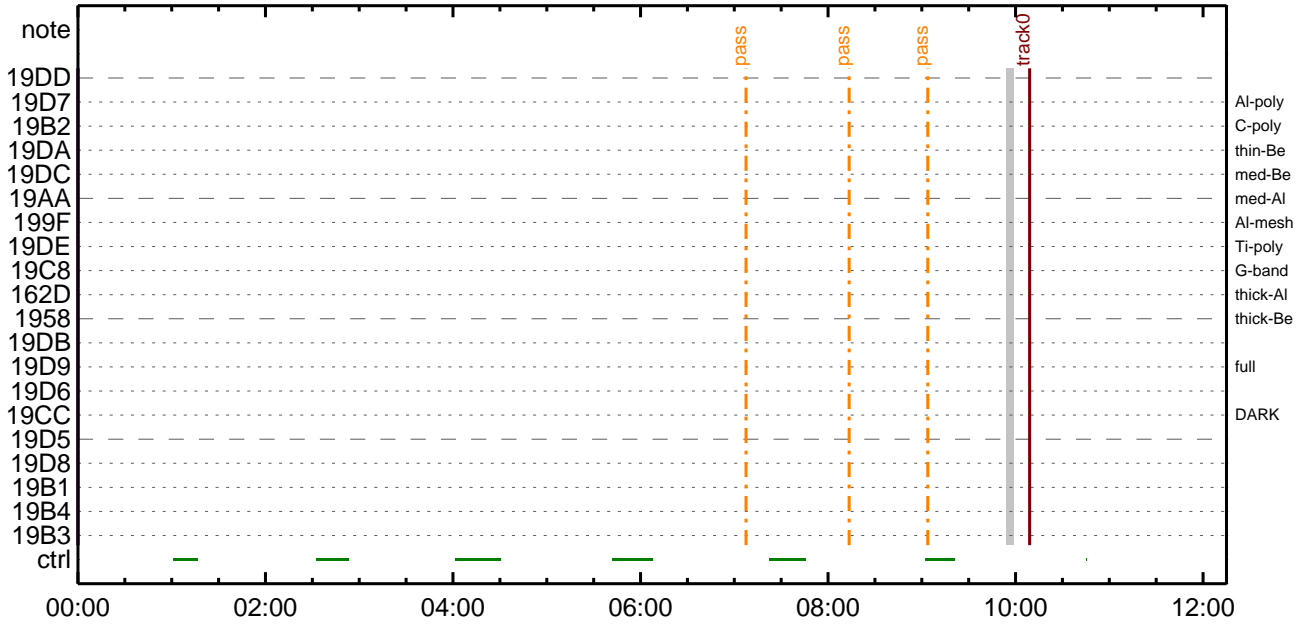
CMDI #0889 2013/11/11



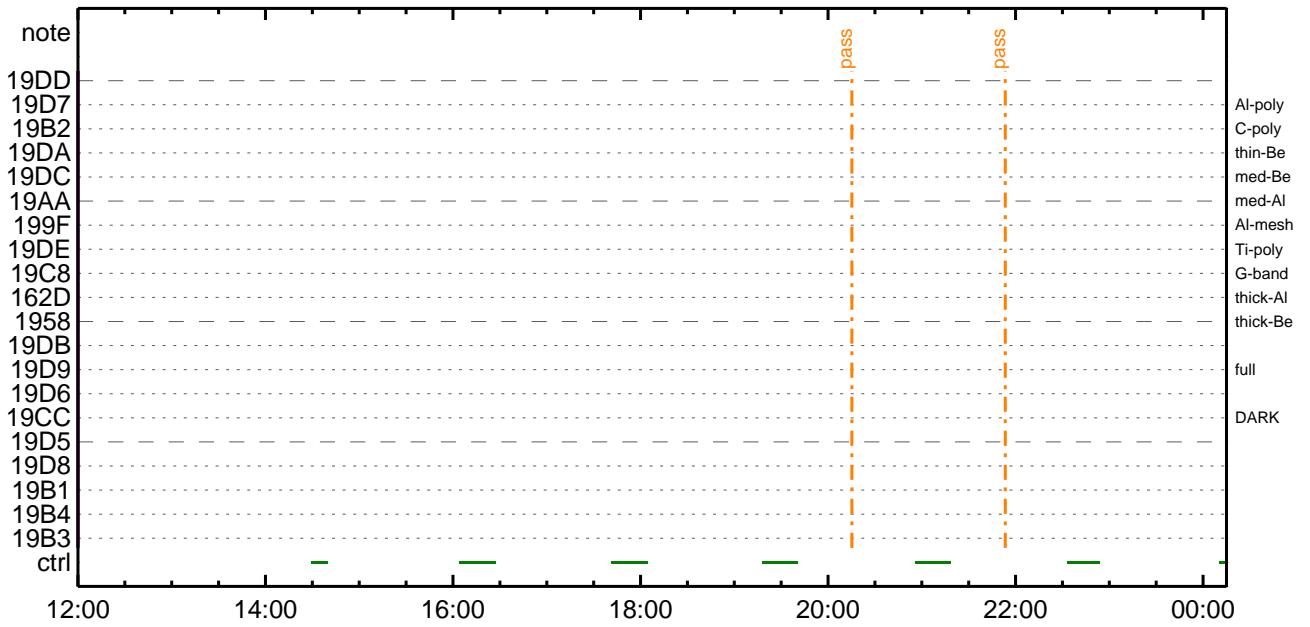
CMDI #0889 2013/11/11



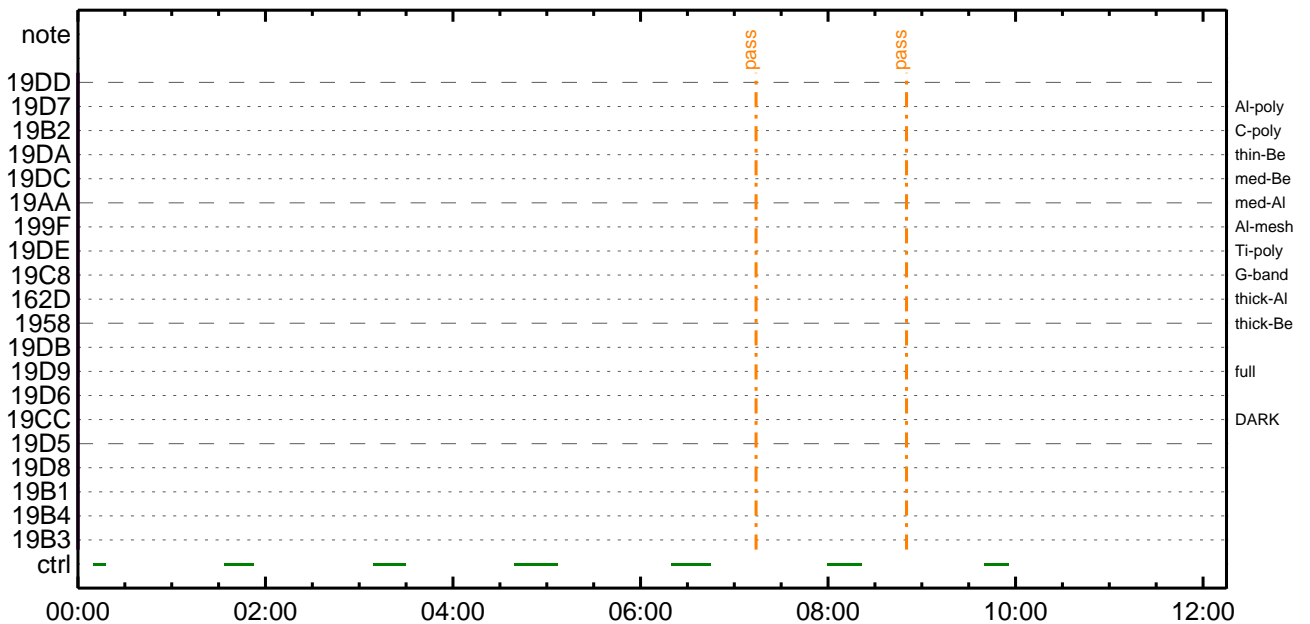
CMDI #0889 2013/11/12



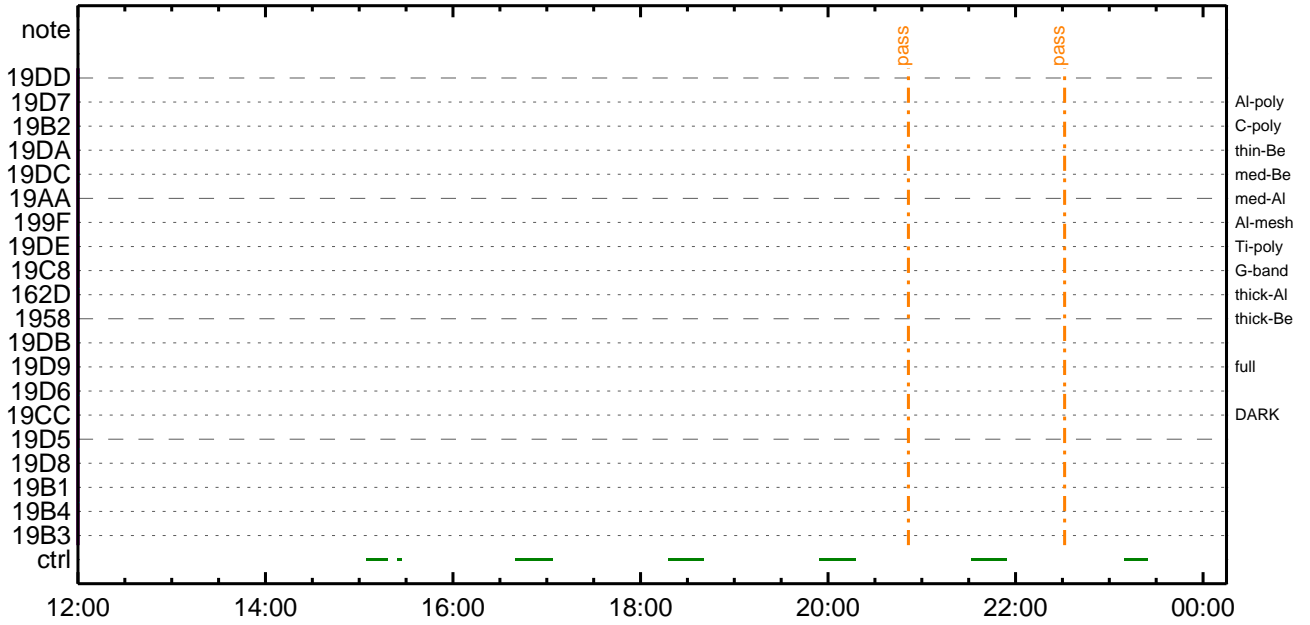
CMDI #0889 2013/11/12



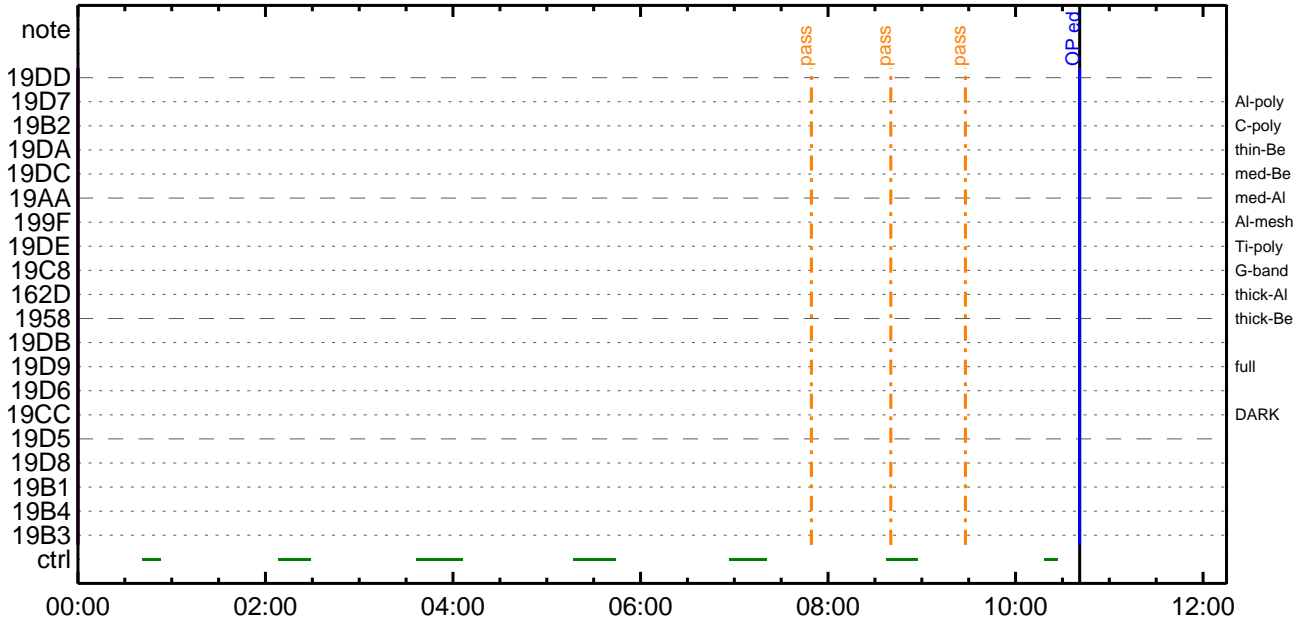
CMDI #0889 2013/11/13



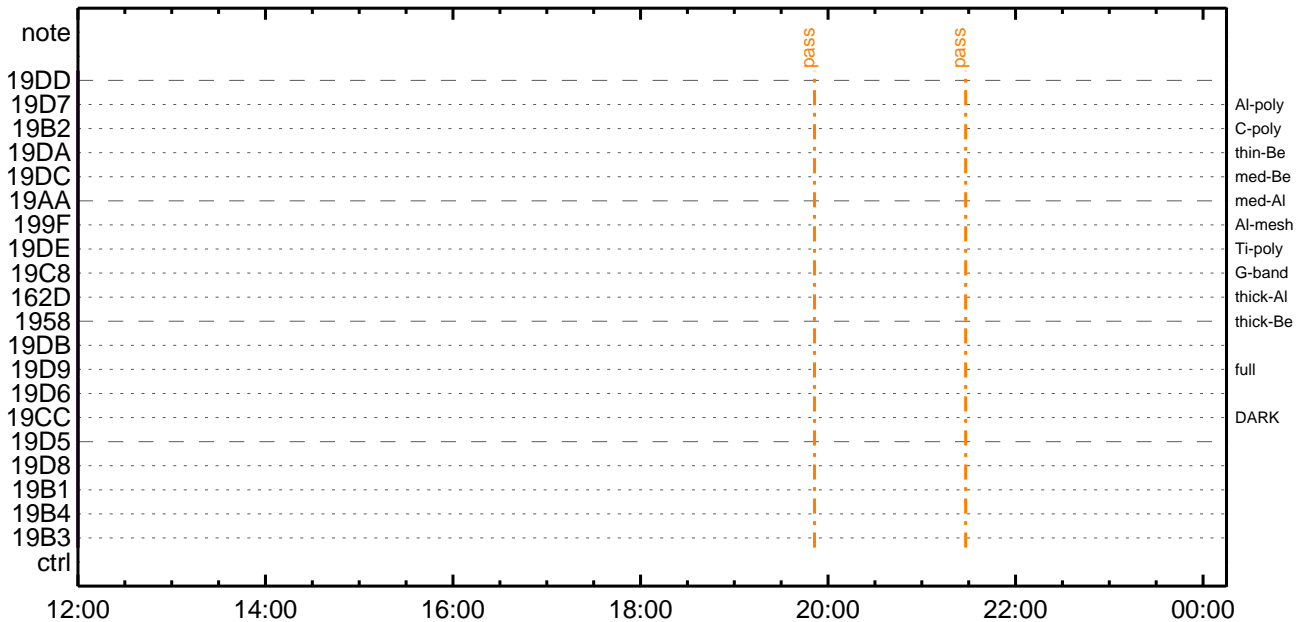
### CMDI #0889 2013/11/13



### CMDI #0889 2013/11/14



### CMDI #0889 2013/11/14





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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-026:OP
0104 ( )
0105 S. OG og-026:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ä´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOYx½ê½çòðÁÔæòÇ¼ª°¬òE¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½, Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²²ò¿;çSET²EEDUMP²îE±²îYÑY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²YóYÉòðÁDî¿(UT)
0183 +. TI 2013-11-09 10:28:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-11-09 10:28:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-11-09 10:28:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2013-11-09 10:32:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]                      EQ          1COUNTUP
0198 C.
0199 C. °Ê²¼¼öîÄë%îíñöîî¥Ä¥§¥Ä¥¹àîü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]                    EQ          ENA
0201 C.          çç[HK1_TI_CMD_NUM]                        EQ          4
0202 C.          çç[HK1_NEXT_EXEC_PIM]                     EQ          DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]                       EQ          0xB3
0204 C.
0205 C. *****
0206 C. TIîî°è¥Ä¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC        (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]                    EQ          07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]                    EQ          2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]                     EQ          3
0215 C.          çç[HK1_DMP_REPEAT_NUM]                   EQ          0
0216 C.          çç[HK1_DMA_DMP_PIM]                       EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC        (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]                       EQ          7
0220 C.          çç[HK1_PKT_GEN_TIME]                       EQ          0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]                   EQ          32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]                   EQ          4M
0223 C.          çç[HK1_DMP_CHK_FLG]                       EQ          EXEC
0224 C.
0225 C. ¥Ä¥Ö¥×½¹î»ò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]                       EQ          NON
0227 C.
0228 C. RAM ID=TI_TBLòîî½è¹ç.ë²îOKòð³îç§
0229 C.
0230 C. DHU¥à;½¥Ê;è¼¥½.¥î;½¥Ê;Èòðîäò¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC        (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]                       EQ          2
0234 C.          çç[HK1_PKT_GEN_TIME]                       EQ          0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]                   EQ          32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]                   EQ          4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2013-11-09 10:32:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC        (41)
0245 C. -----
0246 C.   HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2013-11-09 10:32:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC        (21 02)
0258 +. TI 2013-11-09 10:32:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC        (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]                      EQ          2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2013-11-09 10:32:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC        (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]                      EQ          1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÄîäî»ö¼ÝòÈÄðò¹èDCBC.x²è *****
0276 C. (%á°îî¥Ö¥Ä¥È¥þ¥È¥á¥ç¥èèÈ¼òò¼Ä»Ûò¹è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ð¥¹.î Daily±çîñèÈ´òò¹èèDCBC.x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOS¥Ä¥§¥Ä¥¹-¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

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0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 08 80 80 20 20)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 09 80 80 20 08)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 0a 80 80 08 20)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 0f 80 80 06 06)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 10 80 80 08 08)
0106 + DC 07-F0 MDP_XRT_FLD_ENA
0107 BC (d8)
0108 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0109 BC (c8)
0110 + DC 07-F0 MDP_XRT_AEC_RESET
0111 BC (d0)
0112 + DC 07-F0 MDP_XRT_ARS_DIS
0113 BC (d5)
0114 + DC 07-F0 MDP_XRT_FLD_RESET
0115 BC (da)
0116 + DC 07-F0 MDP_XRT_QT_PROG_SET
0117 BC (c4 02)
0118 + DC 07-F0 MDP_XRT_FL_PROG_SET
0119 BC (c5 0c)
0120 . C. ----- Success Verify ? OK / NG ____
0121 C.
0122 C.
0123 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0124 C.
0125 +. DC 07-F0 MDP_XRT_MODE_OBSV
0126 BC (c2)
0127 +. TI 2013-11-09 10:32:02.0
0128 DC 07-F0 MDP_XRT_MODE_OBSV
0129 BC (c2)
0130 . C. ----- Success Verify ? OK / NG ____
0131 C.
0132 C. ***** XRT END *****
0133 C.
0134 . C. ***** MDP 'ũÃîñî»ö¼ýñËÄðñ¹ñëDCBC•x²è *****
0135 C. (%ã°îÿÓÿÄÿËÿPÿËÿáÿçÿëñE¼ññ¼Ä»Ûñ¹ñè)
0136 . S. DC-BC dcbc-402:DCBC
0137 (MDP_known_event)
0138 C.
0139 C.
0140 . C. ***** ÿDÿ¹•Ï Daily±¿ÎÑñË´Øñ¹ñëDCBC•x²è *****
0141 . S. DC-BC dcbc-153:DCBC
0142 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0143 C.
0144 C.
0145 . C. ;ãLOSÿÄÿSÿÿÄÿ¹¼Ä»Û;ä
0146 C.
0147 . C. ***** LOS *****
0148 C.
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Nov 09, 13 12:22

XRT\_OGLIST\_0889.chk

Page 1/2

\*\*\* OP Sequence for XRT \*\*\*

2013/11/09	10:42:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/09	10:42:56.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/11/09	10:43:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2013/11/09	10:46:07.5	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/11/09	10:46:12.5	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/11/09	10:46:32.5	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/11/09	10:46:34.5	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/11/09	10:46:36.5	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/11/09	10:46:38.5	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/11/09	10:46:40.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/11/09	10:46:42.5	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2013/11/09	10:46:44.5	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2013/11/09	10:46:46.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/09	17:54:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/09	17:54:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/11/09	17:54:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/11/09	17:54:46.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/11/09	17:57:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/11/09	17:57:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/11/09	17:57:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2013/11/09	17:57:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/09	18:04:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/09	18:04:26.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/11/09	18:04:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2013/11/09	18:07:36.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/11/09	18:07:41.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/11/09	18:08:01.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/11/09	18:08:03.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/11/09	18:08:05.5	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/11/09	18:08:07.5	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/11/09	18:08:09.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/11/09	18:08:11.5	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2013/11/09	18:08:13.5	XRT_FL_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2013/11/09	18:08:15.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/10	05:38:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/10	05:38:56.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/11/10	05:39:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/11/10	05:42:06.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/11/10	05:42:11.0	XRT_FOCUS_RECALIBRATE_433_OG [0x1b1]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2013/11/10	05:46:11.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/11/10	05:46:31.0	XRT_FLD_DIS_428_OG [0x1ac]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/11/10	05:46:39.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/11/10	05:46:41.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/11/10	05:46:43.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2013/11/10	05:46:45.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/10	05:48:54.0	XRT_CTRL_MANU_402_OG [0x192]							

2013/11/10	05:48:56.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/11/10	05:49:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00	
2013/11/10	05:52:06.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/10	05:52:11.5	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/11/10	05:52:31.5	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/11/10	05:52:33.5	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/11/10	05:52:35.5	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/11/10	05:52:37.5	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/11/10	05:52:39.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/10	05:52:41.5	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14	
2013/11/10	05:52:43.5	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2013/11/10	05:52:45.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/10	09:44:54.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/10	09:44:56.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/10	09:44:58.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/11/10	09:48:08.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/10	09:50:00.0	XRT_TCIB_XRT_S_HTR_A_ENA_432_OG [0x1b0]	TCIB_XRT_S_HTR_A_ENA	0	04-BC		
2013/11/10	17:42:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2013/11/10	17:52:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00	
2013/11/11	10:10:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 af 1b 01 58	
2013/11/11	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2013/11/11	18:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04 00 00 00 00	
2013/11/12	10:09:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	