

# XRT Timeline to be uploaded on 2013/11/12

Period: 2013/11/12 09:59:00 - 2013/11/16 10:16:00

\* \* \* \* \*

Normal mode

\* \* \* \* \*

XOB #19BA: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms													
Term		Pointing (x, y)						Comment					
11/13 16:03:00 - 11/13 16:09:54		Fixed ( -528.4, -528.4)						# XRT post-bakeout quadrant obs 1/4					
<b>PROG= 06 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 1 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs 1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs 1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec	
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 23 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.41s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Subr= 3 2-time(s) 2.0sec													
└─ Seqn= 12 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs 1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└─ Seqn= 7 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19B9: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms													
Term		Pointing (x, y)						Comment					
11/13 16:13:00 - 11/13 16:19:54		Fixed ( 528.4, -528.4)						2/4					
<b>PROG= 10 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 2 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs 1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs 1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec	
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 23 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.41s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Subr= 3 2-time(s) 2.0sec													
└─ Seqn= 12 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs 1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└─ Seqn= 7 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19B8: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms													
Term		Pointing (x, y)						Comment					
11/13 16:23:00 - 11/13 16:29:54		Fixed ( 528.4, 528.4)						3/4					
<b>PROG= 01 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 3 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs 1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec	
	Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs 1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec	
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 23 2-time(s) 2.0sec													
	Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.41s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Subr= 3 2-time(s) 2.0sec													
└─ Seqn= 12 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs 1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└─ Seqn= 7 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19B7: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms													
Term		Pointing (x, y)						Comment					
11/13 16:33:00 - 11/13 16:39:54		Fixed ( -528.4, 528.4)						4/4					
<b>PROG= 03 1-time(s)</b>													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 4 1-time(s) 12.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs 1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec	

Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	44ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
<b>Subr= 2</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
<b>Seqn= 23</b>		<b>2-time(s)</b>	<b>2.0sec</b>									
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Subr= 3</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
<b>Seqn= 12</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
<b>Seqn= 7</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #19B0: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-band**

Term	Pointing (x, y)	Comment
11/13 17:04:00 - 11/13 17:53:54	Track ( 812.3, -257.3) @ 11/13 16:40:00	# Track AR11890
11/13 18:07:00 - 11/14 05:59:54	Track ( 818.7, -256.6) @ 11/13 18:04:00	# Cont,
11/14 06:13:00 - 11/14 09:27:00	Track ( 867.1, -251.1) @ 11/14 06:10:00	# Cont,

**PROG= 15 Inf.-time(s)**

<b>Subr= 1</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
<b>Seqn= 8</b>		<b>2-time(s)</b>	<b>2.0sec</b>									
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
<b>Subr= 2</b>		<b>2-time(s)</b>	<b>2.0sec</b>									
<b>Seqn= 24</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Seqn= 98</b>		<b>4-time(s)</b>	<b>2.0sec</b>									
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 21</b>		<b>45-time(s)</b>	<b>60.0sec</b>									
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #19DA: Synoptic Q95 2x2 - Al/mesh(5/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/362/1443) + Thin**

Term	Pointing (x, y)	Comment
11/13 17:57:00 - 11/13 18:03:54	Fixed ( 0.0, 0.0)	synoptic, shifted -6.0 min
11/14 06:03:00 - 11/14 06:09:54	Fixed ( 0.0, 0.0)	synoptic

**PROG= 17 1-time(s)**

<b>Subr= 1</b>		<b>1-time(s)</b>	<b>12.0sec</b>									
<b>Seqn= 84</b>		<b>1-time(s)</b>	<b>4.0sec</b>									
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 5</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
<b>Seqn= 86</b>		<b>1-time(s)</b>	<b>4.0sec</b>									
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 95</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 6</b>		<b>1-time(s)</b>	<b>2.0sec</b>									
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \* **Flare mode** \* \* \* \* \*

**XOB #19DE: Flare obs. dynamics - thin-Be high cadence long/short pairs + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loc**

Term	Pointing (x, y)	Comment
11/13 17:04:00 - 11/13 17:53:54	Track ( 812.3, -257.3) @ 11/13 16:40:00	# Track AR11890
11/13 18:07:00 - 11/14 05:59:54	Track ( 818.7, -256.6) @ 11/13 18:04:00	# Cont,

<b>PROG= 13 15-time(s)</b>													
└─ <b>Subr= 1 45-time(s) 2.0sec</b>													
└─ <b>Seqn= 44 1-time(s) 8.0sec</b>													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>													
└─ <b>Seqn= 10 1-time(s) 2.0sec</b>													
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ <b>Seqn= 11 1-time(s) 2.0sec</b>													
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ <b>Seqn= 15 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \*

**Active Region Search**

\* \* \* \* \*

NOT USED

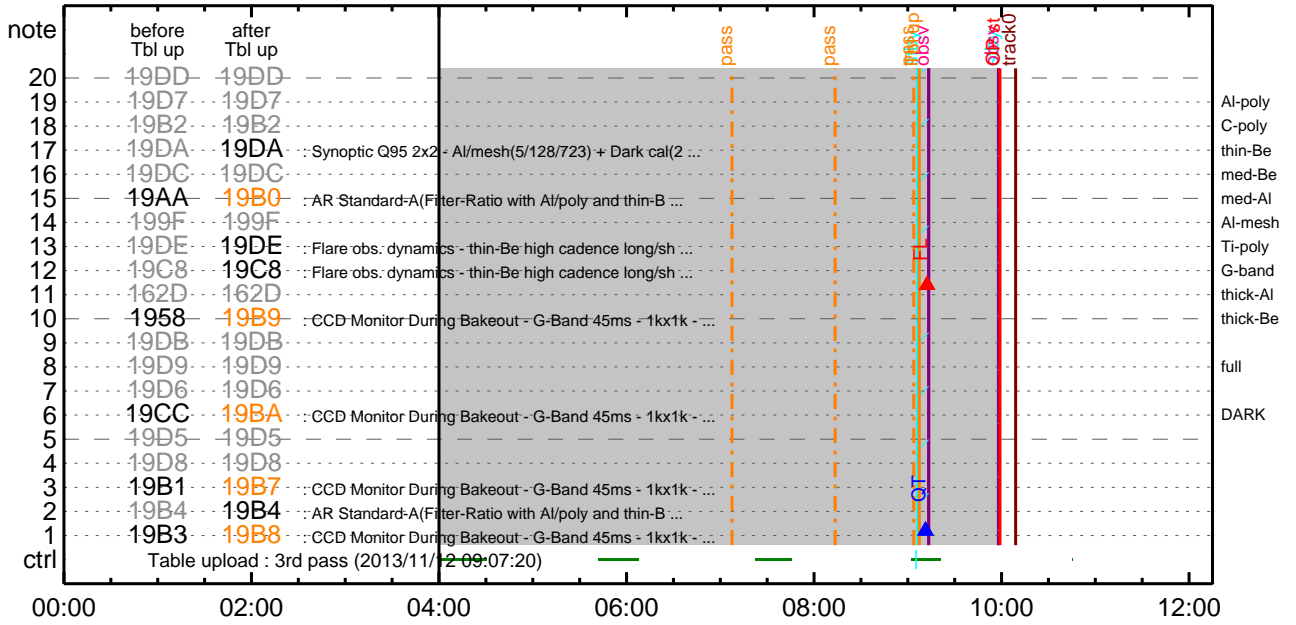
\* \* \* \* \*

**Flare Detection**

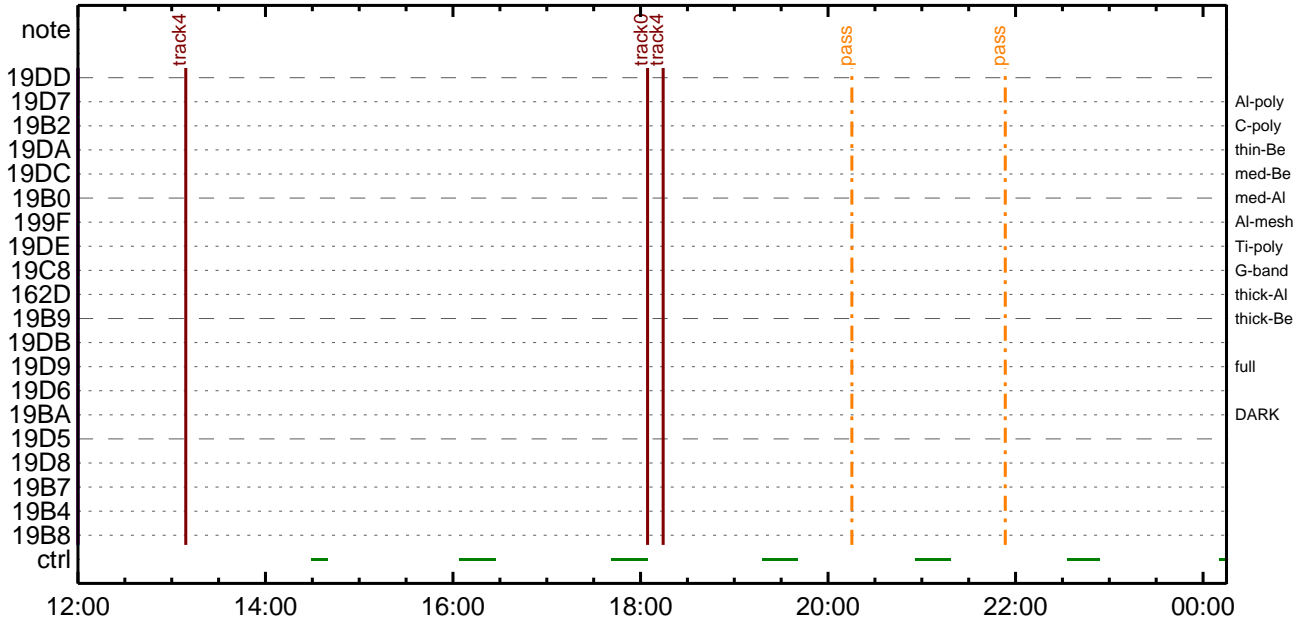
\* \* \* \* \*

<b>FLD Patrol</b>												
Term	Pointing (x, y)								Comment			
11/13 16:40:16 - 11/13 17:54:16	Track ( 812.3, -257.3) @ 11/13 16:40:00								# Track AR11890			
11/13 18:04:16 - 11/14 06:00:16	Track ( 818.7, -256.6) @ 11/13 18:04:00								# Cont,			
11/14 06:10:16 - 11/16 10:16:00	Track ( 867.1, -251.1) @ 11/14 06:10:00								# Cont,			
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

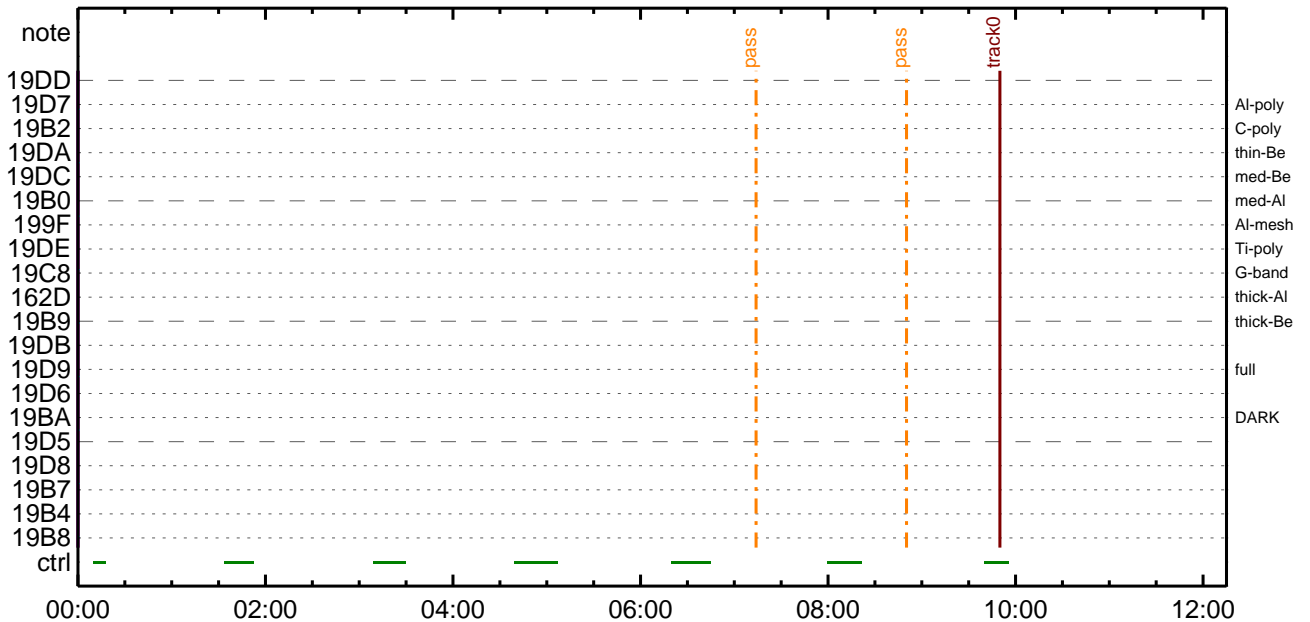
### CMDI #0894 2013/11/12



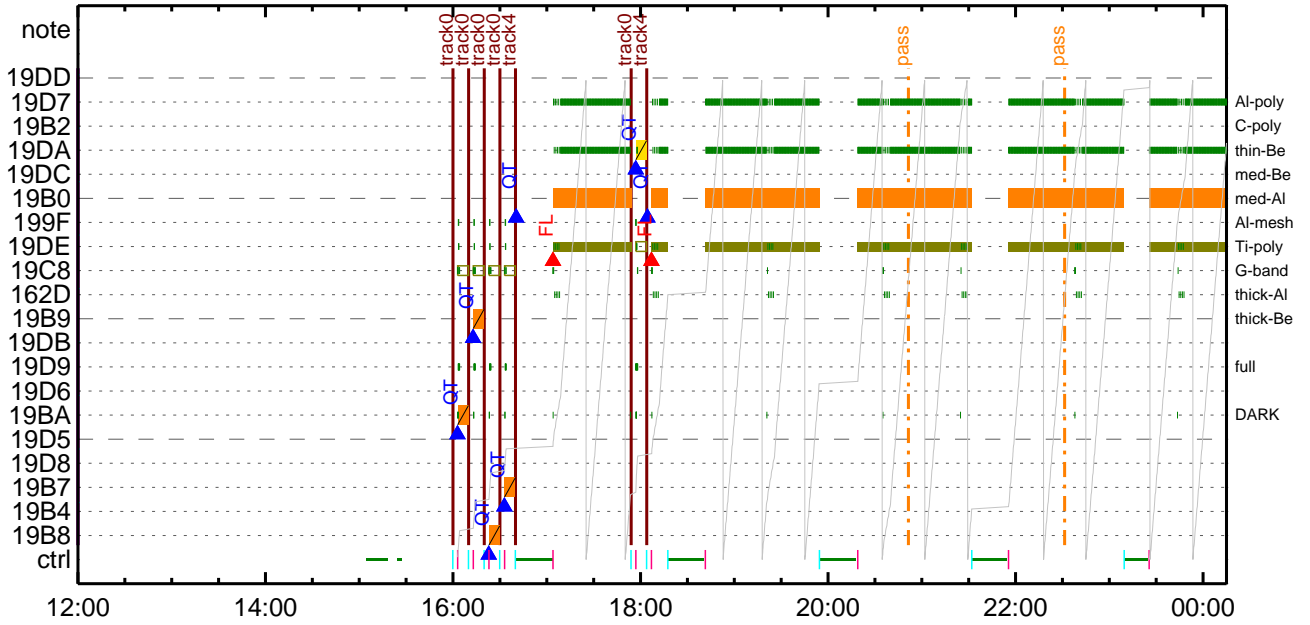
### CMDI #0894 2013/11/12



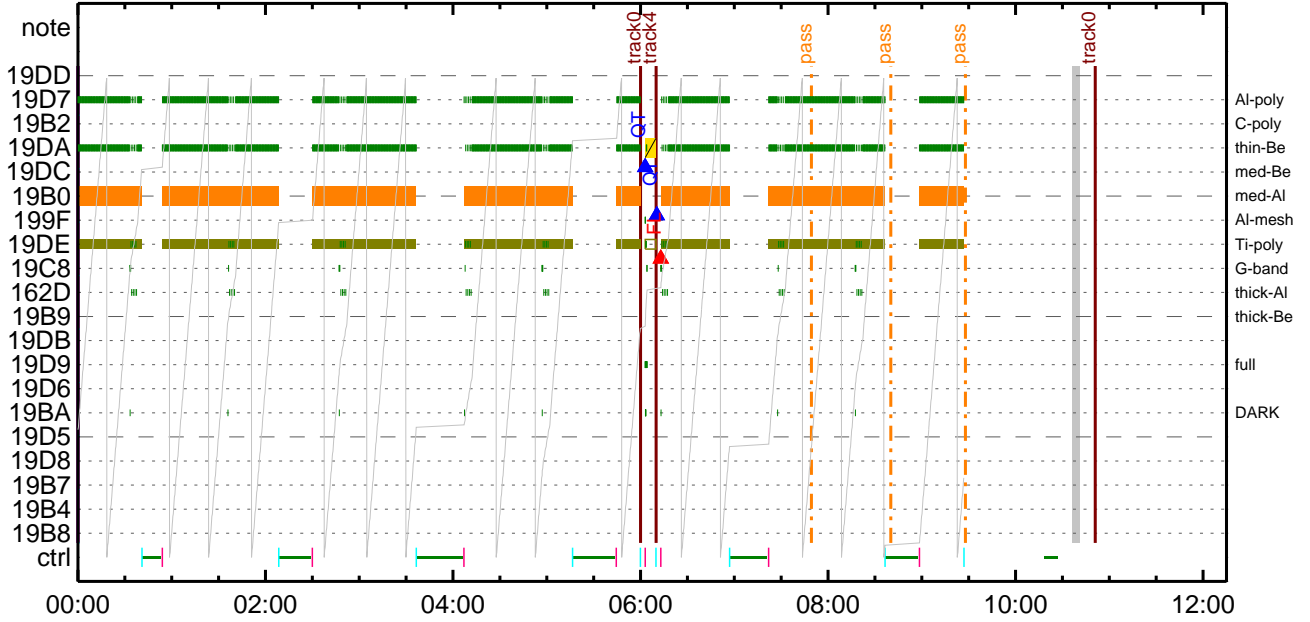
### CMDI #0894 2013/11/13



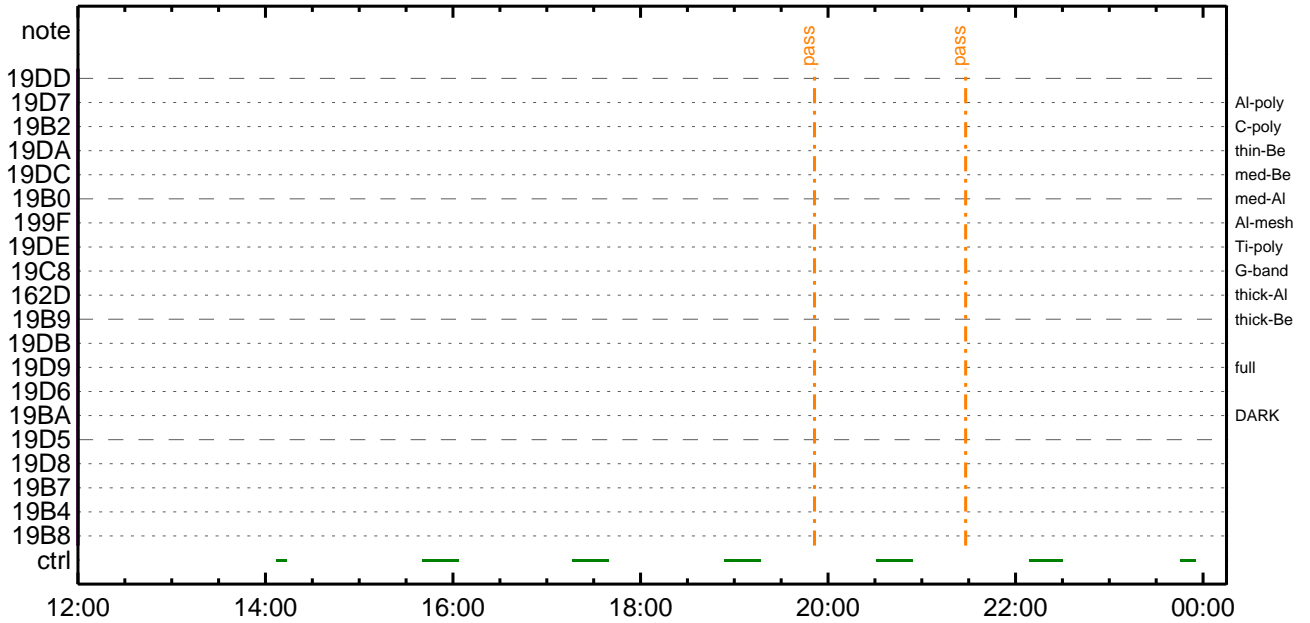
### CMDI #0894 2013/11/13



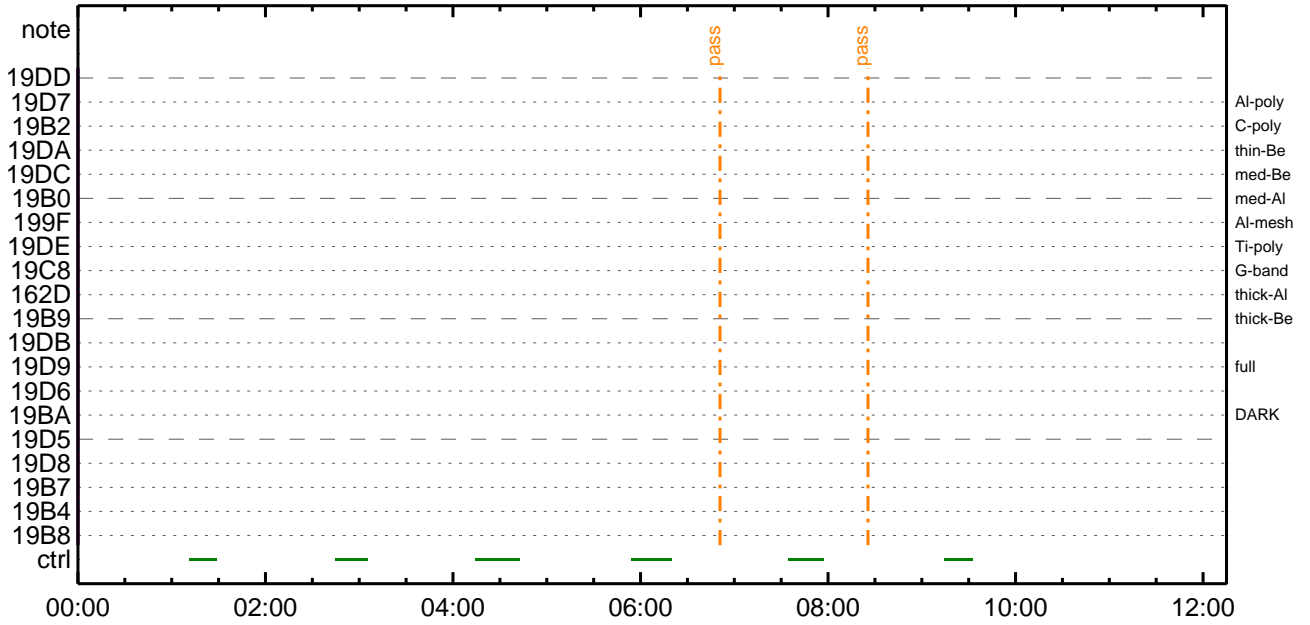
### CMDI #0894 2013/11/14



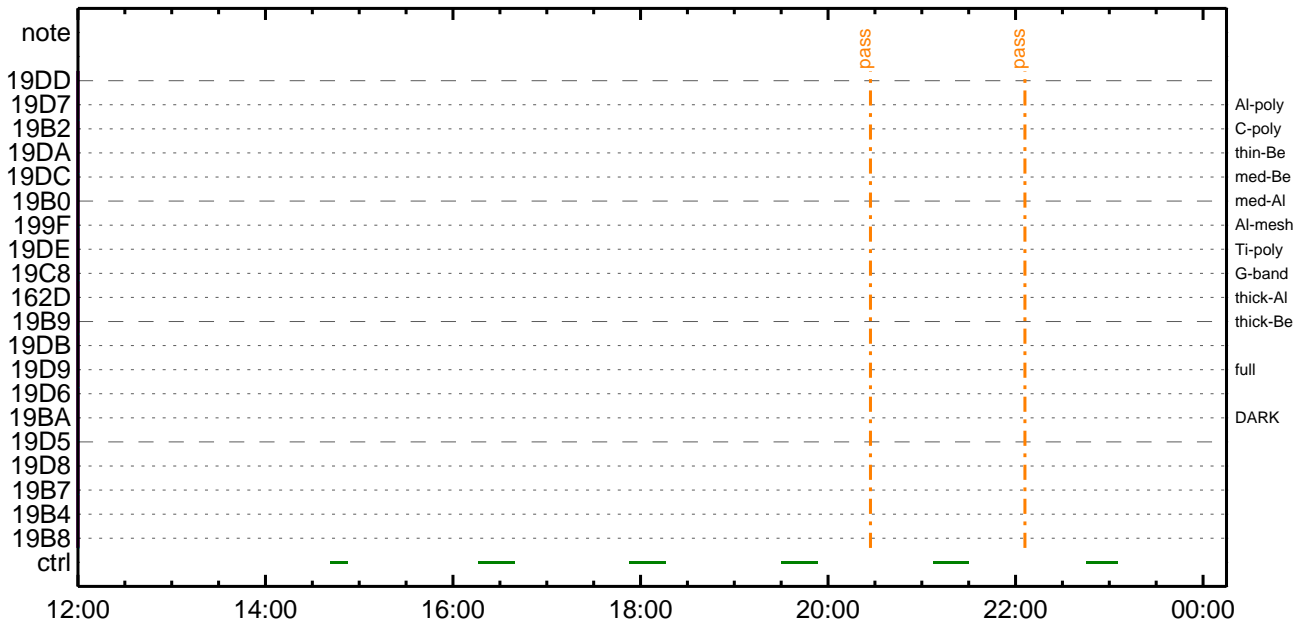
### CMDI #0894 2013/11/14



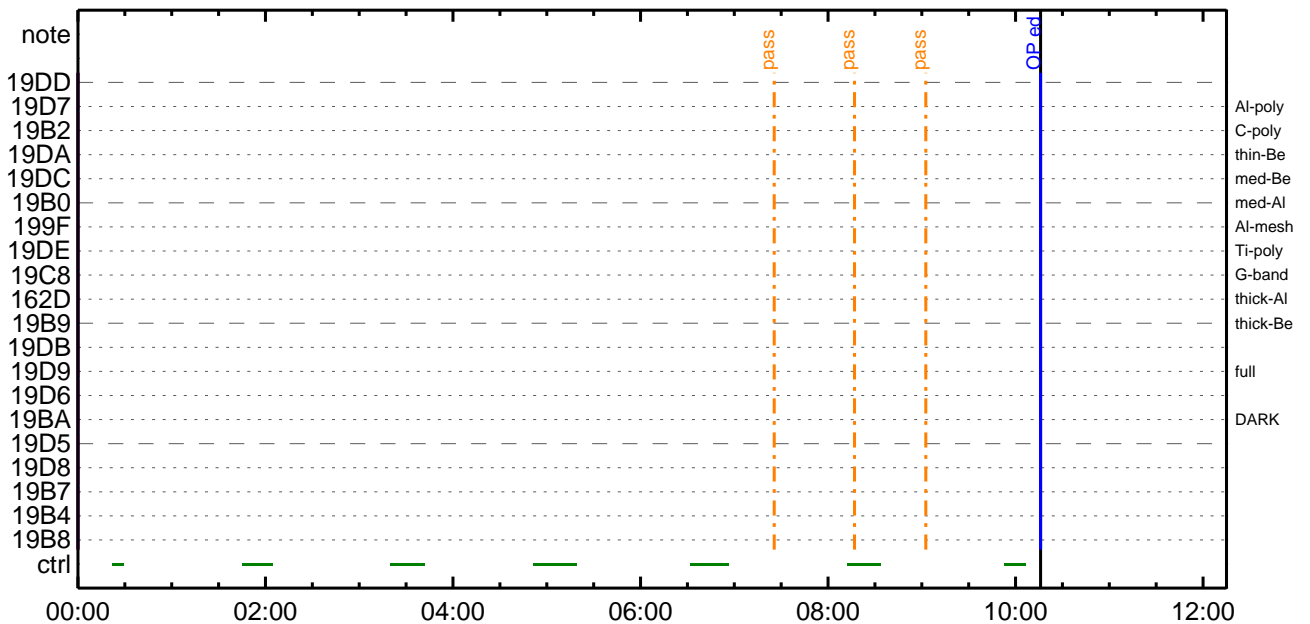
### CMDI #0894 2013/11/15



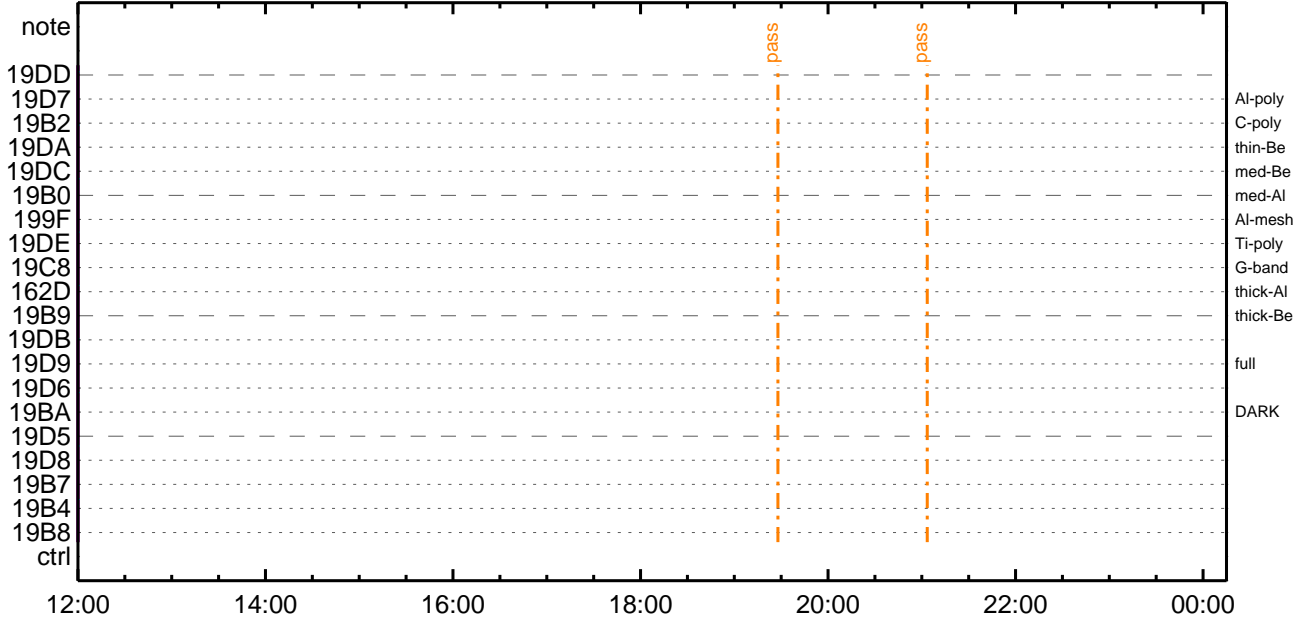
### CMDI #0894 2013/11/15



### CMDI #0894 2013/11/16



CMDI #0894 2013/11/16







0096 . C. SET EDUMP I±°iYNY¹aÇ¹Öa|a³aE;f

0097 . C.

0098 . C. TIY³YFYOËAΔIΔ(UT)

0099 +. TI 2013-11-12 09:54:00.0

0100 DC 01-B3 DHU\_OP\_STOP

0101 . C. φφ[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP

0102 . C.

0103 +. TI 2013-11-12 09:54:01.0

0104 DC 01-B4 DHU\_OP\_COPY

0105 . C. φφ[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP

0106 . C.

0107 +. TI 2013-11-12 09:54:01.0

0108 DC 01-B5 DHU\_OPOG\_COPY

0109 . C. φφ[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP

0110 . C.

0111 +. TI 2013-11-12 09:58:59.5

0112 DC 01-B2 DHU\_OP\_START

0113 . C. φφ[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP

0114 . C.

0115 . C. °E²¼aİÄê%iíÑaİYÁY\$YÁY¹àìÜ

0116 . C. φφ[HK1\_TI\_CMD\_ENA/DIS] EQ ENA

0117 . C. φφ[HK1\_TI\_CMD\_NUM] EQ 4

0118 . C. φφ[HK1\_NEXT\_EXEC\_PIM] EQ DHU

0119 . C. φφ[HK1\_NEXT\_EXEC\_DC] EQ 0xB3

0120 . C.

0121 . C. \*\*\*\*\*

0122 . C. TIÎî°èYÁYOYx

0123 . C. \*\*\*\*\*

0124 . C.

0125 . C. TI\_TBL(0x03AB00-0x03AEFF;§ 1024byte)

0126 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET

0127 BC (03 ab 03 01 02)

0128 . C. φφ[HK1\_DMP\_TOP\_ADRS\_1] EQ 07

0129 . C. φφ[HK1\_DMP\_TOP\_ADRS\_0] EQ 2B

0130 . C. φφ[HK1\_DMP\_BLOCK\_NUM] EQ 3

0131 . C. φφ[HK1\_DMP\_REPEAT\_NUM] EQ 0

0132 . C. φφ[HK1\_DMA\_DMP\_PIM] EQ DHU

0133 +. DC 01-22 DHU\_MODE\_CHNG

0134 BC (07 0b f8)

0135 . C. φφ[HK1\_PKT\_FORM\_NO] EQ 7

0136 . C. φφ[HK1\_PKT\_GEN\_TIME] EQ 0.25 s

0137 . C. φφ[HK1\_S\_TLM\_BIT\_RATE] EQ 32k

0138 . C. φφ[HK1\_X\_TLM\_BIT\_RATE] EQ 4M

0139 . C. φφ[HK1\_DMP\_CHK\_FLG] EQ EXEC

0140 . C.

0141 . C. YÁYOYx½¹î»að³îÇ\$

0142 . C. φφ[HK1\_DMP\_CHK\_FLG] EQ NON

0143 . C.

0144 . C. RAM ID=TI\_TBLaİÈ¹Ç•ê²IOKpað³îÇ\$

0145 . C.

0146 . C. DHUÿâ;¼YÉ;È¼Y½;Yì;¼YÈ;Èpaðîáa¹

0147 +. DC 01-22 DHU\_MODE\_CHNG

0148 BC (02 0a f8)

0149 . C. φφ[HK1\_PKT\_FORM\_NO] EQ 2

0150 . C. φφ[HK1\_PKT\_GEN\_TIME] EQ 0.5S

0151 . C. φφ[HK1\_S\_TLM\_BIT\_RATE] EQ 32K

0152 . C. φφ[HK1\_X\_TLM\_BIT\_RATE] EQ 4M

0153 . C.

0154 . C. \*\*\*\*\*

0155 . C. SOT TI command set

0156 . C. \*\*\*\*\*

0157 . C. Execute, after the success of OP upload.

0158 +. TI 2013-11-12 09:58:16.0

0159 DC 07-F0 MDP\_SOT\_MODE\_STBY

0160 BC (41)

0161 . C. -----

0162 . C. HK1\_TI\_CMD\_NUM = 1 CNTUP [ ]

0163 . C. -----

0164 . C. \*\*\*\*\* SOT END \*\*\*\*\*

0165 . C. Stop EIS observation and temporarily disable EIS mode changes

0166 . C.

0167 . C.

0168 . C. \*\*\*\*\* Start EIS operation (TI set) \*\*\*\*\*

0169 . C. Execute, after the success of OP upload.

0170 . C. Set EIS TI-commands

0171 +. TI 2013-11-12 09:58:30.0

0172 DC 07-FC EIS\_MODE\_MANU

0173 BC (21 02)

0174 +. TI 2013-11-12 09:58:40.0

0175 DC 07-FC EIS\_MODE\_CHG\_DIS

0176 BC (22)

0177 . C. [ ] [HK1\_TI\_CMD\_NUM] EQ 2 COUNTUP

0178 . C. \*\*\*\*\* End EIS operation (TI set) \*\*\*\*\*

0179 . C.

0180 . C.

0181 . C.

0182 . C. \*\*\*\*\* XRT START \*\*\*\*\*

0183 . C. Execute, after the success of OP upload.

0184 +. TI 2013-11-12 09:58:00.0

0185 DC 07-F0 MDP\_XRT\_MODE\_STBY

0186 BC (c3)

0187 . C. [ ] [HK1\_TI\_CMD\_NUM] EQ 1COUNTUP

0188 . C.

0189 . C. \*\*\*\*\* XRT END \*\*\*\*\*

0190 . C.

0191 . C. \*\*\*\*\* MDP `üÄîaİ»ö¼YpÈÄA¹aèDCBC•x²è \*\*\*\*\*

0192 . C. (¼a°iYÁYOYËYpYÈYÁYçYèaÈ¼Yaa¼A»Üa¹aè)

0193 . S. DC-BC dcbc-402:DCBC

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```





(a) Spacecraft Operation Procedure (real-commands)

```
main-033 2013-11-12 12:19:38 162 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÄã•µ°Æ»Í×ÁÇãÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÈ;ÈÈÈ¼°ÇÔã•ã¿¼í¹çãÍ;çÀ®, ùã¹ãèãÈãÇÁ+¿®ã•ãÈããã³ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-266:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Upload DPL table >
0036 C.
0037 C. YçYÁY×Yí;¼YÉãîÁ°ãÈSTS_CHKãðOFFãÈã¹ãè
0038 C.
0039 . S. RAM ram-271:MDP_DPL
0040 ( )
0041 C.
0042 . C. < Dump RAMID=MDP_DPL >
0043 +. DC 07-F0 MDP_DUMP_FGTBL
0044 BC (82 07 00 38 b8 00 40)
0045 C. -----
0046 C. MDP_DPL verify = OK [ ]
0047 C. -----
0048 C.
0049 C. STS_CHKãðONãÈã¹ãè
0050 C.
0051 . C. < Update MDP DSC PAR1 >
0052 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0053 BC (4c)
0054 C. MDP_CMD_CODE = F04C0700[ ]
0055 C. MDP_CMD_CNT (count-up 1) [ ]
0056 C. -----
0057 C.
0058 C.
0059 C. *****
0060 C. SOT TI command set
0061 C. *****
0062 C. Execute, after the success of TBL upload.
0063 +. TI 2013-11-12 09:58:18.0
0064 DC 07-F0 MDP_SOT_MODE_OBSV
0065 BC (40)
0066 . C. -----
0067 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0068 C. -----
0069 C.
0070 C.
0071 C. ***** XRT START *****
0072 C.
0073 +. DC 07-F0 MDP_XRT_CTRL_MANU
0074 BC (c1)
0075 +. DC 07-F0 MDP_XRT_MODE_STBY
0076 BC (c3)
0077 . C. ----- Success Verify ? OK / NG____
0078 C.
0079 C. XRT Obs. Table Upload
0080 . S. RAM ram-291:MDP_OBS_X
0081 ( )
0082 C.
0083 +. DC 07-F0 MDP_DUMP_XRTTBL
0084 BC (84 07 00 00 00 3a d4)
0085 . C. ----- Comparison Check ? OK / ERR ____
0086 C.
0087 C.
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 01 b1 b1 04 04)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 02 b1 b1 08 08)
0092 +. DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 03 b1 b1 08 08)
0094 +. DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 04 b1 b1 06 06)
```

```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 05 85 83 06 06)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 06 85 83 06 06)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 07 85 83 08 08)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 08 c0 c0 10 10)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 09 80 80 20 20)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0a 40 c0 10 10)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 0b 40 40 10 10)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 0c c0 40 10 10)
0112 + DC 07-F0 MDP_XRT_ROI_SET
0113 BC (cd 0d 80 80 20 08)
0114 + DC 07-F0 MDP_XRT_ROI_SET
0115 BC (cd 0e 80 80 08 20)
0116 + DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 0f 80 80 06 06)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 10 80 80 08 08)
0120 + DC 07-F0 MDP_XRT_FLD_ENA
0121 BC (d8)
0122 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0123 BC (c8)
0124 + DC 07-F0 MDP_XRT_AEC_RESET
0125 BC (d0)
0126 + DC 07-F0 MDP_XRT_ARS_DIS
0127 BC (d5)
0128 + DC 07-F0 MDP_XRT_FLD_RESET
0129 BC (da)
0130 + DC 07-F0 MDP_XRT_QT_PROG_SET
0131 BC (c4 02)
0132 + DC 07-F0 MDP_XRT_FL_PROG_SET
0133 BC (c5 0c)
0134 . C. ----- Success Verify ? OK / NG ____
0135 C.
0136 C.
0137 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0138 C.
0139 +. DC 07-F0 MDP_XRT_MODE_OBSV
0140 BC (c2)
0141 +. TI 2013-11-12 09:58:02.0
0142 DC 07-F0 MDP_XRT_MODE_OBSV
0143 BC (c2)
0144 . C. ----- Success Verify ? OK / NG ____
0145 C.
0146 C. ***** XRT END *****
0147 C.
0148 . C. ***** MDP `ûÃîñî»ô%ÿñÈÂð¹ñèDCBC•x²è *****
0149 C. (%ã°îÿÓÿÃÿÈÿPÿÈÿÿãÿçÿèñÈ%¼ññ¼Ã»Ûñ¹ñè)
0150 . S. DC-BC dcbc-402:DCBC
0151 (MDP_known_event)
0152 C.
0153 C.
0154 . C. ***** ÿDÿ¹.Ï Daily±;îÑñÈ´Øñ¹ñèDCBC•x²è *****
0155 . S. DC-BC dcbc-153:DCBC
0156 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0157 C.
0158 C.
0159 . C. ;ãLOSÿÃÿSÿÿÃÿ-¼Ã»Û;ã
0160 C.
0161 . C. ***** LOS *****
0162 C.
```

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\*\*\* OP Sequence for XRT \*\*\*

2013/11/12	10:09:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	f6	25	54	b4
2013/11/12	13:09:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	04	00	00	00	00
2013/11/12	18:04:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2013/11/12	18:14:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	04	00	00	00	00
2013/11/13	09:50:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	54	72	01	58
2013/11/13	09:50:00.5	XRT_TCIB_XRT_S_HTR_A_DIS_437_OG [0x1b5]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2013/11/13	15:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2013/11/13	16:00:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	2e	f9	2e	f9
2013/11/13	16:02:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2013/11/13	16:02:52.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	06		
2013/11/13	16:02:54.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0		d9			
2013/11/13	16:02:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2013/11/13	16:02:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2013/11/13	16:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2013/11/13	16:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2013/11/13	16:10:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	2e	f9	d1	07
2013/11/13	16:12:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2013/11/13	16:12:52.0	XRT_QT_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	0a		
2013/11/13	16:12:54.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0		d9			
2013/11/13	16:12:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2013/11/13	16:12:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2013/11/13	16:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2013/11/13	16:19:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2013/11/13	16:20:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	d1	07	d1	07
2013/11/13	16:22:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2013/11/13	16:22:52.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	01		
2013/11/13	16:22:54.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0		d9			
2013/11/13	16:22:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2013/11/13	16:22:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2013/11/13	16:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2013/11/13	16:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2013/11/13	16:30:00.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	d1	07	2e	f9
2013/11/13	16:32:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2013/11/13	16:32:52.0	XRT_QT_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_QT_PROG_SET	2	07-F0		c4	03		
2013/11/13	16:32:54.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0		d9			
2013/11/13	16:32:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2013/11/13	16:32:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2013/11/13	16:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2013/11/13	16:39:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0		c1			
2013/11/13	16:39:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/11/13	16:40:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	04	00	00	00	00
2013/11/13	16:40:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0		d8			
2013/11/13	16:40:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2013/11/13	16:40:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0		d0			
2013/11/13	16:40:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0		d5			
2013/11/13	16:40:24.0	XRT_FLD_RESET_415_OG [0x19f]							

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2013/11/13	16:40:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/13	17:03:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
2013/11/13	17:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2013/11/13	17:53:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/13	17:53:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/13	17:54:00.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2013/11/13	17:54:16.0	XRT_FLD_DIS_434_OG [0x1b2]	AOCU_NM	5	02-76	00	00 00 00 00
2013/11/13	17:56:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/11/13	17:56:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/11/13	17:56:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/11/13	17:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2013/11/13	18:03:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/13	18:03:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/13	18:04:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2013/11/13	18:04:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	04	00 00 00 00
2013/11/13	18:04:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/11/13	18:04:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/11/13	18:04:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/11/13	18:04:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/11/13	18:04:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/13	18:06:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f
2013/11/13	18:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2013/11/13	18:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/13	18:17:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/13	18:17:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/13	18:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/11/13	18:40:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/13	18:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2013/11/13	19:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/13	19:54:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/13	19:54:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/13	19:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/11/13	20:18:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/13	20:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2013/11/13	21:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/13	21:32:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/13	21:32:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/13	21:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/11/13	21:54:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/13	21:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2013/11/13	23:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/13	23:09:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/13	23:09:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/13	23:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/11/13	23:24:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/13	23:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2013/11/14	00:41:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/14	00:41:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			MDP_XRT_FLD_RESET	1	07-F0	da	



2013/11/14	00:41:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/11/14	00:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/11/14	00:53:00.0	XRT_Custom_430_OG [0x1ae]							
2013/11/14	00:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/14	02:08:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/14	02:08:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2013/11/14	02:08:34.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/11/14	02:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/11/14	02:29:00.0	XRT_Custom_430_OG [0x1ae]							
2013/11/14	02:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/14	03:36:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/14	03:36:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2013/11/14	03:36:34.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/11/14	03:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/11/14	04:06:00.0	XRT_Custom_430_OG [0x1ae]							
2013/11/14	04:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/14	05:16:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/14	05:16:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2013/11/14	05:16:34.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/11/14	05:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/11/14	05:43:30.0	XRT_Custom_430_OG [0x1ae]							
2013/11/14	05:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/14	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/14	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/11/14	06:00:00.0	AOCS_Ore-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00 00				
2013/11/14	06:00:16.0	XRT_FLD_DIS_434_OG [0x1b2] MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/11/14	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/11/14	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/11/14	06:02:58.0	XRT_QT_PROG_SET_446_OG [0x1be] MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2013/11/14	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/14	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/14	06:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2013/11/14	06:10:00.0	AOCS_Ore-point_Start_2_OG [0x098] AOCU_NM	5	02-76	04 00 00 00 00				
2013/11/14	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/11/14	06:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/11/14	06:10:20.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/11/14	06:10:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/11/14	06:10:24.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2013/11/14	06:10:26.0	XRT_QT_PROG_SET_435_OG [0x1b3] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2013/11/14	06:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5] MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d				
2013/11/14	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/14	06:57:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/14	06:57:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2013/11/14	06:57:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/11/14	07:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/11/14	07:21:00.0	XRT_Custom_430_OG [0x1ae]							
2013/11/14	07:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/14	08:36:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/14	08:36:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				

2013/11/14	08:36:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/11/14	08:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/11/14	08:57:30.0	XRT_Custom_430_OG [0x1ae]			
2013/11/14	08:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/11/14	09:27:00.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/11/14	10:51:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00