

XRT Timeline to be uploaded on 2013/11/19

Period: 2013/11/19 10:28:00 - 2013/11/23 09:39:00

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Normal mode

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XOB #19B1: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-bar

Term	Pointing (x, y)	Comment
11/19 10:41:00 - 11/19 17:59:54	Track (159.1, 36.6) @ 11/19 10:38:00	# OP start + 10min, AR 11899 observations.
11/19 18:13:00 - 11/20 06:17:54	Track (228.7, 37.8) @ 11/19 18:10:00	# AR cont.

PROG= 05 Inf.-time(s)													
Subr=	1	1-time(s)	2.0sec										
Seqn=	8	2-time(s)	2.0sec										
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr=	2	2-time(s)	2.0sec										
Seqn=	24	1-time(s)	2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn=	98	4-time(s)	2.0sec										
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn=	21	30-time(s)	90.0sec										
	thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
	Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #19DA: Synoptic Q95 2x2 - Al/mesh(5/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/362/1443) + Thin

Term	Pointing (x, y)	Comment
11/19 18:03:00 - 11/19 18:09:54	Fixed (0.0, 0.0)	synoptic
11/20 06:21:00 - 11/20 06:27:54	Fixed (0.0, 0.0)	synoptic, shifted 18.0 min
11/21 05:54:00 - 11/21 06:00:54	Fixed (0.0, 0.0)	synoptic, shifted -9.0 min

PROG= 17 1-time(s)													
Subr=	1	1-time(s)	12.0sec										
Seqn=	84	1-time(s)	4.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn=	5	1-time(s)	2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn=	86	1-time(s)	4.0sec										
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn=	95	1-time(s)	2.0sec										
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn=	6	1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #19B2: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, shorter thin-Be, thick Al and Al/Poly context, With G-bar

Term	Pointing (x, y)	Comment
11/20 06:31:00 - 11/20 09:01:30	Track (339.4, 40.2) @ 11/20 06:28:00	# AR cont.
11/20 23:33:00 - 11/21 05:50:54	Track (484.3, 44.1) @ 11/20 23:30:00	# AR cont.
11/21 06:04:00 - 11/21 09:39:30	Track (536.5, 45.7) @ 11/21 06:01:00	# AR cont.

PROG= 19 Inf.-time(s)													
Subr=	1	1-time(s)	2.0sec										
Seqn=	8	2-time(s)	2.0sec										
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr=	2	2-time(s)	2.0sec										
Seqn=	24	1-time(s)	2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn=	98	4-time(s)	2.0sec										

Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 21 25-time(s) 120.0sec												
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
thin-Be/Open	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Al-poly/Open	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19DC: Synoptic 9 Filter w/ Triplet Exposures - 2x2 Q98 - Gband(8ms)

Term	Pointing (x, y)	Comment
11/20 15:13:30 - 11/20 16:03:30	Fixed (-15.0, 25.0)	# 11/20. XRT synoptic shifted ~3 hours to here.

PROG= 16 1-time(s)												
Subr= 1 1-time(s) 12.0sec												
Seqn= 84 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 86 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 95 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 2-time(s) 12.0sec												
Seqn= 56 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 22 1-time(s) 2.0sec												
C-poly/Open	C-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
C-poly/Open	C-poly/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 43 1-time(s) 2.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 38 1-time(s) 4.0sec												
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 46 1-time(s) 4.0sec												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #19DE: Flare obs. dynamics - thin-Be high cadence long/short pairs + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loc

Term	Pointing (x, y)	Comment
11/19 10:41:00 - 11/19 17:59:54	Track (159.1, 36.6) @ 11/19 10:38:00	# OP start + 10min, AR 11899 observations.
11/19 18:13:00 - 11/20 06:17:54	Track (228.7, 37.8) @ 11/19 18:10:00	# AR cont.
11/20 06:31:00 - 11/20 09:01:30	Track (339.4, 40.2) @ 11/20 06:28:00	# AR cont.
11/20 23:33:00 - 11/21 05:50:54	Track (484.3, 44.1) @ 11/20 23:30:00	# AR cont.
11/21 06:04:00 - 11/21 09:39:30	Track (536.5, 45.7) @ 11/21 06:01:00	# AR cont.

PROG= 13 15-time(s)												
Subr= 1 45-time(s) 2.0sec												
Seqn= 44 1-time(s) 8.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 15 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs 1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs 2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

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Active Region Search

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NOT USED

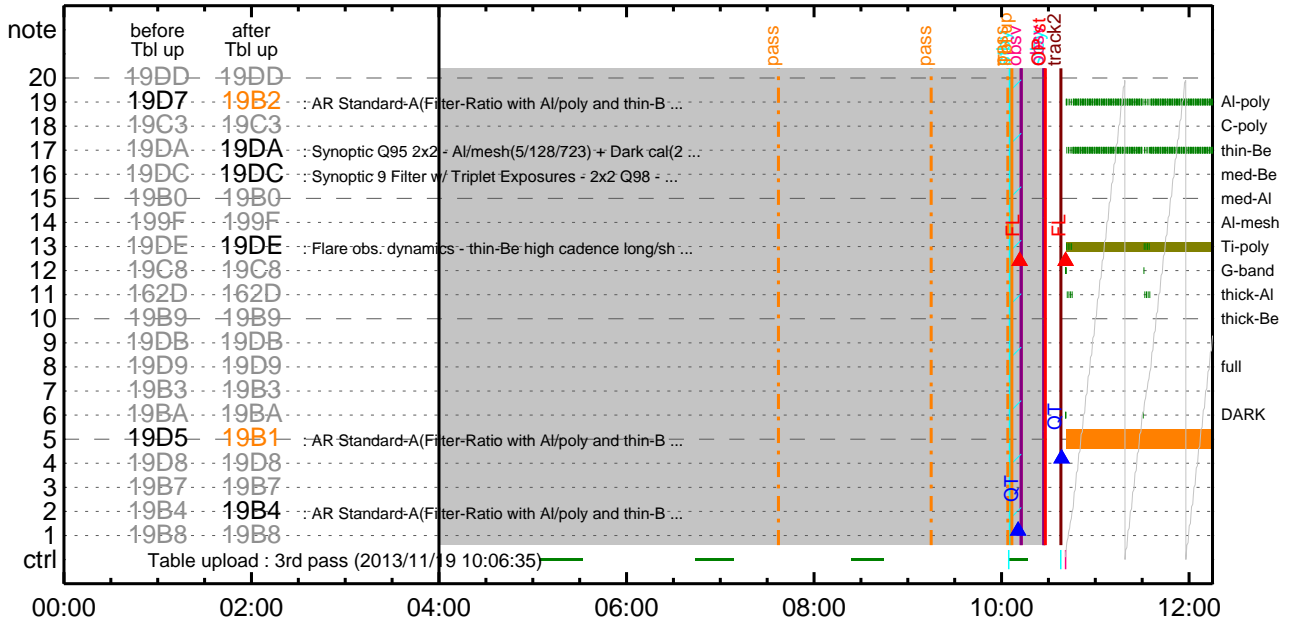
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Flare Detection

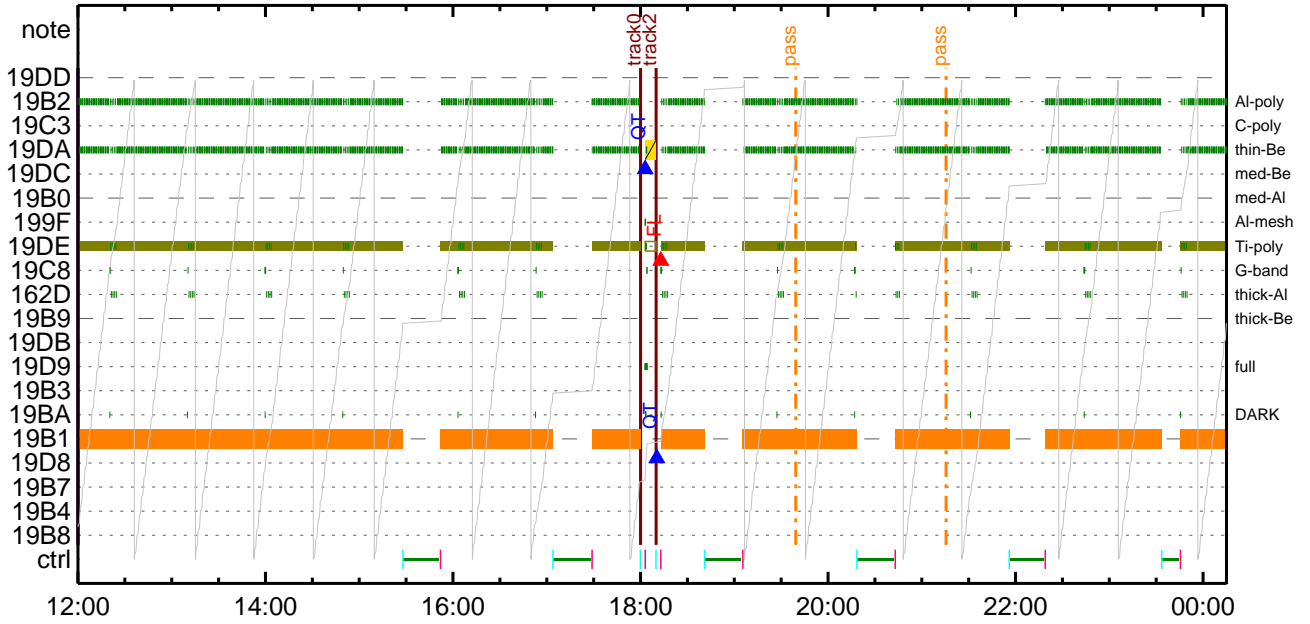
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FLD Patrol											
Term		Pointing (x, y)				Comment					
11/19 18:10:16 - 11/20 06:18:16	Track (228.7, 37.8)	① 11/19 18:10:00				# AR cont.					
11/20 06:28:16 - 11/20 15:10:46	Track (339.4, 40.2)	② 11/20 06:28:00				# AR cont.					
11/20 23:30:16 - 11/21 05:51:16	Track (484.3, 44.1)	③ 11/20 23:30:00				# AR cont.					
11/21 06:01:16 - 11/23 09:39:00	Track (536.5, 45.7)	④ 11/21 06:01:00				# AR cont.					
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs 8x8	Q=50				30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

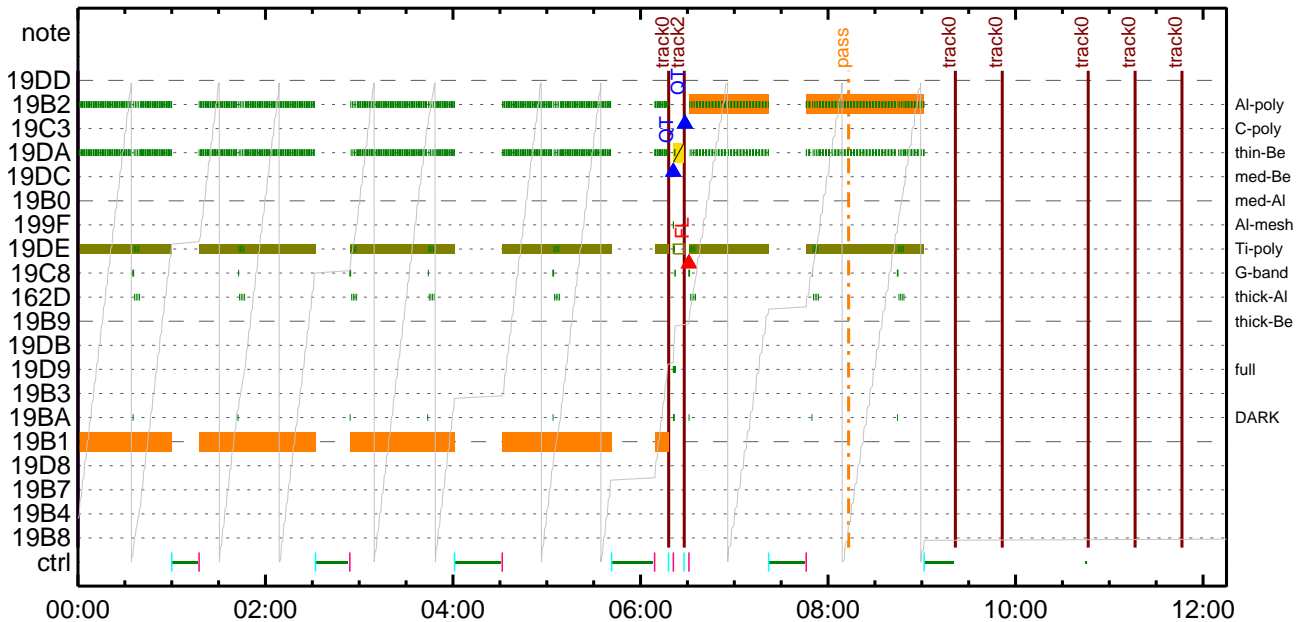
CMDI #0914 2013/11/19



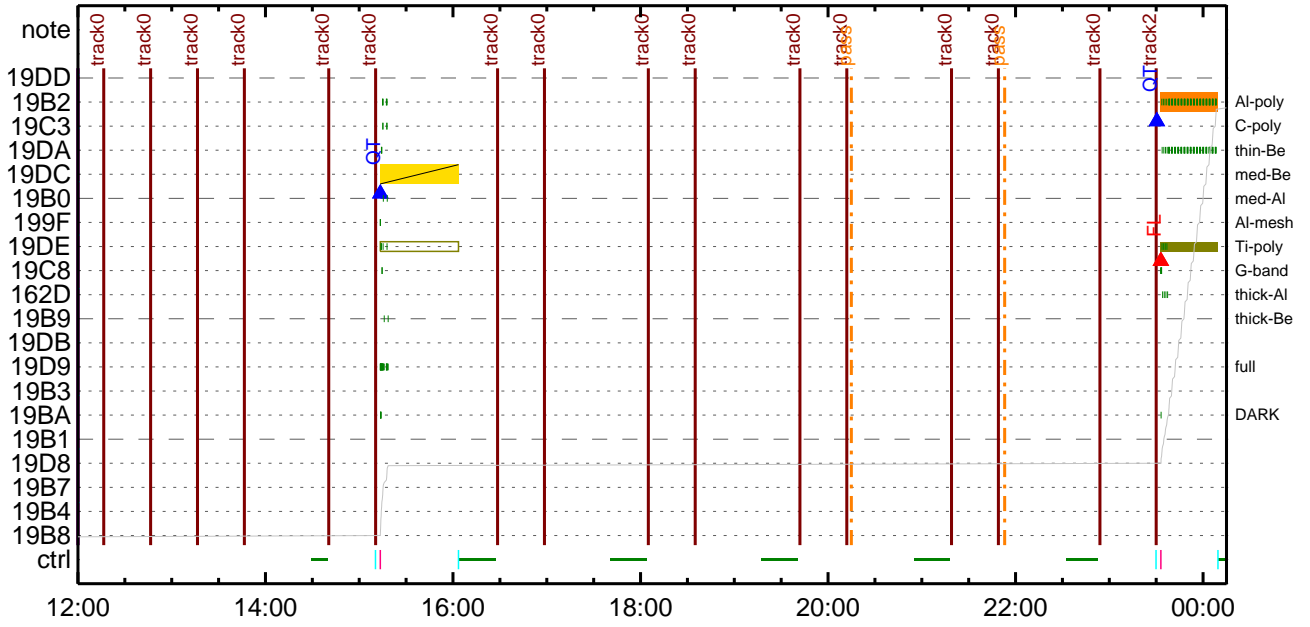
CMDI #0914 2013/11/19



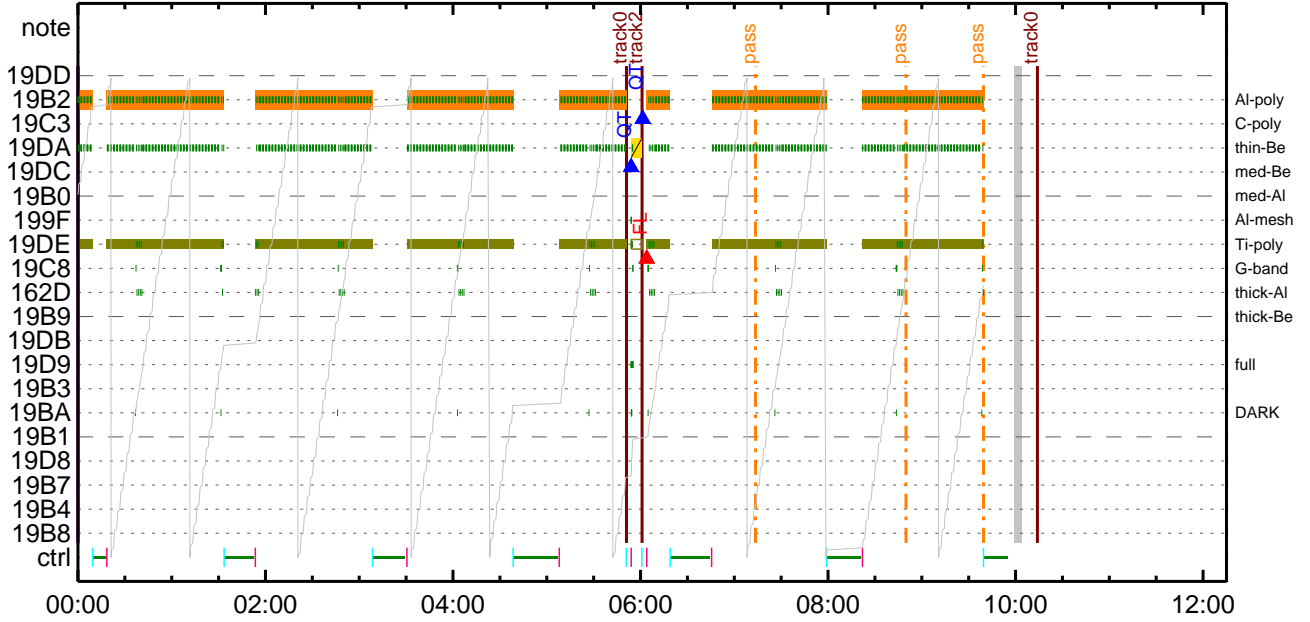
CMDI #0914 2013/11/20



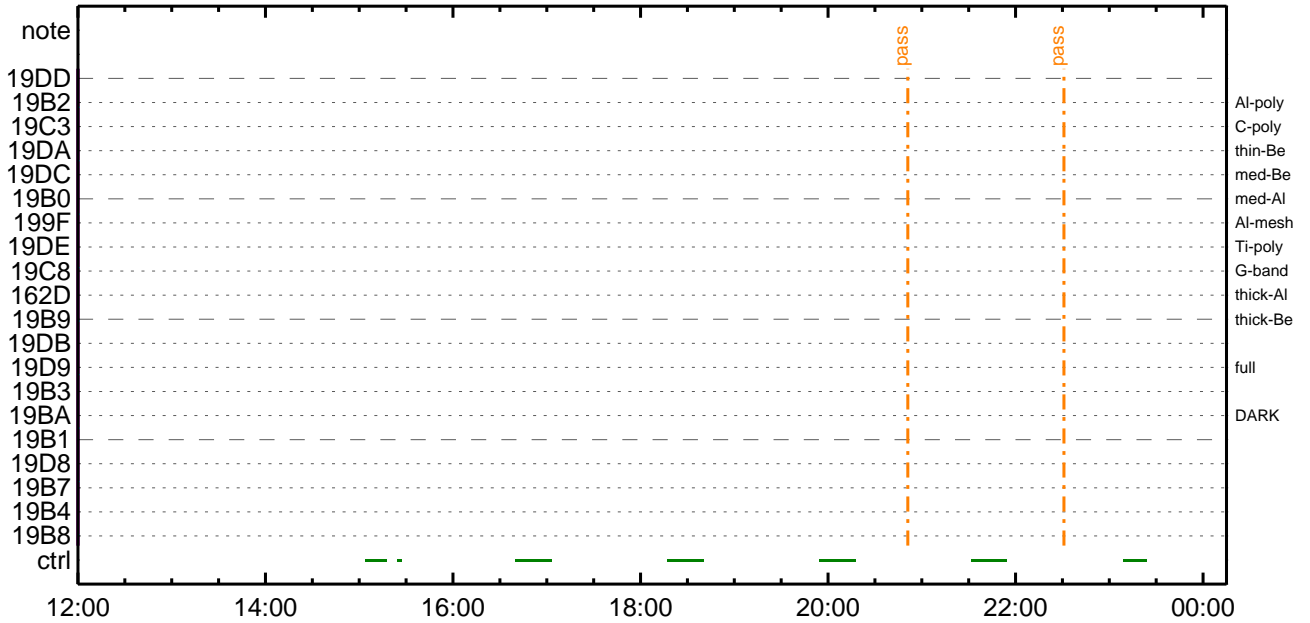
CMDI #0914 2013/11/20



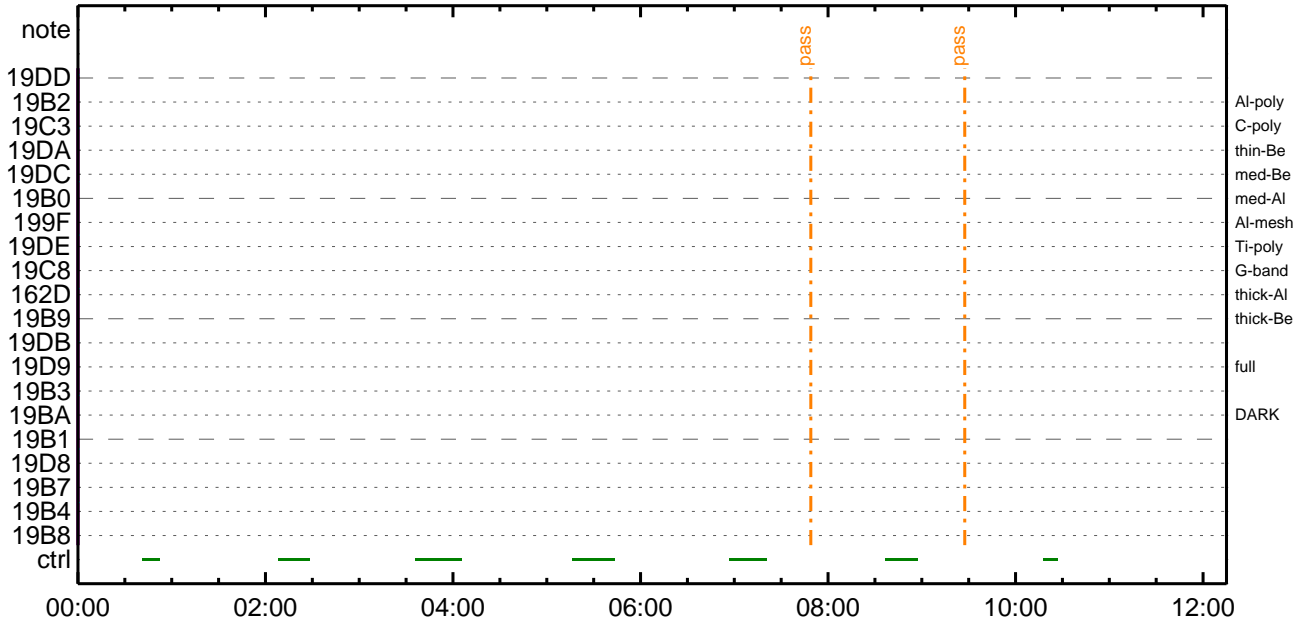
CMDI #0914 2013/11/21



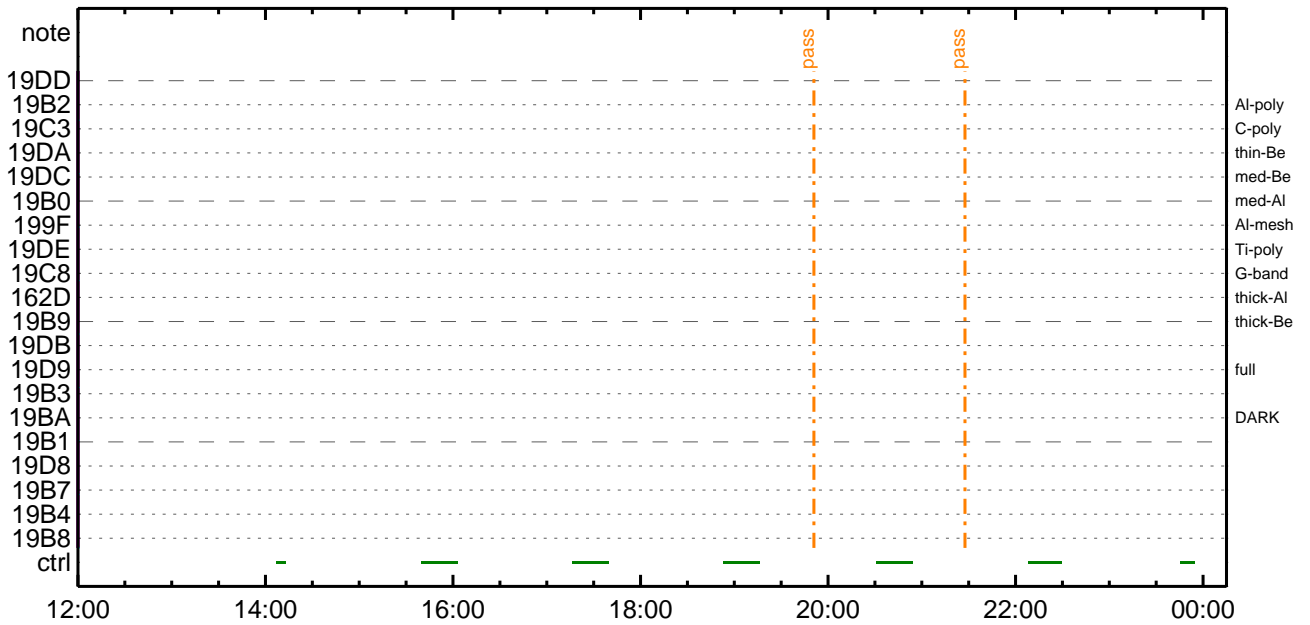
CMDI #0914 2013/11/21



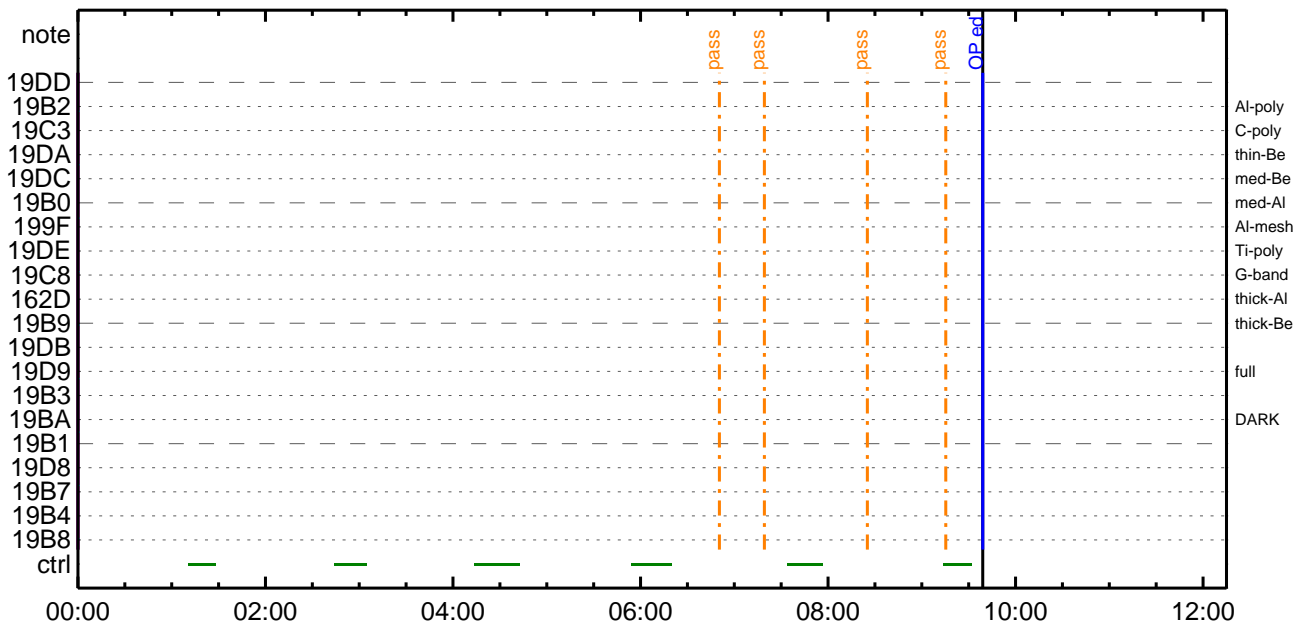
CMDI #0914 2013/11/22



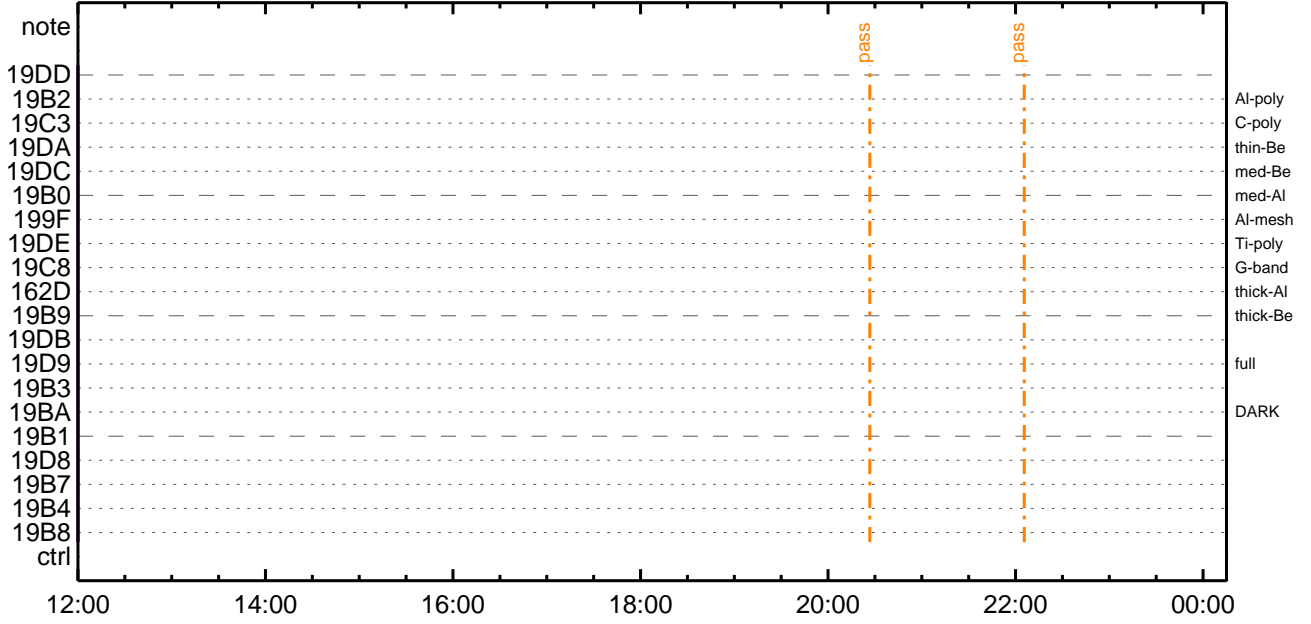
CMDI #0914 2013/11/22



CMDI #0914 2013/11/23



CMDI #0914 2013/11/23




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;YÉ;YAYÓYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;YÉ;ã
0103 S. OP op-047:OP
0104 ( )
0105 S. OG og-047:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAYÓYx;ã
0109 C. NMOG(0x200000-0x207FFF;s 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C.      çç[HK1_DMP_TOP_ADRS_1]      EQ      40
0113 C.      çç[HK1_DMP_TOP_ADRS_0]      EQ      0
0114 C.      çç[HK1_DMP_BLOCK_NUM]       EQ     127
0115 C.      çç[HK1_DMP_REPEAT_NUM]      EQ      0
0116 C.      çç[HK1_DMA_DMP_PIM]         EQ     DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C.      çç[HK1_PKT_FORM_NO]          EQ      7
0120 C.      çç[HK1_PKT_GEN_TIME]         EQ     0.25 s
0121 C.      çç[HK1_S_TLM_BIT_RATE]      EQ     32k
0122 C.      çç[HK1_X_TLM_BIT_RATE]      EQ      4M
0123 C.      çç[HK1_DMP_CHK_FLG]         EQ     EXEC
0124 C. YAYÓYx½ªî»ð³îÇ§
0125 C.      çç[HK1_DMP_CHK_FLG]         EQ     NON
0126 C. RAM ID=NMOG²î½Ê¹ç•è²îOK²³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;s 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C.      çç[HK1_DMP_TOP_ADRS_1]      EQ     41
0132 C.      çç[HK1_DMP_TOP_ADRS_0]      EQ      0
0133 C.      çç[HK1_DMP_BLOCK_NUM]       EQ     127
0134 C.      çç[HK1_DMP_REPEAT_NUM]      EQ      0
0135 C.      çç[HK1_DMA_DMP_PIM]         EQ     DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C.      çç[HK1_PKT_FORM_NO]          EQ      7
0139 C.      çç[HK1_PKT_GEN_TIME]         EQ     0.25 s
0140 C.      çç[HK1_S_TLM_BIT_RATE]      EQ     32k
0141 C.      çç[HK1_X_TLM_BIT_RATE]      EQ      4M
0142 C.      çç[HK1_DMP_CHK_FLG]         EQ     EXEC
0143 C. YAYÓYx½ªî»ð³îÇ§
0144 C.      çç[HK1_DMP_CHK_FLG]         EQ     NON
0145 C. RAM ID=NMOG²î½Ê¹ç•è²îOK²³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;s 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C.      çç[HK1_DMP_TOP_ADRS_1]      EQ     42
0151 C.      çç[HK1_DMP_TOP_ADRS_0]      EQ      0
0152 C.      çç[HK1_DMP_BLOCK_NUM]       EQ     65
0153 C.      çç[HK1_DMP_REPEAT_NUM]      EQ      0
0154 C.      çç[HK1_DMA_DMP_PIM]         EQ     DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C.      çç[HK1_PKT_FORM_NO]          EQ      7
0158 C.      çç[HK1_PKT_GEN_TIME]         EQ     0.25 s
0159 C.      çç[HK1_S_TLM_BIT_RATE]      EQ     32k
0160 C.      çç[HK1_X_TLM_BIT_RATE]      EQ      4M
0161 C.      çç[HK1_DMP_CHK_FLG]         EQ     EXEC
0162 C. YAYÓYx½ªî»ð³îÇ§
0163 C.      çç[HK1_DMP_CHK_FLG]         EQ     NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½Ê¹ç•è²îOK²³îÇ§
0165 C.
0166 C. ***** °Ê²¼²î½Ê¹ç•è²îOK²³îÇ§ *****
0167 C. DHUªâ;½YÉ;Ê½Y½;Yi;½YÉ;Ë²ðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C.      çç[HK1_PKT_FORM_NO]          EQ      2
0171 C.      çç[HK1_PKT_GEN_TIME]         EQ     0.5S
0172 C.      çç[HK1_S_TLM_BIT_RATE]      EQ     32K
0173 C.      çç[HK1_X_TLM_BIT_RATE]      EQ      4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ; § OPOG UPLOAD²-Ã÷çîNG²î½Ê¹ç•è²îOK²³îÇ§
0180 C.      çç[HK1_PKT_GEN_TIME]         EQ     0.5S
0181 C.
0182 C. TIY³YbYóYÉ²ðîã¹çç(UT)
0183 +. TI 2013-11-19 10:23:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C.      çç[HK1_TI_CMD_NUM]           EQ     1COUNTUP
0186 C.
0187 +. TI 2013-11-19 10:23:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C.      çç[HK1_TI_CMD_NUM]           EQ     1COUNTUP
0190 C.
0191 +. TI 2013-11-19 10:23:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C.      çç[HK1_TI_CMD_NUM]           EQ     1COUNTUP

```

```

0194 C.
0195 +. TI 2013-11-19 10:27:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          ¢¢[HK1_TI_CMD_NUM]                    EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄê%îíñαîî¥Ä¥§¥Ä¥-¹àîü
0200 C.          ¢¢[HK1_TI_CMD_ENA/DIS]                EQ      ENA
0201 C.          ¢¢[HK1_TI_CMD_NUM]                    EQ      4
0202 C.          ¢¢[HK1_NEXT_EXEC_PIM]                 EQ      DHU
0203 C.          ¢¢[HK1_NEXT_EXEC_DC]                 EQ      0xB3
0204 C.
0205 . C. *****
0206 C. TIîî°è¥Ä¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          ¢¢[HK1_DMP_TOP_ADRS_1]                EQ      07
0213 C.          ¢¢[HK1_DMP_TOP_ADRS_0]                EQ      2B
0214 C.          ¢¢[HK1_DMP_BLOCK_NUM]                 EQ      3
0215 C.          ¢¢[HK1_DMP_REPEAT_NUM]               EQ      0
0216 C.          ¢¢[HK1_DMA_DMP_PIM]                  EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          ¢¢[HK1_PKT_FORM_NO]                  EQ      7
0220 C.          ¢¢[HK1_PKT_GEN_TIME]                  EQ      0.25 s
0221 C.          ¢¢[HK1_S_TLM_BIT_RATE]                EQ      32k
0222 C.          ¢¢[HK1_X_TLM_BIT_RATE]                EQ      4M
0223 C.          ¢¢[HK1_DMP_CHK_FLG]                  EQ      EXEC
0224 C.
0225 . C. ¥Ä¥Ö¥×½ªî»αð³îÇ§
0226 C.          ¢¢[HK1_DMP_CHK_FLG]                  EQ      NON
0227 C.
0228 . C. RAM ID=TI_TBLαîî¼Ê¹Ç•è²îOKαð³îÇ§
0229 C.
0230 . C. DHU¥â;¼¥Ê;Ê¼¥½. ¥î;¼¥Ê;Êαðîáα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          ¢¢[HK1_PKT_FORM_NO]                  EQ      2
0234 C.          ¢¢[HK1_PKT_GEN_TIME]                  EQ      0.5S
0235 C.          ¢¢[HK1_S_TLM_BIT_RATE]                EQ      32K
0236 C.          ¢¢[HK1_X_TLM_BIT_RATE]                EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2013-11-19 10:27:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 . C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 . C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2013-11-19 10:27:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2013-11-19 10:27:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 . C.          [ ] [HK1_TI_CMD_NUM]                EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2013-11-19 10:27:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 . C.          [ ] [HK1_TI_CMD_NUM]                EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 . C. ***** MDP ´úÄîαî»ö¼ÝαÊÄα¹αèDCBC•x²è *****
0276 C. (¼á°îî¥Ö¥Ä¥Ê¥¥¥¥¥¥¥¥¥èèè¼αα¼Ä»Üα¹αè)
0277 . S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 . C. ***** ¥Ð¥¹•î Daily±çîñαè´Øα¹αèDCBC•x²è *****
0282 . S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 . C. ¦ãLOS¥Ä¥§¥Ä¥-¼Ä»Ü;ã
0287 C.
0288 . C. ***** LOS *****
0289 C.

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-049 2013-11-19 11:54:23 172 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY§YÁY-¼Á»Ü;ã
0005 C.
0006 C. YÁYB;¼Y³YFYOYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èò¿òÀò•µ°È»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈè¼µ•ííÉ;ÈòÈ¼°ÇÓò•ò¿¼l¹çòÍ;çÁ®, ùò¹òÈòòòÇÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-268:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Stop SP table >
0036 +. DC 07-F0 MDP_SP_CTRL_MANU
0037 BC (61)
0038 C. -----
0039 C. MDP_SP_CTRL_MODE = MANU [ ]
0040 C. -----
0041 C.
0042 . C. <Upload SP Observation Table>
0043 . S. RAM ram-286:MDP_OBS_S
0044 ( )
0045 C.
0046 . C. < Dump RAMID=MDP_OBS_S >
0047 +. DC 07-F0 MDP_DUMP_SPTBL
0048 BC (83 07 00 00 00 38 b8)
0049 C. -----
0050 C. MDP_OBS_S verify = OK/NG [ ]
0051 C. -----
0052 C.
0053 . C. < Upload DPL table >
0054 C.
0055 C. YçYÁY×Yí;¼YÉòíÁ°òÈSTS_CHKòòOFFòÈò¹òÈ
0056 C.
0057 . S. RAM ram-271:MDP_DPL
0058 ( )
0059 C.
0060 . C. < Dump RAMID=MDP_DPL >
0061 +. DC 07-F0 MDP_DUMP_FGTBL
0062 BC (82 07 00 38 b8 00 40)
0063 C. -----
0064 C. MDP_DPL verify = OK [ ]
0065 C. -----
0066 C.
0067 C. STS_CHKòòONòÈò¹òÈ
0068 C.
0069 . C. < Update MDP DSC PAR1 >
0070 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0071 BC (4c)
0072 C. MDP_CMD_CODE = F04C0700 [ ]
0073 C. MDP_CMD_CNT (count-up 1) [ ]
0074 C. -----
0075 C.
0076 . C.
0077 C. *****
0078 C. SOT TI command set
0079 C. *****
0080 C. Execute, after the success of TBL upload.
0081 +. TI 2013-11-19 10:27:18.0
0082 DC 07-F0 MDP_SOT_MODE_OBSV
0083 BC (40)
0084 C. -----
0085 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0086 C. -----
0087 C.
0088 C.
0089 C. ***** XRT START *****
0090 C.
0091 +. DC 07-F0 MDP_XRT_CTRL_MANU
0092 BC (c1)
0093 +. DC 07-F0 MDP_XRT_MODE_STBY
0094 BC (c3)
0095 . C. ----- Success Verify ? OK / NG_____
```


Nov 19, 13 11:54

XRT_OGLIST_0914.chk

Page 1/5

*** OP Sequence for XRT ***

2013/11/19	10:37:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/11/19	10:37:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/11/19	10:38:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	02 00 00 00 00		
2013/11/19	10:38:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/11/19	10:38:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/11/19	10:38:20.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/11/19	10:38:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/11/19	10:38:24.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/11/19	10:38:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2013/11/19	10:40:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2013/11/19	10:41:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/11/19	15:28:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/11/19	15:28:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/11/19	15:28:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/11/19	15:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/11/19	15:51:00.0	XRT_Custom_430_OG [0x1ae]					
2013/11/19	15:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/11/19	17:04:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/11/19	17:04:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/11/19	17:04:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/11/19	17:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/11/19	17:28:00.0	XRT_Custom_430_OG [0x1ae]					
2013/11/19	17:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/11/19	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/11/19	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/11/19	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2013/11/19	18:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/11/19	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/11/19	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/11/19	18:02:58.0	XRT_QT_PROG_SET_446_OG [0x1be]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2013/11/19	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/11/19	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/11/19	18:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/11/19	18:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	02 00 00 00 00		
2013/11/19	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/11/19	18:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/11/19	18:10:20.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/11/19	18:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/11/19	18:10:24.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/11/19	18:10:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2013/11/19	18:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2013/11/19	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/11/19	18:41:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/11/19	18:41:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/11/19	18:41:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/11/19	18:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/11/19	19:04:30.0	XRT_Custom_430_OG [0x1ae]					

Nov 19, 13 11:54

XRT_OGLIST_0914.chk

Page 2/5

2013/11/19	19:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/19	20:18:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/19	20:18:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/11/19	20:18:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/11/19	20:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/11/19	20:42:00.0	XRT_Custom_430_OG [0x1ae]							
2013/11/19	20:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/19	21:56:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/19	21:56:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/11/19	21:56:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/11/19	21:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/11/19	22:18:00.0	XRT_Custom_430_OG [0x1ae]							
2013/11/19	22:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/19	23:33:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/19	23:33:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/11/19	23:33:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/11/19	23:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/11/19	23:44:30.0	XRT_Custom_430_OG [0x1ae]							
2013/11/19	23:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/20	01:00:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/20	01:00:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/11/20	01:00:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/11/20	01:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/11/20	01:16:30.0	XRT_Custom_430_OG [0x1ae]							
2013/11/20	01:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/20	02:32:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/20	02:32:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/11/20	02:32:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/11/20	02:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/11/20	02:53:00.0	XRT_Custom_430_OG [0x1ae]							
2013/11/20	02:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/20	04:01:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/20	04:01:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/11/20	04:01:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/11/20	04:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/11/20	04:30:30.0	XRT_Custom_430_OG [0x1ae]							
2013/11/20	04:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/20	05:41:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/20	05:41:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/11/20	05:41:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/11/20	05:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/11/20	06:08:00.0	XRT_Custom_430_OG [0x1ae]							
2013/11/20	06:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/20	06:17:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/20	06:17:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/11/20	06:18:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2013/11/20	06:18:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/11/20	06:20:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/11/20	06:20:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/11/20	06:20:58.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				

Nov 19, 13 11:54

XRT_OGLIST_0914.chk

Page 3/5

2013/11/20	06:21:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/11/20	06:27:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/11/20	06:27:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2013/11/20	06:28:00.0	AOCS_ORe-point_Start_1_OG [0x097]						
		AOCU_NM	5	02-76	02 00 00 00 00			
2013/11/20	06:28:16.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/11/20	06:28:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/11/20	06:28:20.0	XRT_AEC_RESET_413_OG [0x19d]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/11/20	06:28:22.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/11/20	06:28:24.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/11/20	06:28:26.0	XRT_QT_PROG_SET_409_OG [0x199]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13			
2013/11/20	06:30:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d			
2013/11/20	06:31:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/11/20	07:22:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/11/20	07:22:02.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/11/20	07:22:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/11/20	07:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/11/20	07:45:00.0	XRT_Custom_430_OG [0x1ae]						
2013/11/20	07:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/11/20	09:01:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/11/20	09:01:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/11/20	09:01:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/11/20	09:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/11/20	09:21:30.0	AOCS_ORe-point_Start_3_OG [0x099]						
		AOCU_NM	5	02-76	00 54 00 01 58			
2013/11/20	09:51:30.0	AOCS_ORe-point_Start_4_OG [0x09a]						
		AOCU_NM	5	02-76	00 4c e5 01 58			
2013/11/20	10:46:30.0	AOCS_ORe-point_Start_5_OG [0x09b]						
		AOCU_NM	5	02-76	00 43 ff 01 58			
2013/11/20	11:16:30.0	AOCS_ORe-point_Start_6_OG [0x09c]						
		AOCU_NM	5	02-76	00 3b 1a 01 58			
2013/11/20	11:46:30.0	AOCS_ORe-point_Start_7_OG [0x09d]						
		AOCU_NM	5	02-76	00 32 35 01 58			
2013/11/20	12:16:30.0	AOCS_ORe-point_Start_8_OG [0x09e]						
		AOCU_NM	5	02-76	00 29 58 01 58			
2013/11/20	12:46:30.0	AOCS_ORe-point_Start_9_OG [0x09f]						
		AOCU_NM	5	02-76	00 20 72 01 58			
2013/11/20	13:16:30.0	AOCS_ORe-point_Start_10_OG [0x0a0]						
		AOCU_NM	5	02-76	00 17 8d 01 58			
2013/11/20	13:46:30.0	AOCS_ORe-point_Start_11_OG [0x0a1]						
		AOCU_NM	5	02-76	00 0e a7 01 58			
2013/11/20	14:40:30.0	AOCS_ORe-point_Start_12_OG [0x0a2]						
		AOCU_NM	5	02-76	00 05 ca 01 58			
2013/11/20	15:10:24.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/11/20	15:10:26.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2013/11/20	15:10:30.0	AOCS_ORe-point_Start_13_OG [0x0a3]						
		AOCU_NM	5	02-76	00 fd cb 01 58			
2013/11/20	15:10:46.0	XRT_FLD_DIS_434_OG [0x1b2]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/11/20	15:13:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/11/20	15:13:26.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/11/20	15:13:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10			
2013/11/20	15:13:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/11/20	16:03:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/11/20	16:03:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/11/20	16:03:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/11/20	16:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/11/20	16:28:30.0	AOCS_ORe-point_Start_14_OG [0x0a4]						
		AOCU_NM	5	02-76	00 f4 e6 01 58			
2013/11/20	16:58:30.0	AOCS_ORe-point_Start_15_OG [0x0a5]						
		AOCU_NM	5	02-76	00 ec 00 01 58			
2013/11/20	18:05:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]						
		AOCU_NM	5	02-76	00 e3 1b 01 58			

Nov 19, 13 11:54

XRT_OGLIST_0914.chk

Page 4/5

2013/11/20	18:35:00.0	AOCS_ORe-point_Start_17_OG [0x0a7] AOCU_NM	5	02-76	00 da 36 01 58
2013/11/20	19:42:00.0	AOCS_ORe-point_Start_18_OG [0x0a8] AOCU_NM	5	02-76	00 d1 59 01 58
2013/11/20	20:12:00.0	AOCS_ORe-point_Start_19_OG [0x0a9] AOCU_NM	5	02-76	00 c8 73 01 58
2013/11/20	21:19:00.0	AOCS_ORe-point_Start_20_OG [0x0aa] AOCU_NM	5	02-76	00 bf 8e 01 58
2013/11/20	21:49:00.0	AOCS_ORe-point_Start_21_OG [0x0ab] AOCU_NM	5	02-76	00 b6 a8 01 58
2013/11/20	22:54:00.0	AOCS_ORe-point_Start_22_OG [0x0ac] AOCU_NM	5	02-76	00 ad cb 01 58
2013/11/20	23:29:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/11/20	23:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/11/20	23:30:00.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	02 00 00 00 00
2013/11/20	23:30:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2013/11/20	23:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/11/20	23:30:20.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0	d0
2013/11/20	23:30:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2013/11/20	23:30:24.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2013/11/20	23:30:26.0	XRT_QT_PROG_SET_409_OG [0x199] MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2013/11/20	23:32:58.0	XRT_FL_PROG_SET_421_OG [0x1a5] MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2013/11/20	23:33:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/11/21	00:09:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/11/21	00:09:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2013/11/21	00:09:34.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/11/21	00:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/11/21	00:17:30.0	XRT_Custom_430_OG [0x1ae]			
2013/11/21	00:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/11/21	01:33:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/11/21	01:33:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2013/11/21	01:33:34.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/11/21	01:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/11/21	01:52:30.0	XRT_Custom_430_OG [0x1ae]			
2013/11/21	01:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/11/21	03:08:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/11/21	03:08:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2013/11/21	03:08:34.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/11/21	03:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/11/21	03:29:30.0	XRT_Custom_430_OG [0x1ae]			
2013/11/21	03:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/11/21	04:38:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/11/21	04:38:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2013/11/21	04:38:34.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/11/21	04:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/11/21	05:07:00.0	XRT_Custom_430_OG [0x1ae]			
2013/11/21	05:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/11/21	05:50:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/11/21	05:50:56.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/11/21	05:51:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00 00
2013/11/21	05:51:16.0	XRT_FLD_DIS_434_OG [0x1b2] MDP_XRT_FLD_DIS	1	07-F0	d9
2013/11/21	05:53:54.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/11/21	05:53:56.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2013/11/21	05:53:58.0	XRT_QT_PROG_SET_446_OG [0x1be] MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2013/11/21	05:54:00.0	XRT_CTRL_AUTO_408_OG [0x198]			

Nov 19, 13 11:54

XRT_OGLIST_0914.chk

Page 5/5

2013/11/21	06:00:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/21	06:00:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/11/21	06:01:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00	
2013/11/21	06:01:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/11/21	06:01:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/11/21	06:01:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/11/21	06:01:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/11/21	06:01:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/21	06:01:26.0	XRT_QT_PROG_SET_409_OG [0x199]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2013/11/21	06:03:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2013/11/21	06:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/21	06:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/21	06:19:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/21	06:19:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/11/21	06:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/21	06:44:30.0	XRT_Custom_430_OG [0x1ae]					
2013/11/21	06:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/21	07:59:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/21	07:59:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/21	07:59:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/11/21	08:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/21	08:21:00.0	XRT_Custom_430_OG [0x1ae]					
2013/11/21	08:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/21	09:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/21	09:39:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/21	09:39:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/11/21	09:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/21	10:14:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	