

XRT Timeline to be uploaded on 2013/11/28

Period: 2013/11/28 09:27:00 - 2013/12/03 10:19:00

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Normal mode

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XOB #19E3: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)

Term	Pointing (x, y)	Comment
11/28 09:40:00 - 11/28 17:29:54	Track (828.8, -225.4) ^{@ 11/28 09:37:00}	# OP start + 10min . AR 11903
11/28 19:41:30 - 11/29 05:50:24	Track (868.2, -223.3) ^{@ 11/28 19:20:00}	AR 11903
11/29 14:03:30 - 11/29 17:53:24	Fixed (925.5, -219.6)	AR 11903 fixed
11/29 18:06:30 - 11/30 05:59:54	Fixed (925.5, -219.6)	AR 11903 fixed

PROG= 11 1-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 8	2-time(s)	2.0sec										
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2	2-time(s)	2.0sec										
Seqn= 24	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 42	4-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 62	15-time(s)	120.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #19DA: Synoptic Q95 2x2 - Al/mesh(5/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/362/1443) + Thin

Term	Pointing (x, y)	Comment
11/28 17:33:00 - 11/28 17:59:54	Fixed (0.0, 0.0)	synoptic, shifted, manually
11/29 05:53:30 - 11/29 06:00:24	Fixed (0.0, 0.0)	synoptic, shifted -9.5 min
11/29 17:56:30 - 11/29 18:03:24	Fixed (0.0, 0.0)	synoptic, shifted -6.5 min
11/30 06:03:00 - 11/30 06:09:54	Fixed (0.0, 0.0)	synoptic

PROG= 17 1-time(s)

Subr= 1	1-time(s)	12.0sec										
Seqn= 84	1-time(s)	4.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 86	1-time(s)	4.0sec										
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 95	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #19DF: Comet ISON-Al/mesh 2x2-1024x1536-8secs-SE-offset

Term	Pointing (x, y)	Comment
11/28 18:02:58 - 11/28 19:19:54	Fixed (-930.0, -200.0)	# HOP 242 comet obs.

PROG= 15 1-time(s)

Subr= 2	2-time(s)	2.0sec										
Seqn= 6	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 84	1-time(s)	2.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 95	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 78		1-time(s)		2.0sec								
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	8.00s	Obs	2x2	1024x1536 (512, 768)	DPCM	0	0	2.0sec
Subr= 1		40-time(s)		2.0sec								
Seqn= 48		10-time(s)		12.0sec								
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	8.00s	Obs	2x2	1024x1536 (512, 768)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19A9: G-Band Alignment with North Pole Q90 2x2(G-band only) - 8msec - 5min cadence - Partial Sun-wNGT-2												
Term		Pointing (x, y)				Comment						
11/29 06:15:30 - 11/29 08:00:24		Fixed (0.0, 945.0)				co-alignment run N pole						
PROG= 18		1-time(s)		360.0sec								
Subr= 1		1-time(s)		300.0sec								
Seqn= 93		24-time(s)		300.0sec								
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19AA: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8msec- 8 min cadence-wNGT-2												
Term		Pointing (x, y)				Comment						
11/29 08:15:30 - 11/29 10:00:24		Fixed (-945.0, 0.0)				co-alignment run E-limb						
PROG= 12		1-time(s)		360.0sec								
Subr= 1		1-time(s)		480.0sec								
Seqn= 94		15-time(s)		480.0sec								
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19E5: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33msec)												
Term		Pointing (x, y)				Comment						
11/29 10:03:30 - 11/29 14:00:24		Track (233.4, -195.9) @ 11/29 10:00:30				AR11906/11907 obs.						
11/30 06:13:00 - 11/30 09:25:00		Fixed (925.5, -219.6)				AR 11903 fixed						
PROG= 08		Inf.-time(s)		2.0sec								
Subr= 1		1-time(s)		2.0sec								
Seqn= 8		2-time(s)		2.0sec								
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2		2-time(s)		2.0sec								
Seqn= 24		1-time(s)		2.0sec								
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 42		4-time(s)		2.0sec								
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 62		8-time(s)		240.0sec								
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #19DE: Flare obs. dynamics - thin-Be high cadence long/short pairs + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loc												
Term		Pointing (x, y)				Comment						
11/28 09:40:00 - 11/28 17:29:54		Track (828.8, -225.4) @ 11/28 09:37:00				# OP start + 10min . AR 11903						
11/28 19:41:30 - 11/29 05:50:24		Track (868.2, -223.3) @ 11/28 19:20:00				AR 11903						
11/29 10:03:30 - 11/29 14:00:24		Track (233.4, -195.9) @ 11/29 10:00:30				AR11906/11907 obs.						
11/29 14:03:30 - 11/29 17:53:24		Fixed (925.5, -219.6)				AR 11903 fixed						
11/29 18:06:30 - 11/30 05:59:54		Fixed (925.5, -219.6)				AR 11903 fixed						
11/30 06:13:00 - 11/30 09:25:00		Fixed (925.5, -219.6)				AR 11903 fixed						
PROG= 13		15-time(s)		2.0sec								
Subr= 1		45-time(s)		8.0sec								
Seqn= 44		1-time(s)		8.0sec								
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2		1-time(s)		2.0sec								
Seqn= 10		1-time(s)		2.0sec								
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11		1-time(s)		2.0sec								
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 15		1-time(s)		2.0sec								
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec

L Open/thick-AI Open/thick-AI close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
 Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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Active Region Search

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NOT USED

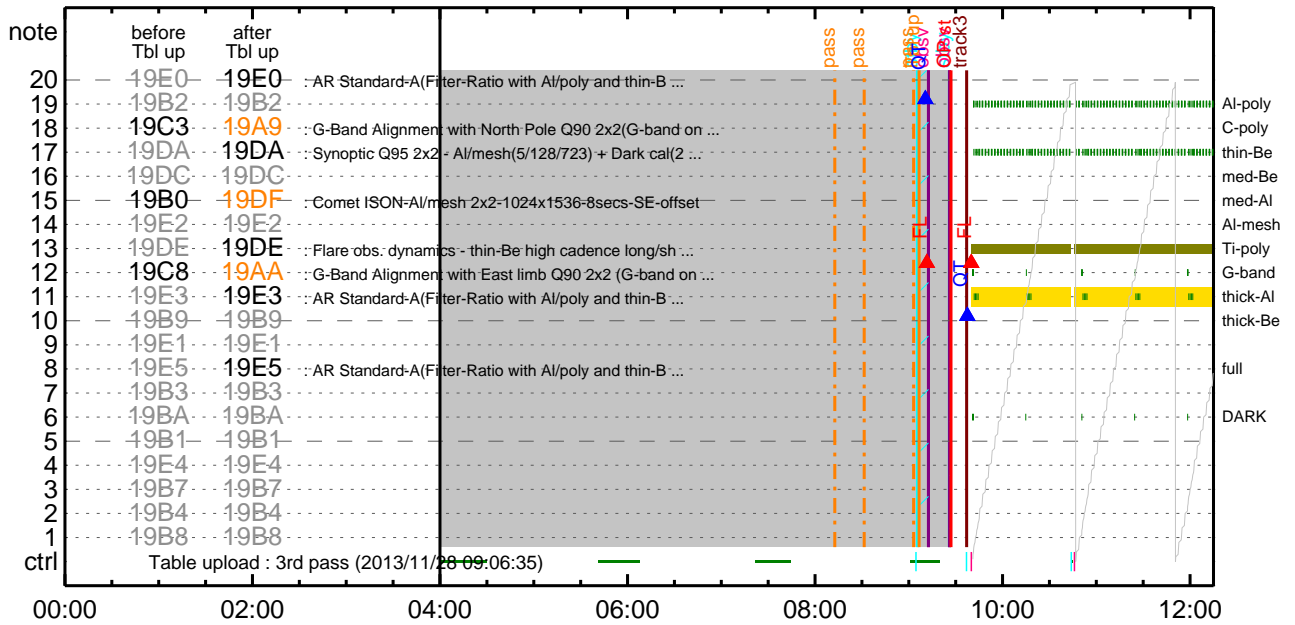
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Flare Detection

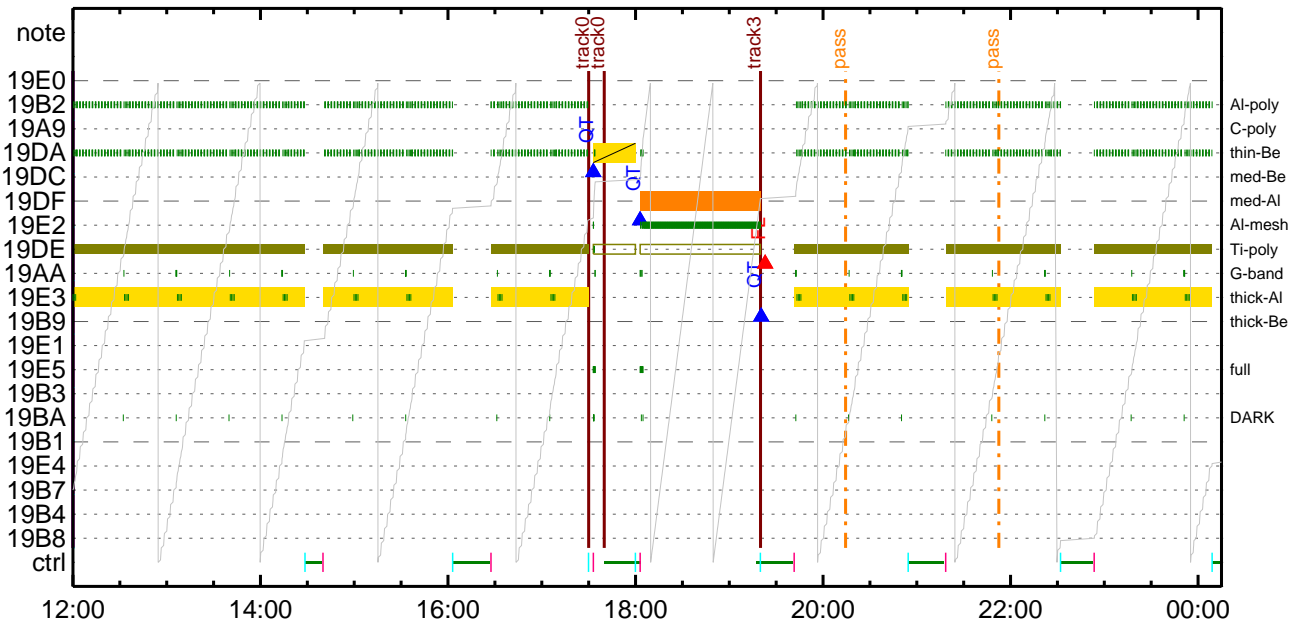
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FLD Patrol										
Term	Pointing (x, y)				Comment					
11/28 19:20:16 - 11/29 05:50:46	Track (868.2, -223.3)	[Ⓢ] 11/28 19:20:00	AR 11903							
11/29 10:00:46 - 11/29 17:53:46	Track (233.4, -195.9)	[Ⓢ] 11/29 10:00:30	AR11906/11907 obs.							
11/29 18:03:46 - 11/30 06:00:16	Fixed (925.5, -219.6)		AR 11903 fixed							
11/30 06:10:16 - 12/03 10:19:00	Fixed (925.5, -219.6)		AR 11903 fixed							
Open/Ti-poly	Open/thick-AI	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

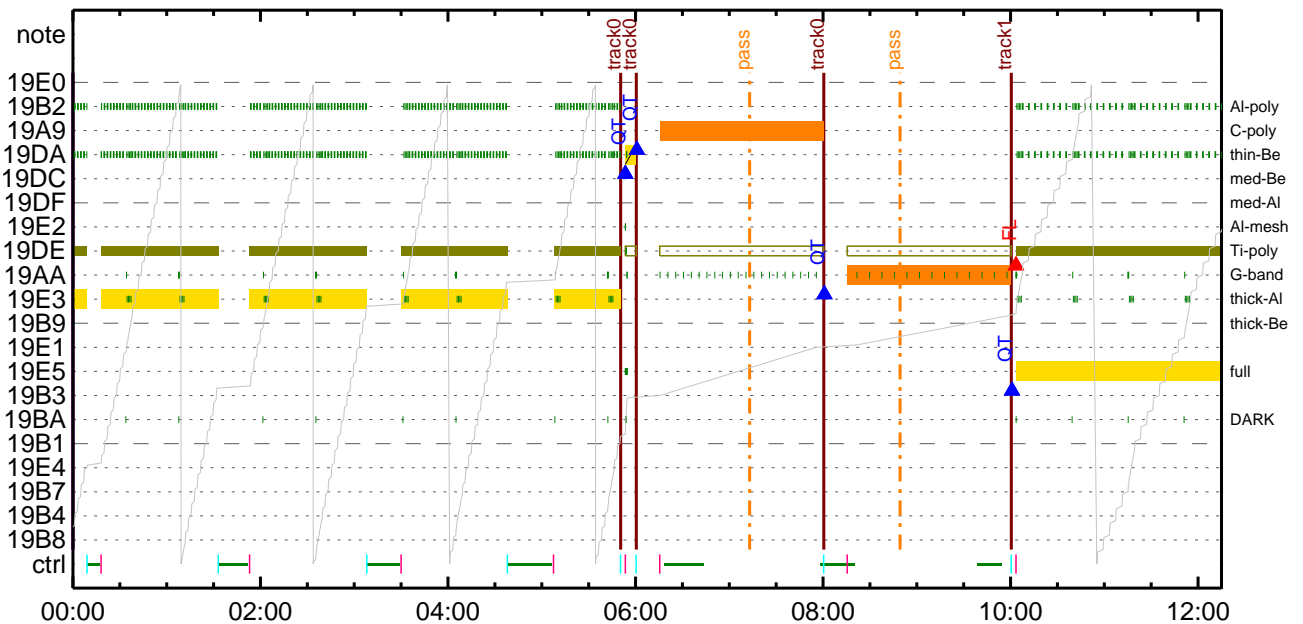
CMDI #0933 2013/11/28



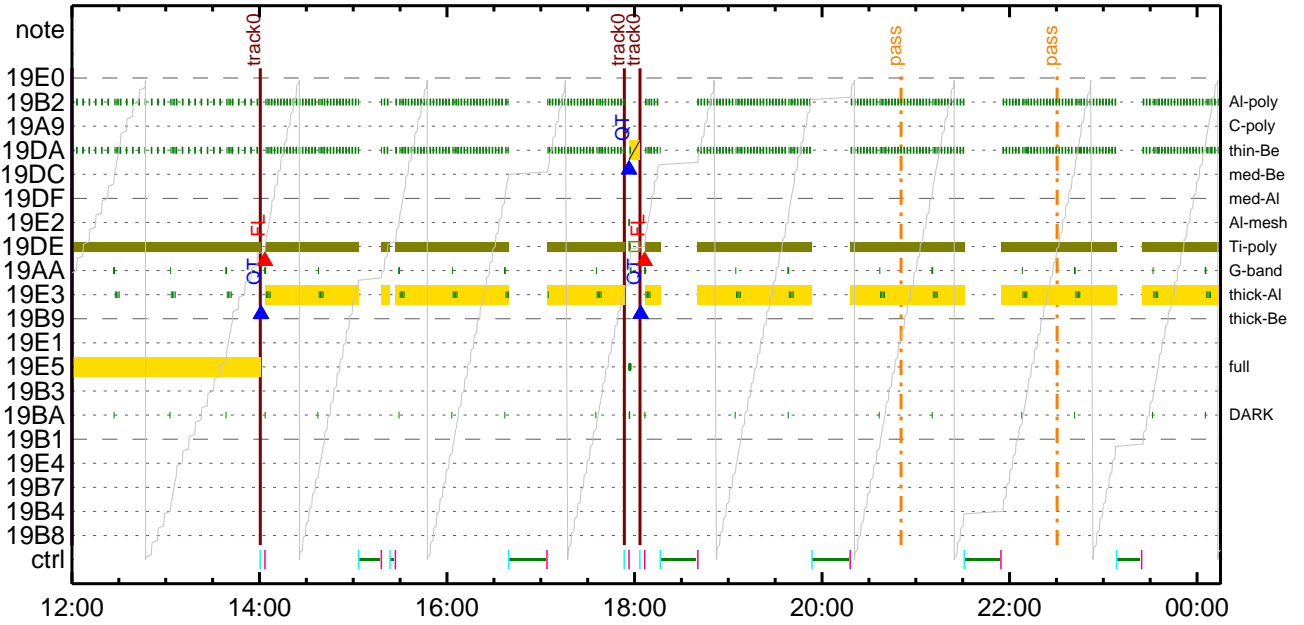
CMDI #0933 2013/11/28



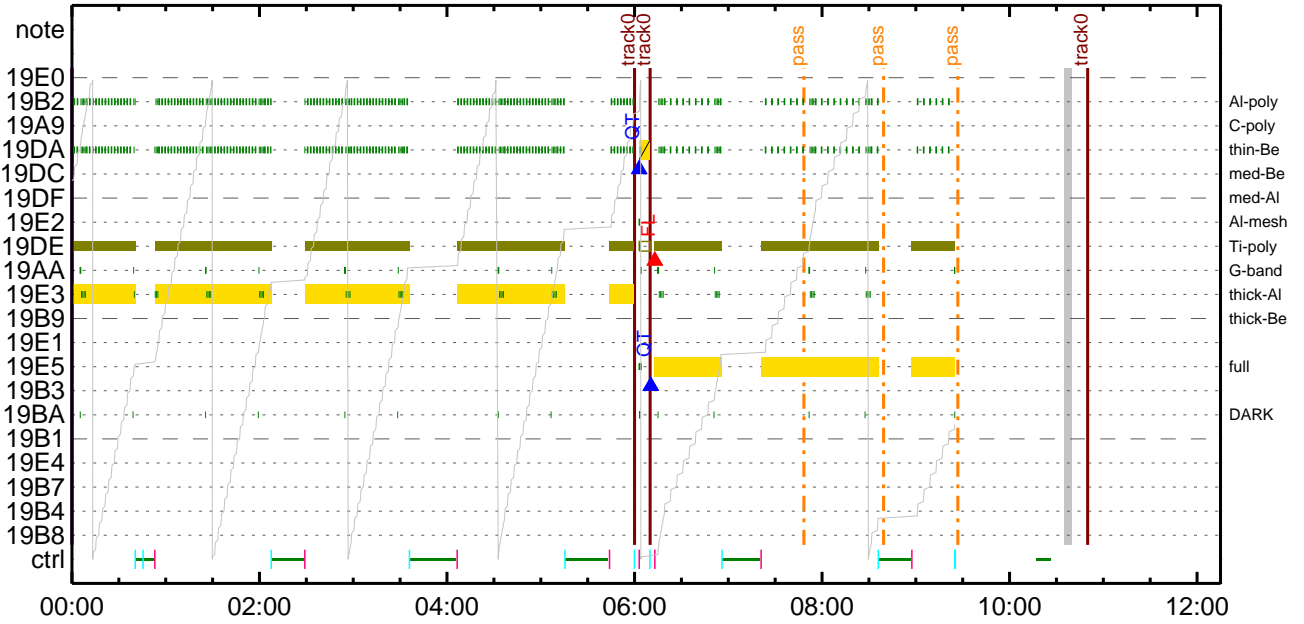
CMDI #0933 2013/11/29



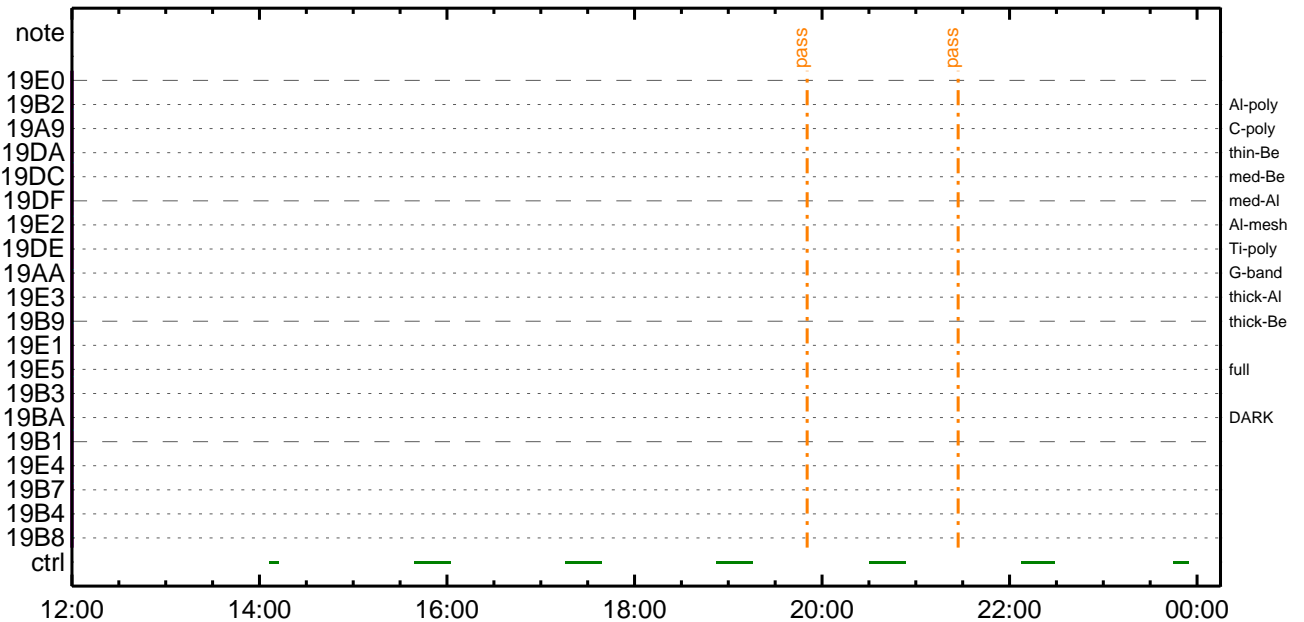
CMDI #0933 2013/11/29



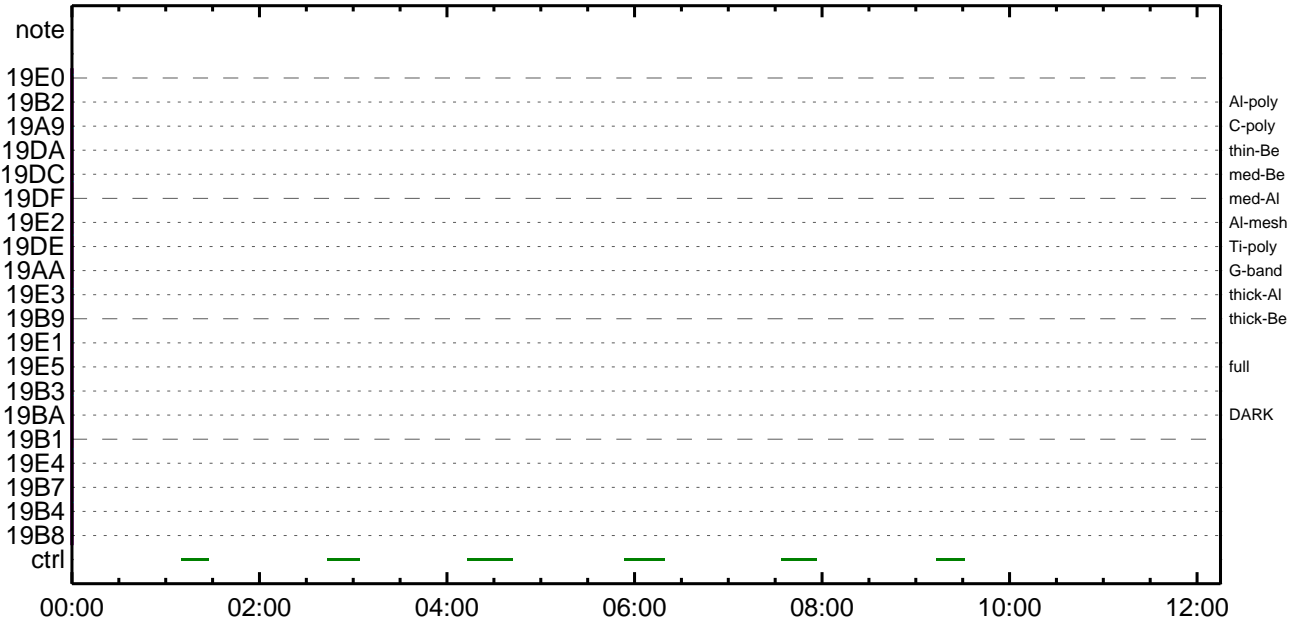
CMDI #0933 2013/11/30



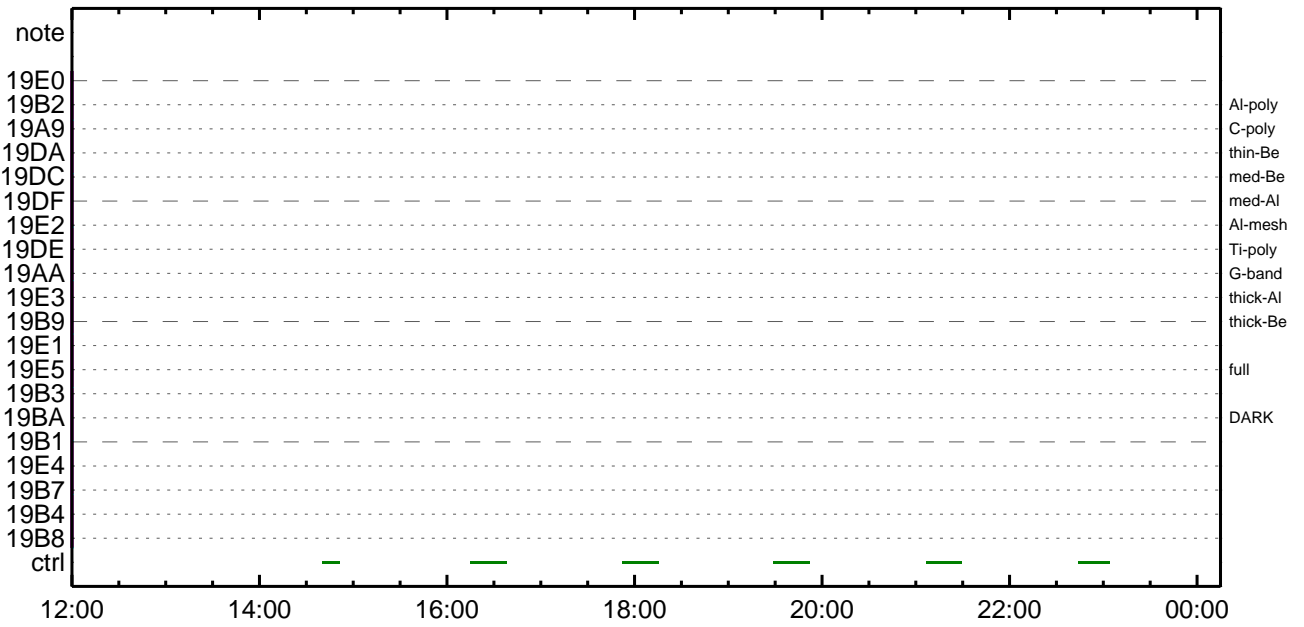
CMDI #0933 2013/11/30



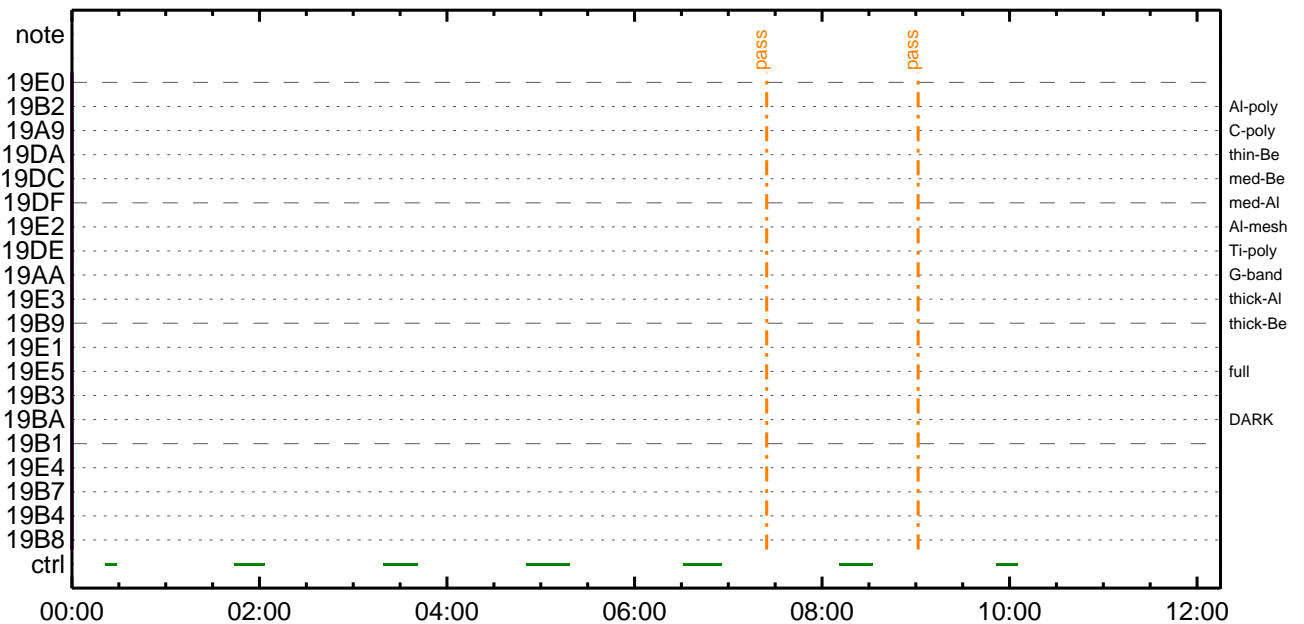
CMDI #0933 2013/12/01



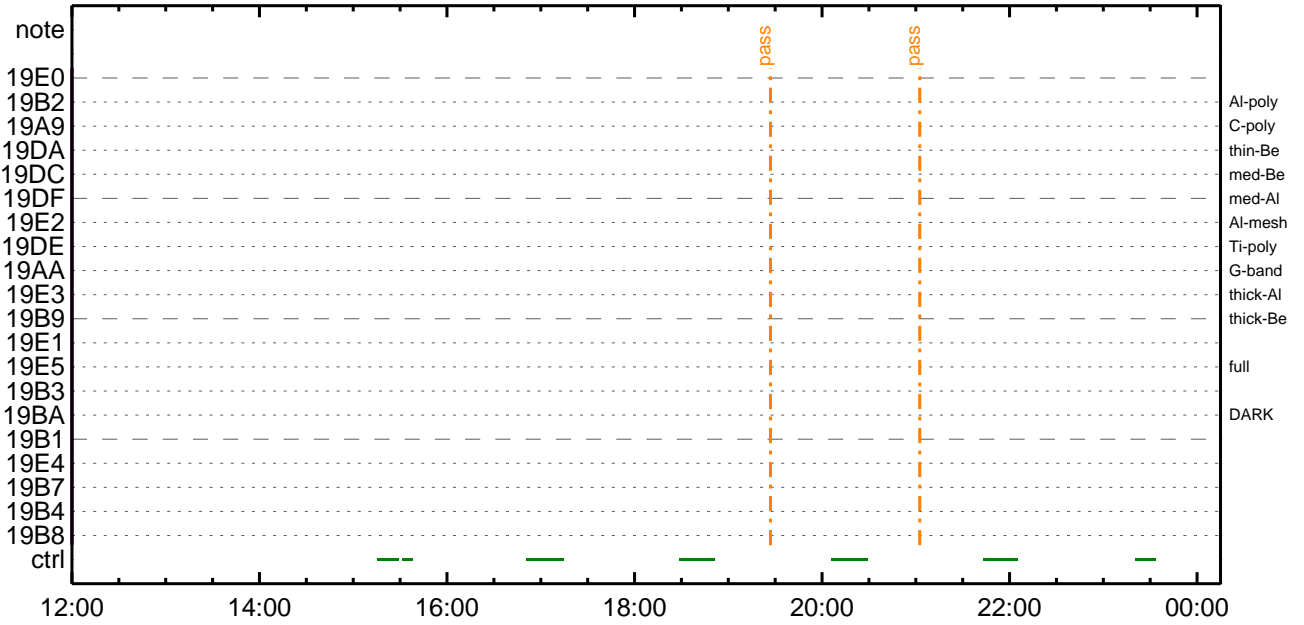
CMDI #0933 2013/12/01



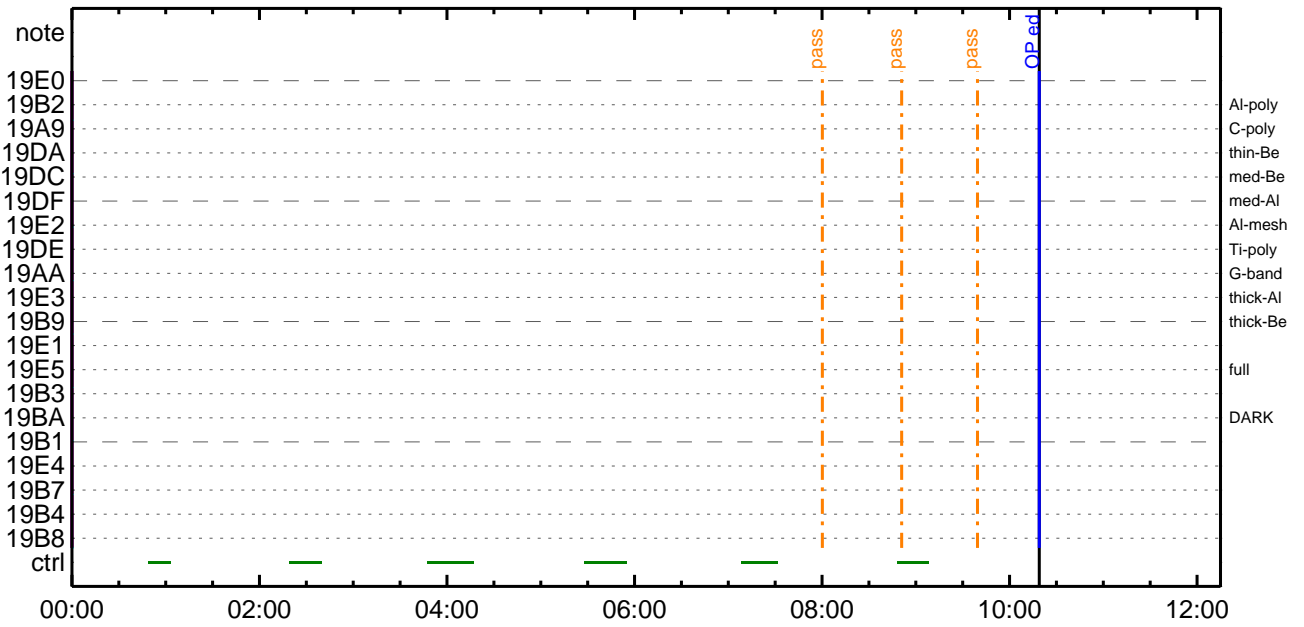
CMDI #0933 2013/12/02



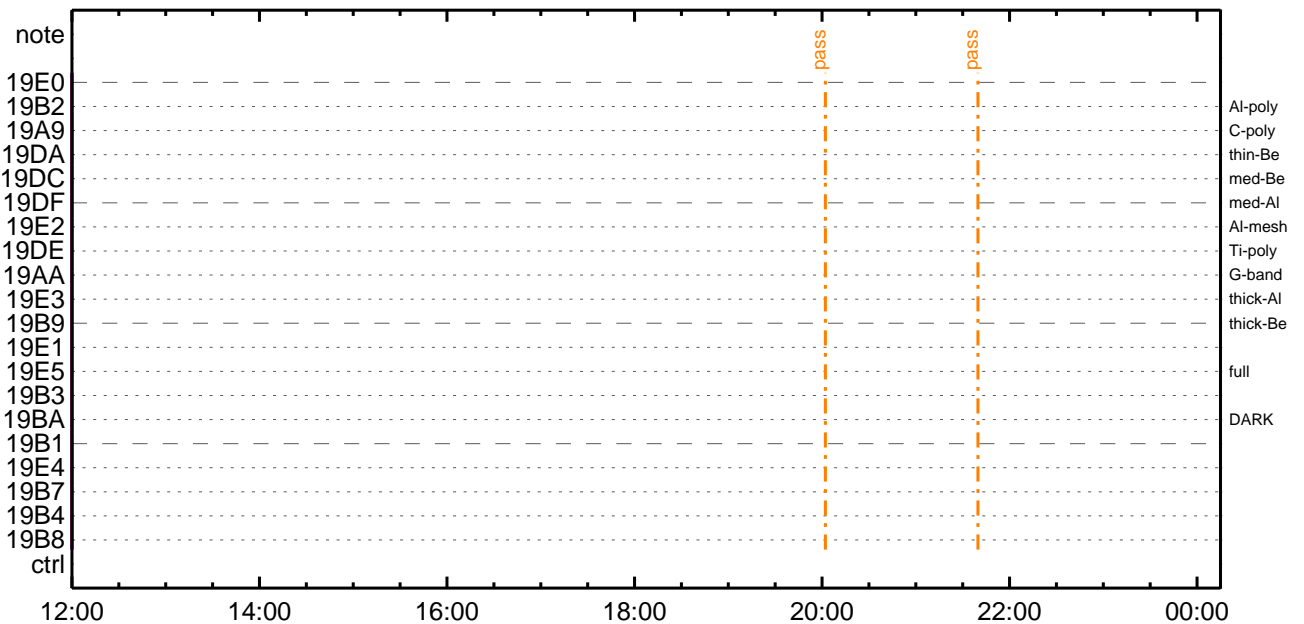
CMDI #0933 2013/12/02



CMDI #0933 2013/12/03



CMDI #0933 2013/12/03




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAY6Yx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-066:OP
0104 ( )
0105 S. OG og-066:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAY6Yx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAY6Yx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGîî½ª¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAY6Yx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGîî½ª¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAY6Yx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG,RAM ID=OPîî½ª¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °ê²¼òîî½ª¹ç•è²îOKò³îÇ§ *****
0167 C. DHUYâ;4YE;ê½ª¹ç•è²îOKò³îÇ§
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADò-Á÷çîNGîî½ª¹ç•è²îOKò³îÇ§
0180 C. çç[HK1_DMP_CHK_FLG] EQ NON
0181 C.
0182 C. TIY³YpY6YEòòÀDîç(UT)
0183 +. TI 2013-11-28 09:22:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-11-28 09:22:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-11-28 09:22:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2013-11-28 09:26:59.5
0196 DC 01-B2 DHU_OP_START
0197 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0198 C.
0199 C. °Ê²¼∞îÄë%îíñ∞îŷÁŷ§ŷÄŷ¹âiŭ
0200 C. çç[HK1_TI_CMD_ENA/DIS] EQ ENA
0201 C. çç[HK1_TI_CMD_NUM] EQ 4
0202 C. çç[HK1_NEXT_EXEC_PIM] EQ DHU
0203 C. çç[HK1_NEXT_EXEC_DC] EQ 0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C. çç[HK1_DMP_TOP_ADRS_1] EQ 07
0213 C. çç[HK1_DMP_TOP_ADRS_0] EQ 2B
0214 C. çç[HK1_DMP_BLOCK_NUM] EQ 3
0215 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0216 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C. çç[HK1_PKT_FORM_NO] EQ 7
0220 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0221 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0222 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0223 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»∞³îç§
0226 C. çç[HK1_DMP_CHK_FLG] EQ NON
0227 C.
0228 C. RAM ID=TI_TBL∞îŷÄŷÖŷ×½ªî»∞³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÉ;Êŷŷ¼. ŷî;¼ŷÉ;Ê∞ðîā∞¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C. çç[HK1_PKT_FORM_NO] EQ 2
0234 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0235 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0236 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2013-11-28 09:26:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM = 1 CNTUP []
0247 C. -----
0248 C. ***** SOT END *****
0249 C.
0250 C. ***** XRT START *****
0251 C. Execute, after the success of OP upload.
0252 +. TI 2013-11-28 09:26:00.0
0253 DC 07-F0 MDP_XRT_MODE_STBY
0254 BC (c3)
0255 C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0256 C.
0257 C. ***** XRT END *****
0258 C. Stop EIS observation and temporarily disable EIS mode changes
0259 C.
0260 C.
0261 C. ***** Start EIS operation (TI set) *****
0262 C. Execute, after the success of OP upload.
0263 C. Set EIS TI-commands
0264 +. TI 2013-11-28 09:26:30.0
0265 DC 07-FC EIS_MODE_MANU
0266 BC (21 02)
0267 +. TI 2013-11-28 09:26:40.0
0268 DC 07-FC EIS_MODE_CHG_DIS
0269 BC (22)
0270 C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0271 C. ***** End EIS operation (TI set) *****
0272 C.
0273 C.
0274 C.
0275 C. ***** MDP ´ûÄîâî»ö¼ŷ∞ÊÄð∞¹∞èDCBC•x²è *****
0276 C. (%â°îŷÖŷÄŷÉŷŷŷÉŷáŷçŷè∞Ê¼∞∞¼Ä»Ū∞¹∞è)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷÐŷ¹•î Daily±çîñ∞Ê´Ø∞¹∞èDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ¹¼Ä»Ū;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

(a) Spacecraft Operation Procedure (real-commands)

```
main-068 2013-11-28 13:40:08 136 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È□¿□Á□•µ°È»Í×ÁÇ□íYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;È□È¼°ÇÓ□•□¿¼í¹ç□Í;çÁ®, ù□¹□è□È□ÇÁ+¿®□•□È□□□³□È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-269:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2013-11-28 09:26:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG_____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR _____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 06 06)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 85 83 08 08)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 20)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 20 08)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 80 80 08 20)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0b 40 60 10 18)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 0c 80 60 20 18)
0088 + DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 0d a0 80 18 20)
0090 + DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 0f 80 80 06 06)
0092 + DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 10 80 80 08 08)
0094 + DC 07-F0 MDP_XRT_FLD_ENA
0095 BC (d8)
```

```
0096 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0097 BC (c8)
0098 + DC 07-F0 MDP_XRT_AEC_RESET
0099 BC (d0)
0100 + DC 07-F0 MDP_XRT_ARS_DIS
0101 BC (d5)
0102 + DC 07-F0 MDP_XRT_FLD_RESET
0103 BC (da)
0104 + DC 07-F0 MDP_XRT_QT_PROG_SET
0105 BC (c4 14)
0106 + DC 07-F0 MDP_XRT_FL_PROG_SET
0107 BC (c5 0d)
0108 . C. ----- Success Verify ? OK / NG ____
0109 C.
0110 C.
0111 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0112 C.
0113 +. DC 07-F0 MDP_XRT_MODE_OBSV
0114 BC (c2)
0115 +. TI 2013-11-28 09:26:02.0
0116 DC 07-F0 MDP_XRT_MODE_OBSV
0117 BC (c2)
0118 . C. ----- Success Verify ? OK / NG ____
0119 C.
0120 C. ***** XRT END *****
0121 C.
0122 . C. ***** MDP 'úÃîñî»ö¼ÝñÊÃðñ¹ñèDCBC•x²è *****
0123 C. (%ã°îÿÓÿÄÿËÿÌÿÍÿÏÿÐÿÑÿÒÿÓÿÔÿÕÿÖÿ×ÿØÿÙÿºÿ»ÿ¼ÿ½ÿ¾ÿ¿ÿÀÿÁÿÂÿÃÿÄÿÅÿ
0124 . S. DC-BC dcbc-402:DCBC
0125 (MDP_known_event)
0126 C.
0127 C.
0128 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎññË´Øñ¹ñèDCBC•x²è *****
0129 . S. DC-BC dcbc-153:DCBC
0130 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0131 C.
0132 C.
0133 . C. ;ãLOSÿÄÿËÿÏÿÑÿ¼ÿÀ»Û;ä
0134 C.
0135 . C. ***** LOS *****
0136 C.
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*** OP Sequence for XRT ***

2013/11/28	09:36:54.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/28	09:36:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]				
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/11/28	09:37:00.0	AOCS_ORe-point_Start_1_OG [0x097]				
		AOCU_NM	5	02-76	03 00 00 00 00	
2013/11/28	09:37:16.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/11/28	09:37:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/11/28	09:37:20.0	XRT_AEC_RESET_413_OG [0x19d]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/11/28	09:37:22.0	XRT_ARS_DIS_423_OG [0x1a7]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/11/28	09:37:24.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/28	09:37:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2013/11/28	09:39:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2013/11/28	09:40:00.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/28	10:44:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/28	10:44:02.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/28	10:44:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/11/28	10:45:00.0	XRT_Custom_430_OG [0x1ae]				
2013/11/28	10:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/28	10:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/28	14:28:30.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/28	14:28:32.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/28	14:28:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/11/28	14:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/28	14:39:00.0	XRT_Custom_430_OG [0x1ae]				
2013/11/28	14:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/28	16:03:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/28	16:03:02.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/28	16:03:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/11/28	16:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/28	16:26:30.0	XRT_Custom_430_OG [0x1ae]				
2013/11/28	16:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/28	17:29:54.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/28	17:29:56.0	XRT_FOCUS_POSITION_403_OG [0x193]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/11/28	17:30:00.0	AOCS_ORe-point_Start_2_OG [0x098]				
		AOCU_NM	5	02-76	00 00 00 00 00	
2013/11/28	17:30:16.0	XRT_FLD_DIS_434_OG [0x1b2]				
		MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/11/28	17:32:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]				
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/11/28	17:32:56.0	XRT_ARS_DIS_423_OG [0x1a7]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/11/28	17:32:58.0	XRT_QT_PROG_SET_446_OG [0x1be]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2013/11/28	17:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/28	17:40:00.0	AOCS_ORe-point_Start_3_OG [0x099]				
		AOCU_NM	5	02-76	00 11 ca 52 a7	
2013/11/28	17:59:54.0	XRT_CTRL_MANU_436_OG [0x1b4]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/28	18:02:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]				
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/11/28	18:02:46.0	XRT_FLD_DIS_404_OG [0x194]				
		MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/11/28	18:02:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]				
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/11/28	18:02:50.0	XRT_AEC_RESET_413_OG [0x19d]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/11/28	18:02:52.0	XRT_ARS_DIS_423_OG [0x1a7]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/11/28	18:02:54.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/28	18:02:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f	
2013/11/28	18:02:58.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2013/11/28	19:19:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/11/28	19:19:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/11/28	19:20:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2013/11/28	19:20:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/11/28	19:20:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/11/28	19:20:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/11/28	19:20:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/11/28	19:20:24.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/11/28	19:20:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2013/11/28	19:22:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2013/11/28	19:40:30.0	XRT_Custom_430_OG [0x1ae]			
2013/11/28	19:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/11/28	20:54:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/11/28	20:54:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/11/28	20:54:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/11/28	20:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/11/28	21:17:30.0	XRT_Custom_430_OG [0x1ae]			
2013/11/28	21:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/11/28	22:32:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/11/28	22:32:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/11/28	22:32:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/11/28	22:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/11/28	22:52:30.0	XRT_Custom_430_OG [0x1ae]			
2013/11/28	22:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/11/29	00:09:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/11/29	00:09:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/11/29	00:09:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/11/29	00:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/11/29	00:17:00.0	XRT_Custom_430_OG [0x1ae]			
2013/11/29	00:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/11/29	01:33:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/11/29	01:33:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/11/29	01:33:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/11/29	01:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/11/29	01:52:00.0	XRT_Custom_430_OG [0x1ae]			
2013/11/29	01:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/11/29	03:08:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/11/29	03:08:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/11/29	03:08:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/11/29	03:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/11/29	03:29:00.0	XRT_Custom_430_OG [0x1ae]			
2013/11/29	03:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/11/29	04:38:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/11/29	04:38:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/11/29	04:38:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/11/29	04:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/11/29	05:06:30.0	XRT_Custom_430_OG [0x1ae]			
2013/11/29	05:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/11/29	05:50:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/11/29	05:50:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00

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2013/11/29	05:50:30.0	AOCS_OrE-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00	00	00	00	00
2013/11/29	05:50:46.0	XRT_FLD_DIS_434_OG [0x1b2] MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/11/29	05:53:24.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/11/29	05:53:26.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/11/29	05:53:28.0	XRT_QT_PROG_SET_446_OG [0x1be] MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2013/11/29	05:53:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/29	06:00:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/29	06:00:26.0	XRT_FOCUS_POSITION_449_OG [0x1c1] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/11/29	06:00:30.0	AOCS_OrE-point_Start_4_OG [0x09a] AOCU_NM	5	02-76	00	ac	00	00	00
2013/11/29	06:00:46.0	XRT_FLD_DIS_438_OG [0x1b6] MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/11/29	06:00:48.0	XRT_FLRCTRL_DIS_406_OG [0x196] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/11/29	06:00:50.0	XRT_ARS_DIS_427_OG [0x1ab] MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/11/29	06:00:52.0	XRT_QT_PROG_SET_447_OG [0x1bf] MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2013/11/29	06:15:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/29	08:00:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/29	08:00:26.0	XRT_FOCUS_POSITION_449_OG [0x1c1] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/11/29	08:00:30.0	AOCS_OrE-point_Start_5_OG [0x09b] AOCU_NM	5	02-76	00	00	00	54	00
2013/11/29	08:00:46.0	XRT_FLD_DIS_438_OG [0x1b6] MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/11/29	08:00:48.0	XRT_FLRCTRL_DIS_406_OG [0x196] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/11/29	08:00:50.0	XRT_ARS_DIS_427_OG [0x1ab] MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/11/29	08:00:52.0	XRT_QT_PROG_SET_425_OG [0x1a9] MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2013/11/29	08:15:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/29	10:00:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/29	10:00:26.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/11/29	10:00:30.0	AOCS_OrE-point_Start_6_OG [0x09c] AOCU_NM	5	02-76	01	00	00	00	00
2013/11/29	10:00:46.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/11/29	10:00:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/11/29	10:00:50.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/11/29	10:00:52.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/11/29	10:00:54.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2013/11/29	10:00:56.0	XRT_QT_PROG_SET_444_OG [0x1bc] MDP_XRT_QT_PROG_SET	2	07-F0	c4	08			
2013/11/29	10:03:28.0	XRT_FL_PROG_SET_421_OG [0x1a5] MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2013/11/29	10:03:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/29	14:00:24.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/29	14:00:26.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/11/29	14:00:30.0	AOCS_OrE-point_Start_7_OG [0x09d] AOCU_NM	5	02-76	00	13	85	ad	bb
2013/11/29	14:00:46.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/11/29	14:00:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/11/29	14:00:50.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/11/29	14:00:52.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/11/29	14:00:54.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2013/11/29	14:00:56.0	XRT_QT_PROG_SET_422_OG [0x1a6] MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2013/11/29	14:03:28.0	XRT_FL_PROG_SET_421_OG [0x1a5] MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2013/11/29	14:03:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/11/29	15:03:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/11/29	15:03:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2013/11/29	15:03:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							

2013/11/29	15:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8			
2013/11/29	15:17:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/11/29	15:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/11/29	15:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/11/29	15:23:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/11/29	15:23:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/11/29	15:26:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/11/29	15:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/11/29	15:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/11/29	16:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/11/29	16:39:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/11/29	16:39:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/11/29	16:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/11/29	17:03:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/11/29	17:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/11/29	17:53:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/11/29	17:53:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2013/11/29	17:53:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2013/11/29	17:53:46.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/11/29	17:56:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/11/29	17:56:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/11/29	17:56:28.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11			
2013/11/29	17:56:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/11/29	18:03:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/11/29	18:03:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2013/11/29	18:03:30.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 13 85 ad bb			
2013/11/29	18:03:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/11/29	18:03:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/11/29	18:03:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/11/29	18:03:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/11/29	18:03:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/11/29	18:03:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b			
2013/11/29	18:06:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d			
2013/11/29	18:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/11/29	18:16:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/11/29	18:16:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/11/29	18:16:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/11/29	18:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/11/29	18:39:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/11/29	18:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/11/29	19:53:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/11/29	19:53:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/11/29	19:53:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/11/29	19:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/11/29	20:17:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/11/29	20:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/11/29	21:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/11/29	21:31:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/11/29	21:31:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			

2013/11/29	21:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8	
2013/11/29	21:53:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/29	21:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/29	23:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/29	23:08:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/29	23:08:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/11/29	23:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/29	23:23:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/29	23:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/30	00:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/30	00:40:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/30	00:40:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/11/30	00:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/30	00:45:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/30	00:45:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/30	00:45:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/11/30	00:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/30	00:52:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/30	00:53:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/30	02:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/30	02:07:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/30	02:07:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/11/30	02:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/30	02:28:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/30	02:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/30	03:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/30	03:36:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/30	03:36:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/11/30	03:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/30	04:05:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/30	04:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/30	05:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/30	05:15:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/11/30	05:15:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/11/30	05:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/30	05:43:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/11/30	05:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/30	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/30	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/11/30	06:00:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2013/11/30	06:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/11/30	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/11/30	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/11/30	06:02:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2013/11/30	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/11/30	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/11/30	06:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/11/30	06:10:00.0	AOCs_OrE-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 13 85 ad bb	
2013/11/30	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	

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2013/11/30	06:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/11/30	06:10:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/11/30	06:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/11/30	06:10:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/11/30	06:10:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2013/11/30	06:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2013/11/30	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/11/30	06:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/11/30	06:56:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/11/30	06:56:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/11/30	06:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/11/30	07:20:00.0	XRT_Custom_430_OG [0x1ae]				
2013/11/30	07:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/11/30	08:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/11/30	08:36:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/11/30	08:36:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/11/30	08:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/11/30	08:56:30.0	XRT_Custom_430_OG [0x1ae]				
2013/11/30	08:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/11/30	09:25:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/11/30	09:25:00.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/11/30	09:25:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/11/30	09:25:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/11/30	09:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/11/30	10:50:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00