

# XRT Timeline to be uploaded on 2013/12/03

Period: 2013/12/03 10:19:00 - 2013/12/07 10:03:00

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Normal mode

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## XOB #19E6: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)

Term	Pointing (x, y)	Comment
12/03 10:37:00 - 12/03 18:06:54	Track ( 40.1, -355.1) <sup>@ 12/03 10:29:00</sup>	# OP start + 10min, AR11909
12/04 05:51:00 - 12/04 17:40:24	Track ( 205.8, -353.3) <sup>@ 12/04 05:48:00</sup>	# AR11909
12/05 06:12:00 - 12/05 10:03:54	Track ( 404.1, -350.8) <sup>@ 12/05 06:09:00</sup>	# AR11909
<b>PROG= 07 Inf.-time(s)</b>		
Subr= 1	1-time(s) 2.0sec	
└─ Seqn= 8	2-time(s) 2.0sec	
└─ Open/G-band	Open/G-band close	Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Subr= 2	2-time(s) 2.0sec	
└─ Seqn= 24	1-time(s) 2.0sec	
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Seqn= 42	4-time(s) 2.0sec	
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
└─ Open/thick-Al	Open/thick-Al close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Seqn= 62	8-time(s) 270.0sec	
└─ thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #19DA: Synoptic Q95 2x2 - Al/mesh(5/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/362/1443) + Thin

Term	Pointing (x, y)	Comment
12/03 18:10:00 - 12/03 18:16:54	Fixed ( 0.0, 0.0)	synoptic, shifted 7.0 min
12/04 05:41:00 - 12/04 05:47:54	Fixed ( 0.0, 0.0)	synoptic, shifted -22.0 min
12/04 17:43:30 - 12/04 17:50:24	Fixed ( 0.0, 0.0)	synoptic, shifted -19.5 min
12/05 06:02:00 - 12/05 06:08:54	Fixed ( 0.0, 0.0)	synoptic, shifted -1.0 min
<b>PROG= 17 1-time(s)</b>		
Subr= 1	1-time(s) 12.0sec	
└─ Seqn= 84	1-time(s) 4.0sec	
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Al-mesh	Open/Al-mesh close	Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 5	1-time(s) 2.0sec	
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ Seqn= 86	1-time(s) 4.0sec	
└─ Open/Ti-poly	Open/Ti-poly close	Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/Ti-poly close	Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/Ti-poly close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 95	1-time(s) 2.0sec	
└─ thin-Be/Open	thin-Be/Open close	Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ thin-Be/Open	thin-Be/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ thin-Be/Open	thin-Be/Open close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Seqn= 6	1-time(s) 2.0sec	
└─ Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #19E4: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)

Term	Pointing (x, y)	Comment
12/03 18:20:00 - 12/04 05:37:54	Track ( 107.5, -354.4) <sup>@ 12/03 18:17:00</sup>	# AR11909
12/04 17:53:30 - 12/05 05:58:54	Track ( 305.9, -352.1) <sup>@ 12/04 17:50:30</sup>	# AR11909
<b>PROG= 04 Inf.-time(s)</b>		
Subr= 1	1-time(s) 2.0sec	
└─ Seqn= 8	2-time(s) 2.0sec	
└─ Open/G-band	Open/G-band close	Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
Subr= 2	2-time(s) 2.0sec	
└─ Seqn= 24	1-time(s) 2.0sec	
└─ Open/Ti-poly	Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
└─ Seqn= 42	4-time(s) 2.0sec	
└─ Al-poly/Open	thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
└─ thin-Be/Open	med-Be/Open close	Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec

Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 62 10-time(s) 180.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Flare mode

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### XOB #19DE: Flare obs. dynamics - thin-Be high cadence long/short pairs + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loc

Term	Pointing (x, y)	Comment
12/03 10:37:00 - 12/03 18:06:54	Track ( 40.1, -355.1) @ 12/03 10:29:00	# OP start + 10min, AR11909
12/03 18:20:00 - 12/04 05:37:54	Track ( 107.5, -354.4) @ 12/03 18:17:00	# AR11909
12/04 05:51:00 - 12/04 17:40:24	Track ( 205.8, -353.3) @ 12/04 05:48:00	# AR11909
12/04 17:53:30 - 12/05 05:58:54	Track ( 305.9, -352.1) @ 12/04 17:50:30	# AR11909
12/05 06:12:00 - 12/05 10:03:54	Track ( 404.1, -350.8) @ 12/05 06:09:00	# AR11909

#### PROG= 13 15-time(s)

<b>Subr= 1 45-time(s) 2.0sec</b>												
<b>Seqn= 44 1-time(s) 8.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 10 1-time(s) 2.0sec</b>												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 11 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn= 15 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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### Active Region Search

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NOT USED

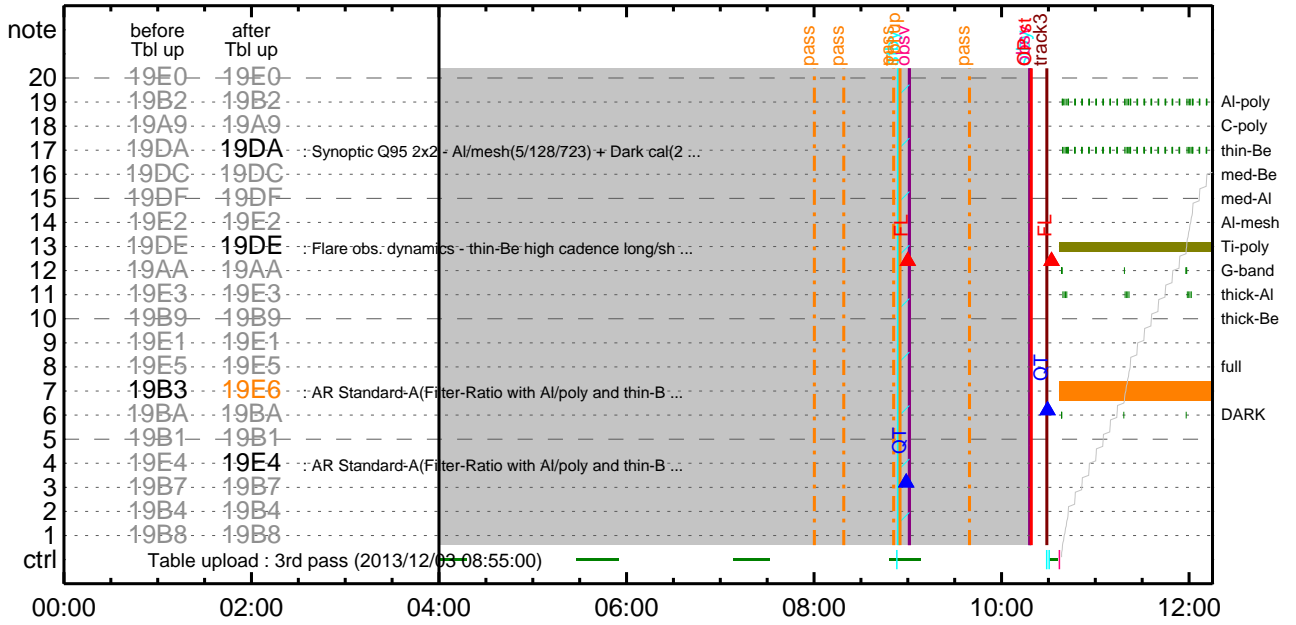
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### Flare Detection

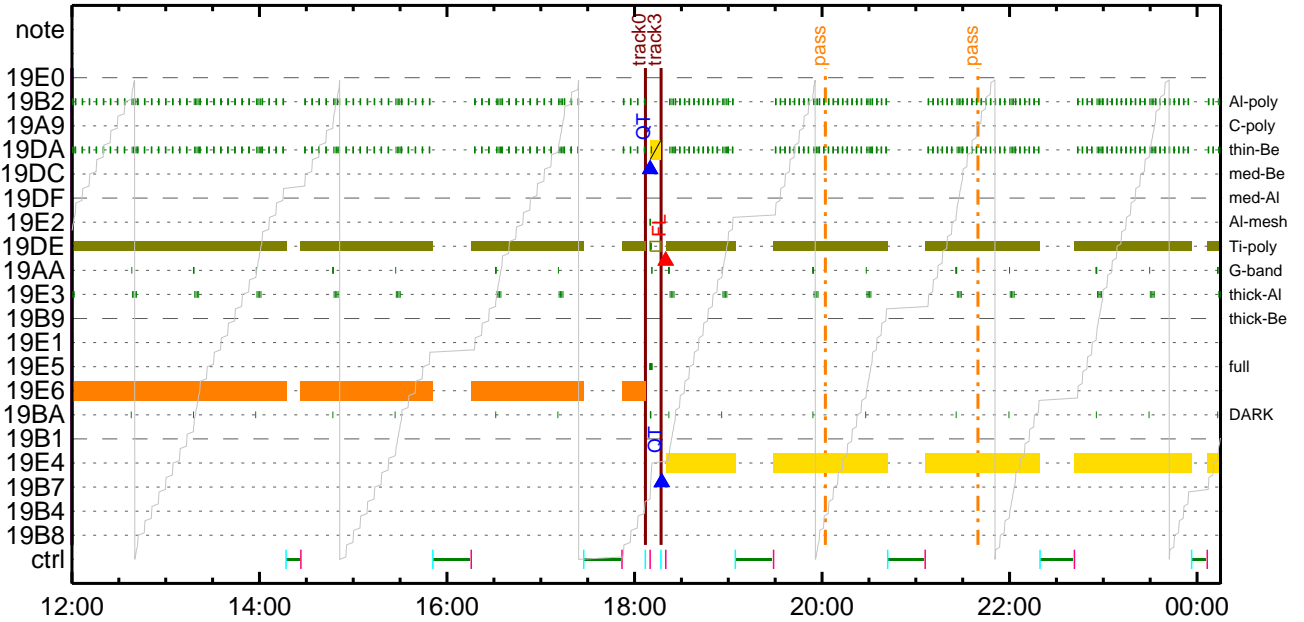
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<b>FLD Patrol</b>												
Term	Pointing (x, y)	Comment										
12/03 18:17:16 - 12/04 05:38:16	Track ( 107.5, -354.4) @ 12/03 18:17:00	# AR11909										
12/04 05:48:16 - 12/04 17:40:46	Track ( 205.8, -353.3) @ 12/04 05:48:00	# AR11909										
12/04 17:50:46 - 12/05 05:59:16	Track ( 305.9, -352.1) @ 12/04 17:50:30	# AR11909										
12/05 06:09:16 - 12/07 10:03:00	Track ( 404.1, -350.8) @ 12/05 06:09:00	# AR11909										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

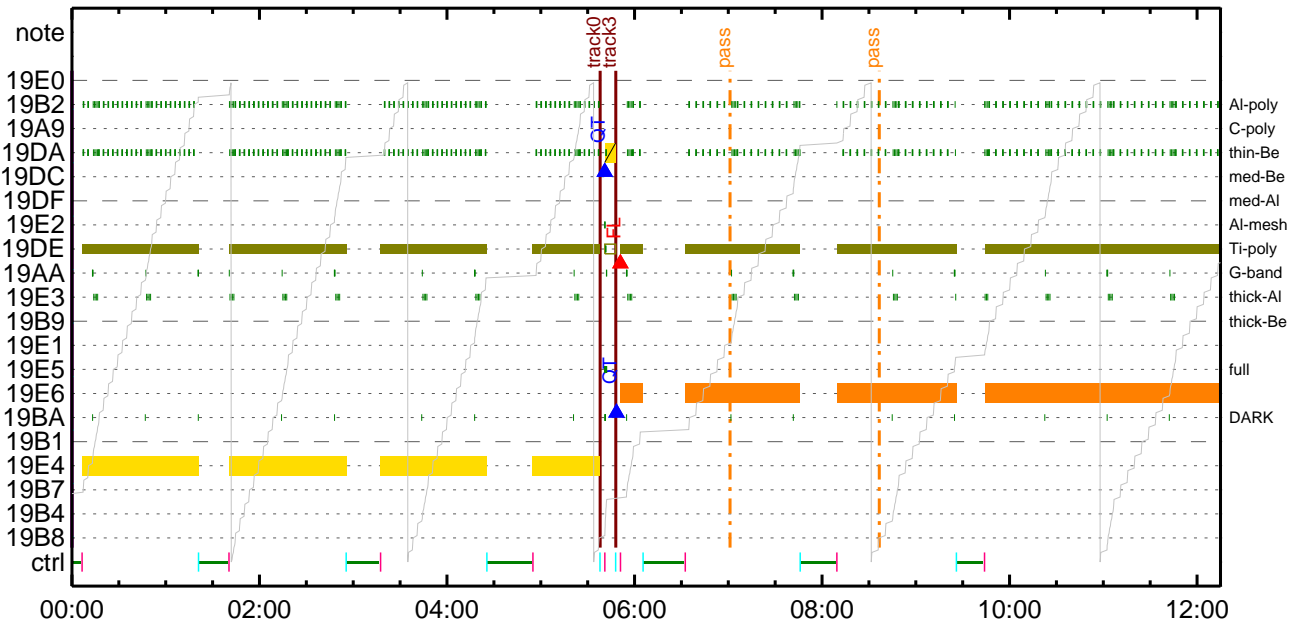
### CMDI #0944 2013/12/03



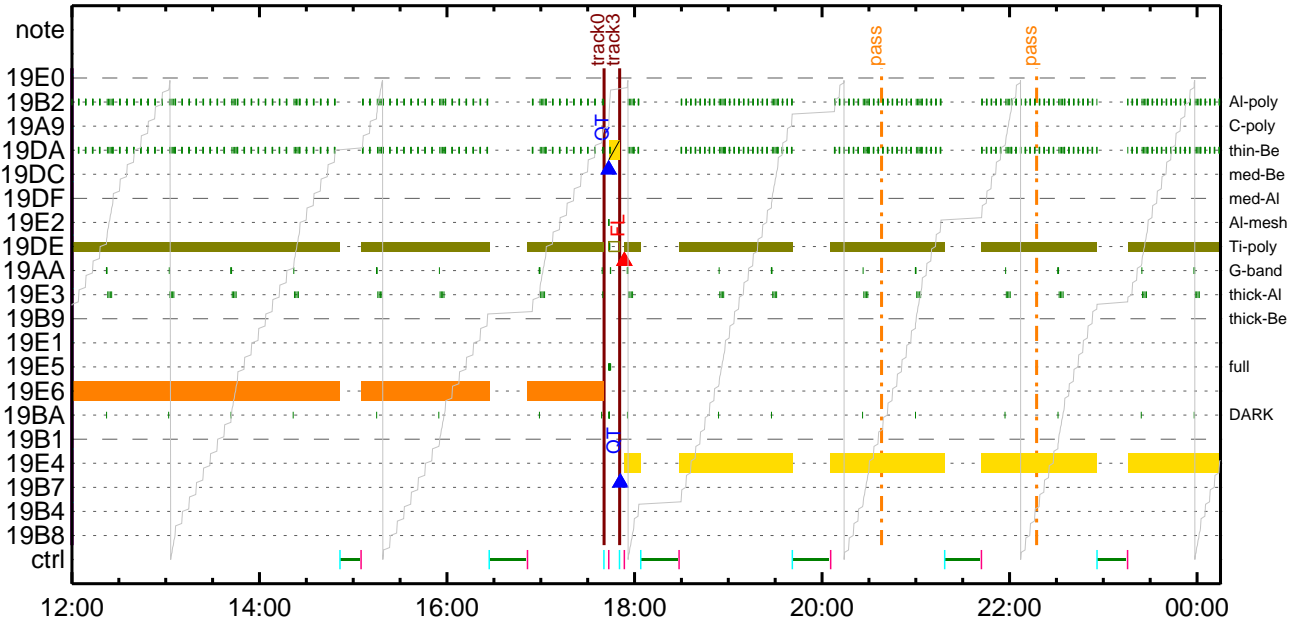
### CMDI #0944 2013/12/03



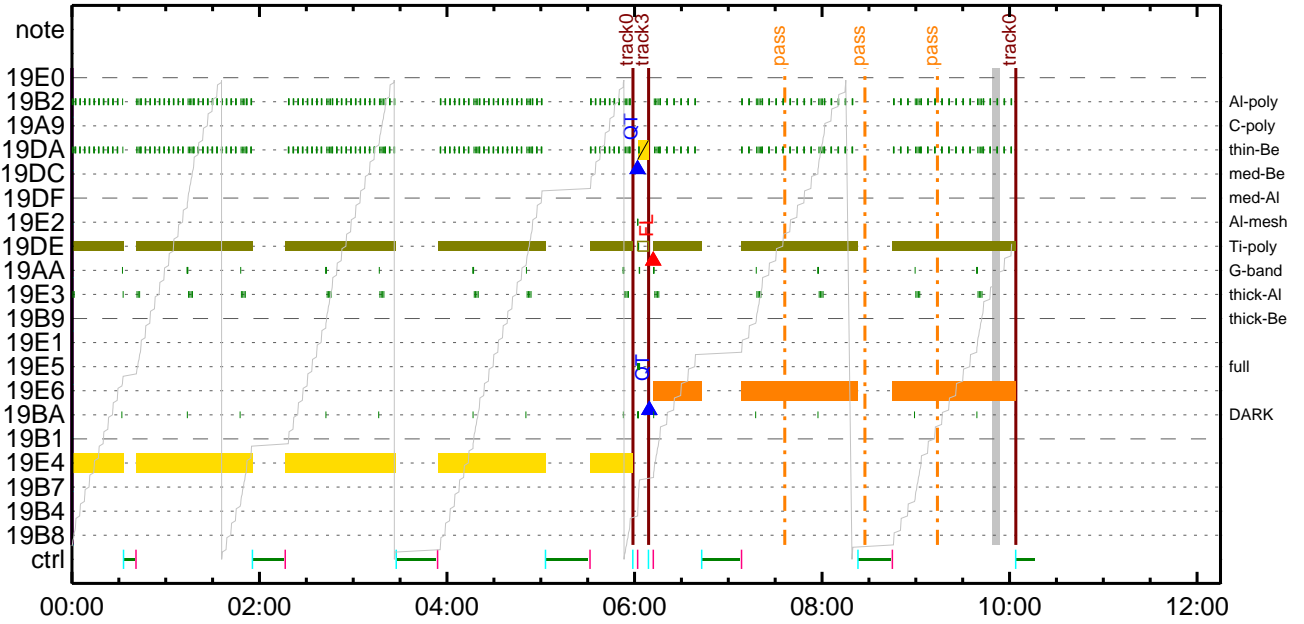
### CMDI #0944 2013/12/04



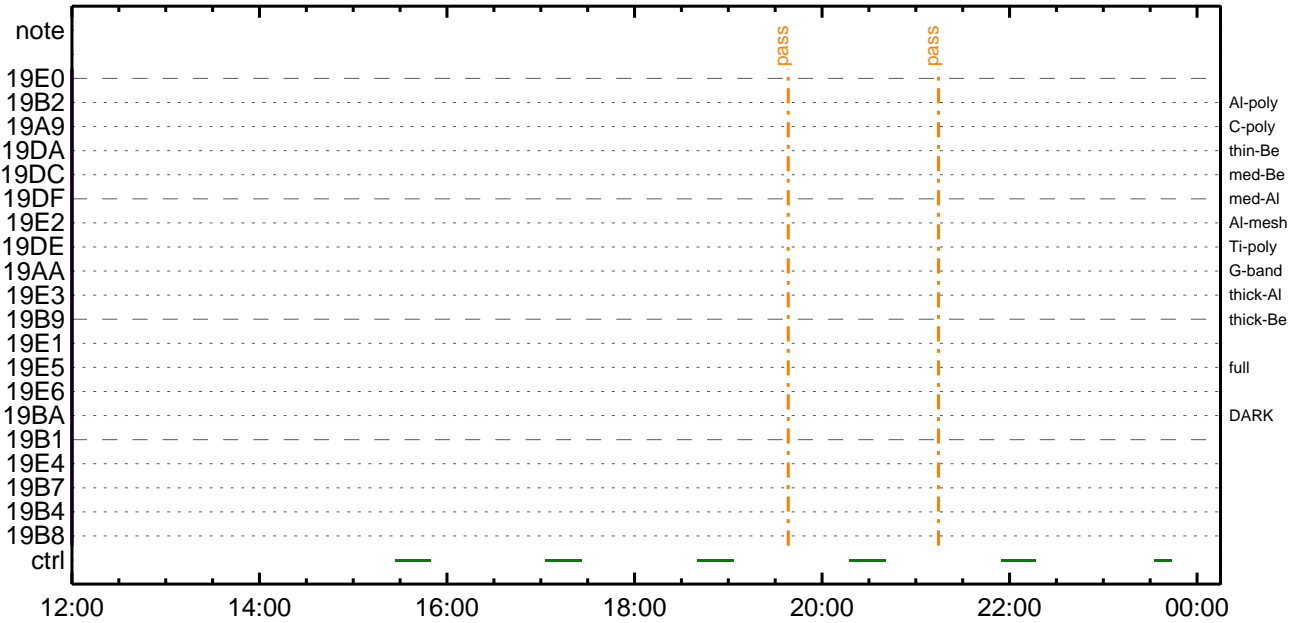
CMDI #0944 2013/12/04



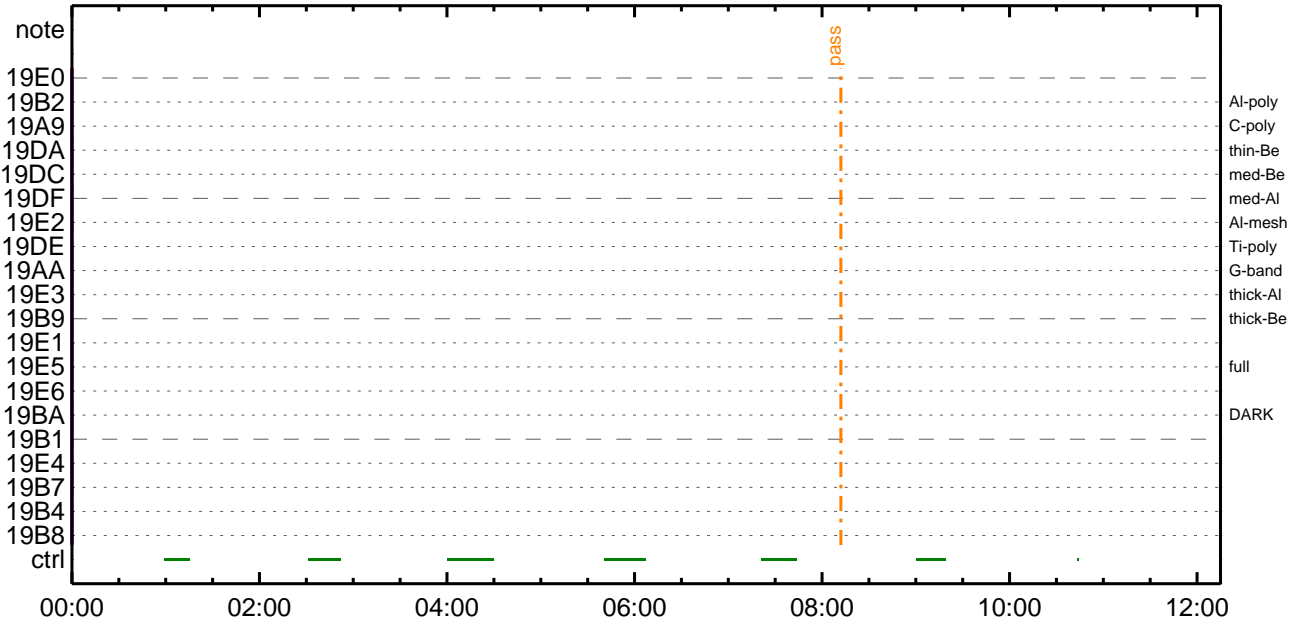
CMDI #0944 2013/12/05



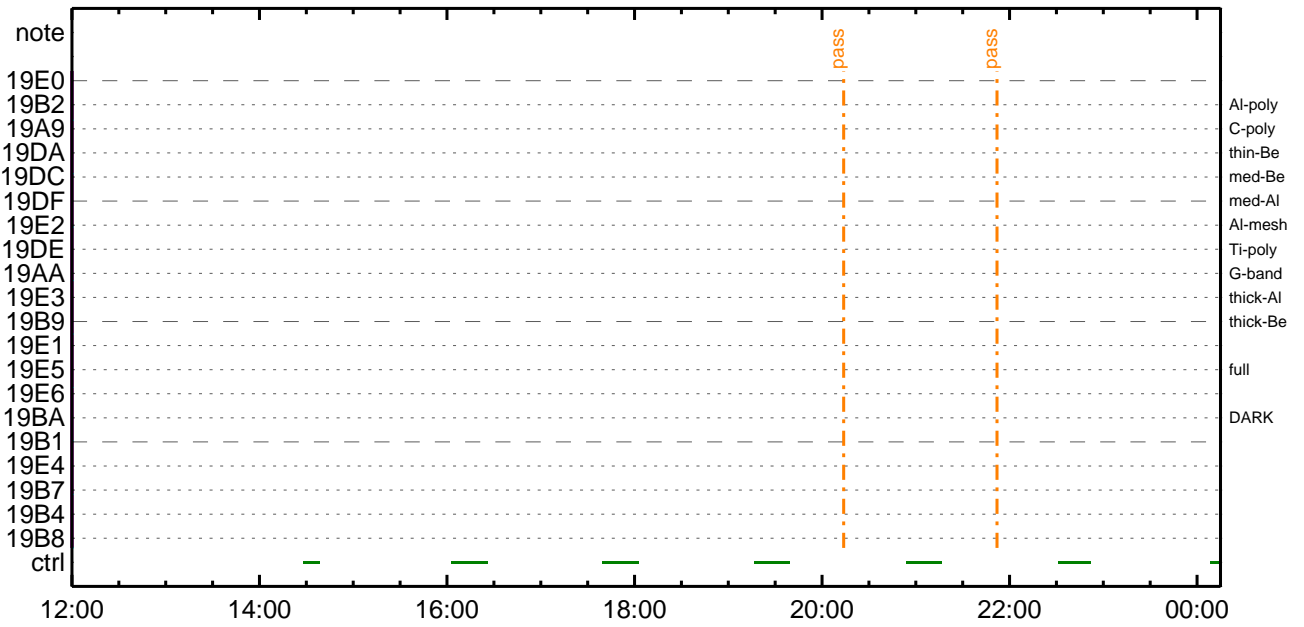
CMDI #0944 2013/12/05



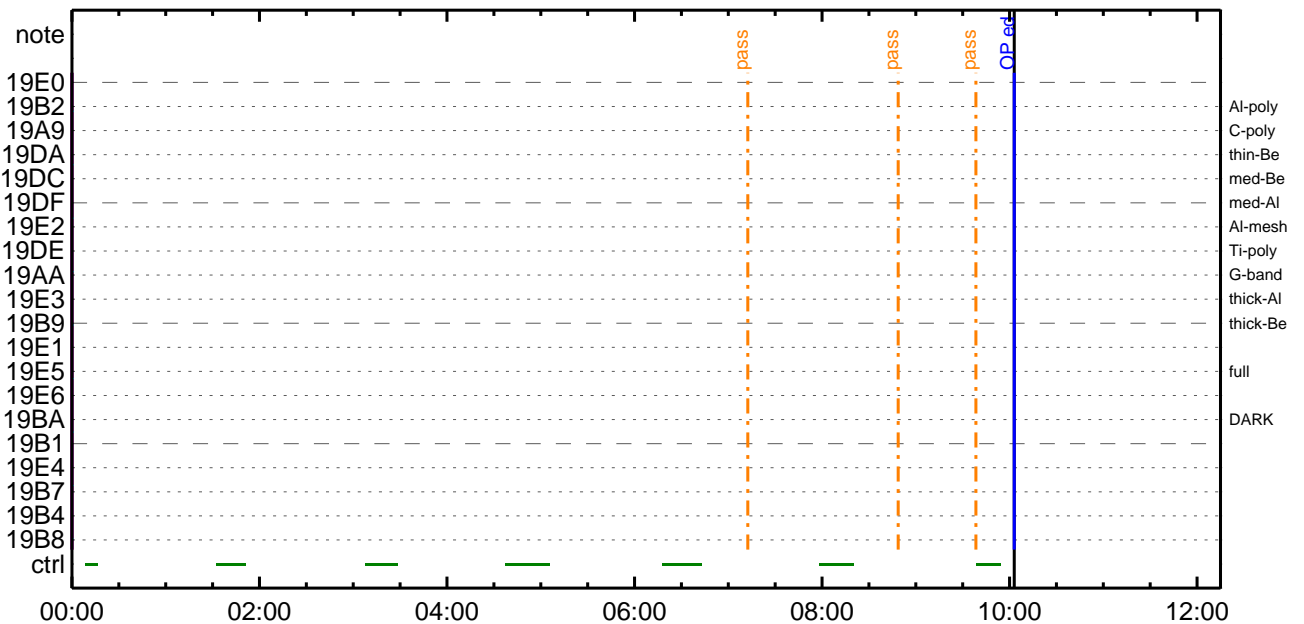
CMDI #0944 2013/12/06



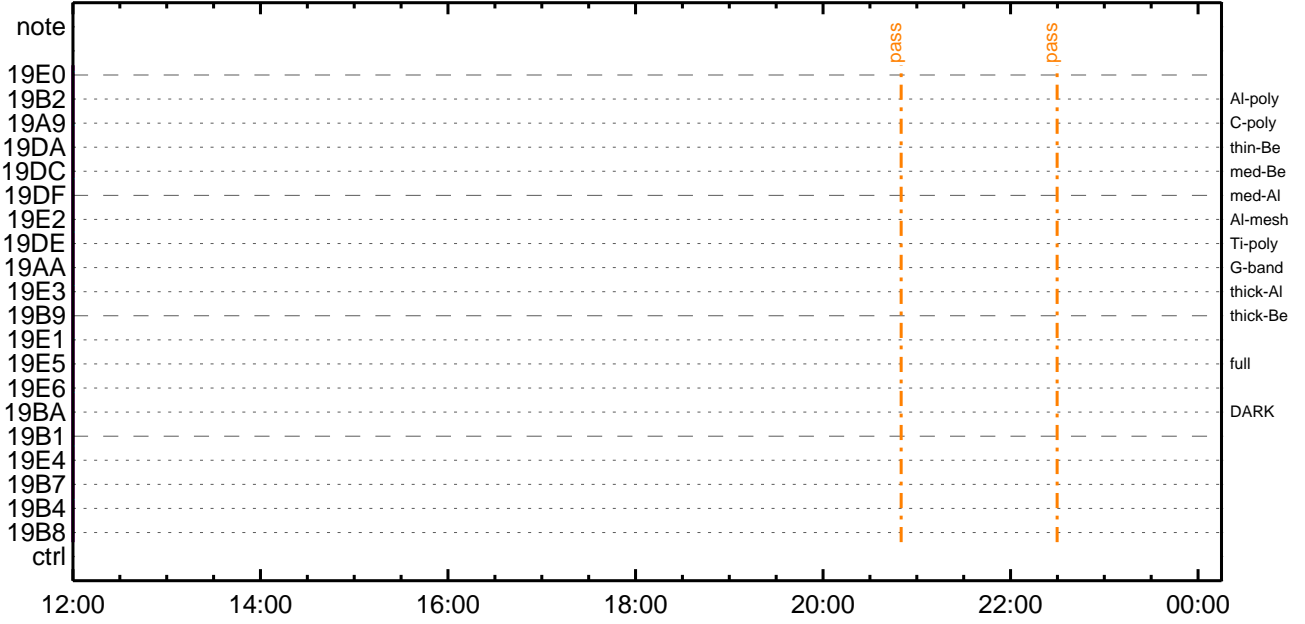
CMDI #0944 2013/12/06



CMDI #0944 2013/12/07



CMDI #0944 2013/12/07















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0096 + DC 07-F0 MDP_XRT_FLD_ENA
0097 BC (d8)
0098 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0099 BC (c8)
0100 + DC 07-F0 MDP_XRT_AEC_RESET
0101 BC (d0)
0102 + DC 07-F0 MDP_XRT_ARS_DIS
0103 BC (d5)
0104 + DC 07-F0 MDP_XRT_FLD_RESET
0105 BC (da)
0106 + DC 07-F0 MDP_XRT_QT_PROG_SET
0107 BC (c4 04)
0108 + DC 07-F0 MDP_XRT_FL_PROG_SET
0109 BC (c5 0d)
0110 . C. ----- Success Verify ? OK / NG ____
0111 C.
0112 C.
0113 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0114 C.
0115 +. DC 07-F0 MDP_XRT_MODE_OBSV
0116 BC (c2)
0117 +. TI 2013-12-03 10:18:02.0
0118 DC 07-F0 MDP_XRT_MODE_OBSV
0119 BC (c2)
0120 . C. ----- Success Verify ? OK / NG ____
0121 C.
0122 C. ***** XRT END *****
0123 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0124 +. DC 07-FC EIS_MODE_MANU
0125 BC (21 02)
0126 . C. Verify EIS in MANUAL mode
0127 . C. Estimated OBSTBL upload time is 16s
0128 C. *****
0129 C. EIS START OBSTBL LOAD
0130 C. *****
0131 . S. RAM ram-820:EIS_OBSTBL
0132 ( )
0133 +. DC 07-FC EIS_DUMP_OBSTBL
0134 BC (07 07 07 00 00 70 00)
0135 C.
0136 C. Execute, after the success of OBSTBL upload.
0137 C. Set EIS TI-commands
0138 +. TI 2013-12-03 10:18:50.0
0139 DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0142 C. *****
0143 C. EIS END OBSTBL LOAD
0144 C. *****
0145 C.
0146 . C. ***** MDP 'uÃîpî»ö¼ÝpËÄðp¹nèDCBC•x²è *****
0147 C. (%ã°iYÓYÁYËYÏYËYáYçYèpE¼p¼Ä»Üp¹nè)
0148 . S. DC-BC dcbc-402:DCBC
0149 (MDP_known_event)
0150 C.
0151 C.
0152 . C. ***** YÐY¹•ï Daily±;îÑpË'Øp¹nèDCBC•x²è *****
0153 . S. DC-BC dcbc-153:DCBC
0154 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0155 C.
0156 C.
0157 . C. ;ãLOS¥Á¥S¥Ä¥¹¼Ä»Ü;ã
0158 C.
0159 . C. ***** LOS *****
0160 C.

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Dec 03, 13 15:23

XRT\_OGLIST\_0944.chk

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\*\*\* OP Sequence for XRT \*\*\*

2013/12/03	10:28:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/03	10:28:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/12/03	10:29:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2013/12/03	10:29:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/12/03	10:29:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/12/03	10:29:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/12/03	10:29:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/12/03	10:29:24.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/03	10:29:26.0	XRT_QT_PROG_SET_441_OG [0x1b9]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2013/12/03	10:30:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/03	10:30:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/03	10:30:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/03	10:31:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2013/12/03	10:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/03	10:36:00.0	XRT_Custom_430_OG [0x1ae]			
2013/12/03	10:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/03	14:17:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/03	14:17:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/03	14:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/03	14:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/03	14:25:30.0	XRT_Custom_430_OG [0x1ae]			
2013/12/03	14:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/03	15:51:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/03	15:51:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/03	15:51:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/03	15:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/03	16:14:30.0	XRT_Custom_430_OG [0x1ae]			
2013/12/03	16:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/03	17:27:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/03	17:27:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/03	17:27:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/03	17:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/03	17:51:00.0	XRT_Custom_430_OG [0x1ae]			
2013/12/03	17:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/03	18:06:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/03	18:06:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/12/03	18:07:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/12/03	18:07:16.0	XRT_FLD_DIS_434_OG [0x1b2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/12/03	18:09:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/12/03	18:09:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/12/03	18:09:58.0	XRT_QT_PROG_SET_446_OG [0x1be]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2013/12/03	18:10:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/03	18:16:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/03	18:16:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/12/03	18:17:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2013/12/03	18:17:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/12/03	18:17:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/12/03	18:17:20.0	XRT_AEC_RESET_413_OG [0x19d]			

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2013/12/03	18:17:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
			MDP_XRT_ARS_DIS	1	07-F0	d5
2013/12/03	18:17:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/03	18:17:26.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2013/12/03	18:19:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2013/12/03	18:20:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/03	19:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/03	19:04:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/03	19:04:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/03	19:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/03	19:28:00.0	XRT_Custom_430_OG [0x1ae]				
2013/12/03	19:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/03	20:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/03	20:42:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/03	20:42:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/03	20:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/03	21:05:00.0	XRT_Custom_430_OG [0x1ae]				
2013/12/03	21:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/03	22:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/03	22:19:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/03	22:19:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/03	22:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/03	22:40:30.0	XRT_Custom_430_OG [0x1ae]				
2013/12/03	22:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/03	23:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/03	23:56:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/03	23:56:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/03	23:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/04	00:05:30.0	XRT_Custom_430_OG [0x1ae]				
2013/12/04	00:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/04	01:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/04	01:21:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/04	01:21:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/04	01:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/04	01:39:30.0	XRT_Custom_430_OG [0x1ae]				
2013/12/04	01:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/04	02:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/04	02:55:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/04	02:55:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/04	02:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/04	03:16:30.0	XRT_Custom_430_OG [0x1ae]				
2013/12/04	03:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/04	04:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/04	04:25:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/04	04:25:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/04	04:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/04	04:54:00.0	XRT_Custom_430_OG [0x1ae]				
2013/12/04	04:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/04	05:37:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/04	05:37:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/12/04	05:38:00.0	AOCS_Ore-point_Start_2_OG [0x098]				



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2013/12/04	05:38:16.0	XRT_FLD_DIS_434_OG [0x1b2]	AOCU_NM	5	02-76	00	00	00	00	00
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2013/12/04	05:40:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]		1	07-F0	c9				
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2013/12/04	05:40:56.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2013/12/04	05:40:58.0	XRT_QT_PROG_SET_446_OG [0x1be]		2	07-F0	c4	11			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	11			
2013/12/04	05:41:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2013/12/04	05:47:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2013/12/04	05:47:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2013/12/04	05:48:00.0	AOCs_OrE-point_Start_1_OG [0x097]		5	02-76	03	00	00	00	00
		AOCU_NM		5	02-76	03	00	00	00	00
2013/12/04	05:48:16.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2013/12/04	05:48:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2013/12/04	05:48:20.0	XRT_AEC_RESET_413_OG [0x19d]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2013/12/04	05:48:22.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2013/12/04	05:48:24.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2013/12/04	05:48:26.0	XRT_QT_PROG_SET_441_OG [0x1b9]		2	07-F0	c4	07			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	07			
2013/12/04	05:50:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]		2	07-F0	c5	0d			
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	0d			
2013/12/04	05:51:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2013/12/04	06:05:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2013/12/04	06:05:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2013/12/04	06:05:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2013/12/04	06:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2013/12/04	06:31:30.0	XRT_Custom_430_OG [0x1ae]								
2013/12/04	06:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2013/12/04	07:46:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2013/12/04	07:46:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2013/12/04	07:46:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2013/12/04	07:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2013/12/04	08:08:30.0	XRT_Custom_430_OG [0x1ae]								
2013/12/04	08:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2013/12/04	09:26:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2013/12/04	09:26:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2013/12/04	09:26:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2013/12/04	09:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2013/12/04	09:43:00.0	XRT_Custom_430_OG [0x1ae]								
2013/12/04	09:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2013/12/04	14:51:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2013/12/04	14:51:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2013/12/04	14:51:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2013/12/04	14:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2013/12/04	15:04:00.0	XRT_Custom_430_OG [0x1ae]								
2013/12/04	15:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2013/12/04	16:27:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2013/12/04	16:27:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2013/12/04	16:27:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2013/12/04	16:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2013/12/04	16:50:30.0	XRT_Custom_430_OG [0x1ae]								
2013/12/04	16:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2013/12/04	17:40:24.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2013/12/04	17:40:26.0	XRT_FOCUS_POSITION_403_OG [0x193]		4	07-F8	22	ff	aa	00	
		XRT_FOCUS_POSITION		4	07-F8	22	ff	aa	00	
2013/12/04	17:40:30.0	AOCs_OrE-point_Start_2_OG [0x098]								

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2013/12/04	17:40:46.0	XRT_FLD_DIS_434_OG [0x1b2]	AOCU_NM	5	02-76	00	00	00	00	00
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2013/12/04	17:43:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]		1	07-F0	c9				
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2013/12/04	17:43:26.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2013/12/04	17:43:28.0	XRT_QT_PROG_SET_446_OG [0x1be]		2	07-F0	c4	11			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	11			
2013/12/04	17:43:30.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2013/12/04	17:50:24.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2013/12/04	17:50:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2013/12/04	17:50:30.0	AOCS_OrE-point_Start_1_OG [0x097]		5	02-76	03	00	00	00	00
		AOCU_NM		5	02-76	03	00	00	00	00
2013/12/04	17:50:46.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2013/12/04	17:50:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2013/12/04	17:50:50.0	XRT_AEC_RESET_413_OG [0x19d]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2013/12/04	17:50:52.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2013/12/04	17:50:54.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2013/12/04	17:50:56.0	XRT_QT_PROG_SET_414_OG [0x19e]		2	07-F0	c4	04			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	04			
2013/12/04	17:53:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]		2	07-F0	c5	0d			
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	0d			
2013/12/04	17:53:30.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2013/12/04	18:04:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2013/12/04	18:04:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2013/12/04	18:04:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2013/12/04	18:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2013/12/04	18:27:30.0	XRT_Custom_430_OG [0x1ae]								
2013/12/04	18:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2013/12/04	19:41:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2013/12/04	19:41:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2013/12/04	19:41:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2013/12/04	19:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2013/12/04	20:04:30.0	XRT_Custom_430_OG [0x1ae]								
2013/12/04	20:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2013/12/04	21:18:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2013/12/04	21:18:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2013/12/04	21:18:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2013/12/04	21:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2013/12/04	21:41:00.0	XRT_Custom_430_OG [0x1ae]								
2013/12/04	21:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2013/12/04	22:56:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2013/12/04	22:56:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2013/12/04	22:56:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2013/12/04	22:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2013/12/04	23:14:30.0	XRT_Custom_430_OG [0x1ae]								
2013/12/04	23:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2013/12/05	00:33:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2013/12/05	00:33:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2013/12/05	00:33:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2013/12/05	00:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2013/12/05	00:40:00.0	XRT_Custom_430_OG [0x1ae]								
2013/12/05	00:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2013/12/05	01:55:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2013/12/05	01:55:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2013/12/05	01:55:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				

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2013/12/05	01:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/12/05	02:15:30.0	XRT_Custom_430_OG [0x1ae]						
2013/12/05	02:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/05	03:27:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/05	03:27:32.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/05	03:27:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/12/05	03:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/12/05	03:53:00.0	XRT_Custom_430_OG [0x1ae]						
2013/12/05	03:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/05	05:03:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/05	05:03:02.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/05	05:03:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/12/05	05:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/12/05	05:30:30.0	XRT_Custom_430_OG [0x1ae]						
2013/12/05	05:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/05	05:58:54.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/05	05:58:56.0	XRT_FOCUS_POSITION_403_OG [0x193]						
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/12/05	05:59:00.0	AOCS_Ore-point_Start_2_OG [0x098]						
			AOCU_NM	5	02-76	00 00 00 00 00		
2013/12/05	05:59:16.0	XRT_FLD_DIS_434_OG [0x1b2]						
			MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/12/05	06:01:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/12/05	06:01:56.0	XRT_ARS_DIS_423_OG [0x1a7]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/12/05	06:01:58.0	XRT_QT_PROG_SET_446_OG [0x1be]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2013/12/05	06:02:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/05	06:08:54.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/05	06:08:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/12/05	06:09:00.0	AOCS_Ore-point_Start_1_OG [0x097]						
			AOCU_NM	5	02-76	03 00 00 00 00		
2013/12/05	06:09:16.0	XRT_FLD_ENA_411_OG [0x19b]						
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/12/05	06:09:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/12/05	06:09:20.0	XRT_AEC_RESET_413_OG [0x19d]						
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/12/05	06:09:22.0	XRT_ARS_DIS_423_OG [0x1a7]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/12/05	06:09:24.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/05	06:09:26.0	XRT_QT_PROG_SET_441_OG [0x1b9]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 07		
2013/12/05	06:11:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]						
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d		
2013/12/05	06:12:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/05	06:43:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/05	06:43:02.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/05	06:43:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/12/05	06:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/12/05	07:07:30.0	XRT_Custom_430_OG [0x1ae]						
2013/12/05	07:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/05	08:23:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/05	08:23:02.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/05	08:23:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/12/05	08:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/12/05	08:44:00.0	XRT_Custom_430_OG [0x1ae]						
2013/12/05	08:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/05	10:03:54.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/05	10:04:00.0	AOCS_Ore-point_Start_2_OG [0x098]						
			AOCU_NM	5	02-76	00 00 00 00 00		