

XRT Timeline to be uploaded on 2013/12/07

Period: 2013/12/07 10:03:00 - 2013/12/12 09:50:00

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Normal mode

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XOB #162D: Full Sun - CME Watch - C/Poly + Thin-Be + Med-Be - 4x4 - AEC2												
Term	Pointing (x, y)		Comment									
12/07 10:16:00 - 12/07 10:54:54	Fixed (-15.0, -850.0)	# OP start + 10min HOP 239 (1/15)									
12/07 10:58:00 - 12/07 11:29:54	Fixed (-15.0, -750.0)	(2/15)									
12/07 11:33:00 - 12/07 12:04:54	Fixed (-15.0, -650.0)	(3/15)									
12/07 12:08:00 - 12/07 12:39:54	Fixed (-15.0, -550.0)	(4/15)									
12/07 12:43:00 - 12/07 13:14:54	Fixed (-15.0, -450.0)	(5/15)									
12/07 13:18:00 - 12/07 13:49:54	Fixed (-15.0, -350.0)	(6/15)									
12/07 13:53:00 - 12/07 14:24:54	Fixed (-15.0, -250.0)	(7/15)									
12/07 14:28:00 - 12/07 15:23:00	Fixed (-15.0, -20.0)	(8/15)									
12/07 15:28:00 - 12/07 15:59:54	Fixed (-15.0, 210.0)	(9/15)									
12/07 16:03:00 - 12/07 16:38:30	Fixed (-15.0, 310.0)	(10/15)									
12/07 17:03:30 - 12/07 17:52:24	Fixed (-15.0, 410.0)	(11/15)									
12/07 18:03:30 - 12/07 18:16:00	Fixed (0.0, 0.0)	synoptic, shifted -7.5 min									
12/07 18:43:00 - 12/07 19:14:54	Fixed (-15.0, 510.0)	(12/15)									
12/07 19:18:00 - 12/07 19:53:00	Fixed (-15.0, 610.0)	(13/15)									
12/07 20:23:00 - 12/07 20:54:54	Fixed (-15.0, 710.0)	(14/15)									
12/07 20:58:00 - 12/07 21:29:54	Fixed (-15.0, 810.0)	(15/15)									
12/08 08:58:00 - 12/08 15:38:30	Fixed (-15.0, 910.0)	HOP 240 (north pole)									
PROG= 05 Inf.-time(s)												
└ Subr= 1 4-time(s) 480.0sec												
└ Seqn= 58 1-time(s) 60.0sec												
└ C-poly/Open med-Be/Open close Safe Norm 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└ Seqn= 79 1-time(s) 60.0sec												
└ thin-Be/Open med-Be/Open close Safe Norm 4.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└ Seqn= 96 1-time(s) 60.0sec												
└ med-Be/Open med-Be/Open close Safe Norm 22.6s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec												
└ Subr= 2 1-time(s) 60.0sec												
└ Seqn= 60 1-time(s) 30.0sec												
└ C-poly/Open C-poly/Open close Safe Dark 1.00s Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #19DA: Synoptic Q95 2x2 - Al/mesh(5/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/362/1443) + Thin-Be												
Term	Pointing (x, y)		Comment									
12/07 17:56:54 - 12/07 18:02:54	Fixed (0.0, 0.0)	synoptic, shifted -7.5 min									
12/08 06:03:00 - 12/08 06:09:54	Fixed (0.0, 0.0)	synoptic									
12/08 17:59:00 - 12/08 18:05:54	Fixed (0.0, 0.0)	synoptic, shifted -4.0 min									
12/09 06:31:30 - 12/09 06:38:24	Fixed (0.0, 0.0)	synoptic, shifted 28.5 min									
12/09 18:18:00 - 12/09 18:24:54	Fixed (0.0, 0.0)	synoptic, shifted 15.0 min									
12/10 06:03:30 - 12/10 06:10:24	Fixed (0.0, 0.0)	synoptic, shifted 0.5 min									
PROG= 17 1-time(s)												
└ Subr= 1 1-time(s) 12.0sec												
└ Seqn= 84 1-time(s) 4.0sec												
└ Open/Al-mesh Open/Al-mesh close Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└ Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└ Seqn= 5 1-time(s) 2.0sec												
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec												
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec												
└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec												
└ Seqn= 86 1-time(s) 4.0sec												
└ Open/Ti-poly Open/Ti-poly close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└ Open/Ti-poly Open/Ti-poly close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└ Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└ Seqn= 95 1-time(s) 2.0sec												
└ thin-Be/Open thin-Be/Open close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└ thin-Be/Open thin-Be/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└ thin-Be/Open thin-Be/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
└ Seqn= 6 1-time(s) 2.0sec												
└ Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec												
└ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #19E4: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)												
Term	Pointing (x, y)		Comment									
12/07 21:54:00 - 12/08 05:59:54	Track (797.6, -340.5)	@ 12/07 21:30:00 AR11909									
12/08 06:13:00 - 12/08 08:35:30	Track (831.4, -340.1)	@ 12/08 06:10:00 AR11909									
12/08 16:03:00 - 12/08 17:55:54	Track (862.6, -339.8)	@ 12/08 15:55:00 AR11909									
12/08 18:09:00 - 12/08 21:59:54	Track (868.6, -339.8)	@ 12/08 18:06:00 AR11909									
12/08 22:03:00 - 12/09 03:59:54	Fixed (888.7, -339.7)	AR 11909 (IRIS)									

PROG= 04 Inf.-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 8 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec													
Seqn= 24 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 42 4-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 62 10-time(s) 180.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19E6: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)

Term	Pointing (x, y)	Comment
12/09 04:03:00 - 12/09 06:28:24	Track (594.4, -242.4) @ 12/09 04:00:00	Observe AR 11916
12/09 06:41:30 - 12/09 17:51:30	Track (612.8, -242.3) @ 12/09 06:38:30	AR 11916
12/09 18:28:00 - 12/10 06:00:24	Track (690.0, -241.7) @ 12/09 18:25:00	AR 11916
12/10 06:13:30 - 12/10 09:51:00	Track (758.2, -241.3) @ 12/10 06:10:30	AR 11916

PROG= 07 Inf.-time(s)													
Subr= 1 1-time(s) 2.0sec													
Seqn= 8 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec													
Seqn= 24 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 42 4-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 62 8-time(s) 270.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

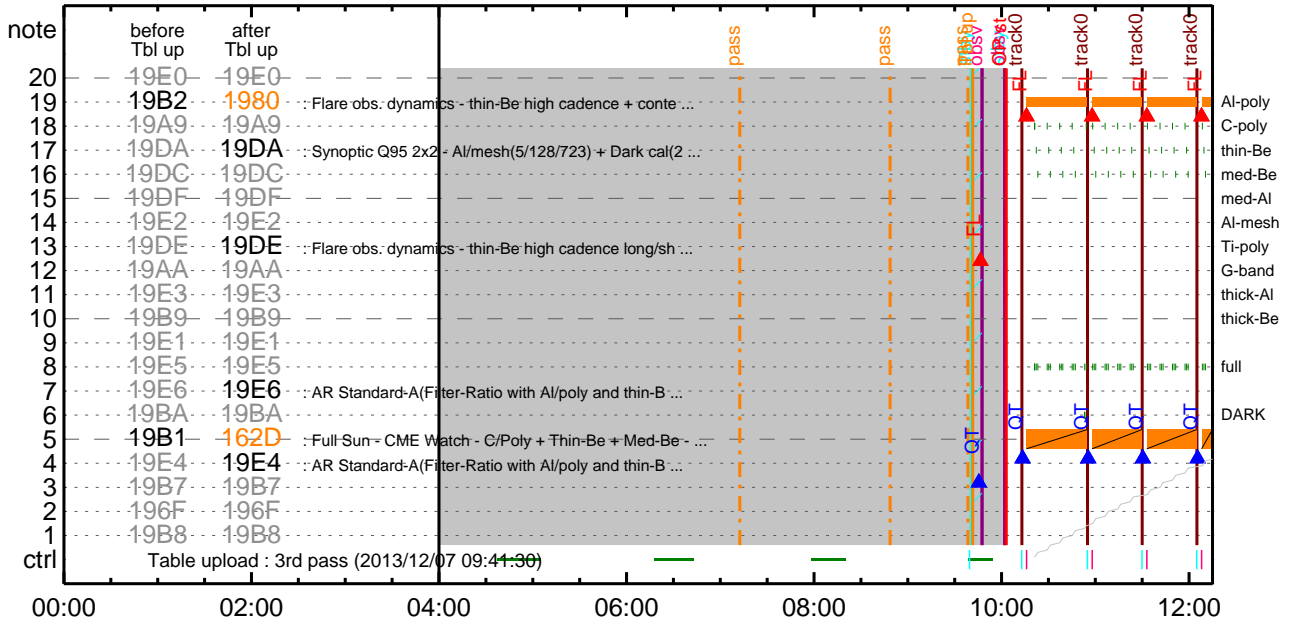
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XOB #1980: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops-2

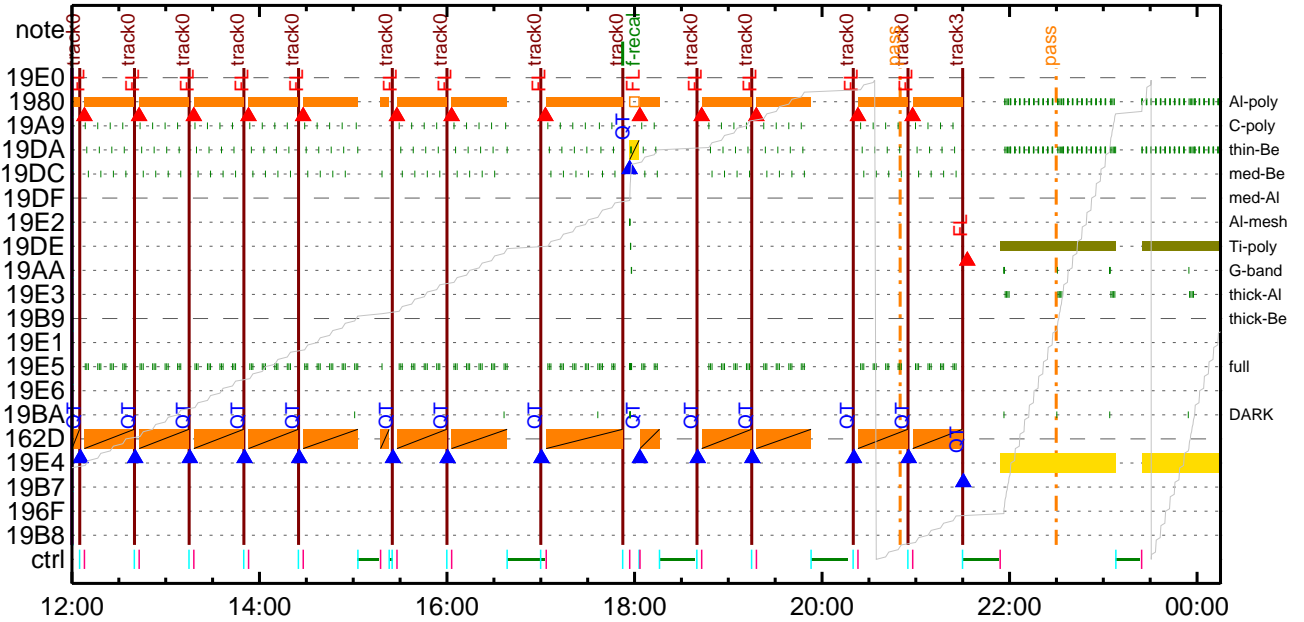
Term	Pointing (x, y)	Comment
12/07 10:16:00 - 12/07 10:54:54	Fixed (-15.0, -850.0)	# OP start + 10min HOP 239 (1/15)
12/07 10:58:00 - 12/07 11:29:54	Fixed (-15.0, -750.0)	(2/15)
12/07 11:33:00 - 12/07 12:04:54	Fixed (-15.0, -650.0)	(3/15)
12/07 12:08:00 - 12/07 12:39:54	Fixed (-15.0, -550.0)	(4/15)
12/07 12:43:00 - 12/07 13:14:54	Fixed (-15.0, -450.0)	(5/15)
12/07 13:18:00 - 12/07 13:49:54	Fixed (-15.0, -350.0)	(6/15)
12/07 13:53:00 - 12/07 14:24:54	Fixed (-15.0, -250.0)	(7/15)
12/07 14:28:00 - 12/07 15:23:00	Fixed (-15.0, -20.0)	(8/15)
12/07 15:28:00 - 12/07 15:59:54	Fixed (-15.0, 210.0)	(9/15)
12/07 16:03:00 - 12/07 16:38:30	Fixed (-15.0, 310.0)	(10/15)
12/07 17:03:30 - 12/07 17:52:24	Fixed (-15.0, 410.0)	(11/15)
12/07 18:03:30 - 12/07 18:16:00	Fixed (0.0, 0.0)	synoptic, shifted -7.5 min
12/07 18:43:00 - 12/07 19:14:54	Fixed (-15.0, 510.0)	(12/15)
12/07 19:18:00 - 12/07 19:53:00	Fixed (-15.0, 610.0)	(13/15)
12/07 20:23:00 - 12/07 20:54:54	Fixed (-15.0, 710.0)	(14/15)
12/07 20:58:00 - 12/07 21:29:54	Fixed (-15.0, 810.0)	(15/15)
12/08 08:58:00 - 12/08 15:38:30	Fixed (-15.0, 910.0)	HOP 240 (north pole)

PROG= 19 15-time(s)													
Subr= 1 45-time(s) 10.0sec													
Seqn= 9 1-time(s) 2.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 10.0sec													
Seqn= 10 1-time(s) 2.0sec													
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec													
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

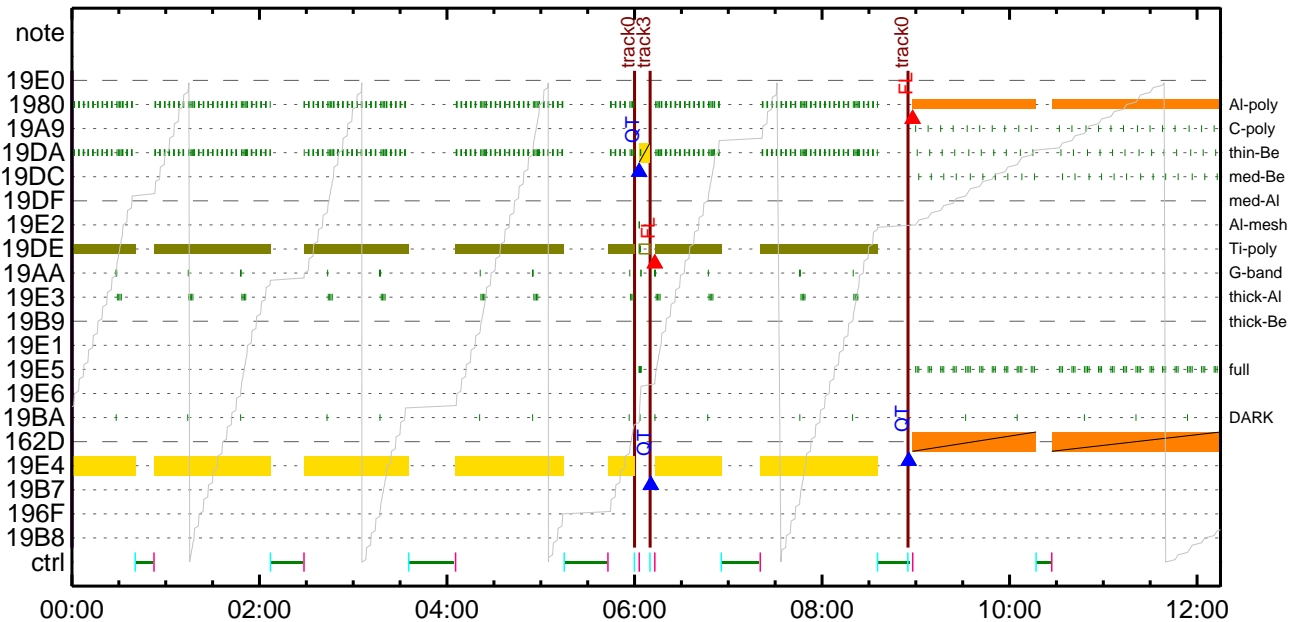
CMDI #0953 2013/12/07



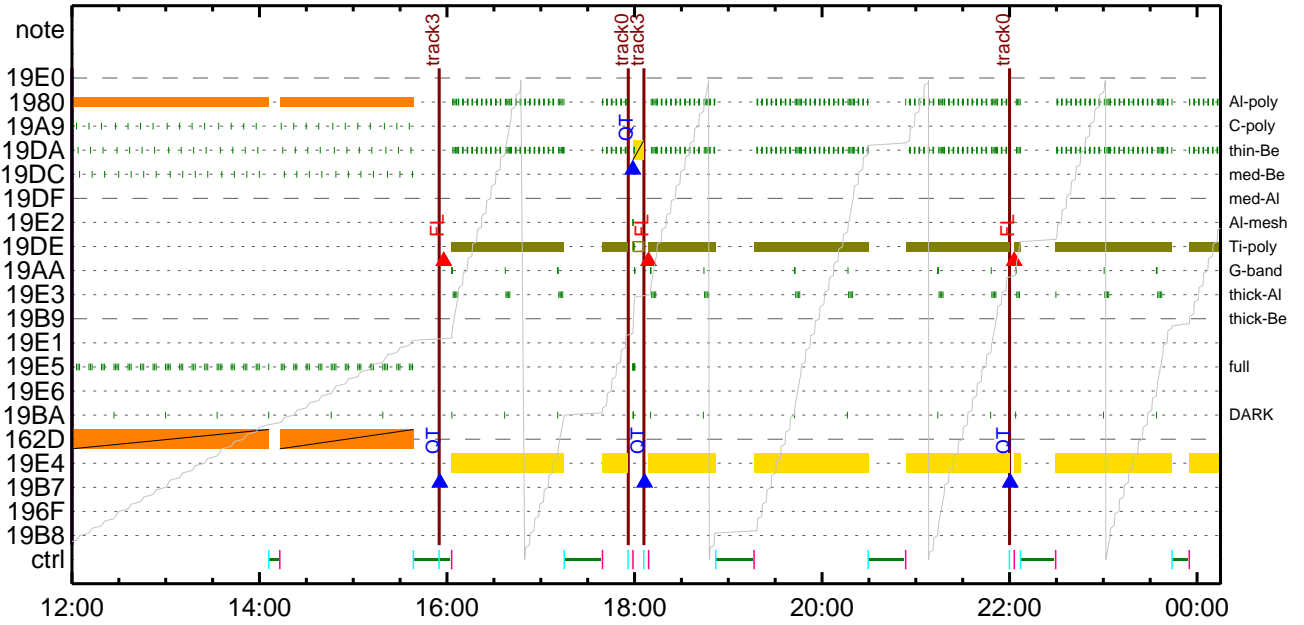
CMDI #0953 2013/12/07



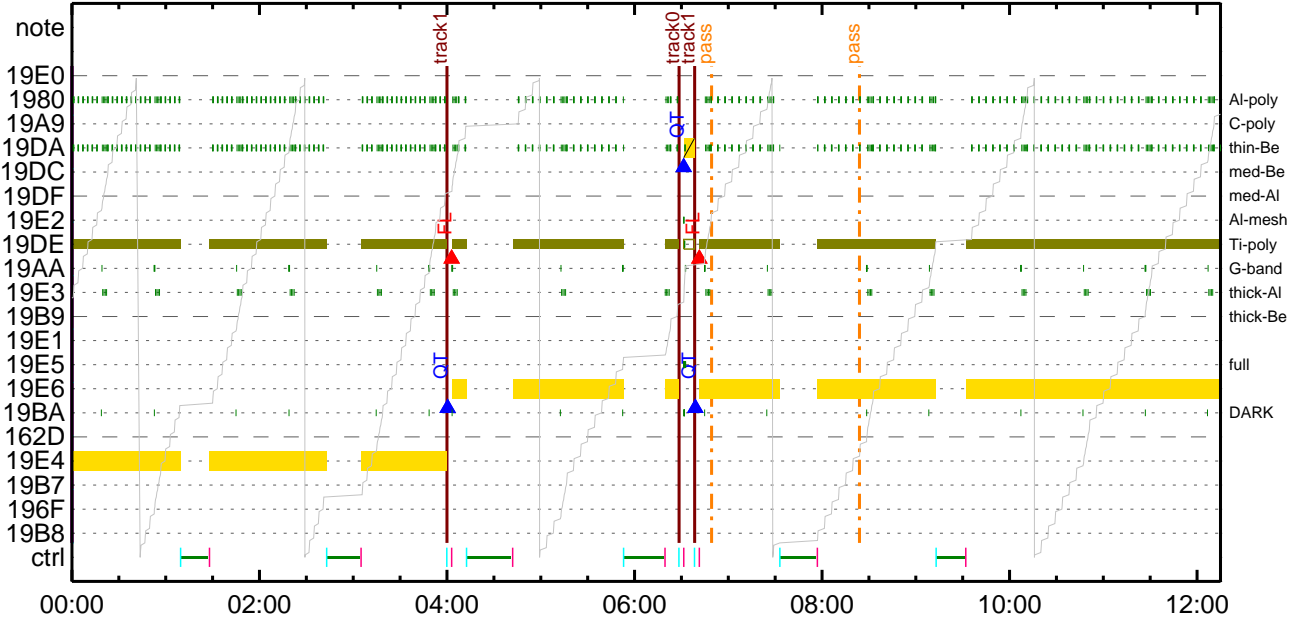
CMDI #0953 2013/12/08



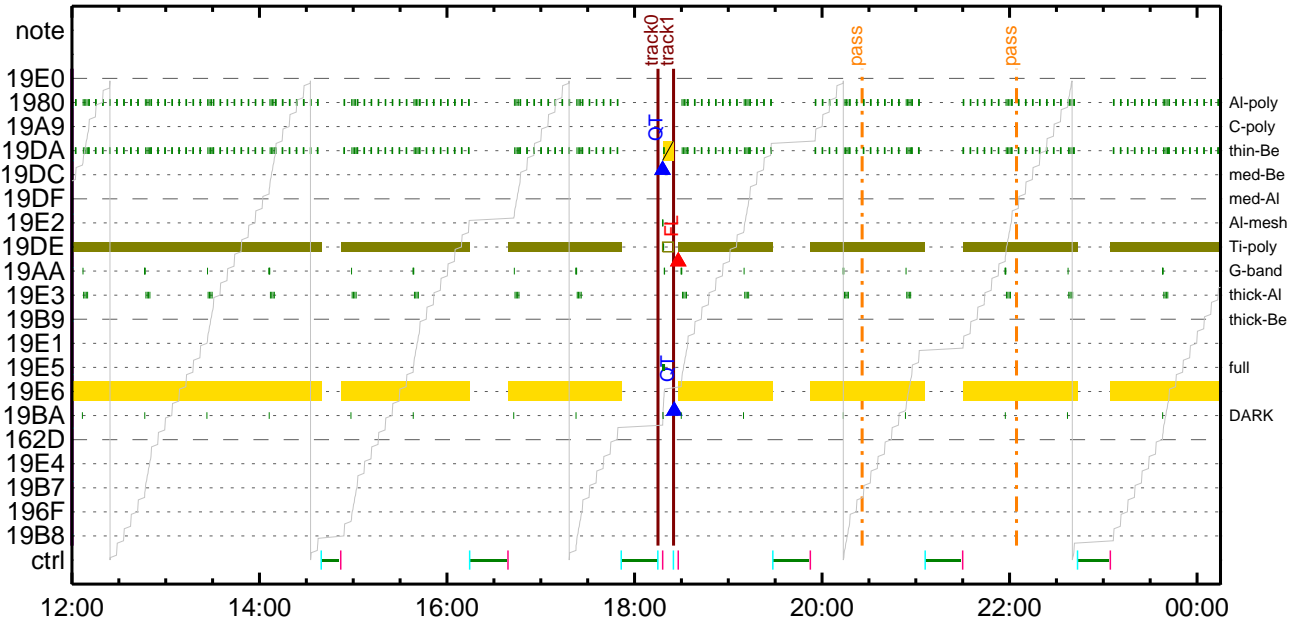
CMDI #0953 2013/12/08



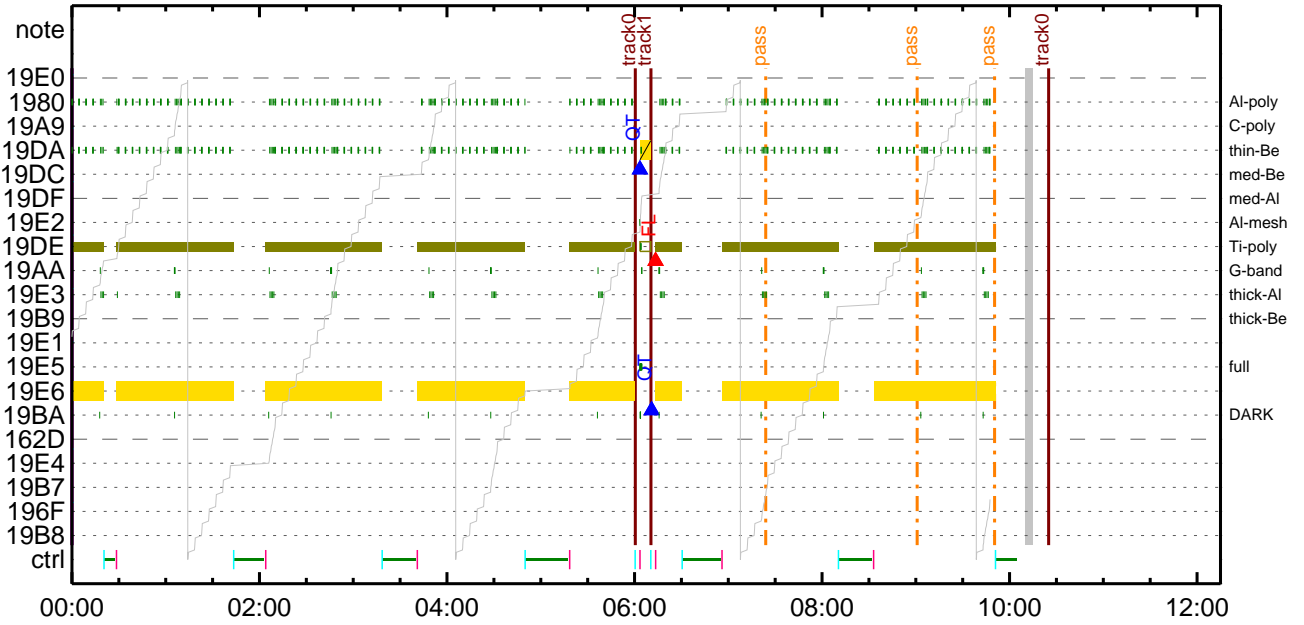
CMDI #0953 2013/12/09



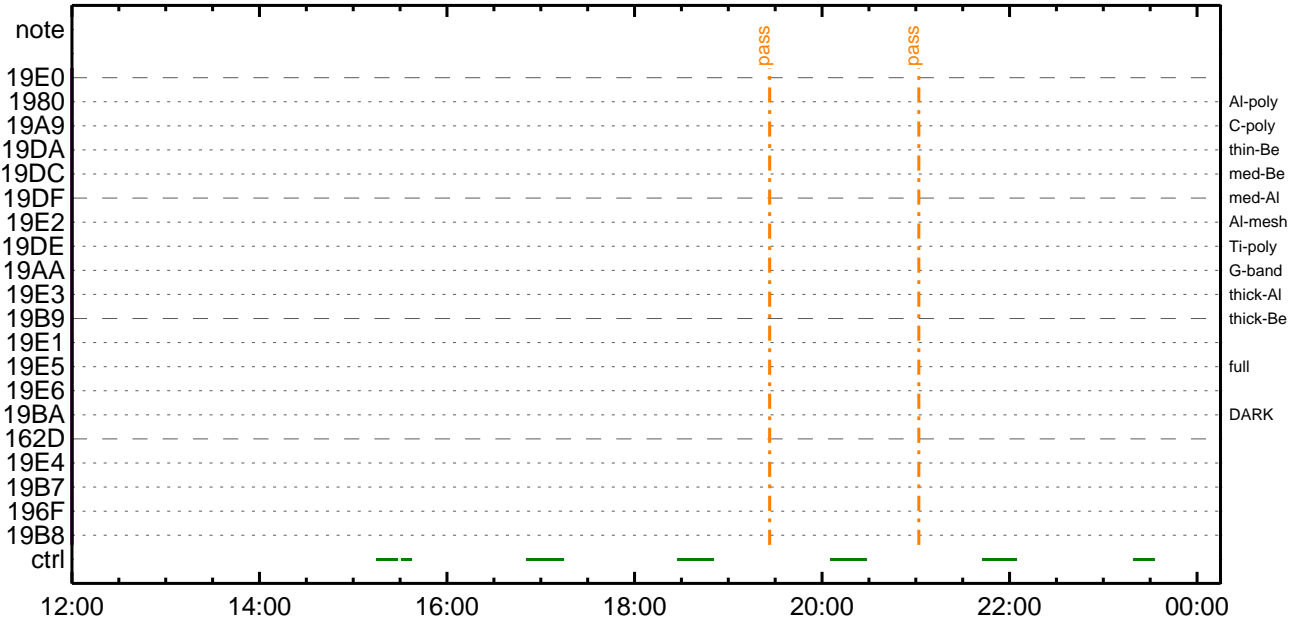
CMDI #0953 2013/12/09



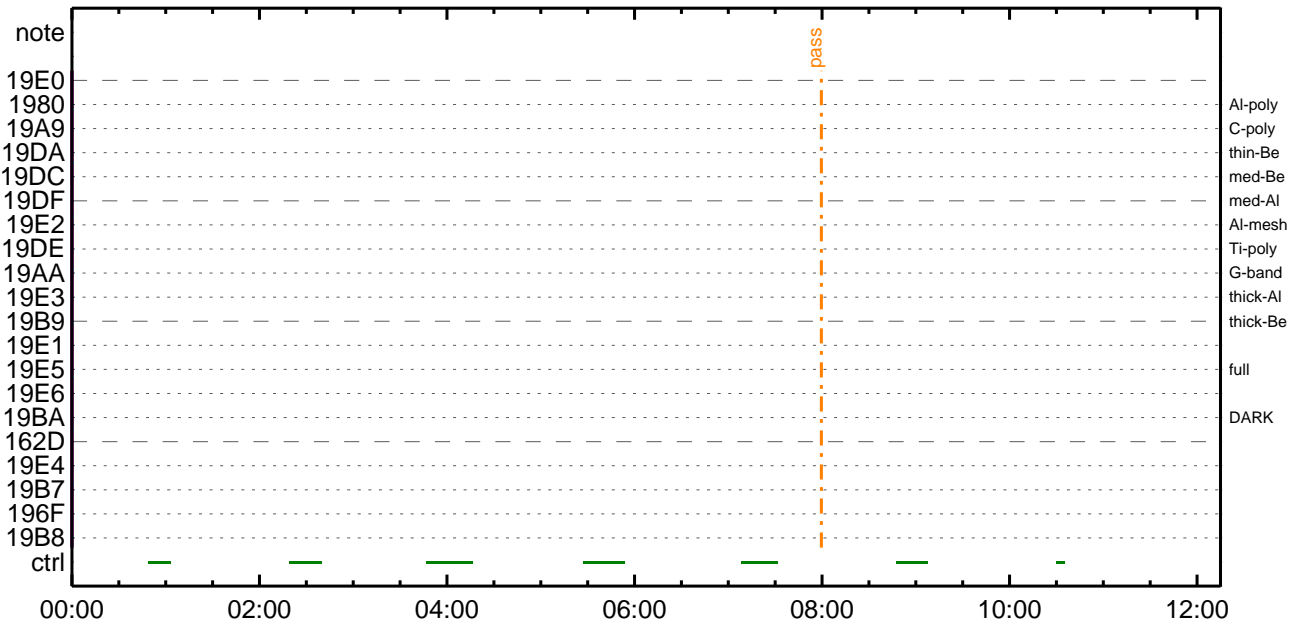
CMDI #0953 2013/12/10



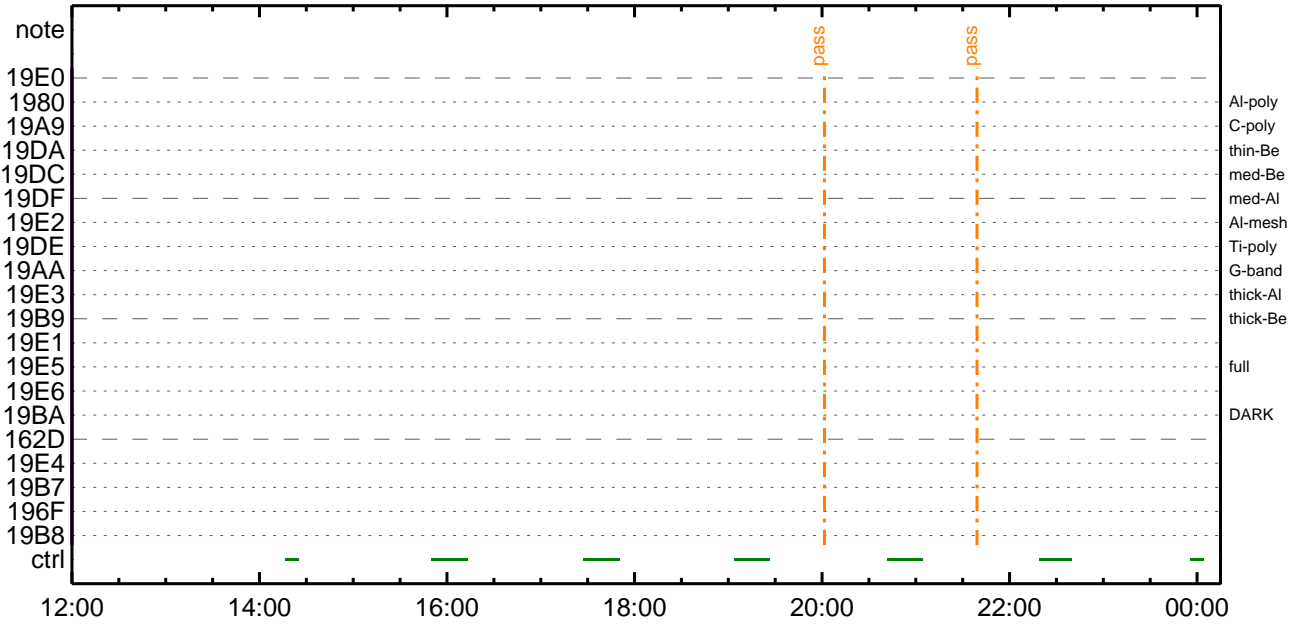
CMDI #0953 2013/12/10



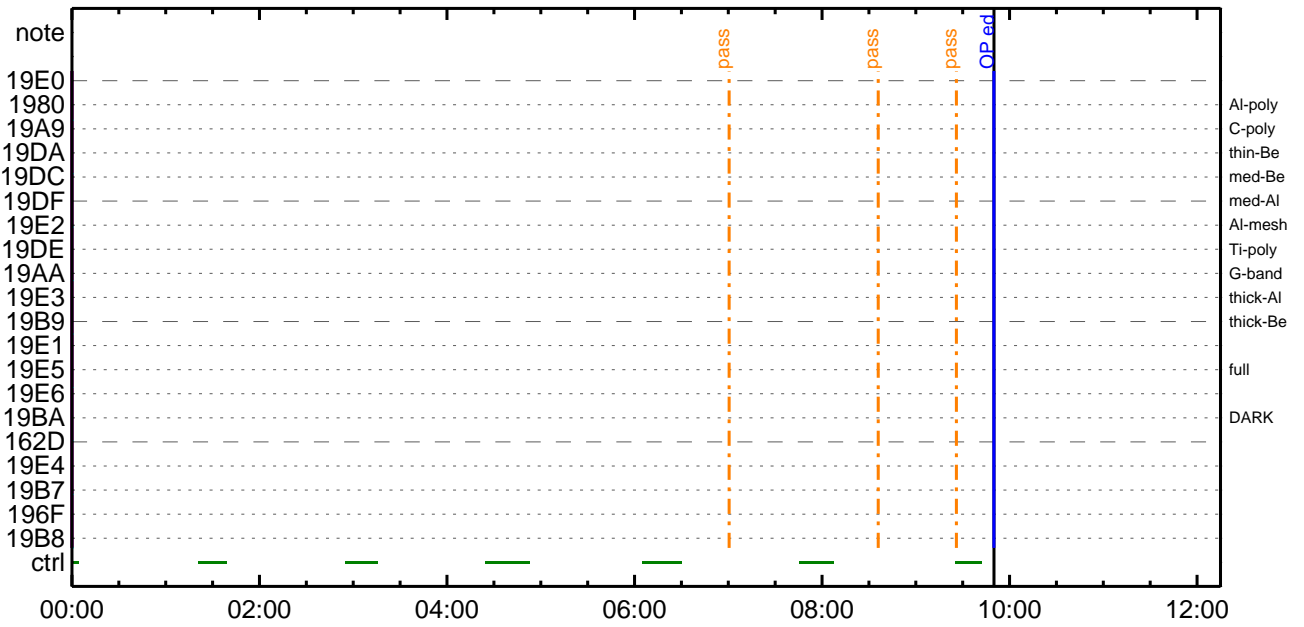
CMDI #0953 2013/12/11



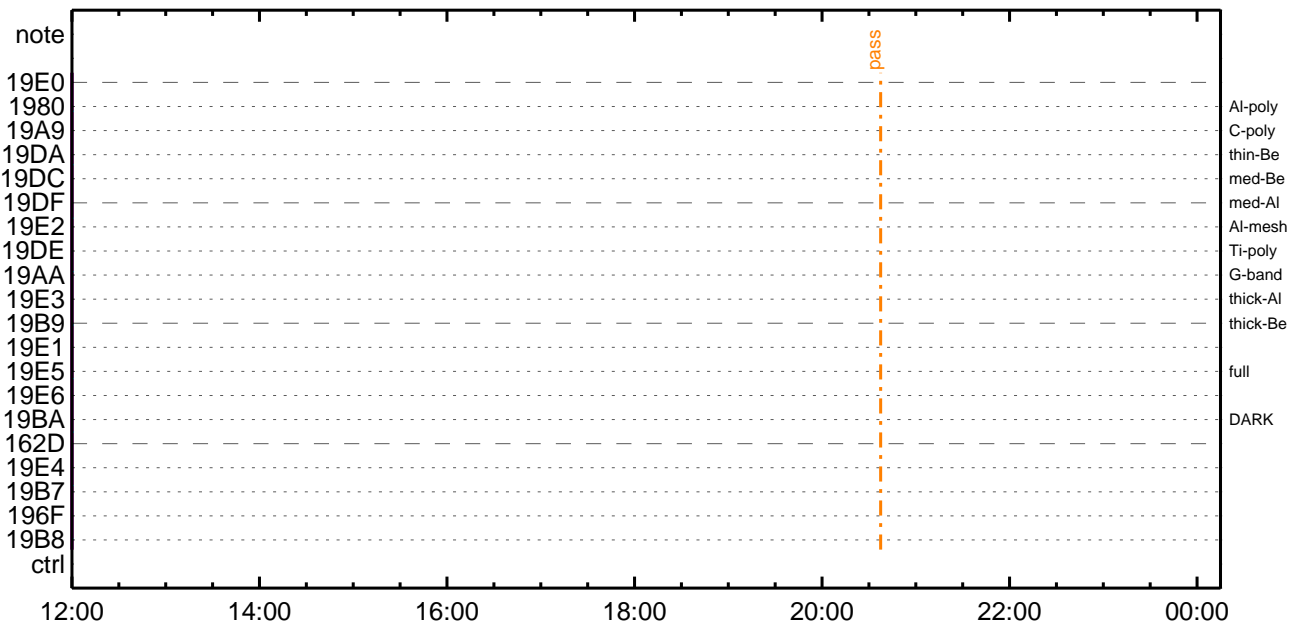
CMDI #0953 2013/12/11



CMDI #0953 2013/12/12



CMDI #0953 2013/12/12




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;aOP/OGY1;4YE;a
0103 S. OP op-086:OP
0104 ( )
0105 S. OG og-086:OG
0106 ( )
0107 C.
0108 C. ;aNMOG&OPf^°eYAYOX;a
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0120 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½^aÎ»oð³ÎÇ§
0125 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG□Î¼Ê¹Ç•ë²İOKoð³ÎÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0139 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½^aÎ»oð³ÎÇ§
0144 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG□Î¼Ê¹Ç•ë²İOKoð³ÎÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0158 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½^aÎ»oð³ÎÇ§
0163 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG,RAM ID=OP□Î¼Ê¹Ç•ë²İOKoð³ÎÇ§
0165 C.
0166 C. ***** oÊ²¼oİ¼Ä´¶Ā°oÊĒ¬oĀ÷¿@ (¼āµ-YAYOXx½^e¼çoðĀŌĀæoÇ¼^a¬°oÊ¼i¹çoçoā) *****
0167 C. DHUYâ;¼YE;Ê¼Y½;Yi;¼YE;Ēoðİā¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0171 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE |§ OPOG UPLOAD□-Ā÷¿@NGuİ¼i¹Ç;ç°Ê²¼oİTI-CMDĀ÷¿@□İ¼Ā¹Ō□•□Ē□□□³□Ē;f
0180 C. □Ē□¿;çSET□ĒDUMP□İĒ±°iYNY¹□Ç¹Ō□|□³□Ē;f
0181 C.
0182 C. TIY³YpYóYĒoðĀĀİ¿(UT)
0183 +. TI 2013-12-07 09:58:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-12-07 09:58:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-12-07 09:58:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-088 2013-12-07 12:01:46 130 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÁ•µ°È»Í×ÁÇ¿ÍYçYÁY×Yí;¼YÉ;ÈÈ¿µ•ííÉ;ÈÈ¿°ÇÓã•¿¿¼í¹ç¿Í;çÁ®, ù¿¹ãÈ¿ãÇÁ+¿®ã•¿È¿¿¿³¿È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-281:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2013-12-07 10:02:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG_____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR _____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 06 06)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 85 83 08 08)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 20)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 20 08)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 80 80 08 20)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0f 80 80 06 06)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 10 80 80 08 08)
0088 + DC 07-F0 MDP_XRT_FLD_ENA
0089 BC (d8)
0090 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0091 BC (c8)
0092 + DC 07-F0 MDP_XRT_AEC_RESET
0093 BC (d0)
0094 + DC 07-F0 MDP_XRT_ARS_DIS
0095 BC (d5)
```

```
0096 + DC 07-F0 MDP_XRT_FLD_RESET
0097 BC (da)
0098 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0099 BC (c4 04)
0100 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0101 BC (c5 0d)
0102 . C. ----- Success Verify ? OK / NG ____
0103 C.
0104 C.
0105 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0106 C.
0107 +. DC 07-F0 MDP_XRT_MODE_OBSV
0108 BC (c2)
0109 +. TI 2013-12-07 10:02:02.0
0110 DC 07-F0 MDP_XRT_MODE_OBSV
0111 BC (c2)
0112 . C. ----- Success Verify ? OK / NG ____
0113 C.
0114 C. ***** XRT END *****
0115 C.
0116 . C. ***** MDP 'úÃîâî»ö¼ÝðËÂð¹ñèDCBC•x²è *****
0117 C. (%ã°îÿÓÿÄÿËÿÏÿËÿáÿçÿèñ¼ññ¼Ã»Ûñ¹ñè)
0118 . S. DC-BC dcbc-402:DCBC
0119 (MDP_known_event)
0120 C.
0121 C.
0122 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎÑñË´Øñ¹ñèDCBC•x²è *****
0123 . S. DC-BC dcbc-153:DCBC
0124 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0125 C.
0126 C.
0127 . C. ;ãLOSÿÁÿ§ÿÄÿ-¼Ã»Û;ã
0128 C.
0129 . C. ***** LOS *****
0130 C.
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*** OP Sequence for XRT ***

2013/12/07	10:12:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/07	10:12:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/12/07	10:13:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 4b 8d 01 58		
2013/12/07	10:13:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/12/07	10:13:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/12/07	10:13:20.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/12/07	10:13:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/12/07	10:13:24.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/07	10:13:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2013/12/07	10:15:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2013/12/07	10:16:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/07	10:54:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/07	10:54:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/12/07	10:55:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 42 a7 01 58		
2013/12/07	10:55:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/12/07	10:55:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/12/07	10:55:20.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/12/07	10:55:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/12/07	10:55:24.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/07	10:55:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2013/12/07	10:57:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2013/12/07	10:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/07	11:29:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/07	11:29:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/12/07	11:30:00.0	AOCS_Ore-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	00 39 ca 01 58		
2013/12/07	11:30:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/12/07	11:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/12/07	11:30:20.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/12/07	11:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/12/07	11:30:24.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/07	11:30:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2013/12/07	11:32:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2013/12/07	11:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/07	12:04:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/07	12:04:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/12/07	12:05:00.0	AOCS_Ore-point_Start_4_OG [0x09a]					
		AOCU_NM	5	02-76	00 30 e5 01 58		
2013/12/07	12:05:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/12/07	12:05:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/12/07	12:05:20.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/12/07	12:05:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/12/07	12:05:24.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/07	12:05:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2013/12/07	12:07:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2013/12/07	12:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/07	12:39:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/07	12:39:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					

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2013/12/07	12:40:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		AOCU_NM		5	02-76	00	28	00	01 58
2013/12/07	12:40:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/12/07	12:40:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/12/07	12:40:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/12/07	12:40:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/12/07	12:40:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/12/07	12:40:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05		
2013/12/07	12:42:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13		
2013/12/07	12:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/12/07	13:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/12/07	13:14:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2013/12/07	13:15:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00	1f	1a	01 58
2013/12/07	13:15:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/12/07	13:15:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/12/07	13:15:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/12/07	13:15:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/12/07	13:15:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/12/07	13:15:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05		
2013/12/07	13:17:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13		
2013/12/07	13:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/12/07	13:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/12/07	13:49:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2013/12/07	13:50:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00	16	35	01 58
2013/12/07	13:50:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/12/07	13:50:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/12/07	13:50:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/12/07	13:50:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/12/07	13:50:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/12/07	13:50:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05		
2013/12/07	13:52:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13		
2013/12/07	13:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/12/07	14:24:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/12/07	14:24:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2013/12/07	14:25:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00	01	ca	01 58
2013/12/07	14:25:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/12/07	14:25:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/12/07	14:25:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/12/07	14:25:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/12/07	14:25:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/12/07	14:25:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05		
2013/12/07	14:27:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13		
2013/12/07	14:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/12/07	15:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/12/07	15:03:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/12/07	15:03:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/12/07	15:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			

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2013/12/07	15:16:30.0	XRT_Custom_430_OG [0x1ae]					
2013/12/07	15:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/07	15:23:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/07	15:23:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/07	15:23:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/12/07	15:24:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/07	15:24:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/12/07	15:25:00.0	AOCS_OrE-point_Start_9_OG [0x09f]					
		AOCU_NM	5	02-76	00 ed 59 01 58		
2013/12/07	15:25:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/12/07	15:25:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/12/07	15:25:20.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/12/07	15:25:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/12/07	15:25:24.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/07	15:25:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2013/12/07	15:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/12/07	15:27:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2013/12/07	15:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/07	15:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/07	15:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/12/07	16:00:00.0	AOCS_OrE-point_Start_10_OG [0x0a0]					
		AOCU_NM	5	02-76	00 e4 73 01 58		
2013/12/07	16:00:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/12/07	16:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/12/07	16:00:20.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/12/07	16:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/12/07	16:00:24.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/07	16:00:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2013/12/07	16:02:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2013/12/07	16:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/07	16:38:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/07	16:38:32.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/07	16:38:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/12/07	16:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/12/07	16:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/07	16:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/12/07	17:00:00.0	AOCS_OrE-point_Start_11_OG [0x0a1]					
		AOCU_NM	5	02-76	00 db 8e 01 58		
2013/12/07	17:00:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/12/07	17:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/12/07	17:00:20.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/12/07	17:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/12/07	17:00:24.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/07	17:00:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 05		
2013/12/07	17:02:30.0	XRT_Custom_430_OG [0x1ae]					
2013/12/07	17:02:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2013/12/07	17:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/07	17:52:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/07	17:52:26.0	XRT_FOCUS_RECALIBRATE_433_OG [0x1b1]					
		XRT_FOCUS_RECAL	2	07-F8	78 00		
2013/12/07	17:52:30.0	AOCS_OrE-point_Start_12_OG [0x0a2]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2013/12/07	17:56:26.0	XRT_FOCUS_POSITION_403_OG [0x193]					

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2013/12/07	17:56:46.0	XRT_FLD_DIS_404_OG [0x194]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
		MDP_XRT_FLD_DIS		1	07-F0	d9			
2013/12/07	17:56:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/12/07	17:56:50.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/12/07	17:56:52.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11		
2013/12/07	17:56:54.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/12/07	18:02:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/12/07	18:02:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2013/12/07	18:03:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/12/07	18:03:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/12/07	18:03:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/12/07	18:03:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/12/07	18:03:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/12/07	18:03:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05		
2013/12/07	18:03:28.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13		
2013/12/07	18:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/12/07	18:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/12/07	18:16:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/12/07	18:16:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/12/07	18:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/12/07	18:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/12/07	18:39:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2013/12/07	18:40:00.0	AOCS_OrE-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00	d2	a8	01 58
2013/12/07	18:40:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/12/07	18:40:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/12/07	18:40:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/12/07	18:40:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/12/07	18:40:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/12/07	18:40:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05		
2013/12/07	18:42:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13		
2013/12/07	18:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/12/07	19:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/12/07	19:14:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2013/12/07	19:15:00.0	AOCS_OrE-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00	c9	cb	01 58
2013/12/07	19:15:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/12/07	19:15:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/12/07	19:15:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/12/07	19:15:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/12/07	19:15:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/12/07	19:15:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05		
2013/12/07	19:17:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13		
2013/12/07	19:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/12/07	19:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/12/07	19:53:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/12/07	19:53:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/12/07	19:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/12/07	20:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			

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2013/12/07	20:19:56.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2013/12/07	20:20:00.0	AOCS_ORe-point_Start_15_OG [0x0a5] AOCU_NM	5	02-76	00	c0	e6	01 58
2013/12/07	20:20:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0		d8		
2013/12/07	20:20:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2013/12/07	20:20:20.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0		d0		
2013/12/07	20:20:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5		
2013/12/07	20:20:24.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2013/12/07	20:20:26.0	XRT_QT_PROG_SET_440_OG [0x1b8] MDP_XRT_QT_PROG_SET	2	07-F0		c4	05	
2013/12/07	20:22:58.0	XRT_FL_PROG_SET_420_OG [0x1a4] MDP_XRT_FL_PROG_SET	2	07-F0		c5	13	
2013/12/07	20:23:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2013/12/07	20:54:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2013/12/07	20:54:56.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2013/12/07	20:55:00.0	AOCS_ORe-point_Start_16_OG [0x0a6] AOCU_NM	5	02-76	00	b8	00	01 58
2013/12/07	20:55:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0		d8		
2013/12/07	20:55:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2013/12/07	20:55:20.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0		d0		
2013/12/07	20:55:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5		
2013/12/07	20:55:24.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2013/12/07	20:55:26.0	XRT_QT_PROG_SET_440_OG [0x1b8] MDP_XRT_QT_PROG_SET	2	07-F0		c4	05	
2013/12/07	20:57:58.0	XRT_FL_PROG_SET_420_OG [0x1a4] MDP_XRT_FL_PROG_SET	2	07-F0		c5	13	
2013/12/07	20:58:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2013/12/07	21:29:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2013/12/07	21:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2013/12/07	21:30:00.0	AOCS_ORe-point_Start_17_OG [0x0a7] AOCU_NM	5	02-76	03	00	00	00 00
2013/12/07	21:30:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0		d8		
2013/12/07	21:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2013/12/07	21:30:20.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0		d0		
2013/12/07	21:30:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5		
2013/12/07	21:30:24.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2013/12/07	21:30:26.0	XRT_QT_PROG_SET_414_OG [0x19e] MDP_XRT_QT_PROG_SET	2	07-F0		c4	04	
2013/12/07	21:32:58.0	XRT_FL_PROG_SET_421_OG [0x1a5] MDP_XRT_FL_PROG_SET	2	07-F0		c5	0d	
2013/12/07	21:53:00.0	XRT_Custom_430_OG [0x1ae]						
2013/12/07	21:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2013/12/07	23:08:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2013/12/07	23:08:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2013/12/07	23:08:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2013/12/07	23:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2013/12/07	23:23:30.0	XRT_Custom_430_OG [0x1ae]						
2013/12/07	23:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2013/12/08	00:40:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2013/12/08	00:40:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2013/12/08	00:40:34.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2013/12/08	00:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2013/12/08	00:51:30.0	XRT_Custom_430_OG [0x1ae]						
2013/12/08	00:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2013/12/08	02:07:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2013/12/08	02:07:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2013/12/08	02:07:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0		e8		

2013/12/08	02:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/08	02:27:30.0	XRT_Custom_430_OG [0x1ae]				
2013/12/08	02:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/08	03:35:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/08	03:35:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/08	03:35:34.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/08	03:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/08	04:04:30.0	XRT_Custom_430_OG [0x1ae]				
2013/12/08	04:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/08	05:15:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/08	05:15:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/08	05:15:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/08	05:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/08	05:42:00.0	XRT_Custom_430_OG [0x1ae]				
2013/12/08	05:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/08	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/08	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/12/08	06:00:00.0	AOCS_ORe-point_Start_12_OG [0x0a2] AOCU_NM	5	02-76	00 00 00 00 00	
2013/12/08	06:00:16.0	XRT_FLD_DIS_434_OG [0x1b2] MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/12/08	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/12/08	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/12/08	06:02:58.0	XRT_QT_PROG_SET_446_OG [0x1be] MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2013/12/08	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/08	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/08	06:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/12/08	06:10:00.0	AOCS_ORe-point_Start_17_OG [0x0a7] AOCU_NM	5	02-76	03 00 00 00 00	
2013/12/08	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/12/08	06:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/12/08	06:10:20.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/12/08	06:10:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/12/08	06:10:24.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/08	06:10:26.0	XRT_QT_PROG_SET_414_OG [0x19e] MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2013/12/08	06:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5] MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2013/12/08	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/08	06:55:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/08	06:55:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/08	06:55:34.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/08	06:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/08	07:19:30.0	XRT_Custom_430_OG [0x1ae]				
2013/12/08	07:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/08	08:35:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/08	08:35:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/08	08:35:34.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/08	08:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/08	08:54:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/08	08:54:56.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/12/08	08:55:00.0	AOCS_ORe-point_Start_18_OG [0x0a8] AOCU_NM	5	02-76	00 af 1b 01 58	
2013/12/08	08:55:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/12/08	08:55:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				

2013/12/08	08:55:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/12/08	08:55:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/12/08	08:55:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/12/08	08:55:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05		
2013/12/08	08:57:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13		
2013/12/08	08:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/12/08	10:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/12/08	10:17:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/12/08	10:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/12/08	10:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/12/08	10:26:00.0	XRT_Custom_430_OG [0x1ae]							
2013/12/08	10:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/12/08	14:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/12/08	14:06:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/12/08	14:06:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/12/08	14:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/12/08	14:12:00.0	XRT_Custom_430_OG [0x1ae]							
2013/12/08	14:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/12/08	15:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/12/08	15:38:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/12/08	15:38:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/12/08	15:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/12/08	15:54:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/12/08	15:54:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00		
2013/12/08	15:55:00.0	AOCs_Or-e-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	03	00 00 00 00		
2013/12/08	15:55:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/12/08	15:55:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/12/08	15:55:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/12/08	15:55:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/12/08	15:55:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/12/08	15:55:26.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04		
2013/12/08	15:57:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d		
2013/12/08	16:02:00.0	XRT_Custom_430_OG [0x1ae]							
2013/12/08	16:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/12/08	17:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/12/08	17:15:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2013/12/08	17:15:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/12/08	17:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/12/08	17:38:30.0	XRT_Custom_430_OG [0x1ae]							
2013/12/08	17:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/12/08	17:55:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/12/08	17:55:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00		
2013/12/08	17:56:00.0	AOCs_Or-e-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00	00 00 00 00		
2013/12/08	17:56:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/12/08	17:58:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/12/08	17:58:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/12/08	17:58:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11		
2013/12/08	17:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			

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2013/12/08	18:05:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/08	18:05:56.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/12/08	18:06:00.0	AOCS_ORe-point_Start_17_OG [0x0a7] AOCU_NM	5	02-76	03 00 00 00 00
2013/12/08	18:06:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2013/12/08	18:06:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/12/08	18:06:20.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0	d0
2013/12/08	18:06:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2013/12/08	18:06:24.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/08	18:06:26.0	XRT_QT_PROG_SET_414_OG [0x19e] MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2013/12/08	18:08:58.0	XRT_FL_PROG_SET_421_OG [0x1a5] MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2013/12/08	18:09:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/08	18:52:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/08	18:52:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/08	18:52:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/08	18:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/08	19:15:30.0	XRT_Custom_430_OG [0x1ae]			
2013/12/08	19:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/08	20:29:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/08	20:29:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/08	20:29:34.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/08	20:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/08	20:52:30.0	XRT_Custom_430_OG [0x1ae]			
2013/12/08	20:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/08	21:59:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/08	21:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/12/08	22:00:00.0	AOCS_ORe-point_Start_19_OG [0x0a9] AOCU_NM	5	02-76	00 1e 35 b0 fe
2013/12/08	22:00:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2013/12/08	22:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/12/08	22:00:20.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0	d0
2013/12/08	22:00:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2013/12/08	22:00:24.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/08	22:00:26.0	XRT_QT_PROG_SET_414_OG [0x19e] MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2013/12/08	22:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5] MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2013/12/08	22:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/08	22:07:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/08	22:07:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/08	22:07:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/08	22:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/08	22:28:30.5	XRT_Custom_430_OG [0x1ae]			
2013/12/08	22:29:30.5	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/08	23:44:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/08	23:44:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/08	23:44:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/08	23:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/08	23:54:00.0	XRT_Custom_430_OG [0x1ae]			
2013/12/08	23:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/09	01:09:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/09	01:09:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/09	01:09:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			

2013/12/09	01:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8	
2013/12/09	01:27:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/09	01:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/09	02:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/09	02:43:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
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2013/12/09	02:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/09	03:04:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/09	03:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/09	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/12/09	03:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	AOCU_NM	5	02-76	01 00 00 00 00	
2013/12/09	04:00:00.0	AOCs_Orе-point_Start_20_OG [0x0aa]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/12/09	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/12/09	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/12/09	04:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/12/09	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/12/09	04:00:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/09	04:00:26.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07	
2013/12/09	04:02:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2013/12/09	04:03:00.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/09	04:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/09	04:12:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/09	04:12:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/09	04:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/09	04:41:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/09	04:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/09	05:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/09	05:53:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/09	05:53:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/09	05:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/09	06:18:30.0	XRT_Custom_430_OG [0x1ae]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/12/09	06:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	AOCU_NM	5	02-76	00 00 00 00 00	
2013/12/09	06:28:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/12/09	06:28:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/12/09	06:28:30.0	AOCs_Orе-point_Start_12_OG [0x0a2]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/12/09	06:28:46.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/12/09	06:31:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2013/12/09	06:31:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2013/12/09	06:31:28.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/09	06:31:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/09	06:38:24.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/12/09	06:38:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	AOCU_NM	5	02-76	01 00 00 00 00	
2013/12/09	06:38:30.0	AOCs_Orе-point_Start_20_OG [0x0aa]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/12/09	06:38:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/12/09	06:38:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/12/09	06:38:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/12/09	06:38:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/12/09	06:38:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	

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2013/12/09	06:38:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2013/12/09	06:41:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2013/12/09	06:41:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/09	07:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/09	07:33:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/09	07:33:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/09	07:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/09	07:56:00.0	XRT_Custom_430_OG [0x1ae]					
2013/12/09	07:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/09	09:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/09	09:13:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/09	09:13:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/09	09:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/09	09:31:00.0	XRT_Custom_430_OG [0x1ae]					
2013/12/09	09:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/09	14:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/09	14:39:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/09	14:39:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/09	14:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/09	14:51:00.0	XRT_Custom_430_OG [0x1ae]					
2013/12/09	14:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/09	16:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/09	16:14:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/09	16:14:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/09	16:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/09	16:38:00.0	XRT_Custom_430_OG [0x1ae]					
2013/12/09	16:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/09	17:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/09	17:51:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/09	17:51:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/09	17:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/09	18:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/09	18:14:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2013/12/09	18:15:00.0	AOCs_Or-e-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00	00 00 00 00
2013/12/09	18:15:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/12/09	18:17:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/12/09	18:17:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/12/09	18:17:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2013/12/09	18:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/09	18:24:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/09	18:24:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2013/12/09	18:25:00.0	AOCs_Or-e-point_Start_20_OG [0x0aa]	AOCU_NM	5	02-76	01	00 00 00 00
2013/12/09	18:25:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/12/09	18:25:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/12/09	18:25:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/12/09	18:25:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/12/09	18:25:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/09	18:25:26.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2013/12/09	18:27:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]					

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2013/12/09	18:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/09	19:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/09	19:28:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/09	19:28:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/09	19:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/09	19:51:30.0	XRT_Custom_430_OG [0x1ae]					
2013/12/09	19:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/09	21:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/09	21:06:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/09	21:06:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/09	21:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/09	21:29:00.0	XRT_Custom_430_OG [0x1ae]					
2013/12/09	21:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/09	22:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/09	22:43:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/09	22:43:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/09	22:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/09	23:03:30.0	XRT_Custom_430_OG [0x1ae]					
2013/12/09	23:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/10	00:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/10	00:20:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/10	00:20:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/10	00:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/10	00:27:30.0	XRT_Custom_430_OG [0x1ae]					
2013/12/10	00:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/10	01:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/10	01:43:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/10	01:43:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/10	01:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/10	02:03:00.0	XRT_Custom_430_OG [0x1ae]					
2013/12/10	02:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/10	03:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/10	03:18:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/10	03:18:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/10	03:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/10	03:40:00.0	XRT_Custom_430_OG [0x1ae]					
2013/12/10	03:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/10	04:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/10	04:50:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/10	04:50:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/10	04:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/10	05:17:30.0	XRT_Custom_430_OG [0x1ae]					
2013/12/10	05:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/10	06:00:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/10	06:00:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2013/12/10	06:00:30.0	AOCS_OrE-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00 00 00 00	00
2013/12/10	06:00:46.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/12/10	06:03:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/12/10	06:03:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/12/10	06:03:28.0	XRT_QT_PROG_SET_446_OG [0x1be]					

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2013/12/10	06:03:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2013/12/10	06:10:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/10	06:10:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/10	06:10:30.0	AOCS_Or-point_Start_20_OG [0x0aa]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2013/12/10	06:10:46.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01	00 00 00 00
2013/12/10	06:10:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/12/10	06:10:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/12/10	06:10:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/12/10	06:10:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/12/10	06:10:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/10	06:13:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2013/12/10	06:13:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d
2013/12/10	06:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/10	06:30:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/10	06:30:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/10	06:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/10	06:55:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/10	06:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2013/12/10	08:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2013/12/10	08:10:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/10	08:10:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/10	08:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/10	08:32:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/10	08:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/10	09:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2013/12/10	09:51:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/10	09:51:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/10	09:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/10	10:25:00.0	AOCS_Or-point_Start_12_OG [0x0a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
		AOCU_NM	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
			AOCU_NM	5	02-76	00	00 00 00 00