

XRT Timeline to be uploaded on 2013/12/10

Period: 2013/12/10 10:15:00 - 2013/12/14 09:26:00

* * * * *

Normal mode

* * * * *

XOB #19E6: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)														
Term			Pointing (x, y)			Comment								
12/10 10:28:00 - 12/10 17:59:54			Track (780.5, -241.3) @ 12/10 10:25:00			# OP start + 10min, AR 11916.								
12/11 06:17:00 - 12/11 08:47:30			Track (-855.9, 215.7) @ 12/11 06:14:00			# Observe returning AR at East limb for EIS.								
12/11 18:19:30 - 12/12 05:37:24			Track (-802.3, 217.1) @ 12/11 18:16:30			# AR cont.								
PROG= 07 Inf.-time(s)														
Subr= 1 1-time(s) 2.0sec														
└─ Seqn= 8 2-time(s) 2.0sec														
└─┬─ Open/G-band			Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec														
└─ Seqn= 24 1-time(s) 2.0sec														
└─┬─ Open/Ti-poly			Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─┬─ Open/G-band			Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 42 4-time(s) 2.0sec														
└─┬─ Al-poly/Open			thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─┬─ thin-Be/Open			med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─┬─ Open/thick-Al			Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 62 8-time(s) 270.0sec														
└─┬─ thin-Be/Open			med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─┬─ Al-poly/Open			thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─┬─ thin-Be/Open			med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─┬─ Al-poly/Open			thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─┬─ thin-Be/Open			med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─┬─ Al-poly/Open			thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter			Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19DA: Synoptic Q95 2x2 - Al/mesh(5/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/362/1443) + Thin														
Term			Pointing (x, y)				Comment							
12/10 18:03:00 - 12/10 18:09:54			Fixed (0.0, 0.0)				synoptic							
12/11 06:07:00 - 12/11 06:13:54			Fixed (0.0, 0.0)				synoptic, shifted 4.0 min							
12/11 18:09:30 - 12/11 18:16:24			Fixed (0.0, 0.0)				synoptic, shifted 6.5 min							
12/12 05:40:30 - 12/12 05:47:24			Fixed (0.0, 0.0)				synoptic, shifted -22.5 min							
PROG= 17 1-time(s)														
└ Subr= 1 1-time(s) 12.0sec														
└ Seqn= 84 1-time(s) 4.0sec														
└└ Open/Al-mesh Open/Al-mesh close Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└└ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└└ Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└ Seqn= 5 1-time(s) 2.0sec														
└└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
└└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
└└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec														
└└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec														
└└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec														
└ Seqn= 86 1-time(s) 4.0sec														
└└ Open/Ti-poly Open/Ti-poly close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└└ Open/Ti-poly Open/Ti-poly close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└└ Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└ Seqn= 95 1-time(s) 2.0sec														
└└ thin-Be/Open thin-Be/Open close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└└ thin-Be/Open thin-Be/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└└ thin-Be/Open thin-Be/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
└ Seqn= 6 1-time(s) 2.0sec														
└└ Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec														
└└ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec														
Default Filter			Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer Interval

XOB #19E8: CME watch - Al/poly, Be/thin - 4x4 - 2048x2048 - 5min cad - AEC2 with G-band(2x2) + G-band VLS Closed														
Term			Pointing (x, y)			Comment								
12/10 18:13:00 - 12/11 06:03:54			Track (817.8, -241.2) @ 12/10 18:10:00			# AR 11916.								
12/11 09:08:30 - 12/11 15:50:00			Fixed (-15.0, -950.0)			# HOP 240 (South Pole)								
12/11 16:14:30 - 12/11 18:06:24			Track (-813.3, 216.8) @ 12/11 16:00:00			# AR obs. cont.								
12/12 05:50:30 - 12/12 09:25:30			Track (-740.9, 218.6) @ 12/12 05:47:30			# AR cont.								
PROG= 09 Inf.-time(s)														
Subr= 1		96-time(s)		300.0sec										
Seqn= 19		1-time(s)		2.0sec										
thin-Be/Open		med-Be/Open		close		Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)		Q=98	2 0 2.0sec
Seqn= 97		1-time(s)		2.0sec										
Al-poly/Open		thin-Be/Open		close		Safe	Norm	500ms	Obs	4x4	2048x2048 (1024, 1024)		Q=98	2 0 2.0sec
Subr= 2		1-time(s)		2.0sec										
Seqn= 14		1-time(s)		2.0sec										
Open/G-band		Open/G-band		open		Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)		Q=95	0 0 2.0sec

Seqn= 12 1-time(s) 2.0sec Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec													
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

* * * * *

Flare mode

* * * * *

XOB #1980: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops-2

Term	Pointing (x, y)	Comment
12/10 10:28:00 - 12/10 17:59:54	Track (780.5, -241.3) @ 12/10 10:25:00	# OP start + 10min, AR 11916.
12/10 18:13:00 - 12/11 06:03:54	Track (817.8, -241.2) @ 12/10 18:10:00	# AR 11916.
12/11 06:17:00 - 12/11 08:47:30	Track (-855.9, 215.7) @ 12/11 06:14:00	# Observe returning AR at East limb for EIS.
12/11 09:08:30 - 12/11 15:50:00	Fixed (-15.0, -950.0)	# HOP 240 (South Pole)
12/11 16:14:30 - 12/11 18:06:24	Track (-813.3, 216.8) @ 12/11 16:00:00	# AR obs. cont.
12/11 18:19:30 - 12/12 05:37:24	Track (-802.3, 217.1) @ 12/11 18:16:30	# AR cont.
12/12 05:50:30 - 12/12 09:25:30	Track (-740.9, 218.6) @ 12/12 05:47:30	# AR cont.

PROG= 19 15-time(s) Subr= 1 45-time(s) 10.0sec Seqn= 9 1-time(s) 2.0sec thin-Be/Open med-Be/Open close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec													
Subr= 2 1-time(s) 10.0sec Seqn= 10 1-time(s) 2.0sec med-Al/Open med-Al/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec													
Seqn= 11 1-time(s) 2.0sec Al-poly/Open Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec													
Seqn= 15 1-time(s) 2.0sec Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec													
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

* * * * *

Active Region Search

* * * * *

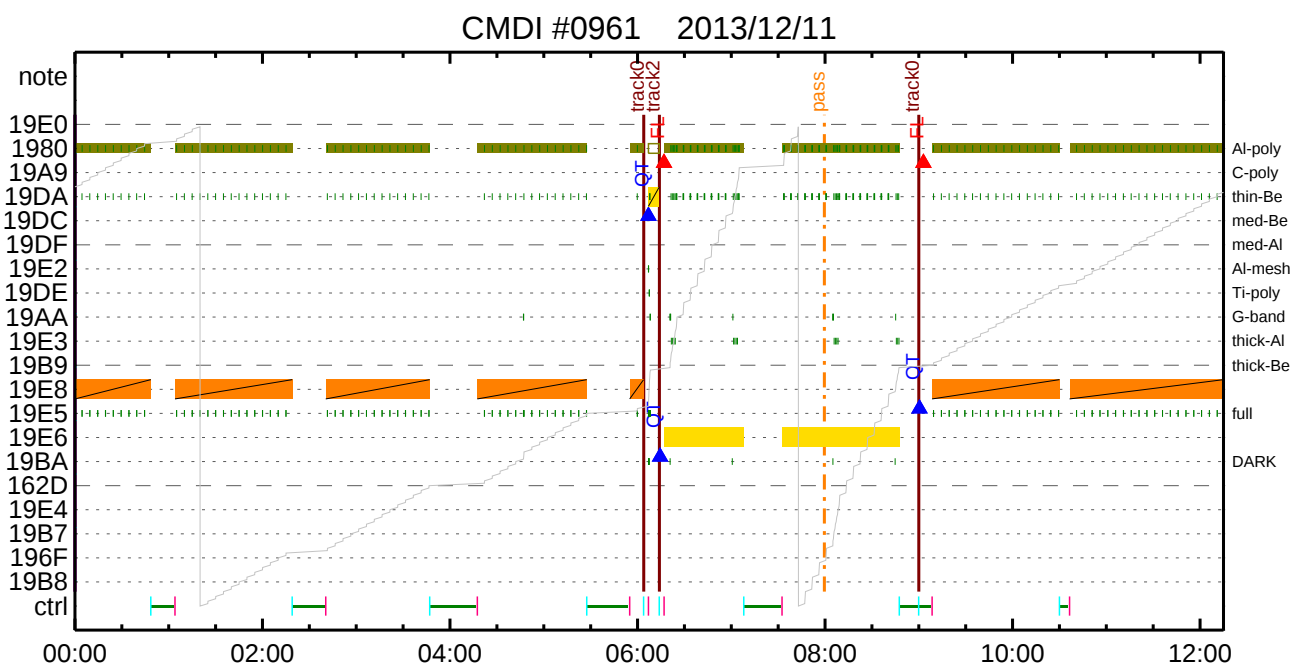
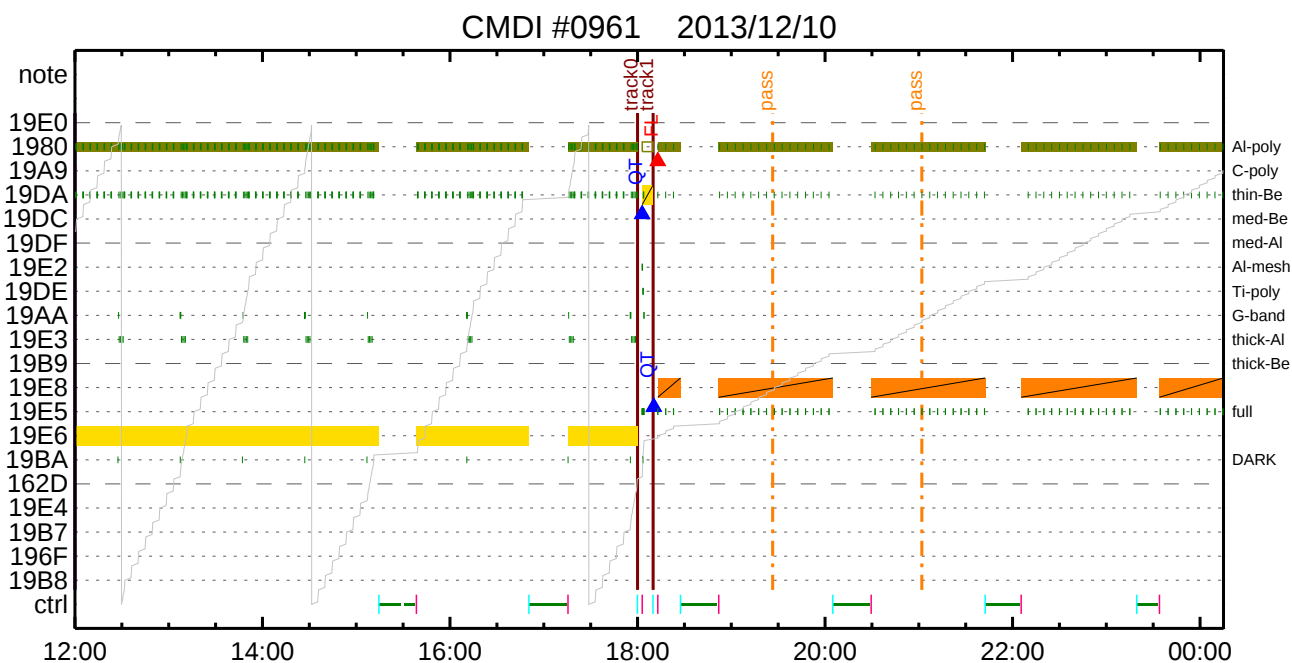
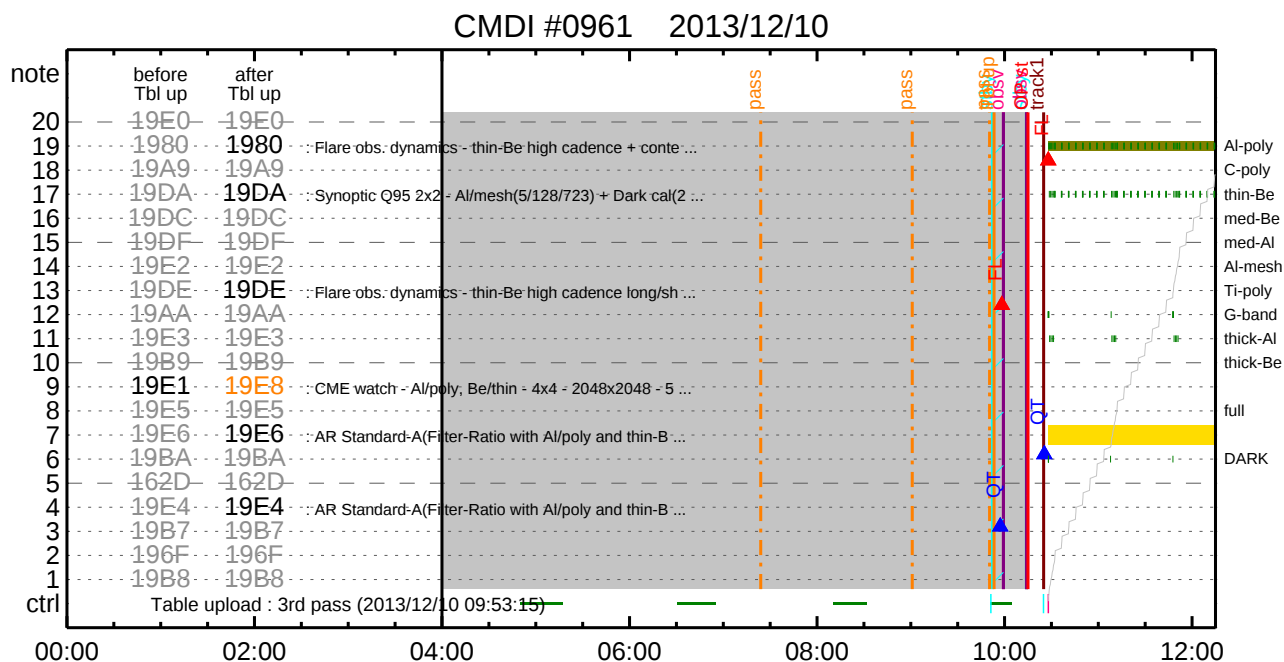
NOT USED

* * * * *

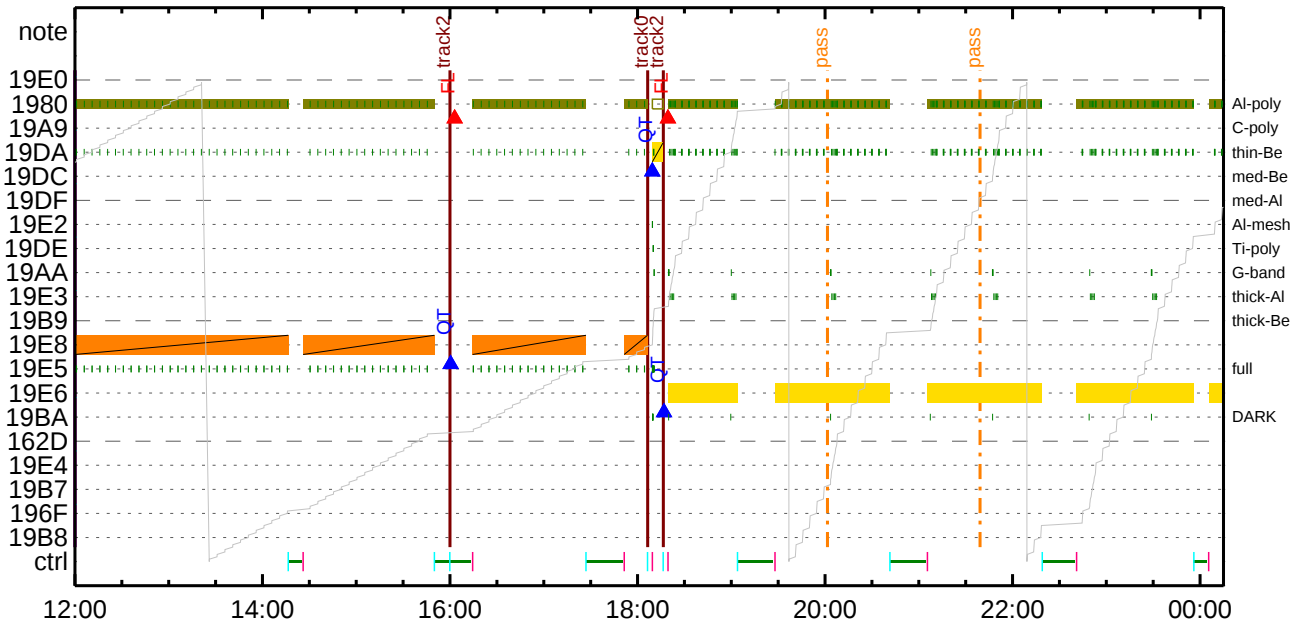
Flare Detection

* * * * *

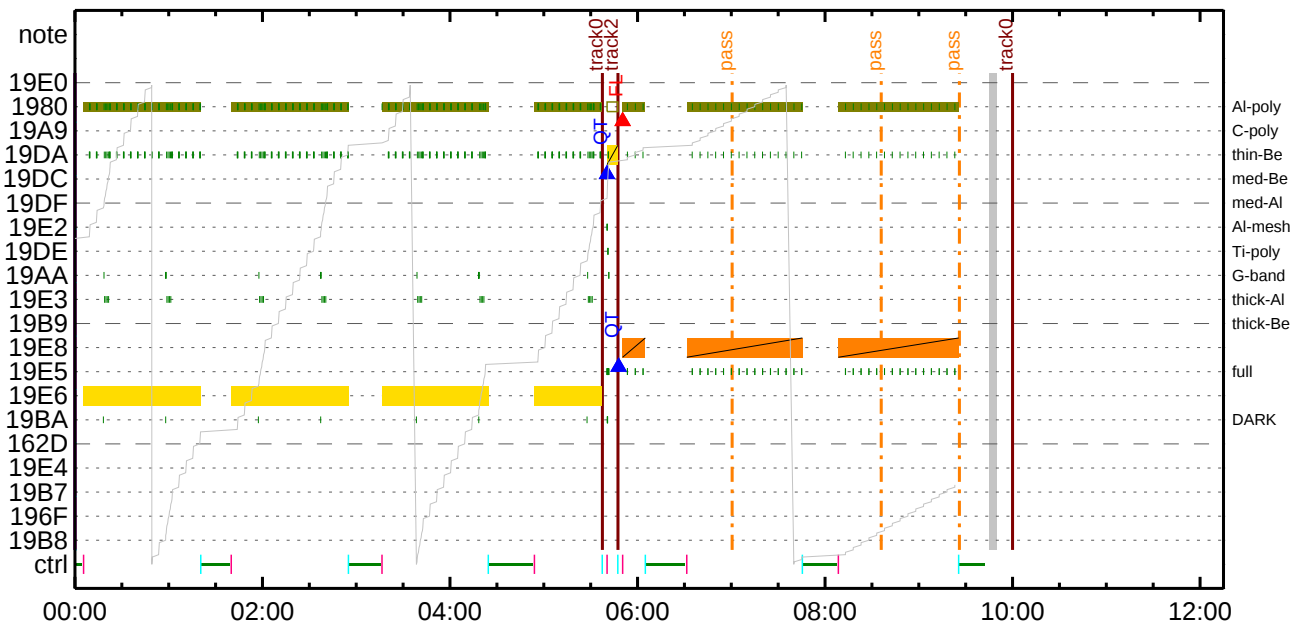
FLD Patrol												
Term		Pointing (x, y)				Comment						
12/10 18:10:16 - 12/11 06:04:16		Track (817.8, -241.2)		@ 12/10 18:10:00		# AR 11916.						
12/11 06:14:16 - 12/11 18:06:46		Track (-855.9, 215.7)		@ 12/11 06:14:00		# Observe returning AR at East limb for EIS.						
12/11 18:16:46 - 12/12 05:37:46		Track (-802.3, 217.1)		@ 12/11 18:16:30		# AR cont.						
12/12 05:47:46 - 12/14 09:26:00		Track (-740.9, 218.6)		@ 12/12 05:47:30		# AR cont.						
Open/Ti-poly		Open/thick-Al		close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval



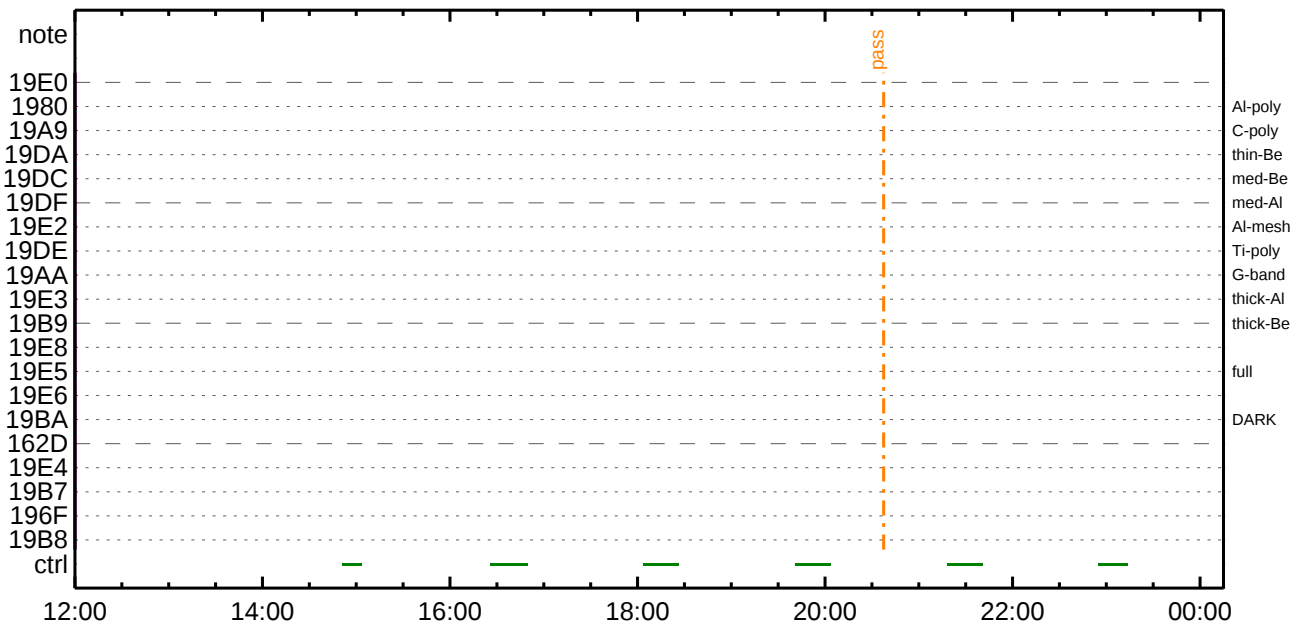
CMDI #0961 2013/12/11



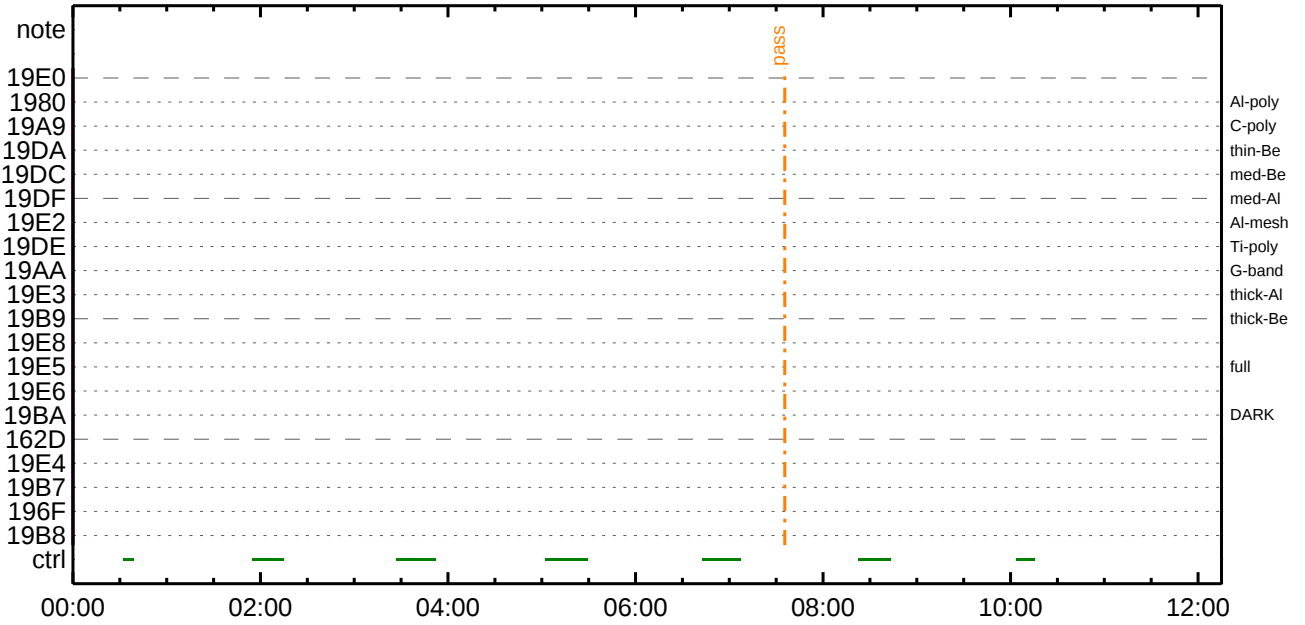
CMDI #0961 2013/12/12



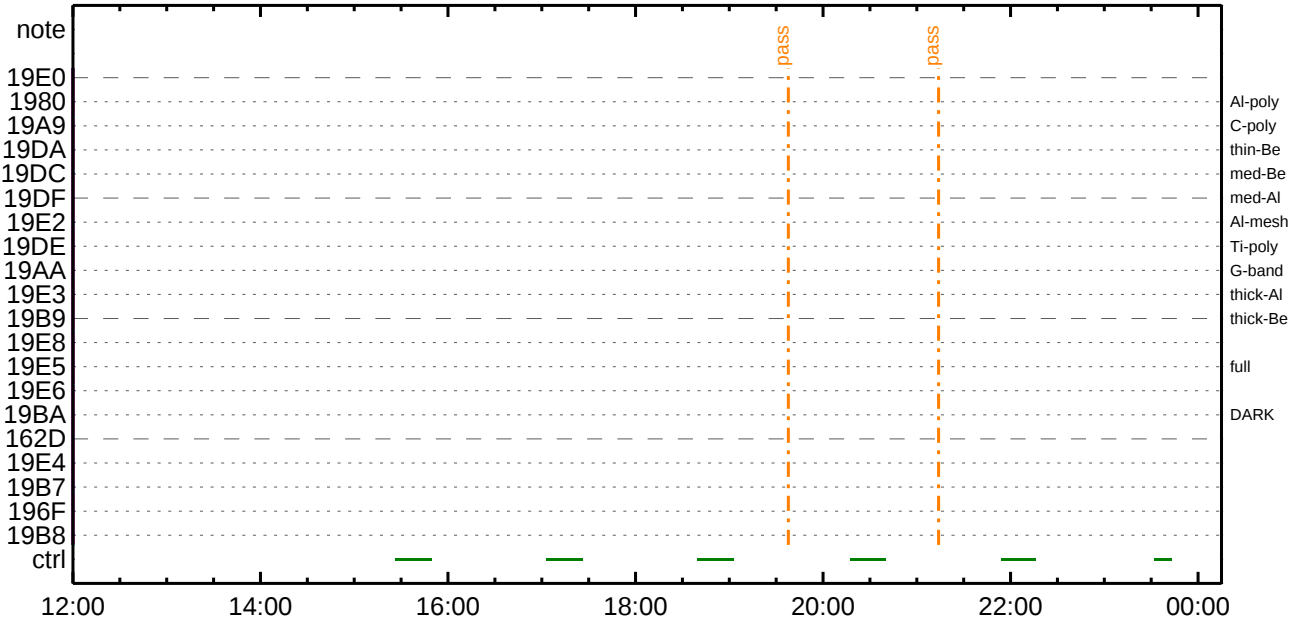
CMDI #0961 2013/12/12



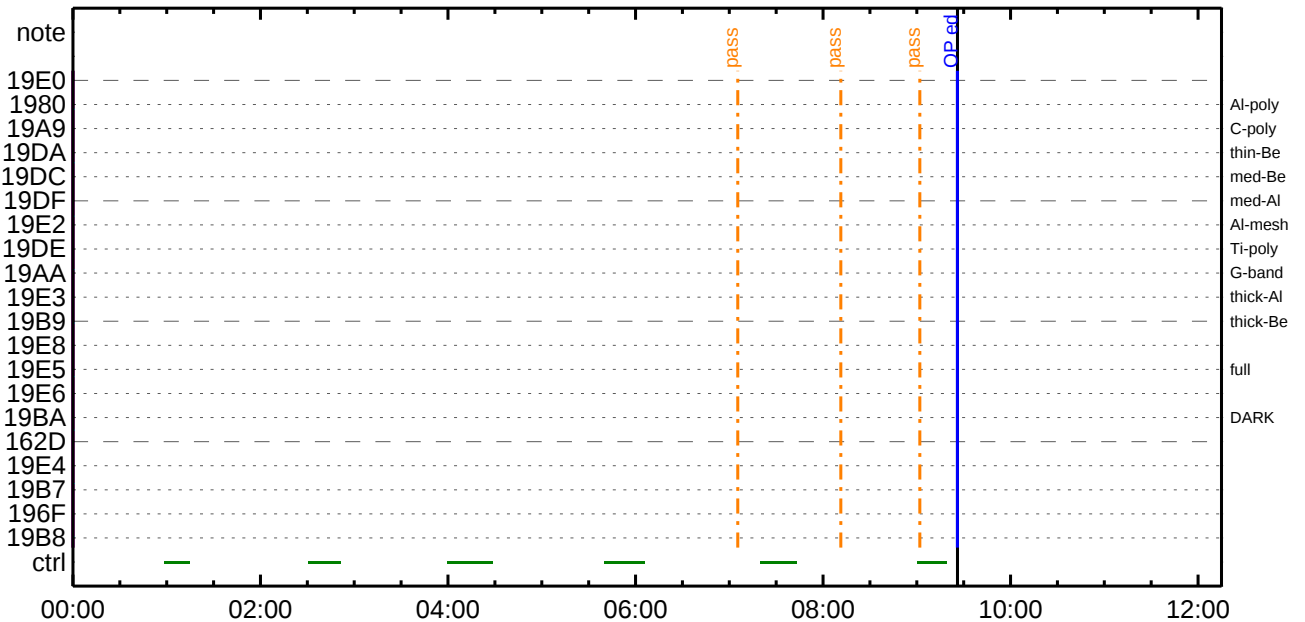
CMDI #0961 2013/12/13



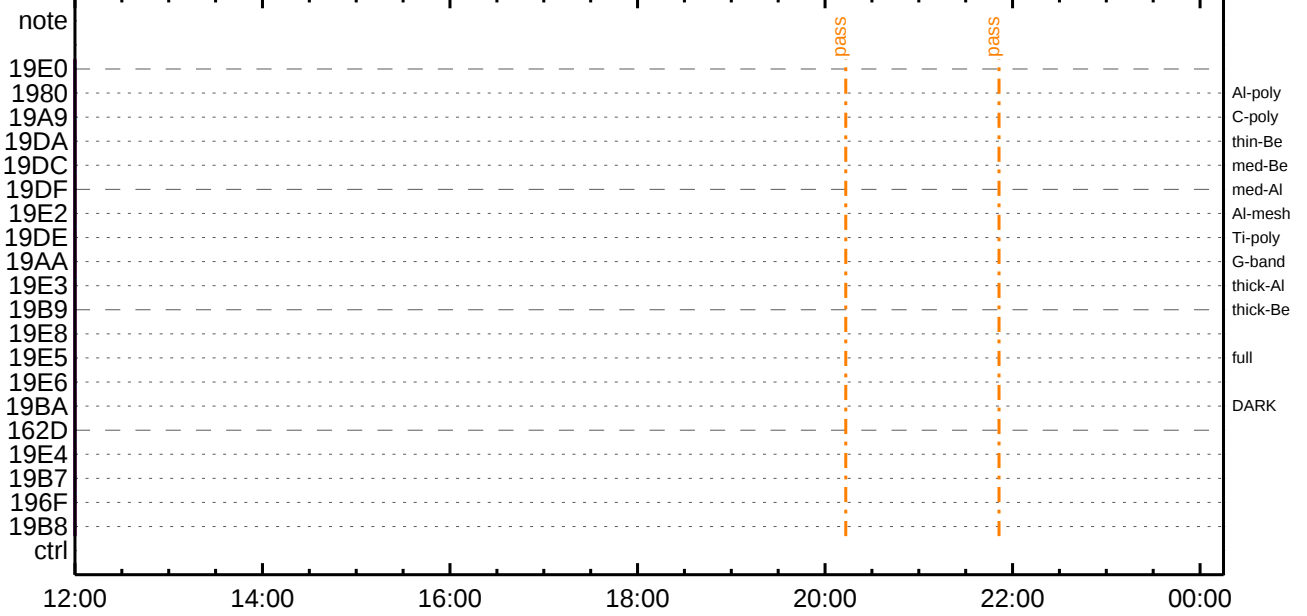
CMDI #0961 2013/12/13



CMDI #0961 2013/12/14



CMDI #0961 2013/12/14



```
main-091 2013-12-10 12:28:11 278 33 SOLAR-B MAIN //
0001 . C.
0002 . C. ***** AOS *****
0003 . C.
0004 . C. ¡ãAOS¥Á¥$¥Ã¥¬¼Å»Ü¡ä
0005 . C.
0006 . C. ¥Ä¥ß¡;¼¥³¥Ð¥Ó¥ÉÄ÷¿®
+ DC 00-00 NULL_DUMMY_CMD
0008 . C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 . C. Äí¡Ê¤¿¤À¤•µ°Æ»Í×AQ¤I¥¢¥Ä¥×¥¡¡;¼¥£¡ËËè½µ•îÎ£¡È¤É¾ºÇÒ¤•¤¿%ì¹Ç¤İ¡ÇÀ®, ù¤±¤ë¤Þ¤ÇÄ÷¿®¤•¤Ê¤¤¤³¤£¡£
0011 + DC 02-8E AOCU_ORB_UPD
0012 . C.
0013 . C.
0014 . C. *****
0015 . C. XÄ÷¿®µ¡ON
0016 . C. *****
0017 . C. ¢¨ °ÆÄ, ÍxÊÝ¤àLOS¤Þ¤Ç¤I»þ´Ö¤ò¹ÍÎ, ¤•¡¢ÊÔÍx¤EXÄÓÓN¤İ¹Ô¤Ê¤İ¤Ê¤¤¤³¤£¡£
0018 . C.
0019 + DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 . C. ¢¢[HK1_XPA_ON/OFF] EQ ON
0025 . C. ¢¢[HK1_XPA_PWR_HI/L0] EQ HI
0026 . C. ¢¢[HK1_XMOD_ON/OFF] EQ ON
0027 . C. ¢¢[HK1_XMOD-QPSK/PM] EQ QPSK
0028 . C.
0029 . C. X¥Ð¥Ó¥É¥¡¥Ä¥¬¼ÒÄÕ¤¬¼ÄÂê¤•¤¿¤¤£¡¢°Ê²¼¤İ°ÆÄ, ¼ê½Ç¤ð¼Ä¹Ô¤±¤£¡£
0030 . C.
0031 . C. *****
0032 . C. DR PT1 ÄÌ¾ï°ÆÄ
0033 . C. *****
0034 . C. ¢¨ RESTART¡ÊPT1¡Ê¤•¤¿¤¤%ì¹Ç¤İ¡¢°Ê²¼¤İ¾Ä¹Ô¤»¤°¡¢DCBC-150¤Ø¿Ê¤à¡£
0035 . C.
0036 . C. ¡ãPT1°ÆÄ, ³«»İ¡ä
0037 + DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 . C. ¢¢[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô, ¡¾Ú)
0043 . C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ¡¾Ú)
0044 . C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ¡¾Ú)
0045 . C.
0046 . C. ¡ã¥¢¥ó¥Æ¥ÊÄÙÄ¸¡ÊÄ•Äº²ÓÊò¡Ê, ä¤İ°ÆÄ, °Æ³«¡ä
0047 + DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 . C. ¢¢[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ô, ¡¾Ú)
0050 . C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ¡¾Ú)
0051 . C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ¡¾Ú)
0052 . C.
0053 . C.
0054 . C. PT1°ÆÄ, ¤¬¼«Æ°Ää»ß¤•¤¿, ä¡¢°Ê²¼¤ð¼Ä¹Ô¤±¤£¡£
0055 . C. ¥¢¥ó¥Æ¥ÊÄÙÄ¸¤âÄ•Äº²ÓÊò¤¬¼á¤¤%ì¹Ç¤İ'°Î»¤±¤ë¤Þ¤ÇÄÔ¤Ä¡£
0056 . C.
0057 . C. *****
0058 . C. DR PT2 ÄÌ¾ï°ÆÄ
0059 . C. *****
0060 . C. ¢¨ RESTART¡ÊPT2¡Ê¤•¤¿¤¤%ì¹Ç¤İ¡¢°Ê²¼¤İ¾Ä¹Ô¤»¤°¡¢DCBC-151¤Ø¿Ê¤à¡£
0061 . C.
0062 . C. ¡ãPT2°ÆÄ, ³«»İ¡ä
0063 + DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 . C. ¢¢[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô, ¡¾Ú)
0069 . C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ¡¾Ú)
0070 . C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ¡¾Ú)
0071 . C.
0072 . C. ¡ã¥¢¥ó¥Æ¥ÊÄÙÄ¸¡ÊÄ•Äº²ÓÊò¡Ê, ä¤İ°ÆÄ, °Æ³«¡ä
0073 + DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 . C. ¢¢[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ô, ¡¾Ú)
0076 . C. ¢¢[HK1_REP_STA/STP] EQ START (¼Ä¹Ô, ¡¾Ú)
0077 . C. ¢¢[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ô, ¡¾Ú)
0078 . C.
0079 . C. *****
0080 . C. DR°ÆÄ, Ää»ß¡¢XÄ÷¿®µ¡OFF
0081 . C. *****
0082 . C.
0083 . C. ¡ãDR°ÆÄ, Ää»ß¡ä
0084 + DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 . C. ¢¢[HK1_REP_STA/STP] EQ STOP
0087 . C. ¢¢[HK1_S_VC4_ON/OFF] EQ OFF
0088 . C. ¢¢[HK1_X_VC4_ON/OFF] EQ OFF
0089 . C.
0090 . C. ¡ãXÄ÷¿®µ¡OFF¡ä
0091 + DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 . C. ¢¢[HK1_XMOD_ON/OFF] EQ OFF
0095 . C. ¢¢[HK1_XPA_ON/OFF] EQ OFF
```

0096	C.				
0097	C.				
0098	. C.	*****			
0099	C.	OP/OG¶i¼¥Éj ¥À¥ó¥×			
0100	C.	*****			
0101	C.				
0102	. C.	iãOP/OG¶i¼¥Éjã			
0103	. S.	OP op-091:OP			
0104	()				
0105	. S.	OG og-091:OG			
0106	()				
0107	C.				
0108	. C.	iänMOG&OPîî°è¥À¥ó¥×jã			
0109	C.	NMOG(0x200000-0x207FFF;§ 32 kbyte)			
0110	+ . DC	01-23 DHU_DMA_DMP_PRM_SET			
0111	BC	(20 00 7f 01 02)			
0112	C.	¢¢[HK1_DMP_TOP_ADRS_1]	EQ	40	
0113	C.	¢¢[HK1_DMP_TOP_ADRS_0]	EQ	0	
0114	C.	¢¢[HK1_DMP_BLOCK_NUM]	EQ	127	
0115	C.	¢¢[HK1_DMP_REPEAT_NUM]	EQ	0	
0116	C.	¢¢[HK1_DMA_DMP_PIM]	EQ	DHU	
0117	+ . DC	01-22 DHU_MODE_CHNG			
0118	BC	(07 0b f8)			
0119	C.	¢¢[HK1_PKT_FORM_NO]	EQ	7	
0120	C.	¢¢[HK1_PKT_GEN_TIME]	EQ	0.25 s	
0121	C.	¢¢[HK1_S_TLM_BIT_RATE]	EQ	32k	
0122	C.	¢¢[HK1_X_TLM_BIT_RATE]	EQ	4M	
0123	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	EXEC	
0124	. C.	¥À¥ó¥×½ªÎ»¤ð³ÎÇ§			
0125	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	NON	
0126	. C.	RAM ID=NMOG¤Î¼Ê¹Ç•ê²ÏOK¤ð³ÎÇ§			
0127	C.				
0128	C.	NMOG(0x208000-0x20FFFF;§ 32 kbyte)			
0129	+ . DC	01-23 DHU_DMA_DMP_PRM_SET			
0130	BC	(20 80 7f 01 02)			
0131	C.	¢¢[HK1_DMP_TOP_ADRS_1]	EQ	41	
0132	C.	¢¢[HK1_DMP_TOP_ADRS_0]	EQ	0	
0133	C.	¢¢[HK1_DMP_BLOCK_NUM]	EQ	127	
0134	C.	¢¢[HK1_DMP_REPEAT_NUM]	EQ	0	
0135	C.	¢¢[HK1_DMA_DMP_PIM]	EQ	DHU	
0136	+ . DC	01-22 DHU_MODE_CHNG			
0137	BC	(07 0b f8)			
0138	C.	¢¢[HK1_PKT_FORM_NO]	EQ	7	
0139	C.	¢¢[HK1_PKT_GEN_TIME]	EQ	0.25 s	
0140	C.	¢¢[HK1_S_TLM_BIT_RATE]	EQ	32k	
0141	C.	¢¢[HK1_X_TLM_BIT_RATE]	EQ	4M	
0142	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	EXEC	
0143	. C.	¥À¥ó¥×½ªÎ»¤ð³ÎÇ§			
0144	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	NON	
0145	. C.	RAM ID=NMOG¤Î¼Ê¹Ç•ê²ÏOK¤ð³ÎÇ§			
0146	C.				
0147	C.	NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)			
0148	+ . DC	01-23 DHU_DMA_DMP_PRM_SET			
0149	BC	(21 00 41 01 02)			
0150	C.	¢¢[HK1_DMP_TOP_ADRS_1]	EQ	42	
0151	C.	¢¢[HK1_DMP_TOP_ADRS_0]	EQ	0	
0152	C.	¢¢[HK1_DMP_BLOCK_NUM]	EQ	65	
0153	C.	¢¢[HK1_DMP_REPEAT_NUM]	EQ	0	
0154	C.	¢¢[HK1_DMA_DMP_PIM]	EQ	DHU	
0155	+ . DC	01-22 DHU_MODE_CHNG			
0156	BC	(07 0b f8)			
0157	C.	¢¢[HK1_PKT_FORM_NO]	EQ	7	
0158	C.	¢¢[HK1_PKT_GEN_TIME]	EQ	0.25 s	
0159	C.	¢¢[HK1_S_TLM_BIT_RATE]	EQ	32k	
0160	C.	¢¢[HK1_X_TLM_BIT_RATE]	EQ	4M	
0161	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	EXEC	
0162	. C.	¥À¥ó¥×½ªÎ»¤ð³ÎÇ§			
0163	C.	¢¢[HK1_DMP_CHK_FLG]	EQ	NON	
0164	. C.	RAM ID=NMOG,RAM ID=OP¤Î¼Ê¹Ç•ê²ÏOK¤ð³ÎÇ§			
0165	C.				
0166	. C.	***** ºÊ²¼¤Î¼Ã¨¶Áº¤ÊÊ-¤ºÁ÷¿® (%ãm-¥À¥ó¥×¼ê½Ç¤ðÁÓÃ¤¤Ç½ª¤¤º¤¼¹¹Ç¤Ç¤â) *****			
0167	C.	DHU¥âj¼¥ÉjÊ¼¥½, ¥i j¼¥ÉjÊ¤ðÎ¤¤¹			
0168	+ . DC	01-22 DHU_MODE_CHNG			
0169	BC	(02 0a f8)			
0170	C.	¢¢[HK1_PKT_FORM_NO]	EQ	2	
0171	C.	¢¢[HK1_PKT_GEN_TIME]	EQ	0.5S	
0172	C.	¢¢[HK1_S_TLM_BIT_RATE]	EQ	32K	
0173	C.	¢¢[HK1_X_TLM_BIT_RATE]	EQ	4M	
0174	C.				
0175	. C.	*****			
0176	C.	TI-CMD SET (OPOG STOP/COPY/START)			
0177	C.	*****			
0178	C.				
0179	. C.	NOTICE ¡§ OPOG UPLOAD¤-Á÷¿®NG¤Î¼¹¹Ç¡¢¢ºÊ²¼¤ÎTI-CMDÁ÷¿®¤Î¼Á¹Ô¤¤¤¤¤¤¤¤¤¤¤¤¤¤			

```

0194 C.
0195 +. TI 2013-12-10 10:14:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.
0198 C.
0199 C.
0200 C.
0201 C.
0202 C.
0203 C.
0204 C.
0205 C.
0206 C.
0207 C.
0208 C.
0209 C.
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C.
0213 C.
0214 C.
0215 C.
0216 C.
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C.
0220 C.
0221 C.
0222 C.
0223 C.
0224 C.
0225 C.
0226 C.
0227 C.
0228 C.
0229 C.
0230 C.
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C.
0234 C.
0235 C.
0236 C.
0237 C.
0238 C.
0239 C.
0240 C.
0241 C.
0242 C.
0243 C.
0244 +. TI 2013-12-10 10:14:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC (21 02)
0247 +. TI 2013-12-10 10:14:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC (22)
0250 C.
0251 C.
0252 C.
0253 C.
0254 C.
0255 C.
0256 C.
0257 +. TI 2013-12-10 10:14:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC (c3)
0260 C.
0261 C.
0262 C.
0263 C.
0264 C.
0265 C.
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C.
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C.
0276 C.
0277 C.
0278 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-092 2013-12-10 12:38:11 175 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Á»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āi;Ē¿¿A¤•µ°Æ»İ×AÇ¿İ¥Ç¥Á¥×¥İj¼¥ÉjĒĒ½µ•İĒĒjĒĒ½°Ç¿¤•¤¿¼¹Ç¿İjÇÀ®, Ü¤¹¤Ē¤¤¤ÇÁ÷¿®¤•¤Ē¤¤¤¤ĒjĒ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ÷¿®µjON
0016 C. *****
0017 C. ¨ °ÆĀ, İ×ĒY¤¤ŁOS¤¤¤Ç¿İ»p´Ŏ¤Ŏ¹İĒ, ¤•jÇĒŎİ×¤ĒXĀŎON¤İ¹Ŏ¤Ēİ¤Ē¤¤¤¤ĒjĒ
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 +. DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 +. DC 03-95 TCIA_XMOD_QPSK
0024 C. ÇÇ[HK1_XPA_ON/OFF] EQ ON
0025 C. ÇÇ[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ÇÇ[HK1_XMOD_ON/OFF] EQ ON
0027 C. ÇÇ[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. X¥Đ¥Ŏ¥É¥İ¥Á¥¼ŎĀŎ¤¬°ĀĀĒ¤•¤¿¤ĒjÇ°Ē²¼¤İ°ÆĀ, ¼Ē½Ç¤Ŏ¼Ā¹Ŏ¤¹¤ĒjĒ
0030 C.
0031 . C. *****
0032 C. DR PT1 Āİ¼İ°ÆĀ
0033 C. *****
0034 C. ¨ ¨ RESTARTjĒPT1jĒ¤•¤¿¤¤¼¹Ç¿İjÇ°Ē²¼¤İ¼Ā¹Ŏ¤»¤°jÇDCBC-150¤Ŏ¿ĒĒ¤¤jĒ
0035 C.
0036 . C. jãPT1°ÆĀ, ³«»İjã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 +. DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 +. DC 06-B3 DR_REP_START
0041 +. DC 01-32 DHU_X_VC4_ON
0042 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ā¹Ŏ, j¼Ŭ)
0043 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Ŏ, j¼Ŭ)
0044 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Ŏ, j¼Ŭ)
0045 C.
0046 . C. jã¥Ç¥Ŏ¥Æ¥ĒĒĀŎjĒĒ•Ā°²ŎĒŎjĒ, Ā¤İ°ÆĀ, °Æ³«jã
0047 +. DC 06-B3 DR_REP_START
0048 +. DC 01-32 DHU_X_VC4_ON
0049 C. ÇÇ[HK1_REP_PT_1/2] EQ PT1 (¼Ā¹Ŏ, j¼Ŭ)
0050 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Ŏ, j¼Ŭ)
0051 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Ŏ, j¼Ŭ)
0052 C.
0053 C.
0054 . C. PT1°ÆĀ, ¤¬¼«Æ°ĀĀ»İ¤•¤¿, ĀjÇ°Ē²¼¤Ŏ¼Ā¹Ŏ¤¹¤ĒjĒ
0055 C. ¥Ç¥Ŏ¥Æ¥ĒĒĀŎ¤¤Ā•Ā°²ŎĒŎ¤¬ŎĀ¤¼¹Ç¿İ °İ»¤¹¤Ē¤¤¤ÇĀŎ¤ĀjĒ
0056 C.
0057 . C. *****
0058 C. DR PT2 Āİ¼İ°ÆĀ
0059 C. *****
0060 C. ¨ ¨ RESTARTjĒPT2jĒ¤•¤¿¤¤¼¹Ç¿İjÇ°Ē²¼¤İ¼Ā¹Ŏ¤»¤°jÇDCBC-151¤Ŏ¿ĒĒ¤¤¤jĒ
0061 C.
0062 . C. jãPT2°ÆĀ, ³«»İjã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 +. DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 +. DC 06-B3 DR_REP_START
0067 +. DC 01-32 DHU_X_VC4_ON
0068 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ā¹Ŏ, j¼Ŭ)
0069 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Ŏ, j¼Ŭ)
0070 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Ŏ, j¼Ŭ)
0071 C.
0072 . C. jã¥Ç¥Ŏ¥Æ¥ĒĒĀŎjĒĒ•Ā°²ŎĒŎjĒ, Ā¤İ°ÆĀ, °Æ³«jã
0073 +. DC 06-B3 DR_REP_START
0074 +. DC 01-32 DHU_X_VC4_ON
0075 C. ÇÇ[HK1_REP_PT_1/2] EQ PT2 (¼Ā¹Ŏ, j¼Ŭ)
0076 C. ÇÇ[HK1_REP_STA/STP] EQ START (¼Ā¹Ŏ, j¼Ŭ)
0077 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ ON (¼Ā¹Ŏ, j¼Ŭ)
0078 C.
0079 . C. *****
0080 C. DR°ÆĀ, Ā¤»İjÇXÁ÷¿®µjOFF
0081 C. *****
0082 C.
0083 . C. jãDR°ÆĀ, Ā¤»İjã
0084 +. DC 06-B4 DR_REP_STOP
0085 +. DC 01-29 DHU_S/X_VC4_OFF
0086 C. ÇÇ[HK1_REP_STA/STP] EQ STOP
0087 C. ÇÇ[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ÇÇ[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. jãXÁ÷¿®µjOFFjã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 +. DC 03-B5 TCIA_XPA_OFF
0094 C. ÇÇ[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ÇÇ[HK1_XPA_ON/OFF] EQ OFF
```

```

0096 C.
0097 C.
0098 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCS Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCS DUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 C.
0129 . C. ***** AOCS Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 +. DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 839066.3 +- 1.0 (s) [ ]
0136 C.
0137 C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_MANU
0140 BC (21 02)
0141 . C. Verify EIS in MANUAL mode
0142 . C. Estimated OBSTBL upload time is 5s
0143 C. *****
0144 C. EIS START OBSTBL LOAD
0145 C. *****
0146 . S. RAM ram-820:EIS_OBSTBL
0147 ( )
0148 +. DC 07-FC EIS_DUMP_OBSTBL
0149 BC (07 07 07 00 00 70 00)
0150 C.
0151 C. Execute, after the success of OBSTBL upload.
0152 C. Set EIS TI-commands
0153 +. TI 2013-12-10 10:14:50.0
0154 DC 07-FC EIS_MODE_CHG_ENA
0155 BC (20)
0156 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0157 C. *****
0158 C. EIS END OBSTBL LOAD
0159 C. *****
0160 C.
0161 . C. ***** MDP '0ÃÎÎ»0%ŸpĒĀĐ□1□ēDCBC•x²è *****
0162 C. (%ã°İ¥0¥Ā¥Ē¥p¥Ē¥Ā¥Ĉ¥ē□Ē½%□□Ā»Ů□1□ē)
0163 . S. DC-BC dcbc-402:DCBC
0164 (MDP_known_event)
0165 C.
0166 C.
0167 . C. ***** ¥Đ¥1•İ Daily±¿İÑ□Ē´Đ□1□ēDCBC•x²è *****
0168 . S. DC-BC dcbc-153:DCBC
0169 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0170 C.
0171 C.
0172 . C. ¡ãLOS¥Ā¥$¥Ā¥¬¼Ā»Ů¡ã
0173 C.
0174 . C. ***** LOS *****
0175 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-093 2013-12-10 12:38:11 130 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. iãAOS¥Á¥S¥Ã¥¼Á»Üjã
0005 C.
0006 C. ¥À¥Bj¼¥³¥D¥Ó¥ÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. ĀiĒ¿¿A•µ°Œ»İ×AÇ¿İ¥Ç¥A¥×¥İj¼¥ÉjĒĒµ•İĒjĒĒ°Ç¿•¿¿İ¹Ç¿İjÇÀ®, ũ•¹•Ē•¿¿ÇÁ÷¿®••Ē•¿¿³ĒĒjĒ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Upload DPL table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 C. ¥Ç¥Ā¥×¥İj¼¥É•Ā°•ĒESTS_CHK•¿OFF•Ē•¹•Ē
0025 C.
0026 . S. RAM ram-271:MDP_DPL
0027 ( )
0028 C.
0029 . C. < Dump RAMID=MDP_DPL >
0030 +. DC 07-F0 MDP_DUMP_FGTBL
0031 BC (82 07 00 38 b8 00 40)
0032 C. -----
0033 C. MDP_DPL verify = OK [ ]
0034 C. -----
0035 C.
0036 C. STS_CHK•¿ON•Ē•¹•Ē
0037 C.
0038 . C. < Update MDP DSC PAR1 >
0039 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0040 BC (4c)
0041 C. MDP_CMD_CODE = F04C0700[ ]
0042 C. MDP_CMD_CNT (count-up 1) [ ]
0043 C. -----
0044 C.
0045 . C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 +. DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR ____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 +. DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 +. DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 +. DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 +. DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 06 06)
0074 +. DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 +. DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 85 83 08 08)
0078 +. DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 20)
0080 +. DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 20 08)
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 80 80 08 20)
0084 +. DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0f 80 80 06 06)
0086 +. DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 10 80 80 08 08)
0088 +. DC 07-F0 MDP_XRT_FLD_ENA
0089 BC (d8)
0090 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0091 BC (c8)
0092 +. DC 07-F0 MDP_XRT_AEC_RESET
0093 BC (d0)
0094 +. DC 07-F0 MDP_XRT_ARS_DIS
0095 BC (d5)
```

```
0096 + DC 07-F0 MDP_XRT_FLD_RESET
0097 BC (da)
0098 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0099 BC (c4 04)
0100 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0101 BC (c5 0d)
0102 . C. ----- Success Verify ? OK / NG ____
0103 C.
0104 C.
0105 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0106 C.
0107 +. DC 07-F0 MDP_XRT_MODE_OBSV
0108 BC (c2)
0109 +. TI 2013-12-10 10:14:02.0
0110 DC 07-F0 MDP_XRT_MODE_OBSV
0111 BC (c2)
0112 . C. ----- Success Verify ? OK / NG ____
0113 C.
0114 C. ***** XRT END *****
0115 C.
0116 . C. ***** MDP '0ÃÎ0Î»0%Ý0ÊÂ0010ëDCBC•x2è *****
0117 C. (%ã°i¥0¥Ã¥Ê¥D¥Ê¥ã¥C¥ê00½%00¥Ã»Û010è)
0118 . S. DC-BC dcbc-402:DCBC
0119 (MDP_known_event)
0120 C.
0121 C.
0122 . C. ***** ¥0¥1•İ Daily±¿İÑ0Ê'0010ëDCBC•x2è *****
0123 . S. DC-BC dcbc-153:DCBC
0124 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0125 C.
0126 C.
0127 . C. iãLOS¥Ã¥S¥Ã¥½Ã»Ûiã
0128 C.
0129 . C. ***** LOS *****
0130 C.
```

Dec 10, 13 12:38

XRT_OGLIST_0961.chk

Page 1/6

*** OP Sequence for XRT ***

2013/12/10	10:24:54.0	XRT_CTRL_MANU_402_0G [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/12/10	10:24:56.0	XRT_FOCUS_POSITION_410_0G [0x19a]		
		XRT_FOCUS_POSITION	4	07-F8 22 fe 97 00
2013/12/10	10:25:00.0	AOCS_ORe-point_Start_1_0G [0x097]		
		AOCU_NM	5	02-76 01 00 00 00 00
2013/12/10	10:25:16.0	XRT_FLD_ENA_411_0G [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2013/12/10	10:25:18.0	XRT_FLRCTRL_ENA_412_0G [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2013/12/10	10:25:20.0	XRT_AEC_RESET_413_0G [0x19d]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2013/12/10	10:25:22.0	XRT_ARS_DIS_423_0G [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2013/12/10	10:25:24.0	XRT_FLD_RESET_415_0G [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/12/10	10:25:26.0	XRT_QT_PROG_SET_441_0G [0x1b9]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 07
2013/12/10	10:27:58.0	XRT_FL_PROG_SET_420_0G [0x1a4]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 13
2013/12/10	10:28:00.0	XRT_CTRL_AUTO_408_0G [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/12/10	15:14:30.0	XRT_CTRL_MANU_400_0G [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/12/10	15:14:32.0	XRT_FLD_RESET_415_0G [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/12/10	15:14:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/12/10	15:17:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/12/10	15:37:30.0	XRT_Custom_430_0G [0x1ae]		
2013/12/10	15:38:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/12/10	16:50:30.0	XRT_CTRL_MANU_400_0G [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/12/10	16:50:32.0	XRT_FLD_RESET_415_0G [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/12/10	16:50:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/12/10	16:53:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/12/10	17:14:30.0	XRT_Custom_430_0G [0x1ae]		
2013/12/10	17:15:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/12/10	17:59:54.0	XRT_CTRL_MANU_402_0G [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/12/10	17:59:56.0	XRT_FOCUS_POSITION_403_0G [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2013/12/10	18:00:00.0	AOCS_ORe-point_Start_2_0G [0x098]		
		AOCU_NM	5	02-76 00 00 00 00 00
2013/12/10	18:00:16.0	XRT_FLD_DIS_434_0G [0x1b2]		
		MDP_XRT_FLD_DIS	1	07-F0 d9
2013/12/10	18:02:54.0	XRT_FLRCTRL_DIS_405_0G [0x195]		
		MDP_XRT_FLRCTRL_DIS	1	07-F0 c9
2013/12/10	18:02:56.0	XRT_ARS_DIS_423_0G [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2013/12/10	18:02:58.0	XRT_QT_PROG_SET_446_0G [0x1be]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 11
2013/12/10	18:03:00.0	XRT_CTRL_AUTO_408_0G [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/12/10	18:09:54.0	XRT_CTRL_MANU_402_0G [0x192]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/12/10	18:09:56.0	XRT_FOCUS_POSITION_403_0G [0x193]		
		XRT_FOCUS_POSITION	4	07-F8 22 ff aa 00
2013/12/10	18:10:00.0	AOCS_ORe-point_Start_1_0G [0x097]		
		AOCU_NM	5	02-76 01 00 00 00 00
2013/12/10	18:10:16.0	XRT_FLD_ENA_411_0G [0x19b]		
		MDP_XRT_FLD_ENA	1	07-F0 d8
2013/12/10	18:10:18.0	XRT_FLRCTRL_ENA_412_0G [0x19c]		
		MDP_XRT_FLRCTRL_ENA	1	07-F0 c8
2013/12/10	18:10:20.0	XRT_AEC_RESET_413_0G [0x19d]		
		MDP_XRT_AEC_RESET	1	07-F0 d0
2013/12/10	18:10:22.0	XRT_ARS_DIS_423_0G [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2013/12/10	18:10:24.0	XRT_FLD_RESET_415_0G [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/12/10	18:10:26.0	XRT_QT_PROG_SET_429_0G [0x1ad]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 09
2013/12/10	18:12:58.0	XRT_FL_PROG_SET_420_0G [0x1a4]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 13
2013/12/10	18:13:00.0	XRT_CTRL_AUTO_408_0G [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2013/12/10	18:27:30.0	XRT_CTRL_MANU_400_0G [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2013/12/10	18:27:32.0	XRT_FLD_RESET_415_0G [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2013/12/10	18:27:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2013/12/10	18:30:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2013/12/10	18:51:00.0	XRT_Custom_430_0G [0x1ae]		

Tuesday December 10, 2013

1/6

Dec 10, 13 12:38

XRT_OGLIST_0961.chk

Page 2/6

2013/12/10	18:52:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/10	20:05:00.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/10	20:05:02.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/10	20:05:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/10	20:08:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/10	20:28:30.0	XRT_Custom_430_0G [0x1ae]			
2013/12/10	20:29:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/10	21:42:30.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/10	21:42:32.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/10	21:42:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/10	21:45:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/10	22:04:30.0	XRT_Custom_430_0G [0x1ae]			
2013/12/10	22:05:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/10	23:19:30.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/10	23:19:32.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/10	23:19:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/10	23:22:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/10	23:33:00.0	XRT_Custom_430_0G [0x1ae]			
2013/12/10	23:34:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/11	00:48:30.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/11	00:48:32.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/11	00:48:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/11	00:51:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/11	01:03:00.0	XRT_Custom_430_0G [0x1ae]			
2013/12/11	01:04:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/11	02:19:00.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/11	02:19:02.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/11	02:19:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/11	02:22:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/11	02:39:30.0	XRT_Custom_430_0G [0x1ae]			
2013/12/11	02:40:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/11	03:47:00.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/11	03:47:02.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/11	03:47:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/11	03:50:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/11	04:16:30.0	XRT_Custom_430_0G [0x1ae]			
2013/12/11	04:17:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/11	05:27:30.0	XRT_CTRL_MANU_400_0G [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/11	05:27:32.0	XRT_FLD_RESET_415_0G [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/11	05:27:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/11	05:30:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/11	05:54:00.0	XRT_Custom_430_0G [0x1ae]			
2013/12/11	05:55:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/11	06:03:54.0	XRT_CTRL_MANU_402_0G [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/11	06:03:56.0	XRT_FOCUS_POSITION_403_0G [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/12/11	06:04:00.0	AOCS_Ore-point_Start_2_0G [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/12/11	06:04:16.0	XRT_FLD_DIS_434_0G [0x1b2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/12/11	06:06:54.0	XRT_FLRCTRL_DIS_405_0G [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/12/11	06:06:56.0	XRT_ARS_DIS_423_0G [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/12/11	06:06:58.0	XRT_QT_PROG_SET_446_0G [0x1be]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11

Dec 10, 13 12:38

XRT_OGLIST_0961.chk

Page 3/6

2013/12/11	06:07:00.0	XRT_CTRL_AUTO_408_0G [0x198]	1	07-F0	c0
2013/12/11	06:13:54.0	XRT_CTRL_MANU_402_0G [0x192]	1	07-F0	c1
2013/12/11	06:13:56.0	XRT_FOCUS_POSITION_410_0G [0x19a]	4	07-F8	22 fe 97 00
2013/12/11	06:14:00.0	AOCS_0Re-point_Start_3_0G [0x099]	5	02-76	02 00 00 00 00
2013/12/11	06:14:16.0	XRT_FLD_ENA_411_0G [0x19b]	1	07-F0	d8
2013/12/11	06:14:18.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	1	07-F0	c8
2013/12/11	06:14:20.0	XRT_AEC_RESET_413_0G [0x19d]	1	07-F0	d0
2013/12/11	06:14:22.0	XRT_ARS_DIS_423_0G [0x1a7]	1	07-F0	d5
2013/12/11	06:14:24.0	XRT_FLD_RESET_415_0G [0x19f]	1	07-F0	da
2013/12/11	06:14:26.0	XRT_QT_PROG_SET_441_0G [0x1b9]	2	07-F0	c4 07
2013/12/11	06:16:58.0	XRT_FL_PROG_SET_420_0G [0x1a4]	2	07-F0	c5 13
2013/12/11	06:17:00.0	XRT_CTRL_AUTO_408_0G [0x198]	1	07-F0	c0
2013/12/11	07:08:00.0	XRT_CTRL_MANU_400_0G [0x190]	1	07-F0	c1
2013/12/11	07:08:02.0	XRT_FLD_RESET_415_0G [0x19f]	1	07-F0	da
2013/12/11	07:08:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]	1	07-F0	e8
2013/12/11	07:11:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	1	07-F0	e9
2013/12/11	07:31:30.0	XRT_Custom_430_0G [0x1ae]			
2013/12/11	07:32:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	1	07-F0	c0
2013/12/11	08:47:30.0	XRT_CTRL_MANU_400_0G [0x190]	1	07-F0	c1
2013/12/11	08:47:32.0	XRT_FLD_RESET_415_0G [0x19f]	1	07-F0	da
2013/12/11	08:47:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]	1	07-F0	e8
2013/12/11	08:50:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	1	07-F0	e9
2013/12/11	08:59:54.0	XRT_CTRL_MANU_402_0G [0x192]	1	07-F0	c1
2013/12/11	08:59:56.0	XRT_FOCUS_POSITION_403_0G [0x193]	4	07-F8	22 ff aa 00
2013/12/11	09:00:00.0	AOCS_0Re-point_Start_4_0G [0x09a]	5	02-76	00 54 72 01 58
2013/12/11	09:00:16.0	XRT_FLD_ENA_411_0G [0x19b]	1	07-F0	d8
2013/12/11	09:00:18.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	1	07-F0	c8
2013/12/11	09:00:20.0	XRT_AEC_RESET_413_0G [0x19d]	1	07-F0	d0
2013/12/11	09:00:22.0	XRT_ARS_DIS_423_0G [0x1a7]	1	07-F0	d5
2013/12/11	09:00:24.0	XRT_FLD_RESET_415_0G [0x19f]	1	07-F0	da
2013/12/11	09:00:26.0	XRT_QT_PROG_SET_429_0G [0x1ad]	2	07-F0	c4 09
2013/12/11	09:02:58.0	XRT_FL_PROG_SET_420_0G [0x1a4]	2	07-F0	c5 13
2013/12/11	09:07:30.0	XRT_Custom_430_0G [0x1ae]			
2013/12/11	09:08:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	1	07-F0	c0
2013/12/11	10:30:00.0	XRT_CTRL_MANU_400_0G [0x190]	1	07-F0	c1
2013/12/11	10:30:02.0	XRT_FLD_RESET_415_0G [0x19f]	1	07-F0	da
2013/12/11	10:30:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]	1	07-F0	e8
2013/12/11	10:33:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	1	07-F0	e9
2013/12/11	10:35:30.0	XRT_Custom_430_0G [0x1ae]			
2013/12/11	10:36:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	1	07-F0	c0
2013/12/11	14:16:30.0	XRT_CTRL_MANU_400_0G [0x190]	1	07-F0	c1
2013/12/11	14:16:32.0	XRT_FLD_RESET_415_0G [0x19f]	1	07-F0	da
2013/12/11	14:16:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]	1	07-F0	e8
2013/12/11	14:19:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	1	07-F0	e9
2013/12/11	14:25:00.0	XRT_Custom_430_0G [0x1ae]			
2013/12/11	14:26:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	1	07-F0	c0
2013/12/11	15:50:00.0	XRT_CTRL_MANU_400_0G [0x190]	1	07-F0	c1
2013/12/11	15:50:02.0	XRT_FLD_RESET_415_0G [0x19f]	1	07-F0	da
2013/12/11	15:50:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]			

Dec 10, 13 12:38

XRT_OGLIST_0961.chk

Page 4/6

2013/12/11	15:53:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/11	15:59:54.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/11	15:59:56.0	XRT_FOCUS_POSITION_403_0G [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/11	16:00:00.0	AOCS_ORe-point_Start_3_0G [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/12/11	16:00:16.0	XRT_FLD_ENA_411_0G [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00
2013/12/11	16:00:18.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/12/11	16:00:20.0	XRT_AEC_RESET_413_0G [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/12/11	16:00:22.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/12/11	16:00:24.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/12/11	16:00:26.0	XRT_QT_PROG_SET_429_0G [0x1ad]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/11	16:02:58.0	XRT_FL_PROG_SET_420_0G [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2013/12/11	16:13:30.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2013/12/11	16:14:30.0	XRT_CTRL_AUTO_424_0G [0x1a8]	XRT_Custom_430_0G [0x1ae]			
2013/12/11	17:27:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/11	17:27:02.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/11	17:27:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/11	17:30:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/11	17:50:30.5	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/11	17:51:30.5	XRT_CTRL_AUTO_424_0G [0x1a8]	XRT_Custom_430_0G [0x1ae]			
2013/12/11	18:06:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/11	18:06:26.0	XRT_FOCUS_POSITION_403_0G [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/11	18:06:30.0	AOCS_ORe-point_Start_2_0G [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/12/11	18:06:46.0	XRT_FLD_DIS_434_0G [0x1b2]	AOCU_NM	5	02-76	00 00 00 00 00
2013/12/11	18:09:24.0	XRT_FLRCTRL_DIS_405_0G [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2013/12/11	18:09:26.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/12/11	18:09:28.0	XRT_QT_PROG_SET_446_0G [0x1be]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/12/11	18:09:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2013/12/11	18:16:24.0	XRT_CTRL_MANU_402_0G [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/11	18:16:26.0	XRT_FOCUS_POSITION_410_0G [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/11	18:16:30.0	AOCS_ORe-point_Start_3_0G [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/12/11	18:16:46.0	XRT_FLD_ENA_411_0G [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00
2013/12/11	18:16:48.0	XRT_FLRCTRL_ENA_412_0G [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/12/11	18:16:50.0	XRT_AEC_RESET_413_0G [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/12/11	18:16:52.0	XRT_ARS_DIS_423_0G [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/12/11	18:16:54.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/12/11	18:16:56.0	XRT_QT_PROG_SET_441_0G [0x1b9]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/11	18:19:28.0	XRT_FL_PROG_SET_420_0G [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2013/12/11	18:19:30.0	XRT_CTRL_AUTO_408_0G [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2013/12/11	19:04:00.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/11	19:04:02.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/11	19:04:04.0	XRT_PREFLR_STRT_418_0G [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/11	19:07:14.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/11	19:27:00.0	XRT_Custom_430_0G [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/11	19:28:00.0	XRT_CTRL_AUTO_424_0G [0x1a8]	XRT_Custom_430_0G [0x1ae]			
2013/12/11	20:41:30.0	XRT_CTRL_MANU_400_0G [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/11	20:41:32.0	XRT_FLD_RESET_415_0G [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/11	20:41:34.0	XRT_PREFLR_STRT_418_0G [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/11	20:44:44.0	XRT_PREFLR_STOP_419_0G [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8

Dec 10, 13 12:38

XRT_OGLIST_0961.chk

Page 5/6

2013/12/11	21:04:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/11	21:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/11	22:19:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/11	22:19:02.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/11	22:19:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/11	22:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/11	22:40:00.0	XRT_Custom_430_OG [0x1ae]				
2013/12/11	22:41:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/11	23:56:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/11	23:56:02.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/11	23:56:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/11	23:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/12	00:04:30.0	XRT_Custom_430_OG [0x1ae]				
2013/12/12	00:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/12	01:20:30.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/12	01:20:32.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/12	01:20:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/12	01:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/12	01:39:00.0	XRT_Custom_430_OG [0x1ae]				
2013/12/12	01:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/12	02:55:00.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/12	02:55:02.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/12	02:55:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/12	02:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/12	03:15:30.0	XRT_Custom_430_OG [0x1ae]				
2013/12/12	03:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/12	04:24:30.0	XRT_CTRL_MANU_400_OG [0x190]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/12	04:24:32.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/12	04:24:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/12	04:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/12	04:53:00.0	XRT_Custom_430_OG [0x1ae]				
2013/12/12	04:54:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/12	05:37:24.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/12	05:37:26.0	XRT_FOCUS_POSITION_403_OG [0x193]				
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/12/12	05:37:30.0	AOCS_ORe-point_Start_2_OG [0x098]				
			AOCU_NM	5	02-76	00 00 00 00 00
2013/12/12	05:37:46.0	XRT_FLD_DIS_434_OG [0x1b2]				
			MDP_XRT_FLD_DIS	1	07-F0	d9
2013/12/12	05:40:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/12/12	05:40:26.0	XRT_ARS_DIS_423_OG [0x1a7]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2013/12/12	05:40:28.0	XRT_QT_PROG_SET_446_OG [0x1be]				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2013/12/12	05:40:30.0	XRT_CTRL_AUTO_408_OG [0x198]				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/12	05:47:24.0	XRT_CTRL_MANU_402_OG [0x192]				
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/12	05:47:26.0	XRT_FOCUS_POSITION_403_OG [0x193]				
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/12/12	05:47:30.0	AOCS_ORe-point_Start_3_OG [0x099]				
			AOCU_NM	5	02-76	02 00 00 00 00
2013/12/12	05:47:46.0	XRT_FLD_ENA_411_OG [0x19b]				
			MDP_XRT_FLD_ENA	1	07-F0	d8
2013/12/12	05:47:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/12/12	05:47:50.0	XRT_AEC_RESET_413_OG [0x19d]				
			MDP_XRT_AEC_RESET	1	07-F0	d0
2013/12/12	05:47:52.0	XRT_ARS_DIS_423_OG [0x1a7]				
			MDP_XRT_ARS_DIS	1	07-F0	d5
2013/12/12	05:47:54.0	XRT_FLD_RESET_415_OG [0x19f]				
			MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/12	05:47:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 09

Dec 10, 13 12:38

XRT_OGLIST_0961.chk

Page 6/6

2013/12/12	05:50:28.0	XRT_FL_PROG_SET_420_OG [0x1a4]			
		MDP_XRT_FL_PROG_SET	2	07-F0 c5	13
2013/12/12	05:50:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2013/12/12	06:05:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2013/12/12	06:05:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2013/12/12	06:05:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2013/12/12	06:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2013/12/12	06:30:30.5	XRT_Custom_430_OG [0x1ae]			
2013/12/12	06:31:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2013/12/12	07:45:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2013/12/12	07:45:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2013/12/12	07:45:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2013/12/12	07:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2013/12/12	08:07:30.0	XRT_Custom_430_OG [0x1ae]			
2013/12/12	08:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0 c0	
2013/12/12	09:25:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0 c1	
2013/12/12	09:25:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0 da	
2013/12/12	09:25:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0 e8	
2013/12/12	09:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0 e9	
2013/12/12	10:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76 00 00 00 00 00	