

XRT Timeline to be uploaded on 2013/12/14

Period: 2013/12/14 09:26:00 - 2013/12/19 09:13:00

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Normal mode

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XOB #19DA: Synoptic Q95 2x2 - Al/mesh(5/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/362/1443) + Thin

Term	Pointing (x, y)	Comment
12/15 17:56:24 - 12/15 18:01:54	Fixed (0.0, 0.0)	synoptic, shifted -8.0 min
12/16 04:08:00 - 12/16 04:14:54	Fixed (0.0, 0.0)	synoptic, shifted 2 hours
12/16 17:58:30 - 12/16 18:05:24	Fixed (0.0, 0.0)	synoptic, shifted -4.5 min
12/17 04:03:00 - 12/17 04:09:54	Fixed (0.0, 0.0)	synoptic, shifted 2 hours 28.0 min
PROG= 17 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 84 1-time(s) 4.0sec		
└─ Open/Al-mesh Open/Al-mesh close	Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Open/Al-mesh Open/Al-mesh close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Open/Al-mesh Open/Al-mesh close	Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Seqn= 5 1-time(s) 2.0sec		
└─ Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
└─ Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
└─ Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024)	Q=98 0 0 2.0sec
└─ Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024)	DPCM 0 0 2.0sec
└─ Open/Ti-poly Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024)	DPCM 0 0 2.0sec
└─ Seqn= 86 1-time(s) 4.0sec		
└─ Open/Ti-poly Open/Ti-poly close	Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Open/Ti-poly Open/Ti-poly close	Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Open/Ti-poly Open/Ti-poly close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Seqn= 95 1-time(s) 2.0sec		
└─ thin-Be/Open thin-Be/Open close	Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ thin-Be/Open thin-Be/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ thin-Be/Open thin-Be/Open close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Seqn= 6 1-time(s) 2.0sec		
└─ Open/G-band Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024)	Q=90 0 0 2.0sec
└─ Open/G-band Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #19E0: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)

Term	Pointing (x, y)	Comment
12/15 18:05:00 - 12/15 23:59:54	Track (-29.6, 93.2) @ 12/15 18:02:00	# AR cont.
12/16 00:51:30 - 12/16 03:35:00	Track (33.2, 93.8) @ 12/16 00:40:00	# AR 11921 (HOP 173 from 2-4UT).
12/16 04:18:00 - 12/16 09:33:14	Track (573.8, -284.1) @ 12/16 04:15:00	# HOP 173 (AR 11917) until 6UT.
PROG= 20 Inf.-time(s)		
└─ Subr= 1 1-time(s) 2.0sec		
└─ Seqn= 8 2-time(s) 2.0sec		
└─ Open/G-band Open/G-band close	Safe Norm 44ms Obs 1x1 384x384 (1064, 1048)	DPCM 0 0 2.0sec
└─ Subr= 2 2-time(s) 2.0sec		
└─ Seqn= 24 1-time(s) 2.0sec		
└─ Open/Ti-poly Open/thick-Al close	Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048)	Q=98 0 0 2.0sec
└─ Open/G-band Open/G-band open	Safe Norm 32ms Obs 1x1 384x384 (1064, 1048)	Q=98 0 0 2.0sec
└─ Seqn= 42 4-time(s) 2.0sec		
└─ Al-poly/Open thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
└─ thin-Be/Open med-Be/Open close	Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048)	Q=95 3 0 2.0sec
└─ Open/thick-Al Open/thick-Al close	Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
└─ Seqn= 62 36-time(s) 50.0sec		
└─ thin-Be/Open med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
└─ Al-poly/Open thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 0 2.0sec
└─ thin-Be/Open med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048)	Q=95 3 1 2.0sec
└─ Al-poly/Open thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 1 2.0sec
└─ thin-Be/Open med-Be/Open close	Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048)	Q=95 3 2 2.0sec
└─ Al-poly/Open thin-Be/Open close	Safe Norm 500ms Obs 1x1 384x384 (1064, 1048)	Q=95 3 2 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #19BA: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment
12/16 00:03:00 - 12/16 00:09:54	Fixed (-528.4, -528.4)	# XRT post bake-out quadrants 1/4
PROG= 06 1-time(s)		
└─ Subr= 1 1-time(s) 12.0sec		
└─ Seqn= 1 1-time(s) 12.0sec		
└─ Open/G-band Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (1536, 1536)	Q=90 0 0 2.0sec
└─ Open/G-band Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (1536, 1536)	Q=90 0 0 2.0sec
└─ Open/thick-Be Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (1536, 1536)	Q=98 0 0 2.0sec
└─ Open/thick-Be Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (1536, 1536)	Q=98 0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec		
└─ Seqn= 23 2-time(s) 2.0sec		
└─ Open/Al-mesh Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Open/Ti-poly Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024)	Q=95 0 0 2.0sec
└─ Subr= 3 2-time(s) 2.0sec		

Subr= 1	1-time(s)	2.0sec																		
└─ Seqn= 8	2-time(s)	2.0sec																		
└─ Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec								
Subr= 2	2-time(s)	2.0sec																		
└─ Seqn= 24	1-time(s)	2.0sec																		
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec								
└─ Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec								
└─ Seqn= 42	4-time(s)	2.0sec																		
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec								
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec								
└─ Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec								
└─ Seqn= 62	15-time(s)	120.0sec																		
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec								
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec								
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec								
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec								
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec								
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec								
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval									

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Flare mode

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XOB #1980: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops-2

Term	Pointing (x, y)	Comment
12/15 18:05:00 - 12/15 23:59:54	Track (-29.6, 93.2) @ 12/15 18:02:00	# AR cont.
12/16 00:51:30 - 12/16 03:35:00	Track (33.2, 93.8) @ 12/16 00:40:00	# AR 11921 (HOP 173 from 2-4UT).
12/16 04:18:00 - 12/16 09:33:14	Track (573.8, -284.1) @ 12/16 04:15:00	# HOP 173 (AR 11917) until 6UT.
12/16 09:36:20 - 12/16 13:59:54	Fixed (-952.3, -206.6)	# Observe new region at East limb for EIS.
12/16 14:03:00 - 12/16 17:55:24	Track (645.5, 287.0) @ 12/16 14:00:00	# AR 11923 / Coronal Hole Boundary observations for EIS.
12/16 18:08:30 - 12/17 03:59:54	Track (196.7, 95.0) @ 12/16 18:05:30	# return to AR 11921 (HOP 173 from 2-4UT).
12/17 04:41:30 - 12/17 09:46:54	Track (724.5, -285.3) @ 12/17 04:10:00	# HOP 173 (4-6UT) on AR 11917.

PROG= 19 15-time(s)

Subr= 1	45-time(s)	10.0sec																		
└─ Seqn= 9	1-time(s)	2.0sec																		
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec								
Subr= 2	1-time(s)	10.0sec																		
└─ Seqn= 10	1-time(s)	2.0sec																		
└─ med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec								
└─ Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec								
└─ Seqn= 11	1-time(s)	2.0sec																		
└─ Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec								
└─ Seqn= 15	1-time(s)	2.0sec																		
└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec								
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec								
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec								
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval									

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Active Region Search

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NOT USED

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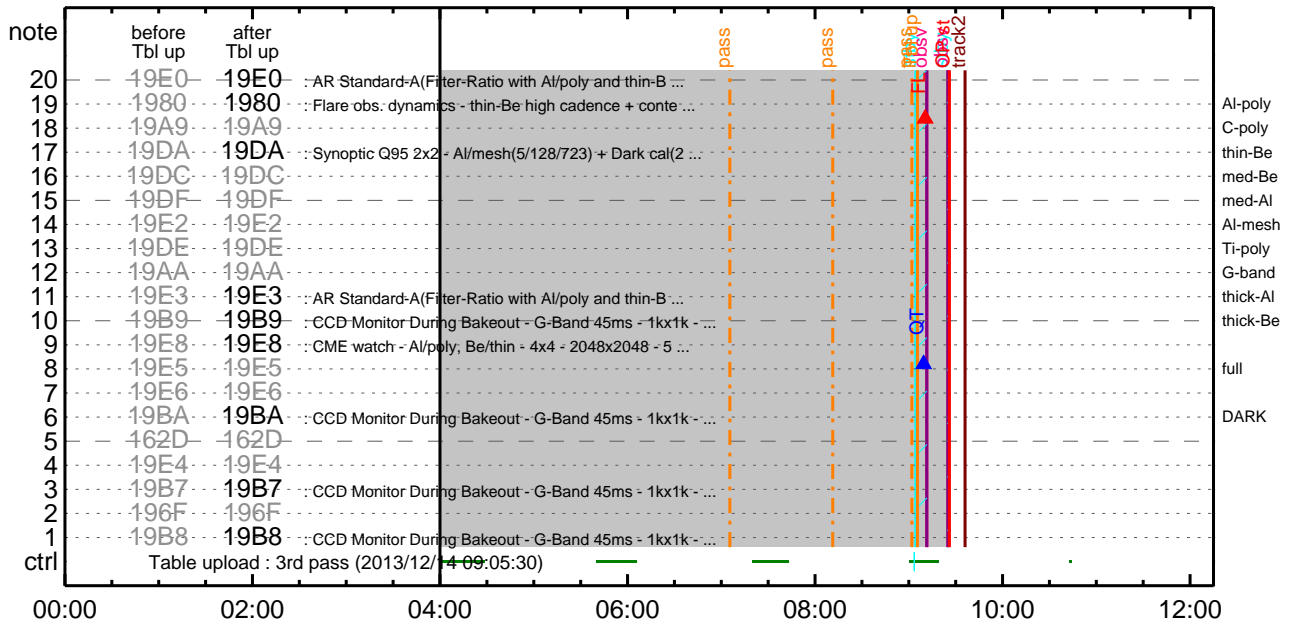
Flare Detection

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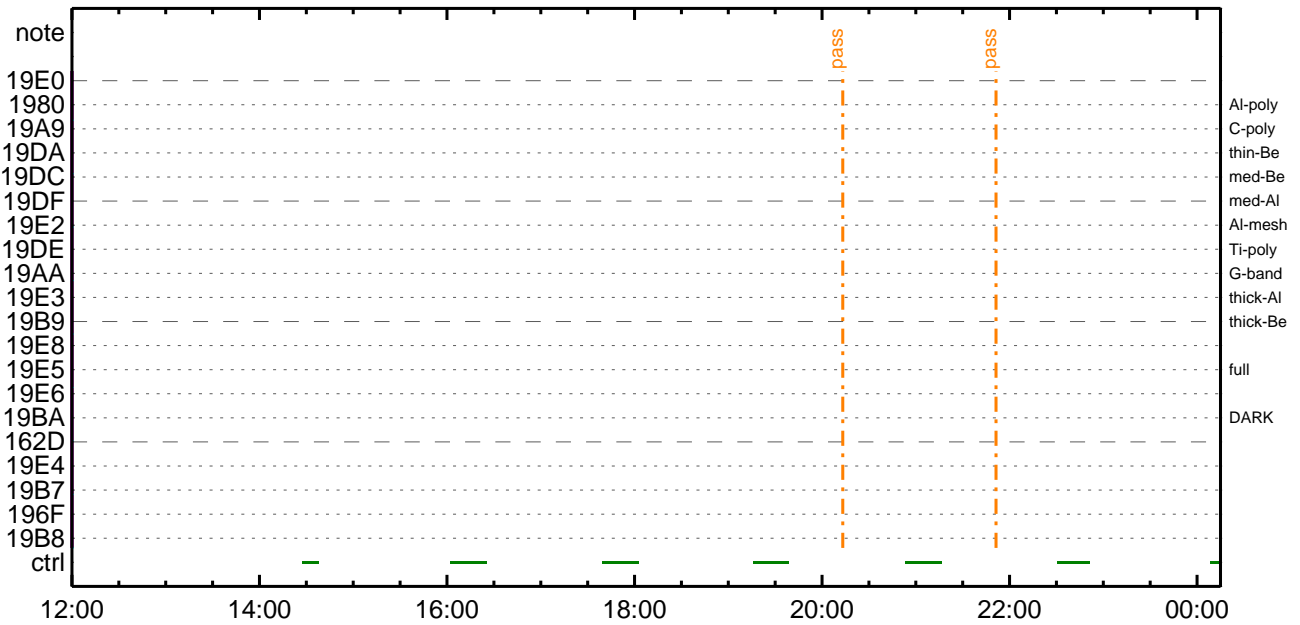
FLD Patrol

Term	Pointing (x, y)	Comment
12/15 18:02:16 - 12/16 00:02:54	Track (-29.6, 93.2) @ 12/15 18:02:00	# AR cont.
12/16 00:40:16 - 12/16 04:05:16	Track (33.2, 93.8) @ 12/16 00:40:00	# AR 11921 (HOP 173 from 2-4UT).
12/16 04:15:16 - 12/16 17:55:46	Track (573.8, -284.1) @ 12/16 04:15:00	# HOP 173 (AR 11917) until 6UT.
12/16 18:05:46 - 12/17 04:00:16	Track (196.7, 95.0) @ 12/16 18:05:30	# return to AR 11921 (HOP 173 from 2-4UT).
12/17 04:10:16 - 12/19 09:13:00	Track (724.5, -285.3) @ 12/17 04:10:00	# HOP 173 (4-6UT) on AR 11917.
Open/Ti-poly	Open/thick-Al	close Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

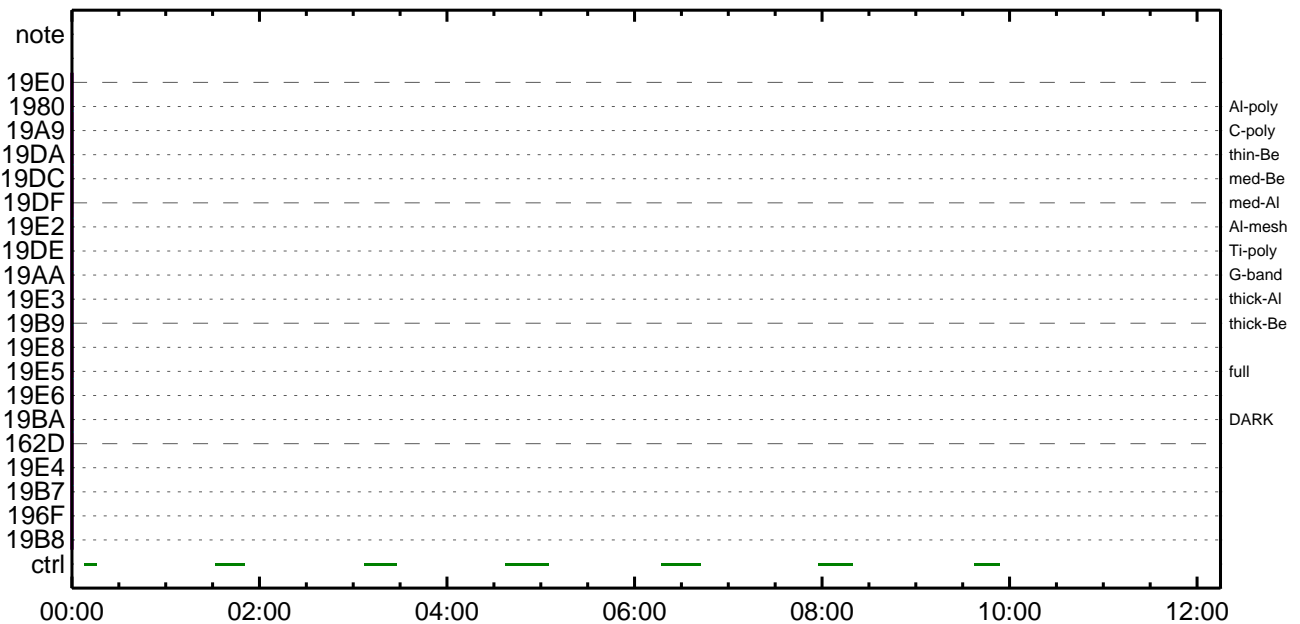
CMDI #0972 2013/12/14



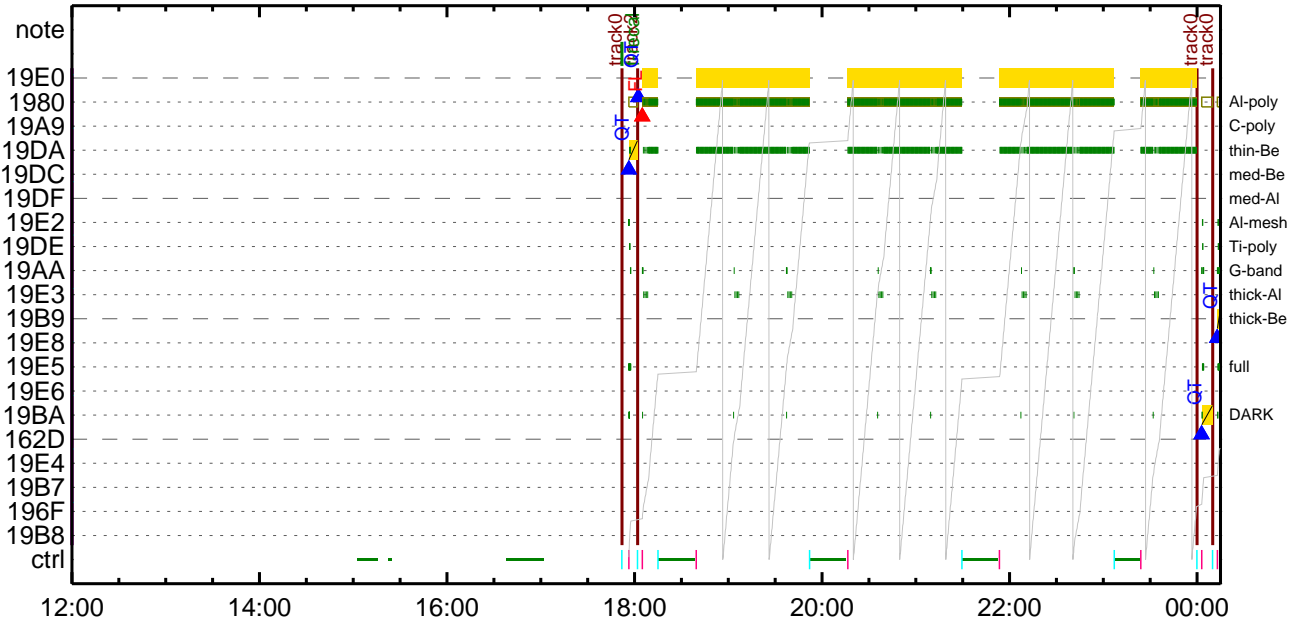
CMDI #0972 2013/12/14



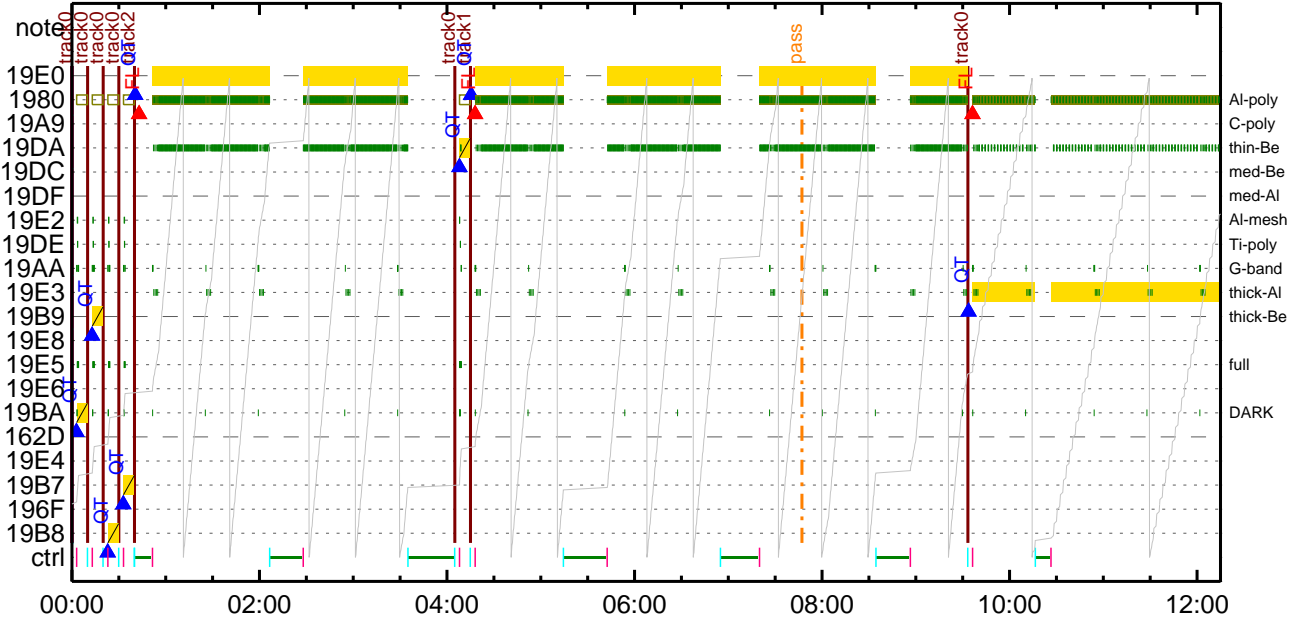
CMDI #0972 2013/12/15



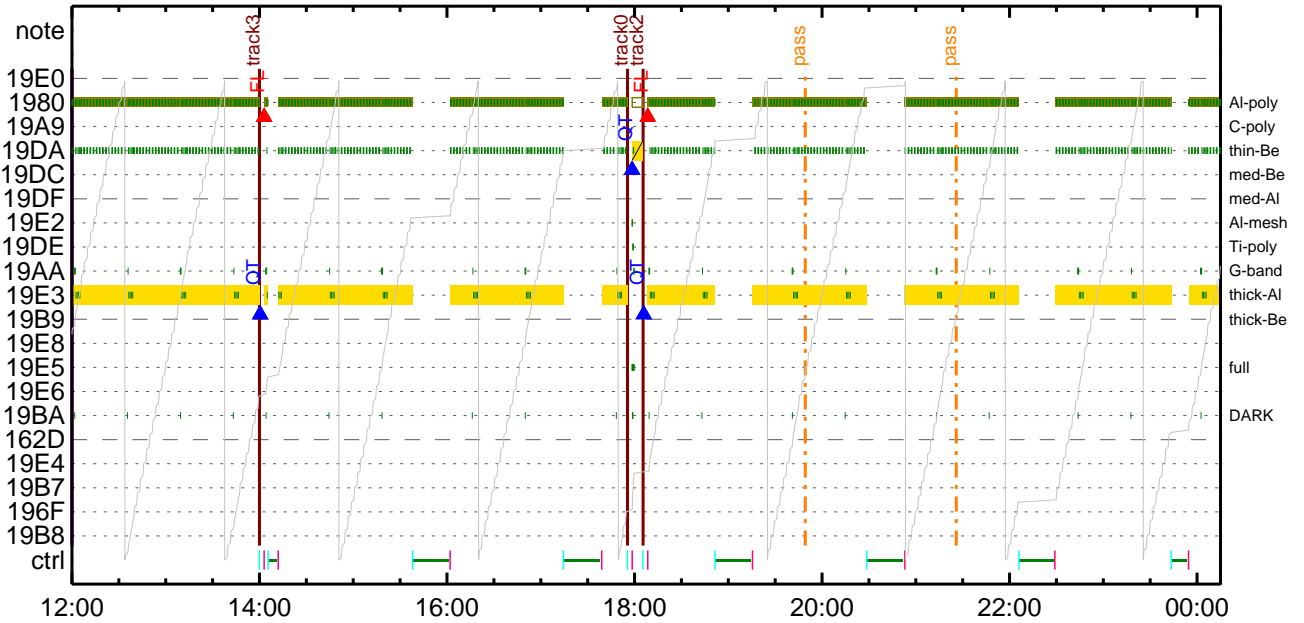
CMDI #0972 2013/12/15



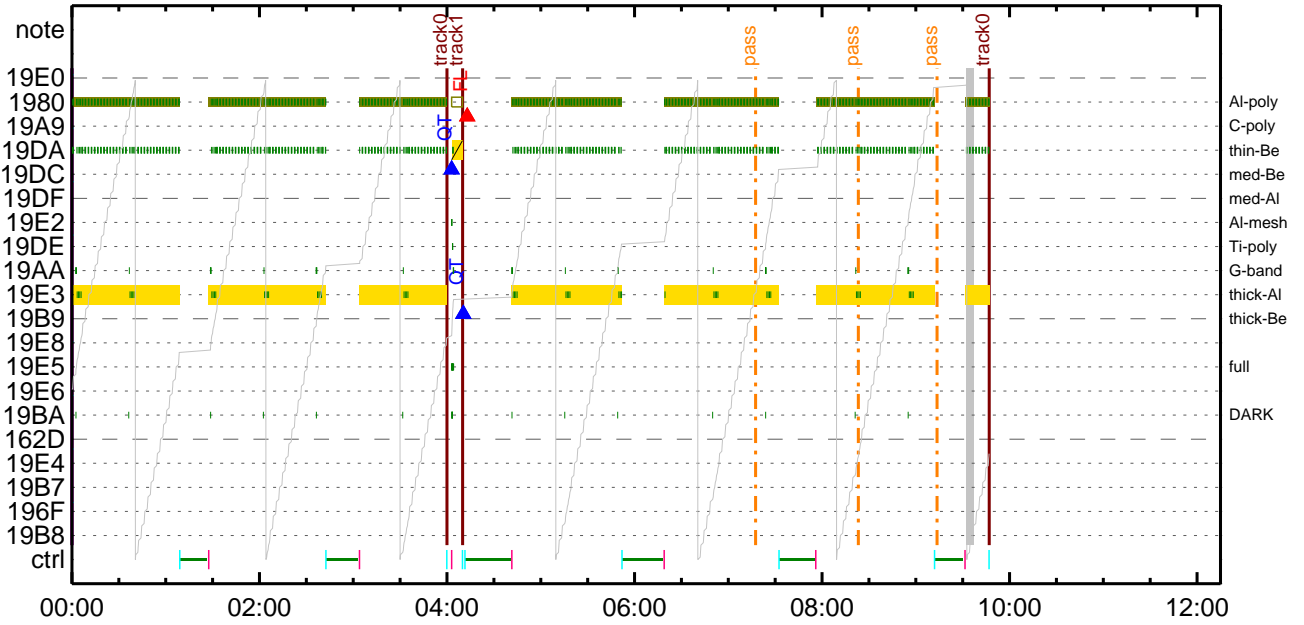
CMDI #0972 2013/12/16



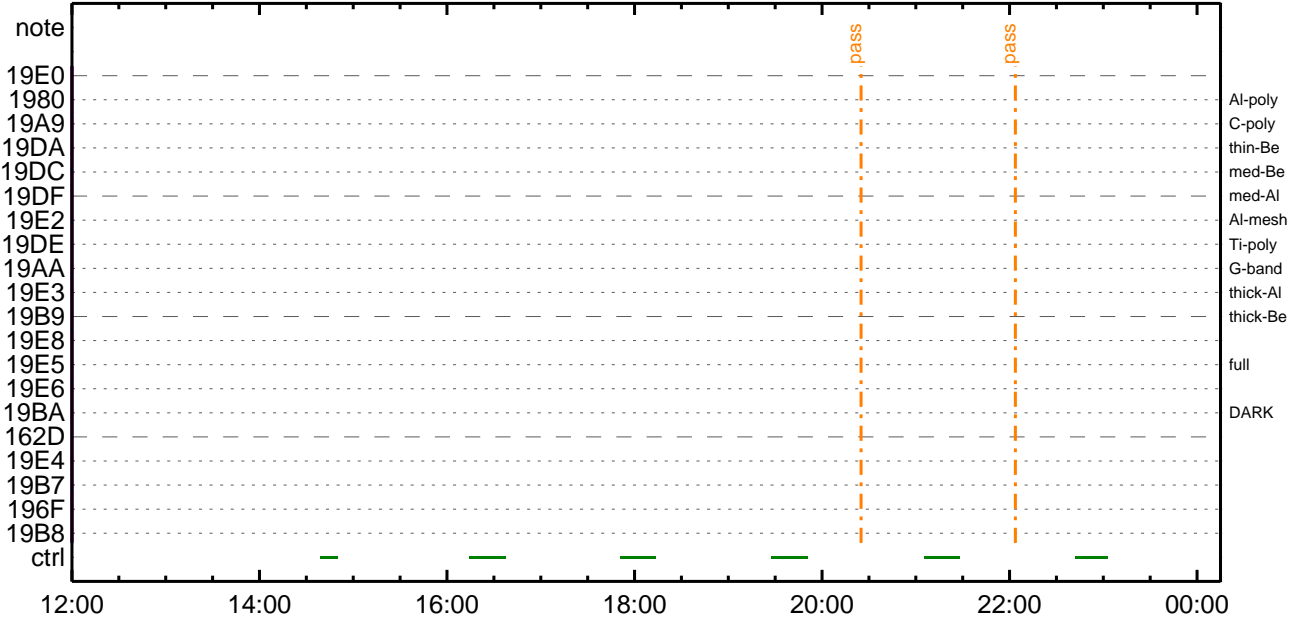
CMDI #0972 2013/12/16



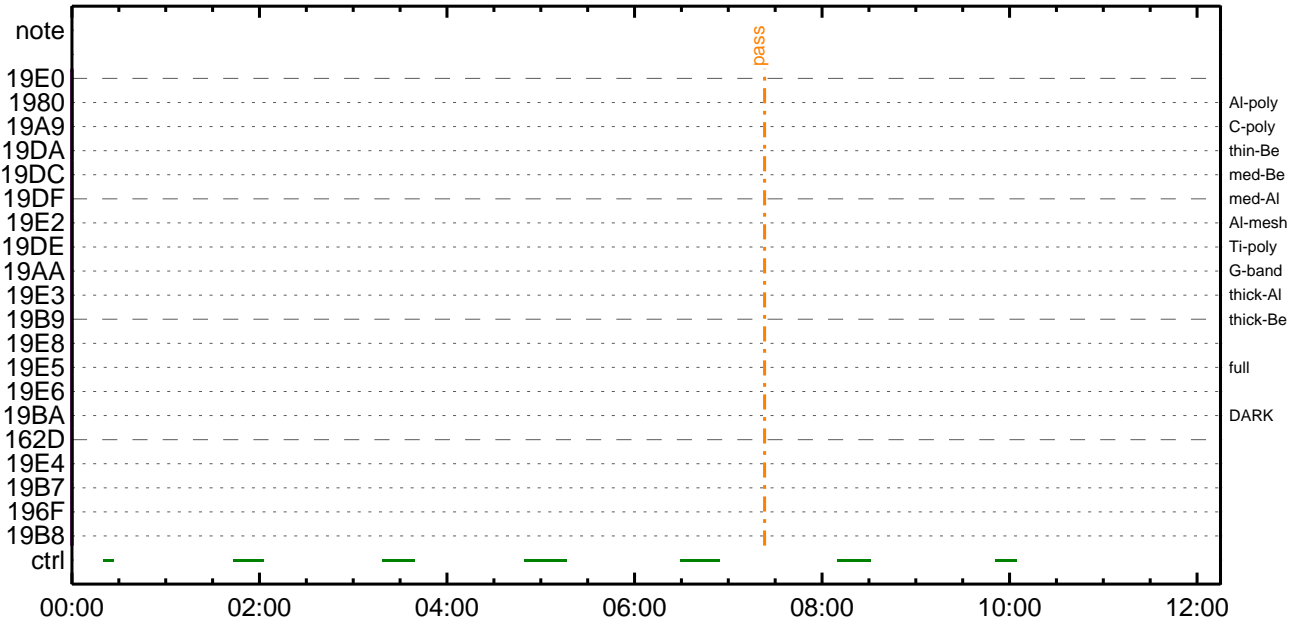
CMDI #0972 2013/12/17



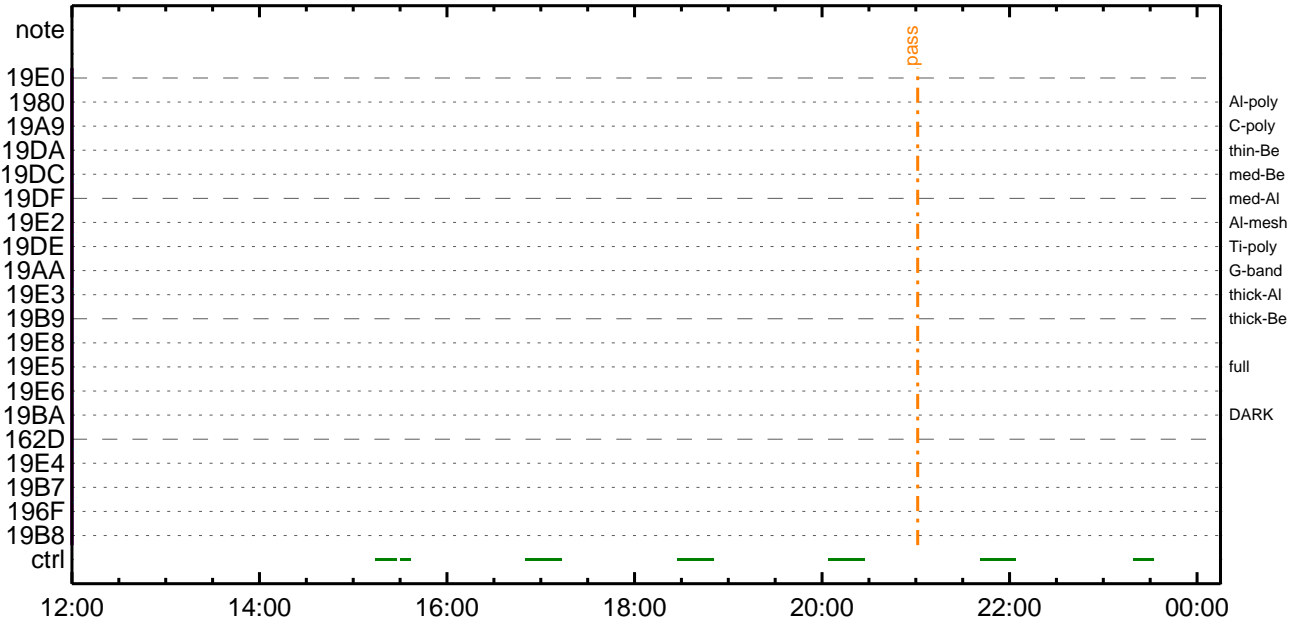
CMDI #0972 2013/12/17



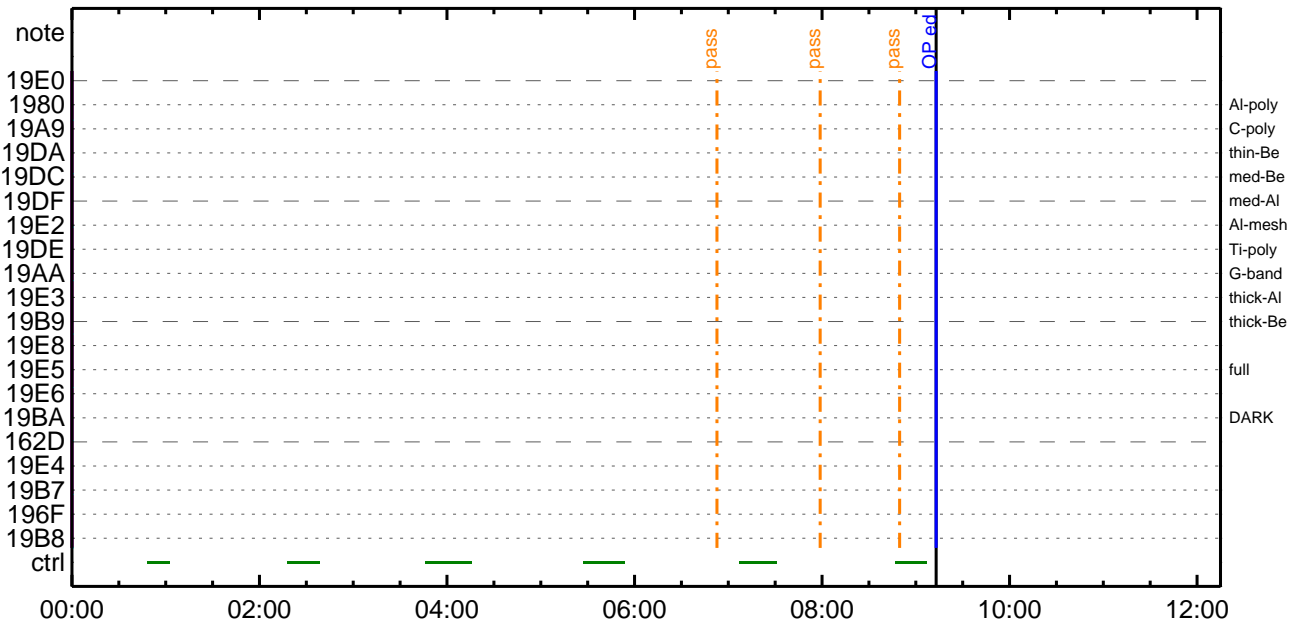
CMDI #0972 2013/12/18



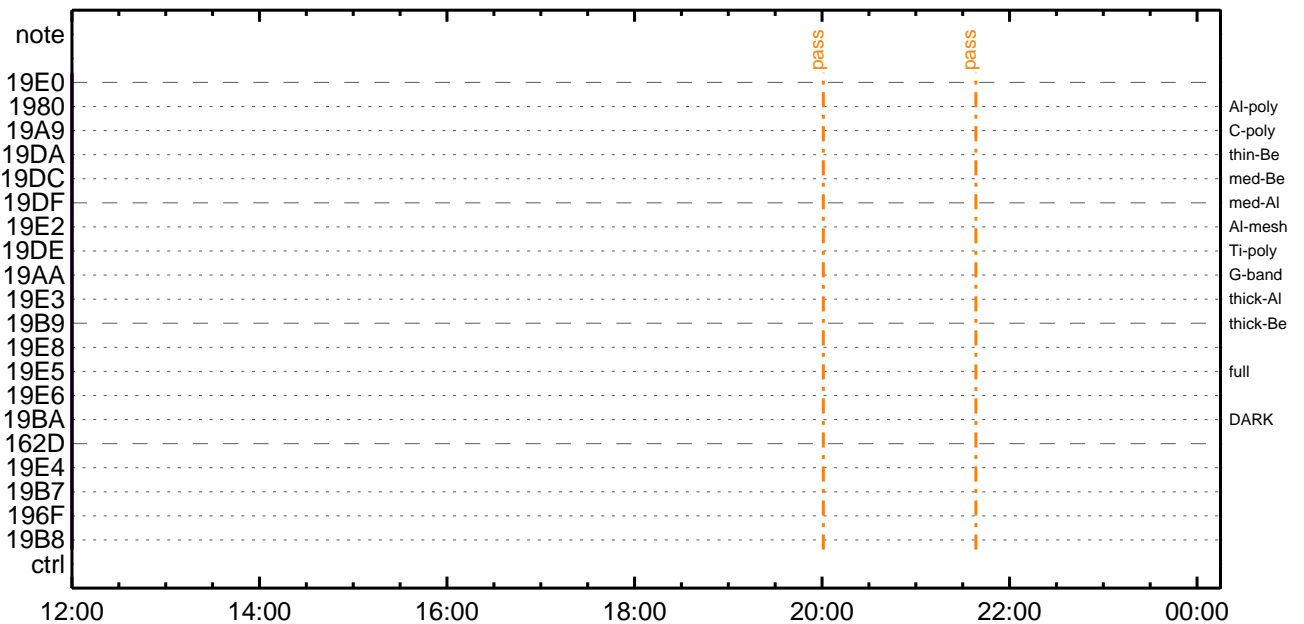
CMDI #0972 2013/12/18



CMDI #0972 2013/12/19



CMDI #0972 2013/12/19



(a) Spacecraft Operation Procedure (real-commands)

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main-099 2013-12-14 13:24:02 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YFYÖYÉÁ÷¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Èò¿òÁò•µ°Ë»Í×ÁÇòÍYçYÁY×Yí;¼YÉ;ÈÈè¿µ•íÉ;ÈòÈ¼°ÇÒò•ò¿¼í¹çòí;çÁ®, ùò¹òÈòòòçÁ÷¿®ò•òÈòòòòòÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÁYÖY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ä
0019 . S. OP op-099:OP
0020 ()
0021 . S. OG og-099:OG
0022 ()
0023 C.
0024 . C. ;ãNMOG&OPí°èYÁYÖY×;ä
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÀYÖY×½ªí»òð³ÍÇ§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOGòí¼È¹ç•è²ÍOKòð³ÍÇ§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÀYÖY×½ªí»òð³ÍÇ§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOGòí¼È¹ç•è²ÍOKòð³ÍÇ§
0062 C.
0063 C. NMOG(0x210000-0x210FFF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÀYÖY×½ªí»òð³ÍÇ§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OPòí¼È¹ç•è²ÍOKòð³ÍÇ§
0081 C.
0082 . C. ***** òÈ²¼òí¼Á´¶¼òÈÈ-òòÁ÷¿® (¼áµ-YÀYÖY×¼È½çòðÁÓÁæòç¾ªò"òÈ¼í¹çòçòá) *****
0083 C. DHUYá;¼YÉ;È¼Y½;Yí;¼YÉ;ÈòòÍáò¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE |§ OPOG UPLOADò-Á÷¿®NGòí¼Í¹ç;ç°È²¼òí¼TI-CMDÁ÷¿®òí¼Á¹Ôò•òÈòòòòòÈ;f

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0096 C.      0302; SET EDUMP I±°iYNY¹aÇ¹Ôa|a³aE; f
0097 C.
0098 C.      TI³YFÿÖYÉaðÄDİ¿ (UT)
0099 +. TI 2013-12-14 09:21:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.      çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0102 C.
0103 +. TI 2013-12-14 09:21:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.      çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0106 C.
0107 +. TI 2013-12-14 09:21:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.      çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0110 C.
0111 +. TI 2013-12-14 09:25:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.      çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0114 C.
0115 C.      °E²¼aİÄè%îÍÑaİYÁY§YÁY-¹àİÜ
0116 C.      çç[HK1_TI_CMD_ENA/DIS] EQ ENA
0117 C.      çç[HK1_TI_CMD_NUM] EQ 4
0118 C.      çç[HK1_NEXT_EXEC_PIM] EQ DHU
0119 C.      çç[HK1_NEXT_EXEC_DC] EQ 0xB3
0120 C.
0121 C.      *****
0122 C.      TIİİ°èYÁYÖY×
0123 C.      *****
0124 C.
0125 C.      TI_TBL(0x03AB00-0x03AEFF; § 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC      (03 ab 03 01 02)
0128 C.      çç[HK1_DMP_TOP_ADRS_1] EQ 07
0129 C.      çç[HK1_DMP_TOP_ADRS_0] EQ 2B
0130 C.      çç[HK1_DMP_BLOCK_NUM] EQ 3
0131 C.      çç[HK1_DMP_REPEAT_NUM] EQ 0
0132 C.      çç[HK1_DMA_DMP_PIM] EQ DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC      (07 0b f8)
0135 C.      çç[HK1_PKT_FORM_NO] EQ 7
0136 C.      çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0137 C.      çç[HK1_S_TLM_BIT_RATE] EQ 32k
0138 C.      çç[HK1_X_TLM_BIT_RATE] EQ 4M
0139 C.      çç[HK1_DMP_CHK_FLG] EQ EXEC
0140 C.
0141 C.      YÁYÖY×½ªİ»að³İÇ§
0142 C.      çç[HK1_DMP_CHK_FLG] EQ NON
0143 C.
0144 C.      RAM ID=TI_TBLaİ%È¹Ç•è²İOKað³İÇ§
0145 C.
0146 C.      DHUÿâ;¼YÉ;È¼Y¼;Yİ;¼YÈ;Èaðİáa¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC      (02 0a f8)
0149 C.      çç[HK1_PKT_FORM_NO] EQ 2
0150 C.      çç[HK1_PKT_GEN_TIME] EQ 0.5S
0151 C.      çç[HK1_S_TLM_BIT_RATE] EQ 32K
0152 C.      çç[HK1_X_TLM_BIT_RATE] EQ 4M
0153 C.
0154 C.      *****
0155 C.      SOT TI command set
0156 C.      *****
0157 C.      Execute, after the success of OP upload.
0158 +. TI 2013-12-14 09:25:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC      (41)
0161 C.      -----
0162 C.      HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C.      -----
0164 C.      ***** SOT END *****
0165 C.
0166 C.      ***** XRT START *****
0167 C.      Execute, after the success of OP upload.
0168 +. TI 2013-12-14 09:25:00.0
0169 DC 07-F0 MDP_XRT_MODE_STBY
0170 BC      (c3)
0171 C.      [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0172 C.
0173 C.      ***** XRT END *****
0174 C.      Stop EIS observation and temporarily disable EIS mode changes
0175 C.
0176 C.
0177 C.      ***** Start EIS operation (TI set) *****
0178 C.      Execute, after the success of OP upload.
0179 C.      Set EIS TI-commands
0180 +. TI 2013-12-14 09:25:30.0
0181 DC 07-FC EIS_MODE_MANU
0182 BC      (21 02)
0183 +. TI 2013-12-14 09:25:40.0
0184 DC 07-FC EIS_MODE_CHG_DIS
0185 BC      (22)
0186 C.      [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0187 C.      ***** End EIS operation (TI set) *****
0188 C.
0189 C.
0190 C.
0191 C.      ***** MDP 'üÄİaİ»ö¼YöÈÄa¹aèDCBC•x²è *****
0192 C.      (%â°İYÖYÁYÈYÿYÁYÇYèaÈ%¼a¼Ä»Üa¹aé)
0193 C.      DC-BC dcbc-402:DCBC
```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```


(a) Spacecraft Operation Procedure (real-commands)

```
main-101 2013-12-14 13:24:02 138 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYŞ;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëø¿òÁø•µ°E×Í×ÁÇøíYçYÁY×Yí;¼YÉ;ËÈè%µ•ííÉ;ËøÈ¼°ÇÓø•ø¿¼í¹çøí;çÁ®. ùø¹øÈøÈøçÁ+¿®ø•øÈøøøøøÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 C. ***** XRT START *****
0016 C.
0017 +. DC 07-F0 MDP_XRT_CTRL_MANU
0018 BC (c1)
0019 + DC 07-F0 MDP_XRT_MODE_STBY
0020 BC (c3)
0021 . C. ----- Success Verify ? OK / NG____
0022 C.
0023 C. XRT Obs. Table Upload
0024 . S. RAM ram-291:MDP_OBS_X
0025 ( )
0026 C.
0027 +. DC 07-F0 MDP_DUMP_XRTTBL
0028 BC (84 07 00 00 00 3a d4)
0029 . C. ----- Comparison Check ? OK / ERR ____
0030 C.
0031 C.
0032 +. DC 07-F0 MDP_XRT_ROI_SET
0033 BC (cd 01 b1 b1 04 04)
0034 + DC 07-F0 MDP_XRT_ROI_SET
0035 BC (cd 02 b1 b1 08 08)
0036 + DC 07-F0 MDP_XRT_ROI_SET
0037 BC (cd 03 b1 b1 08 08)
0038 + DC 07-F0 MDP_XRT_ROI_SET
0039 BC (cd 04 b1 b1 06 06)
0040 + DC 07-F0 MDP_XRT_ROI_SET
0041 BC (cd 05 85 83 06 06)
0042 + DC 07-F0 MDP_XRT_ROI_SET
0043 BC (cd 06 80 80 20 20)
0044 + DC 07-F0 MDP_XRT_ROI_SET
0045 BC (cd 07 85 83 06 06)
0046 + DC 07-F0 MDP_XRT_ROI_SET
0047 BC (cd 08 80 80 20 08)
0048 + DC 07-F0 MDP_XRT_ROI_SET
0049 BC (cd 09 80 80 08 20)
0050 + DC 07-F0 MDP_XRT_ROI_SET
0051 BC (cd 0a 85 83 08 08)
0052 + DC 07-F0 MDP_XRT_ROI_SET
0053 BC (cd 0b c0 c0 10 10)
0054 + DC 07-F0 MDP_XRT_ROI_SET
0055 BC (cd 0c 40 c0 10 10)
0056 + DC 07-F0 MDP_XRT_ROI_SET
0057 BC (cd 0d 40 40 10 10)
0058 + DC 07-F0 MDP_XRT_ROI_SET
0059 BC (cd 0e c0 40 10 10)
0060 + DC 07-F0 MDP_XRT_ROI_SET
0061 BC (cd 0f 80 80 06 06)
0062 + DC 07-F0 MDP_XRT_ROI_SET
0063 BC (cd 10 80 80 08 08)
0064 + DC 07-F0 MDP_XRT_FLD_ENA
0065 BC (d8)
0066 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0067 BC (c8)
0068 + DC 07-F0 MDP_XRT_AEC_RESET
0069 BC (d0)
0070 + DC 07-F0 MDP_XRT_ARS_DIS
0071 BC (d5)
0072 + DC 07-F0 MDP_XRT_FLD_RESET
0073 BC (da)
0074 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0075 BC (c4 09)
0076 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0077 BC (c5 13)
0078 . C. ----- Success Verify ? OK / NG ____
0079 C.
0080 C.
0081 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0082 C.
0083 +. DC 07-F0 MDP_XRT_MODE_OBSV
0084 BC (c2)
0085 +. TI 2013-12-14 09:25:02.0
0086 DC 07-F0 MDP_XRT_MODE_OBSV
0087 BC (c2)
0088 . C. ----- Success Verify ? OK / NG ____
0089 C.
0090 C. ***** XRT END *****
0091 . C. *****
0092 C. SOT table upload
0093 C. *****
0094 . C. < Stop FG table >
0095 +. DC 07-F0 MDP_FG_CTRL_MANU
```

```

0096 BC (51)
0097 . C. -----
0098 C. MDP_FG_CTRL_MODE = MANU [ ]
0099 C. -----
0100 C.
0101 . C. <Upload FG Observation Table>
0102 . S. RAM ram-263:MDP_OBS_F
0103 ( )
0104 C.
0105 . C. < Dump RAMID=MDP_OBS_F >
0106 +. DC 07-F0 MDP_DUMP_FGTBL
0107 BC (82 07 00 00 00 38 b8)
0108 C. -----
0109 C. MDP_OBS_F verify = OK/NG [ ]
0110 C. -----
0111 C.
0112 C. *****
0113 C. SOT TI command set
0114 C. *****
0115 C. Execute, after the success of TBL upload.
0116 +. TI 2013-12-14 09:25:18.0
0117 DC 07-F0 MDP_SOT_MODE_OBSV
0118 BC (40)
0119 . C. -----
0120 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0121 C. -----
0122 C.
0123 C.
0124 . C. ***** MDP 'uÃÏaÎ»ô¼YαÊÃÐα¹αêDCBC•x²è *****
0125 C. (¼ã°ÏYÔYÃYÈYÞYÈYãYçYèαE¼αα¼Ã»Ûα¹αè)
0126 . S. DC-BC dcbc-402:DCBC
0127 (MDP_known_event)
0128 C.
0129 C.
0130 . C. ***** YDY¹•Ï Daily±;ÏÑαÊ'Øα¹αêDCBC•x²è *****
0131 . S. DC-BC dcbc-153:DCBC
0132 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0133 C.
0134 C.
0135 . C. ;ãLOSYÃYSYÃY-¼Ã»Û;ã
0136 C.
0137 . C. ***** LOS *****
0138 C.

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*** OP Sequence for XRT ***

2013/12/14	09:36:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02	00	00	00	00
2013/12/15	11:52:00.0	XRT_TCIB_XRT_S_HTR_A_DIS_437_OG [0x1b5]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2013/12/15	17:51:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/15	17:51:56.0	XRT_FOCUS_RECALIBRATE_433_OG [0x1b1]							
		XRT_FOCUS_RECAL	2	07-F8	78	00			
2013/12/15	17:52:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2013/12/15	17:55:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2013/12/15	17:56:16.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/12/15	17:56:18.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/12/15	17:56:20.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/12/15	17:56:22.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2013/12/15	17:56:24.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/15	18:01:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/15	18:01:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/12/15	18:02:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02	00	00	00	00
2013/12/15	18:02:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/12/15	18:02:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/12/15	18:02:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/12/15	18:02:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/12/15	18:02:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/12/15	18:02:26.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2013/12/15	18:04:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2013/12/15	18:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/15	18:15:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/15	18:15:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/12/15	18:15:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/12/15	18:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/12/15	18:38:30.5	XRT_Custom_430_OG [0x1ae]							
2013/12/15	18:39:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/15	19:52:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/15	19:52:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/12/15	19:52:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/12/15	19:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/12/15	20:15:30.0	XRT_Custom_430_OG [0x1ae]							
2013/12/15	20:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/15	21:29:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/15	21:29:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/12/15	21:29:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/12/15	21:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/12/15	21:52:30.0	XRT_Custom_430_OG [0x1ae]							
2013/12/15	21:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/15	23:07:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/15	23:07:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/12/15	23:07:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/12/15	23:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/12/15	23:23:00.0	XRT_Custom_430_OG [0x1ae]							
2013/12/15	23:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/15	23:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/16	00:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							

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2013/12/16	00:02:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	AOCU_NM	5	02-76	00	2e f9	2e f9
		XRT_FOCUS_POSITION		4	07-F8	22	ff aa	00
2013/12/16	00:02:52.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06	
2013/12/16	00:02:54.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/12/16	00:02:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/12/16	00:02:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/12/16	00:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/16	00:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/16	00:10:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	2e f9	d1 07
2013/12/16	00:12:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa	00
2013/12/16	00:12:52.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a	
2013/12/16	00:12:54.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/12/16	00:12:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/12/16	00:12:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/12/16	00:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/16	00:19:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/16	00:20:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	d1 07	d1 07
2013/12/16	00:22:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa	00
2013/12/16	00:22:52.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01	
2013/12/16	00:22:54.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/12/16	00:22:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/12/16	00:22:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/12/16	00:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/16	00:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/16	00:30:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00	d1 07	2e f9
2013/12/16	00:32:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa	00
2013/12/16	00:32:52.0	XRT_QT_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03	
2013/12/16	00:32:54.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/12/16	00:32:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/12/16	00:32:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/12/16	00:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/16	00:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/16	00:39:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97	00
2013/12/16	00:40:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02	00 00	00 00
2013/12/16	00:40:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/16	00:40:02.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/16	00:40:04.5	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/12/16	00:40:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/12/16	00:40:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/12/16	00:40:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/12/16	00:40:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/12/16	00:40:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/16	00:40:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14	
2013/12/16	00:42:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13	
2013/12/16	00:43:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/12/16	00:50:30.0	XRT_Custom_430_OG [0x1ae]						
2013/12/16	00:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/16	02:06:30.0	XRT_CTRL_MANU_400_OG [0x190]						

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2013/12/16	02:06:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/16	02:06:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/16	02:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/16	02:27:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/16	02:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/16	03:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/16	03:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/16	03:35:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/16	03:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/16	04:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/16	04:04:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/12/16	04:05:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2013/12/16	04:05:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/12/16	04:07:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/12/16	04:07:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/12/16	04:07:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2013/12/16	04:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/16	04:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/16	04:14:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/12/16	04:15:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	01 00 00 00 00	
2013/12/16	04:15:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/12/16	04:15:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/12/16	04:15:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/12/16	04:15:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/12/16	04:15:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/16	04:15:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14	
2013/12/16	04:17:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13	
2013/12/16	04:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/16	05:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/16	05:14:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/16	05:14:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/16	05:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/16	05:41:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/16	05:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/16	06:55:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/16	06:55:02.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/16	06:55:04.5	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/16	06:58:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/16	07:19:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/16	07:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/16	08:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/16	08:34:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/16	08:34:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/16	08:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/16	08:55:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/16	08:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/16	09:33:14.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/12/16	09:33:16.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	

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2013/12/16	09:33:20.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	12	5e	54	a3
2013/12/16	09:33:36.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0					d8
2013/12/16	09:33:38.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0					c8
2013/12/16	09:33:40.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0					d0
2013/12/16	09:33:42.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2013/12/16	09:33:44.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2013/12/16	09:33:46.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 0b
2013/12/16	09:36:18.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0					c5 13
2013/12/16	09:36:20.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2013/12/16	10:16:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/12/16	10:16:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2013/12/16	10:16:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2013/12/16	10:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2013/12/16	10:25:30.0	XRT_Custom_430_OG [0x1ae]							
2013/12/16	10:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2013/12/16	13:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/12/16	13:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97		00
2013/12/16	14:00:00.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	03	00	00	00	00
2013/12/16	14:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0					d8
2013/12/16	14:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0					c8
2013/12/16	14:00:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0					d0
2013/12/16	14:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2013/12/16	14:00:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2013/12/16	14:00:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 0b
2013/12/16	14:02:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0					c5 13
2013/12/16	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2013/12/16	14:05:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/12/16	14:05:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2013/12/16	14:05:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2013/12/16	14:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2013/12/16	14:11:00.0	XRT_Custom_430_OG [0x1ae]							
2013/12/16	14:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2013/12/16	15:38:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/12/16	15:38:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2013/12/16	15:38:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2013/12/16	15:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2013/12/16	16:01:00.0	XRT_Custom_430_OG [0x1ae]							
2013/12/16	16:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2013/12/16	17:14:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/12/16	17:14:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2013/12/16	17:14:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2013/12/16	17:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2013/12/16	17:38:00.0	XRT_Custom_430_OG [0x1ae]							
2013/12/16	17:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2013/12/16	17:55:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2013/12/16	17:55:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2013/12/16	17:55:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2013/12/16	17:55:46.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2013/12/16	17:58:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							

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2013/12/16	17:58:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
			MDP_XRT_ARS_DIS	1	07-F0	d5
2013/12/16	17:58:28.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2013/12/16	17:58:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/16	18:05:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/16	18:05:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/12/16	18:05:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00
2013/12/16	18:05:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/12/16	18:05:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/12/16	18:05:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2013/12/16	18:05:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/12/16	18:05:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/16	18:05:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2013/12/16	18:08:28.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13
2013/12/16	18:08:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/16	18:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/16	18:51:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/16	18:51:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/16	18:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/16	19:14:30.0	XRT_Custom_430_OG [0x1ae]				
2013/12/16	19:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/16	20:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/16	20:28:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/16	20:28:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/16	20:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/16	20:52:00.0	XRT_Custom_430_OG [0x1ae]				
2013/12/16	20:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/16	22:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/16	22:06:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/16	22:06:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/16	22:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/16	22:28:00.0	XRT_Custom_430_OG [0x1ae]				
2013/12/16	22:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/16	23:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/16	23:43:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/16	23:43:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/16	23:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/16	23:53:30.0	XRT_Custom_430_OG [0x1ae]				
2013/12/16	23:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/17	01:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/17	01:09:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/17	01:09:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/17	01:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/17	01:26:30.0	XRT_Custom_430_OG [0x1ae]				
2013/12/17	01:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/17	02:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/17	02:42:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/17	02:42:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/17	02:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/17	03:03:00.0	XRT_Custom_430_OG [0x1ae]				

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2013/12/17	03:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/12/17	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/12/17	03:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]						
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2013/12/17	04:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			
2013/12/17	04:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]						
		MDP_XRT_FLD_DIS	1	07-F0	d9			
2013/12/17	04:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2013/12/17	04:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/12/17	04:02:58.0	XRT_QT_PROG_SET_446_OG [0x1be]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11			
2013/12/17	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/12/17	04:09:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/12/17	04:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2013/12/17	04:10:00.0	AOCS_ORe-point_Start_7_OG [0x09d]						
		AOCU_NM	5	02-76	01 00 00 00 00			
2013/12/17	04:10:16.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2013/12/17	04:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2013/12/17	04:10:20.0	XRT_AEC_RESET_413_OG [0x19d]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2013/12/17	04:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2013/12/17	04:10:24.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/12/17	04:10:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b			
2013/12/17	04:11:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/12/17	04:11:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/12/17	04:11:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/12/17	04:12:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13			
2013/12/17	04:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/12/17	04:40:30.0	XRT_Custom_430_OG [0x1ae]						
2013/12/17	04:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/12/17	05:52:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/12/17	05:52:02.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/12/17	05:52:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/12/17	05:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/12/17	06:18:00.0	XRT_Custom_430_OG [0x1ae]						
2013/12/17	06:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/12/17	07:32:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/12/17	07:32:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/12/17	07:32:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/12/17	07:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/12/17	07:55:00.0	XRT_Custom_430_OG [0x1ae]						
2013/12/17	07:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/12/17	09:12:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/12/17	09:12:02.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2013/12/17	09:12:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2013/12/17	09:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2013/12/17	09:30:30.0	XRT_Custom_430_OG [0x1ae]						
2013/12/17	09:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2013/12/17	09:46:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2013/12/17	09:47:00.0	AOCS_ORe-point_Start_2_OG [0x098]						
		AOCU_NM	5	02-76	00 00 00 00 00			