

XRT Timeline to be uploaded on 2013/12/17

Period: 2013/12/17 09:37:00 - 2013/12/21 09:53:00

* * * * *

Normal mode

* * * * *

XOB #19E5: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)

Term	Pointing (x, y)	Comment
12/17 09:50:00 - 12/17 17:50:30	Track (754.7, -285.8) @ 12/17 09:47:00	# OP start + 10min, AR 11917.
12/17 18:27:00 - 12/18 03:59:54	Track (410.0, 125.6) @ 12/17 18:24:00	# AR 11921 (HOP 173 from 2-4UT).
12/18 04:13:00 - 12/18 06:29:30	Track (837.7, -287.9) @ 12/18 04:10:00	# HOP 173 AR 11917.
12/18 11:33:00 - 12/18 17:59:54	Track (863.7, -289.0) @ 12/18 11:30:00	# return to AR 11917.
12/18 18:13:00 - 12/19 01:59:54	Track (883.7, -290.0) @ 12/18 18:10:00	# AR 11917 cont.
12/19 02:03:00 - 12/19 04:19:54	Track (656.3, 123.9) @ 12/19 02:00:00	# HOP 173 (AR 11921).
12/19 04:33:00 - 12/19 09:22:54	Fixed (905.0, -290.0)	# AR 11917 (HOP 173 4-6UT).

PROG= 08 Inf-time(s)												
Subr=	1	1-time(s)	2.0sec									
└─ Seqn=	8	2-time(s)	2.0sec									
└─ Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Subr=	2	2-time(s)	2.0sec									
└─ Seqn=	24	1-time(s)	2.0sec									
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn=	42	4-time(s)	2.0sec									
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn=	62	8-time(s)	240.0sec									
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #19DA: Synoptic Q95 2x2 - Al/mesh(5/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/362/1443) + Thin-Be

Term	Pointing (x, y)	Comment
12/17 18:17:00 - 12/17 18:23:54	Fixed (0.0, 0.0)	synoptic, shifted 14.0 min
12/18 04:03:00 - 12/18 04:09:54	Fixed (0.0, 0.0)	synoptic shifted 2 hours.
12/18 18:03:00 - 12/18 18:09:54	Fixed (0.0, 0.0)	synoptic
12/19 04:23:00 - 12/19 04:29:54	Fixed (0.0, 0.0)	synoptic, shifted 2 hours 3.0 min

PROG= 17 1-time(s)												
Subr=	1	1-time(s)	12.0sec									
└─ Seqn=	84	1-time(s)	4.0sec									
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn=	5	1-time(s)	2.0sec									
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn=	86	1-time(s)	4.0sec									
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn=	95	1-time(s)	2.0sec									
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn=	6	1-time(s)	2.0sec									
└─ Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
└─ Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #199F: HOP130-Synoptic 3 Filter- 2x2 Q95 (Almesh 12/128/723ms; Tipoly 24/362/1443ms; Althick 65536ms) with G-band

Term	Pointing (x, y)	Comment
12/18 07:30:00 - 12/18 08:09:30	Fixed (0.0, 0.0)	# 3/15
12/18 08:32:00 - 12/18 09:50:00	Fixed (-468.0, 0.0)	# 4/15
12/18 10:05:30 - 12/18 11:29:54	Fixed (-411.0, 847.0)	# 9/15

PROG= 14 2-time(s)												
Subr=	1	1-time(s)	180.0sec									
└─ Seqn=	85	1-time(s)	25.0sec									
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 86		1-time(s)		25.0sec											
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 75		1-time(s)		2.0sec											
Open/thick-Al	Open/thick-Al	close	Safe	Norm	64.0s	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec		
Seqn= 66		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec		
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048	(1024, 1024)	DPCM	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

* * * * *

Flare mode

* * * * *

XOB #1980: Flare obs. dynamics - thin-Be high cadence + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loops-2

Term	Pointing (x, y)	Comment
12/17 09:50:00 - 12/17 17:50:30	Track (754.7, -285.8) @ 12/17 09:47:00	# OP start + 10min, AR 11917.
12/17 18:27:00 - 12/18 03:59:54	Track (410.0, 125.6) @ 12/17 18:24:00	# AR 11921 (HOP 173 from 2-4UT).
12/18 04:13:00 - 12/18 06:29:30	Track (837.7, -287.9) @ 12/18 04:10:00	# HOP 173 AR 11917.
12/18 11:33:00 - 12/18 17:59:54	Track (863.7, -289.0) @ 12/18 11:30:00	# return to AR 11917.
12/18 18:13:00 - 12/19 01:59:54	Track (883.7, -290.0) @ 12/18 18:10:00	# AR 11917 cont.
12/19 02:03:00 - 12/19 04:19:54	Track (656.3, 123.9) @ 12/19 02:00:00	# HOP 173 (AR 11921).
12/19 04:33:00 - 12/19 09:22:54	Fixed (905.0, -290.0)	# AR 11917 (HOP 173 4-6UT).

PROG= 19 15-time(s)

Subr= 1		45-time(s)		10.0sec											
Seqn= 9		1-time(s)		2.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Subr= 2		1-time(s)		10.0sec											
Seqn= 10		1-time(s)		2.0sec											
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384	(1024, 1024)	Q=95	3	0	2.0sec		
Seqn= 11		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512	(1024, 1024)	Q=95	2	0	2.0sec		
Seqn= 15		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384	(1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512	(1024, 1024)	Q=98	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval			

* * * * *

Active Region Search

* * * * *

NOT USED

* * * * *

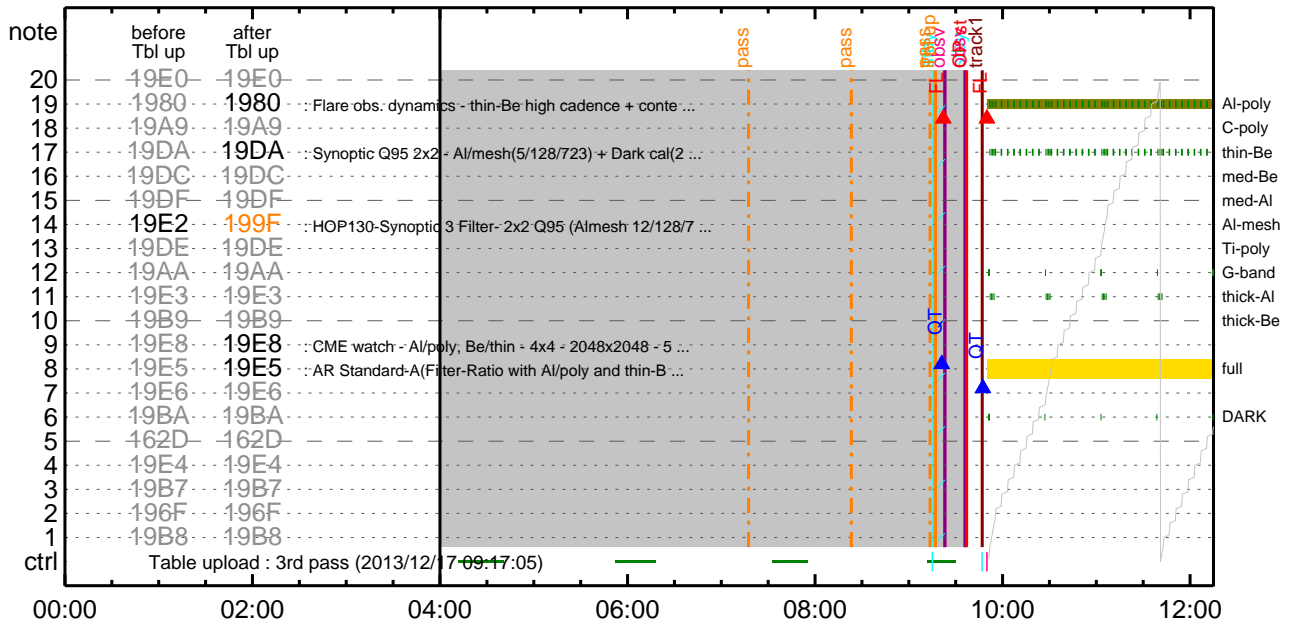
Flare Detection

* * * * *

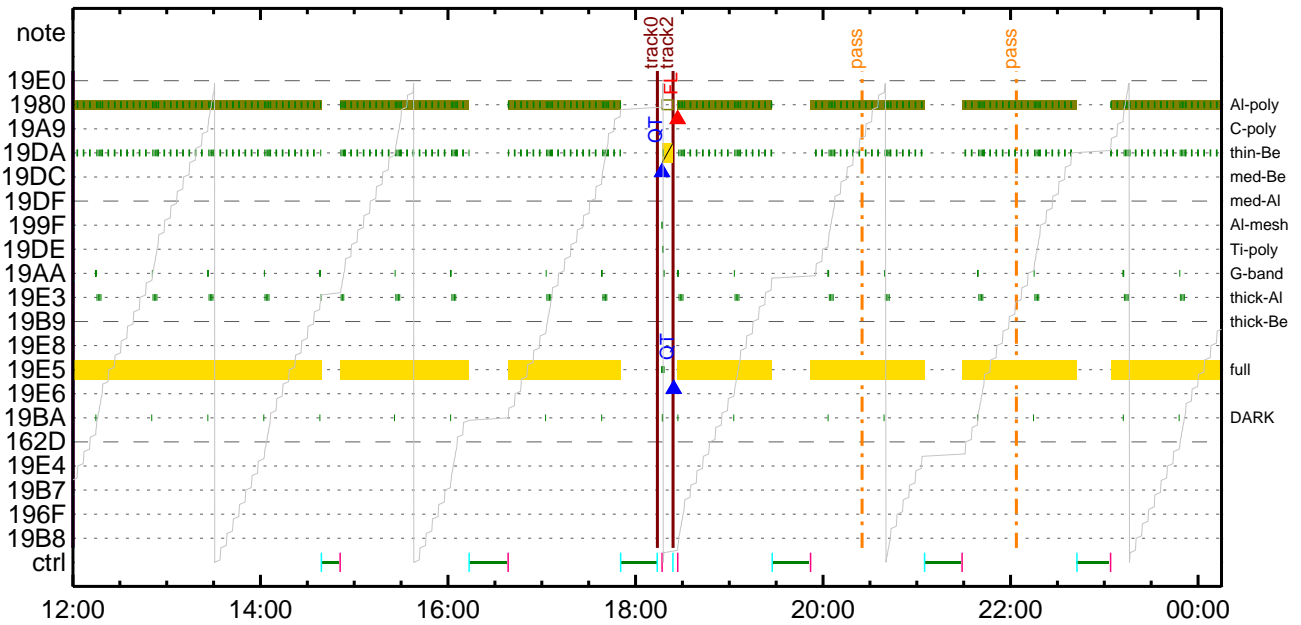
FLD Patrol

Term	Pointing (x, y)	Comment
12/17 18:24:16 - 12/18 04:00:16	Track (410.0, 125.6) @ 12/17 18:24:00	# AR 11921 (HOP 173 from 2-4UT).
12/18 04:10:16 - 12/18 07:27:16	Track (837.7, -287.9) @ 12/18 04:10:00	# HOP 173 AR 11917.
12/18 11:30:16 - 12/18 18:00:16	Track (863.7, -289.0) @ 12/18 11:30:00	# return to AR 11917.
12/18 18:10:16 - 12/19 04:20:16	Track (883.7, -290.0) @ 12/18 18:10:00	# AR 11917 cont.
12/19 04:30:16 - 12/21 09:53:00	Fixed (905.0, -290.0)	# AR 11917 (HOP 173 4-6UT).
Open/Ti-poly	Open/thick-Al	close Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

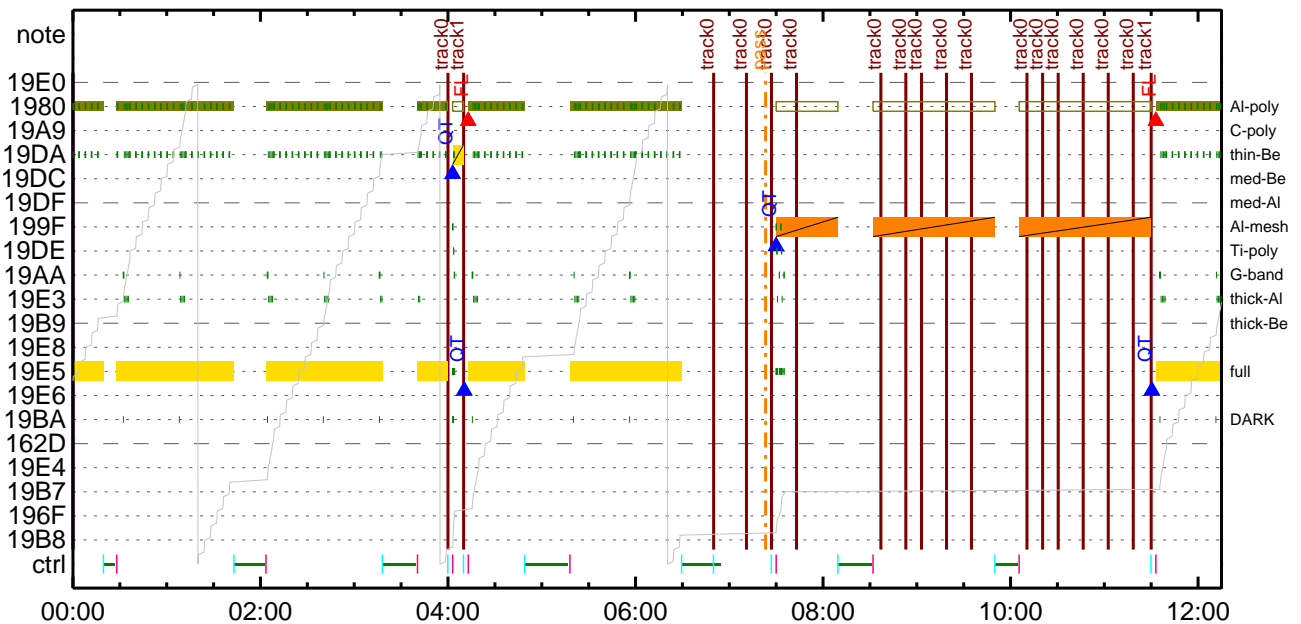
CMDI #0977 2013/12/17



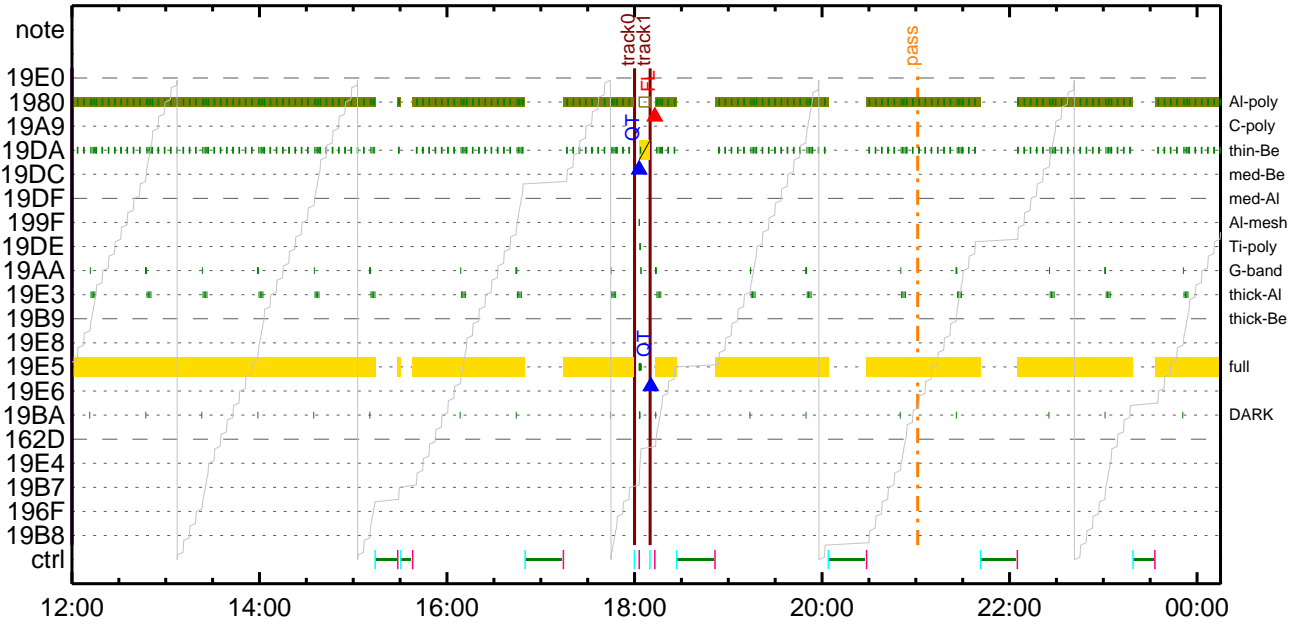
CMDI #0977 2013/12/17



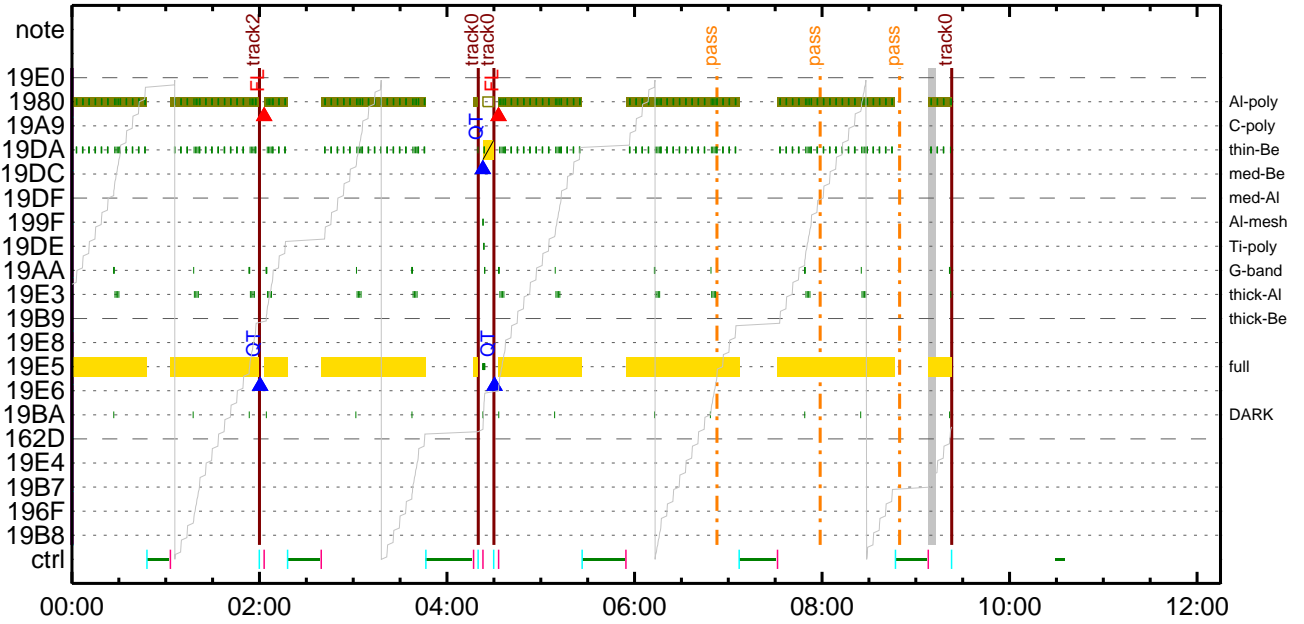
CMDI #0977 2013/12/18



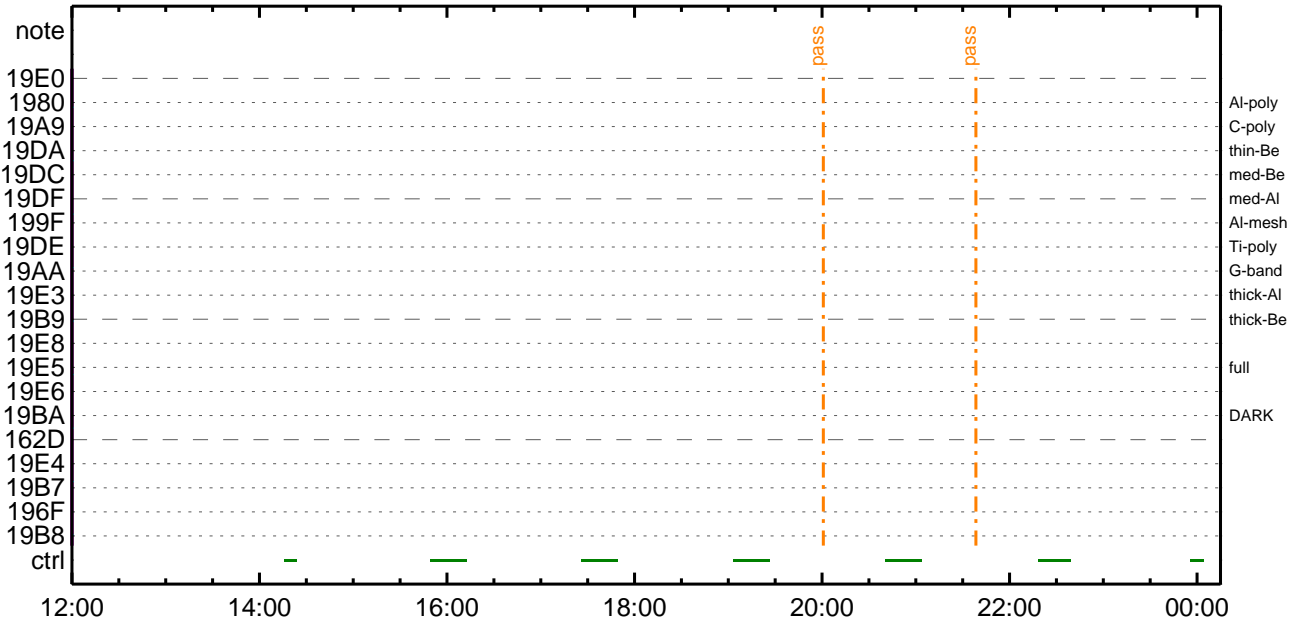
CMDI #0977 2013/12/18



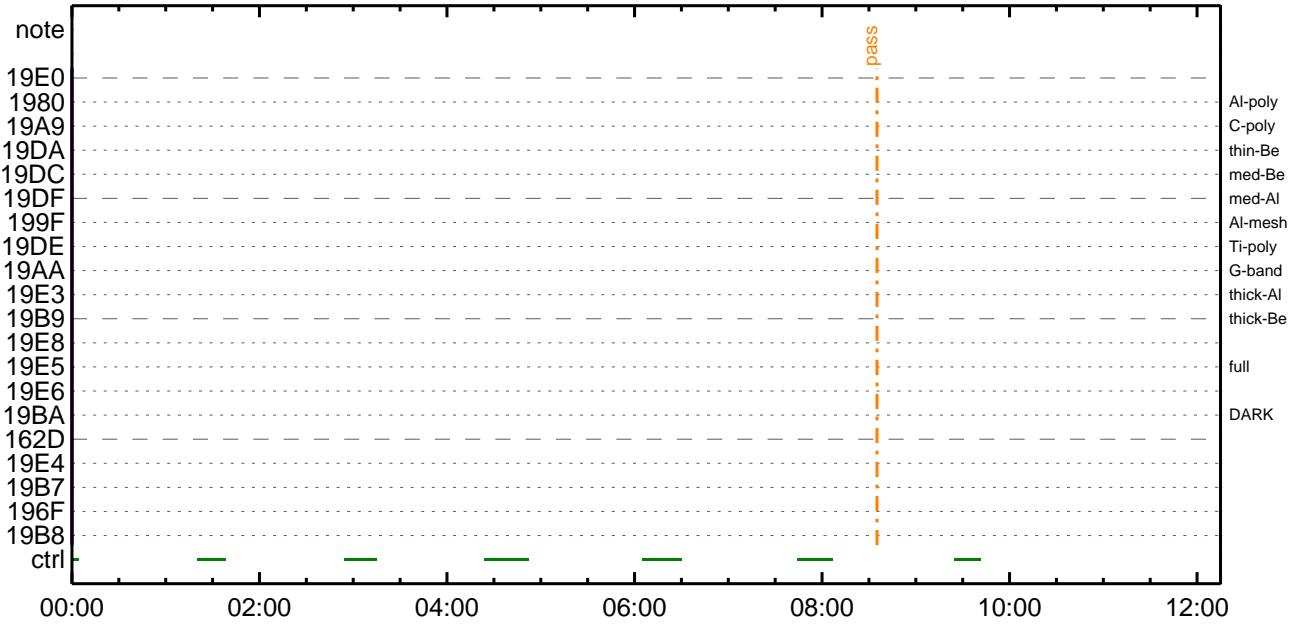
CMDI #0977 2013/12/19



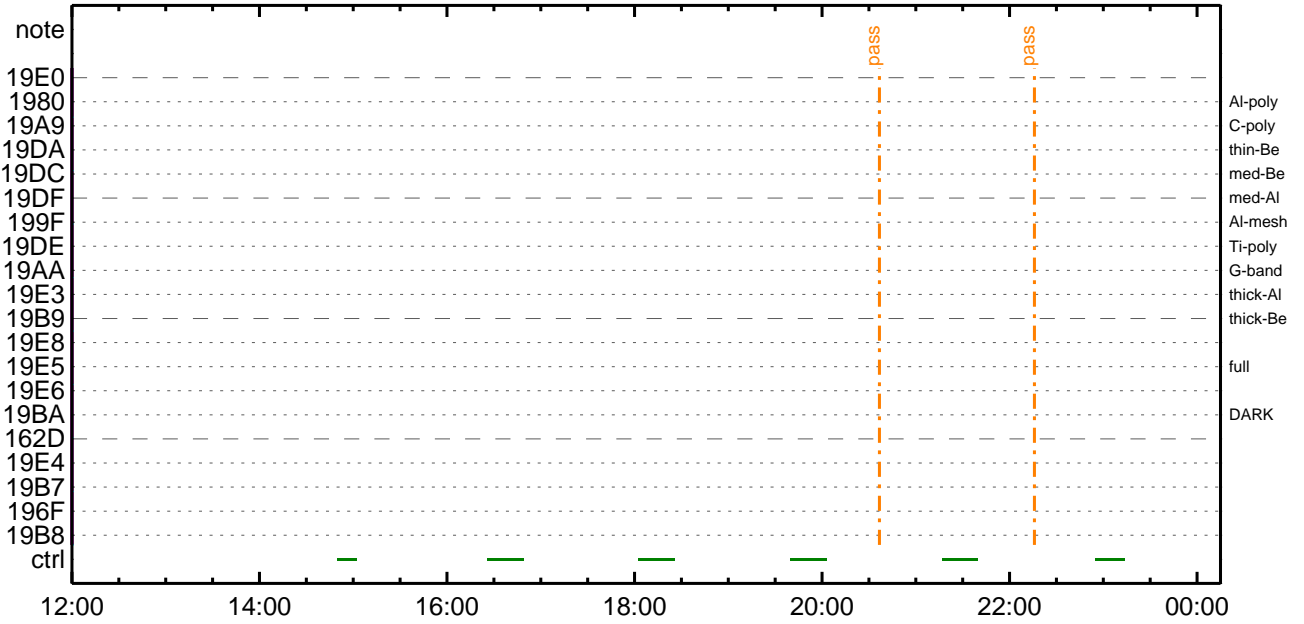
CMDI #0977 2013/12/19



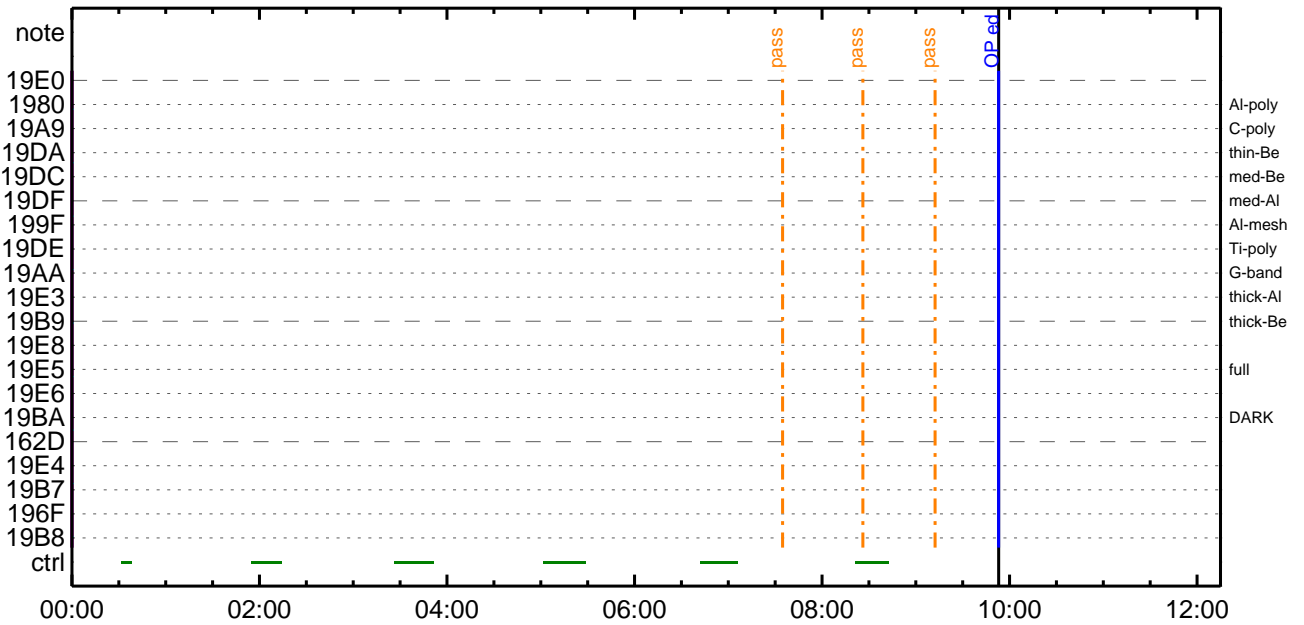
CMDI #0977 2013/12/20



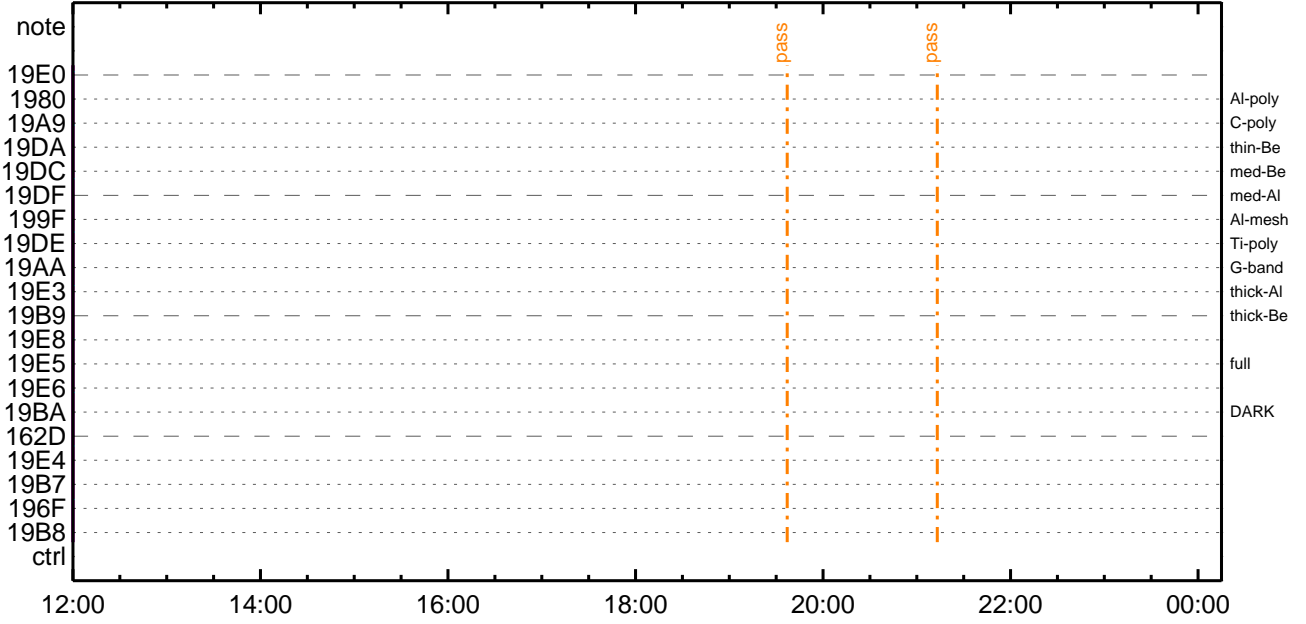
CMDI #0977 2013/12/20



CMDI #0977 2013/12/21



CMDI #0977 2013/12/21




```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```


(a) Spacecraft Operation Procedure (real-commands)

```
main-105 2013-12-17 14:02:54 154 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÀã•µ°Æ»Í×ÁÇãíYçYÁY×Yí;¼YÉ;ÈÈ¿µ•ííÈ;ÈãÈ¼°ÇÒã•ã¿¼í¹çãí;çÀ®, ùã¹ãÈãÈãÇÁ+¿®ã•ãÈããã³ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-264:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Upload DPL table >
0036 C.
0037 C. YçYÁY×Yí;¼YÉãîÁ°ãÈSTS_CHKãðOFFãÈã¹ãÈ
0038 C.
0039 . S. RAM ram-271:MDP_DPL
0040 ( )
0041 C.
0042 . C. < Dump RAMID=MDP_DPL >
0043 +. DC 07-F0 MDP_DUMP_FGTBL
0044 BC (82 07 00 38 b8 00 40)
0045 C. -----
0046 C. MDP_DPL verify = OK [ ]
0047 C. -----
0048 C.
0049 C. STS_CHKãðONãÈã¹ãÈ
0050 C.
0051 . C. < Update MDP DSC PAR1 >
0052 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0053 BC (4c)
0054 C. MDP_CMD_CODE = F04C0700[ ]
0055 C. MDP_CMD_CNT (count-up 1) [ ]
0056 C. -----
0057 C.
0058 C.
0059 C. *****
0060 C. SOT TI command set
0061 C. *****
0062 C. Execute, after the success of TBL upload.
0063 +. TI 2013-12-17 09:36:18.0
0064 DC 07-F0 MDP_SOT_MODE_OBSV
0065 BC (40)
0066 . C. -----
0067 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0068 C. -----
0069 C.
0070 C.
0071 C. ***** XRT START *****
0072 C.
0073 +. DC 07-F0 MDP_XRT_CTRL_MANU
0074 BC (c1)
0075 +. DC 07-F0 MDP_XRT_MODE_STBY
0076 BC (c3)
0077 . C. ----- Success Verify ? OK / NG____
0078 C.
0079 C. XRT Obs. Table Upload
0080 . S. RAM ram-291:MDP_OBS_X
0081 ( )
0082 C.
0083 +. DC 07-F0 MDP_DUMP_XRTTBL
0084 BC (84 07 00 00 00 3a d4)
0085 . C. ----- Comparison Check ? OK / ERR ____
0086 C.
0087 C.
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 01 b1 b1 04 04)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 02 b1 b1 08 08)
0092 +. DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 03 b1 b1 08 08)
0094 +. DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 04 b1 b1 06 06)
```

```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 05 85 83 06 06)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 06 80 80 20 20)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 07 85 83 06 06)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 08 85 83 08 08)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 09 80 80 20 08)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0a 80 80 08 20)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 0f 80 80 06 06)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 10 80 80 08 08)
0112 + DC 07-F0 MDP_XRT_FLD_ENA
0113 BC (d8)
0114 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0115 BC (c8)
0116 + DC 07-F0 MDP_XRT_AEC_RESET
0117 BC (d0)
0118 + DC 07-F0 MDP_XRT_ARS_DIS
0119 BC (d5)
0120 + DC 07-F0 MDP_XRT_FLD_RESET
0121 BC (da)
0122 + DC 07-F0 MDP_XRT_QT_PROG_SET
0123 BC (c4 09)
0124 + DC 07-F0 MDP_XRT_FL_PROG_SET
0125 BC (c5 13)
0126 . C. ----- Success Verify ? OK / NG ____
0127 C.
0128 C.
0129 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0130 C.
0131 + DC 07-F0 MDP_XRT_MODE_OBSV
0132 BC (c2)
0133 + TI 2013-12-17 09:36:02.0
0134 DC 07-F0 MDP_XRT_MODE_OBSV
0135 BC (c2)
0136 . C. ----- Success Verify ? OK / NG ____
0137 C.
0138 C. ***** XRT END *****
0139 C.
0140 . C. ***** MDP 'uÃîqî»ö¼ýqÊÄð¹ñèDCBC•x²è *****
0141 C. (%á°îÿÓÿÁÿÈÿPÿËÿáÿçÿèq¼q¼q¼»Û¹qè)
0142 . S. DC-BC dcbc-402:DCBC
0143 (MDP_known_event)
0144 C.
0145 C.
0146 . C. ***** ÿDÿ¹•ï Daily±¿îÑqË´Ø¹qèDCBC•x²è *****
0147 . S. DC-BC dcbc-153:DCBC
0148 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0149 C.
0150 C.
0151 . C. ;ãLOSÿÁÿSÿËÿËÿ-¼Ä»Û;ä
0152 C.
0153 . C. ***** LOS *****
0154 C.
```

Dec 17, 13 14:03

XRT_OGLIST_0977.chk

Page 1/6

*** OP Sequence for XRT ***

2013/12/17	09:46:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/17	09:46:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/12/17	09:47:00.0	AOCS_OrE-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	01 00 00 00 00		
2013/12/17	09:47:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/12/17	09:47:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/12/17	09:47:20.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/12/17	09:47:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/12/17	09:47:24.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/17	09:47:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2013/12/17	09:49:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2013/12/17	09:50:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/17	14:39:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/17	14:39:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/17	14:39:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/12/17	14:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/12/17	14:50:00.0	XRT_Custom_430_OG [0x1ae]					
2013/12/17	14:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/17	16:13:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/17	16:13:32.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/17	16:13:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/12/17	16:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/12/17	16:37:30.0	XRT_Custom_430_OG [0x1ae]					
2013/12/17	16:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/17	17:50:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/17	17:50:32.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/17	17:50:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/12/17	17:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/12/17	18:13:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/17	18:13:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2013/12/17	18:14:00.0	AOCS_OrE-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2013/12/17	18:14:16.0	XRT_FLD_DIS_434_OG [0x1b2]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/12/17	18:16:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/12/17	18:16:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/12/17	18:16:58.0	XRT_QT_PROG_SET_446_OG [0x1be]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2013/12/17	18:17:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/17	18:23:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/17	18:23:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2013/12/17	18:24:00.0	AOCS_OrE-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	02 00 00 00 00		
2013/12/17	18:24:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2013/12/17	18:24:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2013/12/17	18:24:20.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2013/12/17	18:24:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/12/17	18:24:24.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/17	18:24:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2013/12/17	18:26:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 13		
2013/12/17	18:27:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/17	19:27:30.0	XRT_CTRL_MANU_400_OG [0x190]					

2013/12/17	19:27:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/17	19:27:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/17	19:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/17	19:51:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/17	19:52:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/17	21:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/17	21:05:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/17	21:05:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/17	21:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/17	21:28:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/17	21:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/17	22:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/17	22:42:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/17	22:42:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/17	22:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/17	23:03:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/17	23:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/18	00:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/18	00:19:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/18	00:19:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/18	00:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/18	00:27:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/18	00:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/18	01:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/18	01:43:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/18	01:43:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/18	01:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/18	02:02:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/18	02:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/18	03:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/18	03:18:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/18	03:18:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/18	03:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/18	03:39:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/18	03:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/18	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/18	03:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/12/18	04:00:00.0	AOCS_Or-e-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2013/12/18	04:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9
2013/12/18	04:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/12/18	04:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2013/12/18	04:02:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2013/12/18	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/18	04:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/18	04:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/12/18	04:10:00.0	AOCS_Or-e-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00
2013/12/18	04:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2013/12/18	04:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/12/18	04:10:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0

Dec 17, 13 14:03

XRT_OGLIST_0977.chk

Page 3/6

2013/12/18	04:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/12/18	04:10:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/18	04:10:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08	
2013/12/18	04:12:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	13	
2013/12/18	04:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/18	04:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/18	04:49:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/18	04:49:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/12/18	04:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/12/18	05:17:00.0	XRT_Custom_430_OG [0x1ae]						
2013/12/18	05:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/18	06:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/18	06:29:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/18	06:29:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/12/18	06:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/12/18	06:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/18	06:50:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	00	00 ac cd
2013/12/18	07:11:00.0	AOCS_ORe-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	00	00 d6 67
2013/12/18	07:26:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/18	07:26:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa 00
2013/12/18	07:27:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00 00 00
2013/12/18	07:27:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2013/12/18	07:29:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2013/12/18	07:29:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2013/12/18	07:29:58.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e	
2013/12/18	07:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/18	07:43:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00	00	00 29 99
2013/12/18	08:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/18	08:09:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/18	08:09:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/12/18	08:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/12/18	08:31:00.0	XRT_Custom_430_OG [0x1ae]						
2013/12/18	08:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/18	08:37:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00	00	00 53 33
2013/12/18	08:53:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00	d6	36 b7 8e
2013/12/18	09:03:00.0	AOCS_ORe-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00	b4	b5 db 75
2013/12/18	09:19:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00	ac	5b 00 00
2013/12/18	09:35:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	00	b4	b5 24 8b
2013/12/18	09:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2013/12/18	09:50:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2013/12/18	09:50:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2013/12/18	09:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2013/12/18	10:04:30.0	XRT_Custom_430_OG [0x1ae]						
2013/12/18	10:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2013/12/18	10:10:30.0	AOCS_ORe-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00	d6	36 48 72
2013/12/18	10:20:30.0	AOCS_ORe-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00	29	ca b7 8e
2013/12/18	10:30:30.0	AOCS_ORe-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00	4b	4b db 75
2013/12/18	10:46:30.0	AOCS_ORe-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00	53	a5 00 00

Dec 17, 13 14:03

XRT_OGLIST_0977.chk

Page 4/6

2013/12/18	11:02:30.0	AOCS_ORe-point_Start_16_OG [0x0a6] AOCU_NM	5	02-76	00	4b	4b	24	8b
2013/12/18	11:18:30.0	AOCS_ORe-point_Start_17_OG [0x0a7] AOCU_NM	5	02-76	00	29	db	48	72
2013/12/18	11:29:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1			
2013/12/18	11:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/12/18	11:30:00.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	01	00	00	00	00
2013/12/18	11:30:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0		d8			
2013/12/18	11:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2013/12/18	11:30:20.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0		d0			
2013/12/18	11:30:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5			
2013/12/18	11:30:24.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da			
2013/12/18	11:30:26.0	XRT_QT_PROG_SET_444_OG [0x1bc] MDP_XRT_QT_PROG_SET	2	07-F0		c4	08		
2013/12/18	11:32:58.0	XRT_FL_PROG_SET_420_OG [0x1a4] MDP_XRT_FL_PROG_SET	2	07-F0		c5	13		
2013/12/18	11:33:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2013/12/18	15:14:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0		c1			
2013/12/18	15:14:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da			
2013/12/18	15:14:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2013/12/18	15:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2013/12/18	15:27:30.0	XRT_Custom_430_OG [0x1ae]							
2013/12/18	15:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2013/12/18	15:30:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0		c1			
2013/12/18	15:30:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da			
2013/12/18	15:30:34.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2013/12/18	15:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2013/12/18	15:37:00.0	XRT_Custom_430_OG [0x1ae]							
2013/12/18	15:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2013/12/18	16:50:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0		c1			
2013/12/18	16:50:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da			
2013/12/18	16:50:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2013/12/18	16:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2013/12/18	17:13:30.0	XRT_Custom_430_OG [0x1ae]							
2013/12/18	17:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2013/12/18	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1			
2013/12/18	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2013/12/18	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00	00	00	00	00
2013/12/18	18:00:16.0	XRT_FLD_DIS_434_OG [0x1b2] MDP_XRT_FLD_DIS	1	07-F0		d9			
2013/12/18	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2013/12/18	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5			
2013/12/18	18:02:58.0	XRT_QT_PROG_SET_446_OG [0x1be] MDP_XRT_QT_PROG_SET	2	07-F0		c4	11		
2013/12/18	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2013/12/18	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1			
2013/12/18	18:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/12/18	18:10:00.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	01	00	00	00	00
2013/12/18	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0		d8			
2013/12/18	18:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2013/12/18	18:10:20.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0		d0			
2013/12/18	18:10:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5			
2013/12/18	18:10:24.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da			
2013/12/18	18:10:26.0	XRT_QT_PROG_SET_444_OG [0x1bc] MDP_XRT_QT_PROG_SET	2	07-F0		c4	08		

Dec 17, 13 14:03

XRT_OGLIST_0977.chk

Page 5/6

2013/12/18	18:12:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2013/12/18	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/18	18:27:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/18	18:27:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/12/18	18:27:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/12/18	18:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/12/18	18:50:31.0	XRT_Custom_430_OG [0x1ae]							
2013/12/18	18:51:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/18	20:04:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/18	20:04:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/12/18	20:04:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/12/18	20:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/12/18	20:27:30.0	XRT_Custom_430_OG [0x1ae]							
2013/12/18	20:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/18	21:41:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/18	21:41:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/12/18	21:41:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/12/18	21:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/12/18	22:04:00.0	XRT_Custom_430_OG [0x1ae]							
2013/12/18	22:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/18	23:19:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/18	23:19:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/12/18	23:19:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/12/18	23:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/12/18	23:32:00.0	XRT_Custom_430_OG [0x1ae]							
2013/12/18	23:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/19	00:48:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/19	00:48:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/12/19	00:48:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/12/19	00:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/12/19	01:02:00.0	XRT_Custom_430_OG [0x1ae]							
2013/12/19	01:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/19	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/19	01:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/12/19	02:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	02	00	00	00	00
2013/12/19	02:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/12/19	02:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/12/19	02:00:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/12/19	02:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/12/19	02:00:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/12/19	02:00:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	08			
2013/12/19	02:02:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	13			
2013/12/19	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/19	02:18:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/19	02:18:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/12/19	02:18:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/12/19	02:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/12/19	02:38:30.0	XRT_Custom_430_OG [0x1ae]							
2013/12/19	02:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/19	03:46:30.0	XRT_CTRL_MANU_400_OG [0x190]							

Dec 17, 13 14:03

XRT_OGLIST_0977.chk

Page 6/6

2013/12/19	03:46:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/19	03:46:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/19	03:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/19	04:16:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/19	04:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/19	04:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/19	04:19:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/12/19	04:20:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2013/12/19	04:20:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/12/19	04:22:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/12/19	04:22:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/12/19	04:22:58.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2013/12/19	04:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/19	04:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/19	04:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/12/19	04:30:00.0	AOCs_OrE-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00 19 ca af 8e	
2013/12/19	04:30:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/12/19	04:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/12/19	04:30:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/12/19	04:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/12/19	04:30:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/19	04:30:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08	
2013/12/19	04:32:58.0	XRT_FL_PROG_SET_420_OG [0x1a4]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 13	
2013/12/19	04:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/19	05:26:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/19	05:26:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/19	05:26:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/19	05:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/19	05:53:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/19	05:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/19	07:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/19	07:07:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/19	07:07:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/19	07:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/19	07:30:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/19	07:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/19	08:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/19	08:47:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/19	08:47:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/19	08:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/19	09:07:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/19	09:08:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/19	09:22:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/19	09:23:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	