XRT Timeline to be uploaded on 2013/12/21

Period: 2013/12/21 09:53:00 - 2013/12/26 10:13:00

✲ ✲ ✲ ✲ ✲ **Normal mode** ✲ ✲ ✲ ✲ ✲

XOB #19E9: CH monitoring 3-filter - Ti/poly, Al/mesh, Thin-Be 90s cadence, G-band - 2x2 - 512x512

Term	Pointing (x, y)	Comment
12/21 10:15:30 - 12/21 17:59:54	Track (-66.7, 651.9) @ 12/21 10:03:00	# OP start + 10min/CH obs with IRIS

PROG= 15 Inf.-time(s)

- **Subr= 1 1-time(s) 2.0sec**
  - **Seqn= 13 2-time(s) 2.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec

- **Subr= 2 1-time(s) 2.0sec**
  - **Seqn= 49 1-time(s) 2.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	512x512 (1064, 1048)	Q=90	0	0	2.0sec

- **Subr= 3 40-time(s) 2.0sec**
  - **Seqn= 50 1-time(s) 90.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.00s	Obs	2x2	512x512 (1064, 1048)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	2x2	512x512 (1064, 1048)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	8.00s	Obs	2x2	512x512 (1064, 1048)	Q=95	0	0	2.0sec

XOB #19DA: Synoptic Q95 2x2 - Al/mesh(5/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/362/1443) + Thin

Term	Pointing (x, y)	Comment
12/21 18:07:00 - 12/21 18:09:54	Fixed (0.0, 0.0)	synoptic
12/22 06:18:00 - 12/22 06:24:54	Fixed (0.0, 0.0)	synoptic, shifted 15.0 min
12/22 18:04:30 - 12/22 18:11:24	Fixed (0.0, 0.0)	synoptic, shifted 1.5 min
12/23 05:51:30 - 12/23 05:58:24	Fixed (0.0, 0.0)	synoptic, shifted -11.5 min
12/23 17:54:00 - 12/23 18:00:54	Fixed (0.0, 0.0)	synoptic, shifted -9.0 min
12/24 06:03:00 - 12/24 06:09:54	Fixed (0.0, 0.0)	synoptic

PROG= 17 1-time(s)

- **Subr= 1 1-time(s) 12.0sec**
  - **Seqn= 84 1-time(s) 4.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

  - **Seqn= 5 1-time(s) 2.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

  - **Seqn= 86 1-time(s) 4.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

  - **Seqn= 95 1-time(s) 2.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

  - **Seqn= 6 1-time(s) 2.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

XOB #19E4: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms

Term	Pointing (x, y)	Comment
12/21 18:13:00 - 12/22 06:14:54	Track (604.6, -257.5) @ 12/21 18:10:00	#AR11928 monitoring
12/22 06:28:00 - 12/22 17:38:00	Track (684.0, -259.0) @ 12/22 06:25:00	#AR11928 monitoring
12/22 18:14:30 - 12/23 05:48:24	Track (751.4, -260.9) @ 12/22 18:11:30	#AR11928 monitoring
12/23 06:01:30 - 12/23 17:50:54	Track (809.2, -263.2) @ 12/23 05:58:30	#AR11928 monitoring
12/23 18:04:00 - 12/23 23:59:54	Track (-232.1, -240.8) @ 12/23 18:01:00	#AR11931 monitoring
12/24 00:03:00 - 12/24 05:59:54	Track (877.5, -267.4) @ 12/24 00:00:00	#AR11928 monitoring
12/24 06:13:00 - 12/24 09:24:00	Track (894.9, -269.1) @ 12/24 06:10:00	#AR11928 monitoring

PROG= 04 Inf.-time(s)

- **Subr= 1 1-time(s) 2.0sec**
  - **Seqn= 8 2-time(s) 2.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec

- **Subr= 2 2-time(s) 2.0sec**
  - **Seqn= 24 1-time(s) 2.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec

  - **Seqn= 42 4-time(s) 2.0sec**

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

  - **Seqn= 62 10-time(s) 180.0sec**

thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

✲ ✲ ✲ ✲ ✲ Flare mode ✲ ✲ ✲ ✲ ✲

XOB #19DE: Flare obs. dynamics - thin-Be high cadence long/short pairs + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loc

Term	Pointing (x, y)	Comment
12/21 10:15:30 - 12/21 17:59:54	Track (-66.7, 651.9) @ 12/21 10:03:00	# OP start + 10min/CH obs with IRIS
12/21 18:13:00 - 12/22 06:14:54	Track (604.6, -257.5) @ 12/21 18:10:00	#AR11928 monitoring
12/22 06:28:00 - 12/22 17:38:00	Track (684.0, -259.0) @ 12/22 06:25:00	#AR11928 monitoring
12/22 18:14:30 - 12/23 05:48:24	Track (751.4, -260.9) @ 12/22 18:11:30	#AR11928 monitoring
12/23 06:01:30 - 12/23 17:50:54	Track (809.2, -263.2) @ 12/23 05:58:30	#AR11928 monitoring
12/23 18:04:00 - 12/23 23:59:54	Track (-232.1, -240.8) @ 12/23 18:01:00	#AR11931 monitoring
12/24 00:03:00 - 12/24 05:59:54	Track (877.5, -267.4) @ 12/24 00:00:00	#AR11928 monitoring
12/24 06:13:00 - 12/24 09:24:00	Track (894.9, -269.1) @ 12/24 06:10:00	#AR11928 monitoring

PROG= 13 15-time(s)

Subr= 1 45-time(s) 2.0sec

Seqn= 44 1-time(s) 8.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Subr= 2 1-time(s) 2.0sec

Seqn= 10 1-time(s) 2.0sec

med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 11 1-time(s) 2.0sec

Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec

Seqn= 15 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval

✲ ✲ ✲ ✲ ✲ Active Region Search ✲ ✲ ✲ ✲ ✲

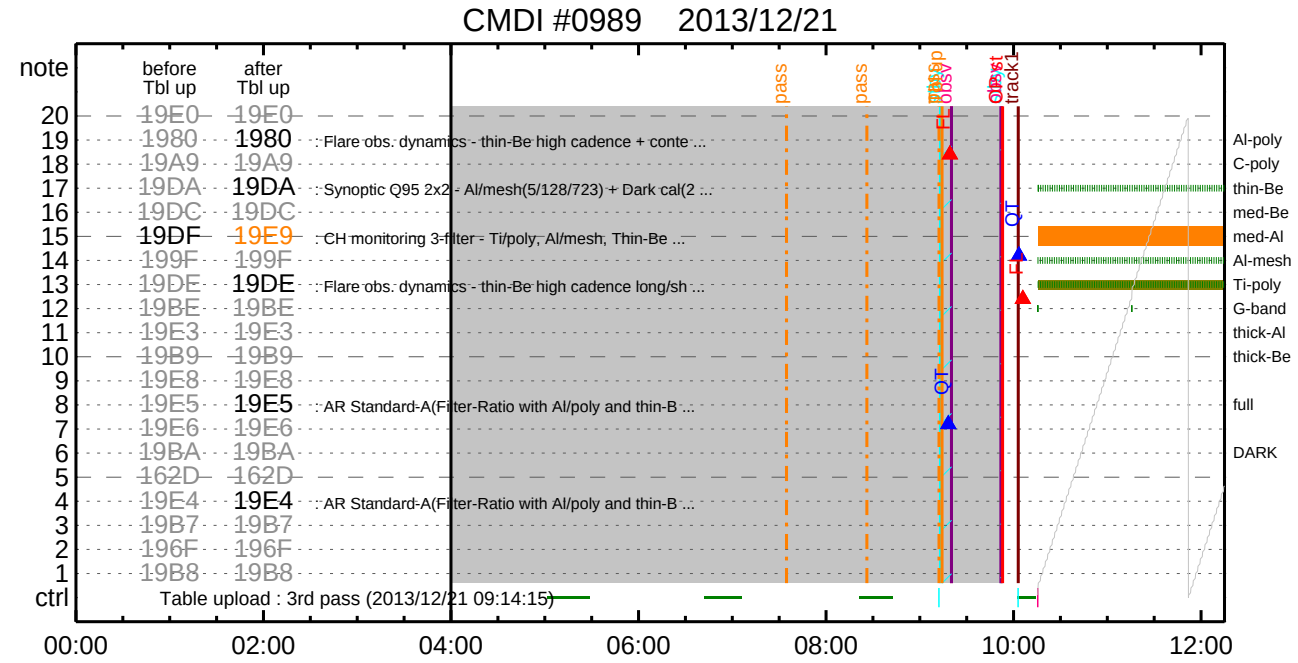
NOT USED

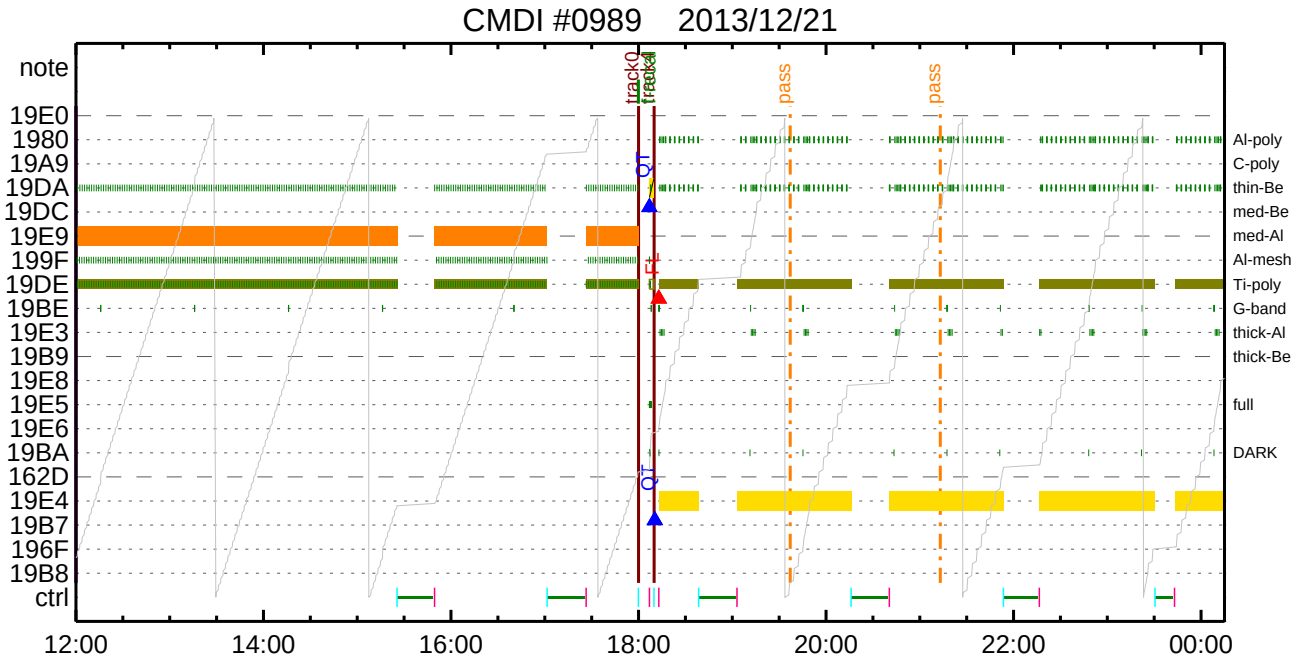
✲ ✲ ✲ ✲ ✲ Flare Detection ✲ ✲ ✲ ✲ ✲

FLD Patrol

Term	Pointing (x, y)	Comment
12/21 18:10:16 - 12/22 06:15:16	Track (604.6, -257.5) @ 12/21 18:10:00	#AR11928 monitoring
12/22 06:25:16 - 12/22 18:01:46	Track (684.0, -259.0) @ 12/22 06:25:00	#AR11928 monitoring
12/22 18:11:46 - 12/23 05:48:46	Track (751.4, -260.9) @ 12/22 18:11:30	#AR11928 monitoring
12/23 05:58:46 - 12/23 17:51:16	Track (809.2, -263.2) @ 12/23 05:58:30	#AR11928 monitoring
12/23 18:01:16 - 12/24 06:00:16	Track (-232.1, -240.8) @ 12/23 18:01:00	#AR11931 monitoring
12/24 06:10:16 - 12/26 10:13:00	Track (894.9, -269.1) @ 12/24 06:10:00	#AR11928 monitoring

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC	Buffer	Interval


CMDI #0989 2013/12/21
note
before Tbl up
after Tbl up
20 19E0 19E0
19 1980 1980 : Flare obs. dynamics - thin-Be high cadence + conte ...
18 19A9 19A9
17 19DA 19DA : Synoptic Q95 2x2 - Al/mesh(5/128/723) + Dark cal(2 ...
16 19DC 19DC
15 19DF 19E9 : CH monitoring 3-filter - Ti/poly, Al/mesh, Thin-Be ...
14 199F 199F
13 19DE 19DE : Flare obs. dynamics - thin-Be high cadence long/sh ...
12 19BE 19BE
11 19E3 19E3
10 19B9 19B9
9 19E8 19E8
8 19E5 19E5 : AR Standard-A(Filter-Ratio with Al/poly and thin-B ...
7 19E6 19E6
6 19BA 19BA
5 162D 162D
4 19E4 19E4 : AR Standard-A(Filter-Ratio with Al/poly and thin-B ...
3 19B7 19B7
2 196F 196F
1 19B8 19B8
ctrl
Table upload : 3rd pass (2013/12/21 09:14:15)
pass
pass
QT
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0989 2013/12/21
note
19E0
1980
19A9
19DA
19DC
19E9
199F
19DE
19BE
19E3
19B9
19E8
19E5
19E6
19BA
162D
19E4
19B7
196F
19B8
ctrl
pass
pass
QT
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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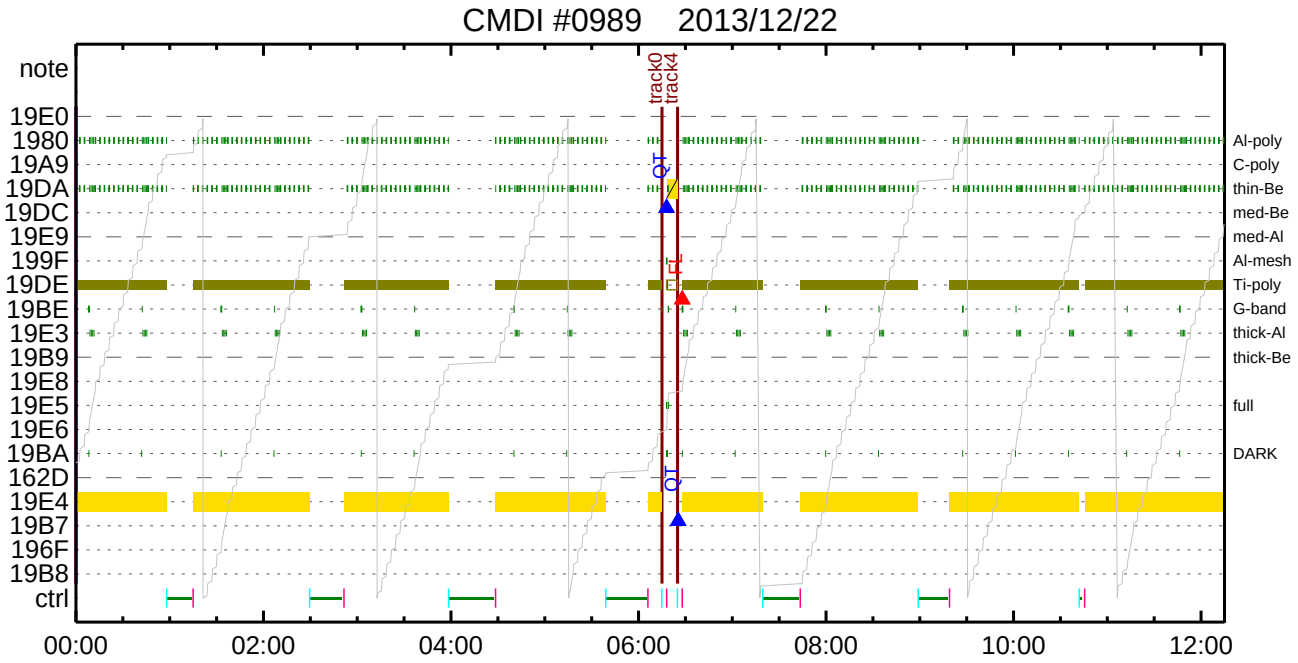
CMDI #0989 2013/12/22
note
19E0
1980
19A9
19DA
19DC
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19E5
19E6
19BA
162D
19E4
19B7
196F
19B8
ctrl
track0
track4
QT
QT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
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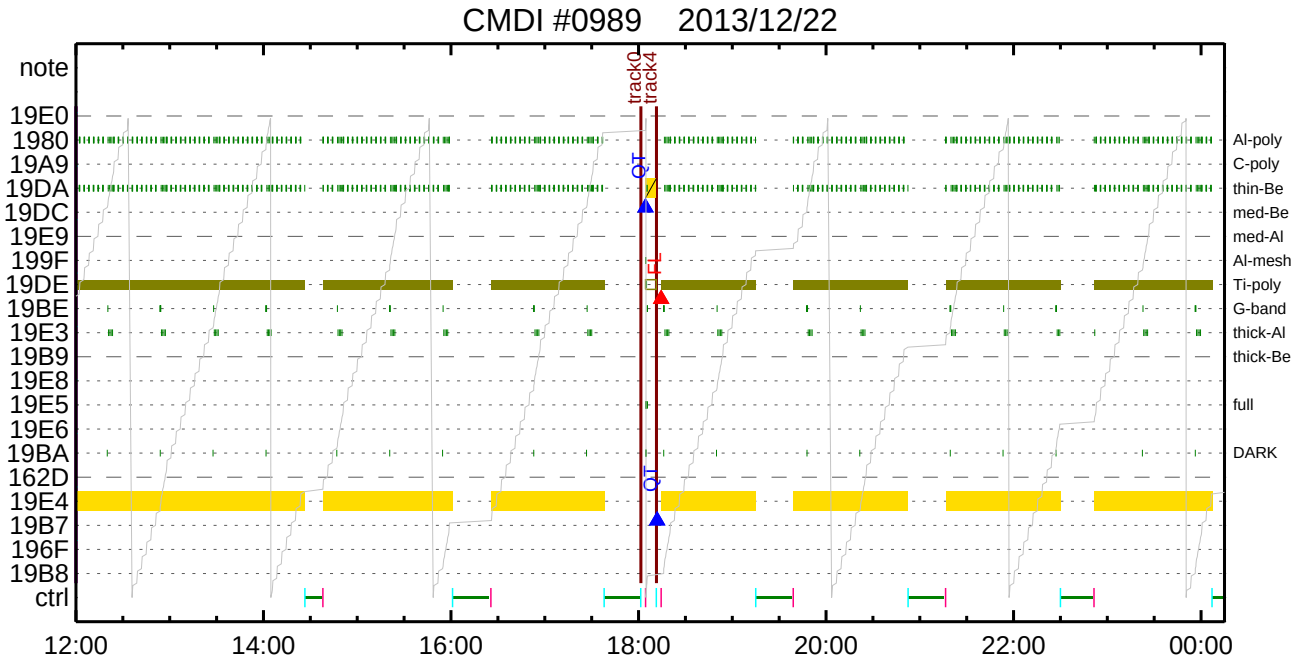
CMDI #0989 2013/12/22
note
19E0
1980
19A9
19DA
19DC
19E9
199F
19DE
19BE
19E3
19B9
19E8
19E5
19E6
19BA
162D
19E4
19B7
196F
19B8
ctrl
track0
track4
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
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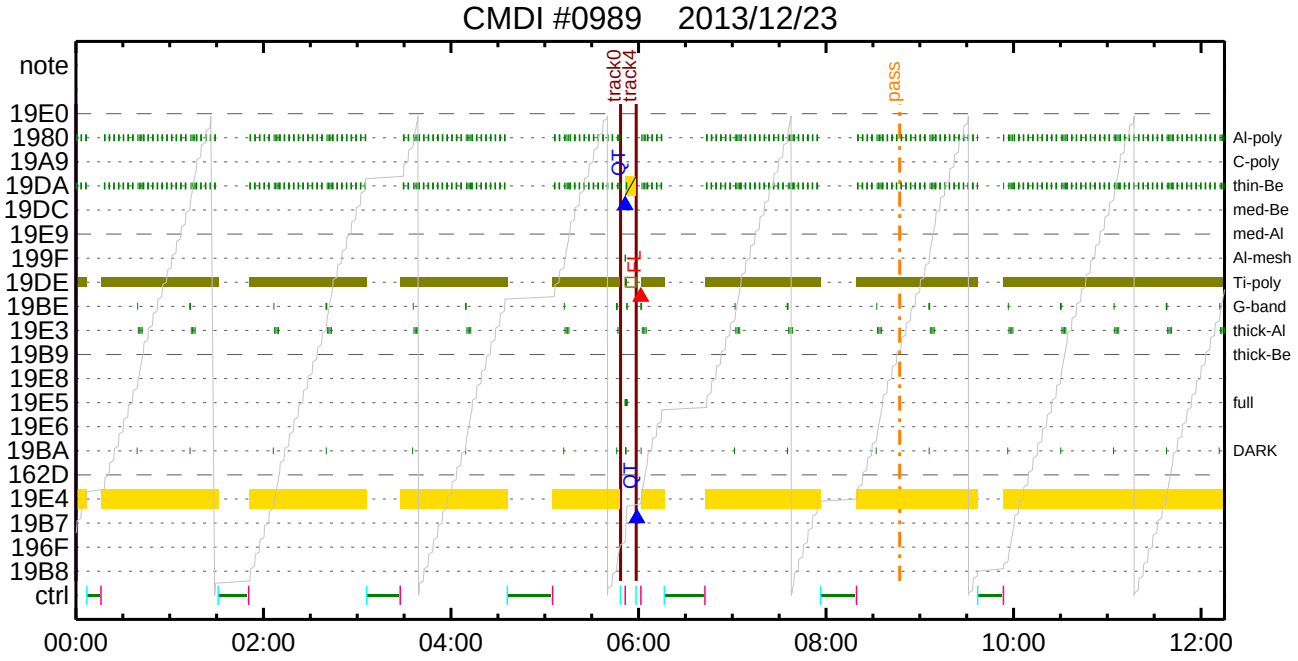
CMDI #0989 2013/12/23
note
19E0
1980
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162D
19E4
19B7
196F
19B8
ctrl
track0
track4
pass
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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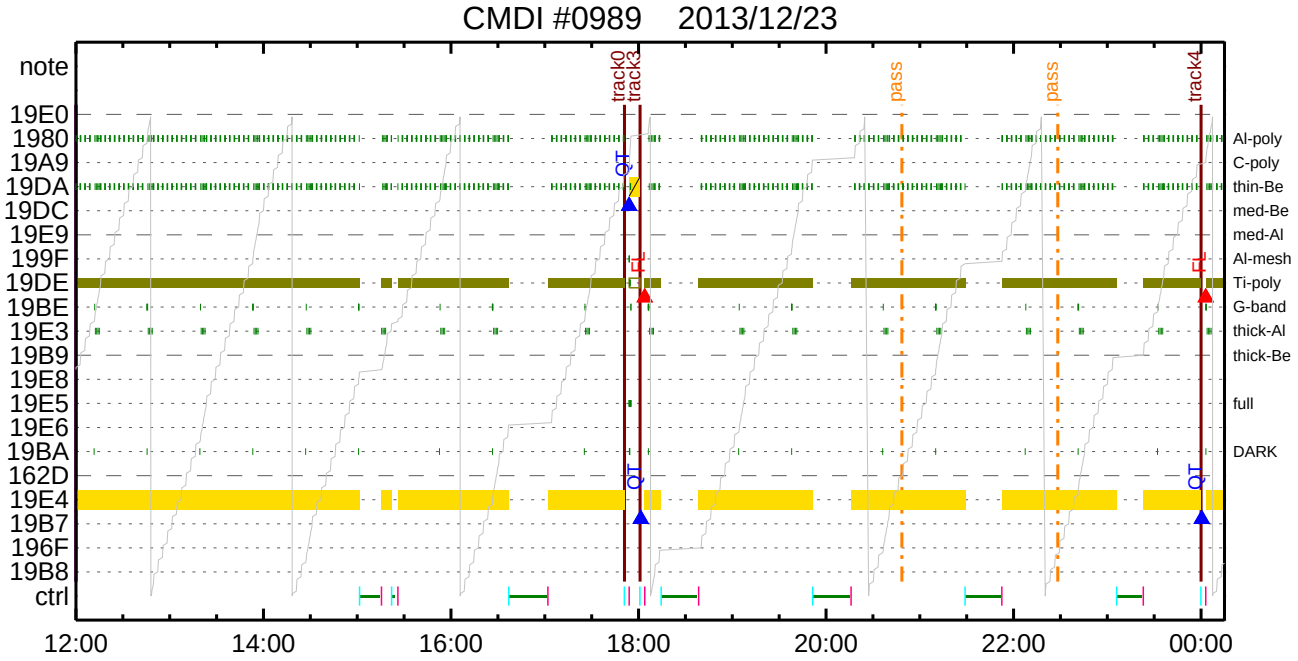
CMDI #0989 2013/12/23
note
19E0
1980
19A9
19DA
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19E9
199F
19DE
19BE
19E3
19B9
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19E6
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162D
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19B7
196F
19B8
ctrl
track0
track3
pass
pass
track4
QT
FL
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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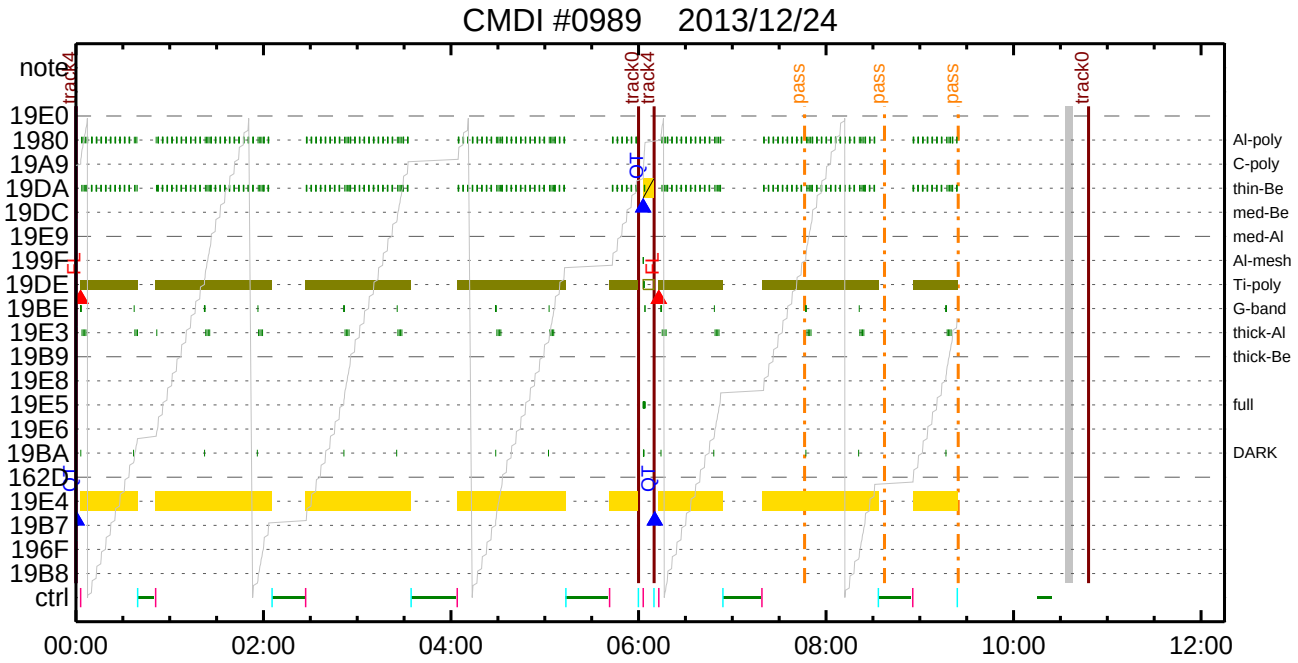
CMDI #0989 2013/12/24
note
19E0
1980
19A9
19DA
19DC
19E9
199F
19DE
19BE
19E3
19B9
19E8
19E5
19E6
19BA
162D
19E4
19B7
196F
19B8
ctrl
track4
track0
track4
pass
pass
pass
track0
OT
FL
OT
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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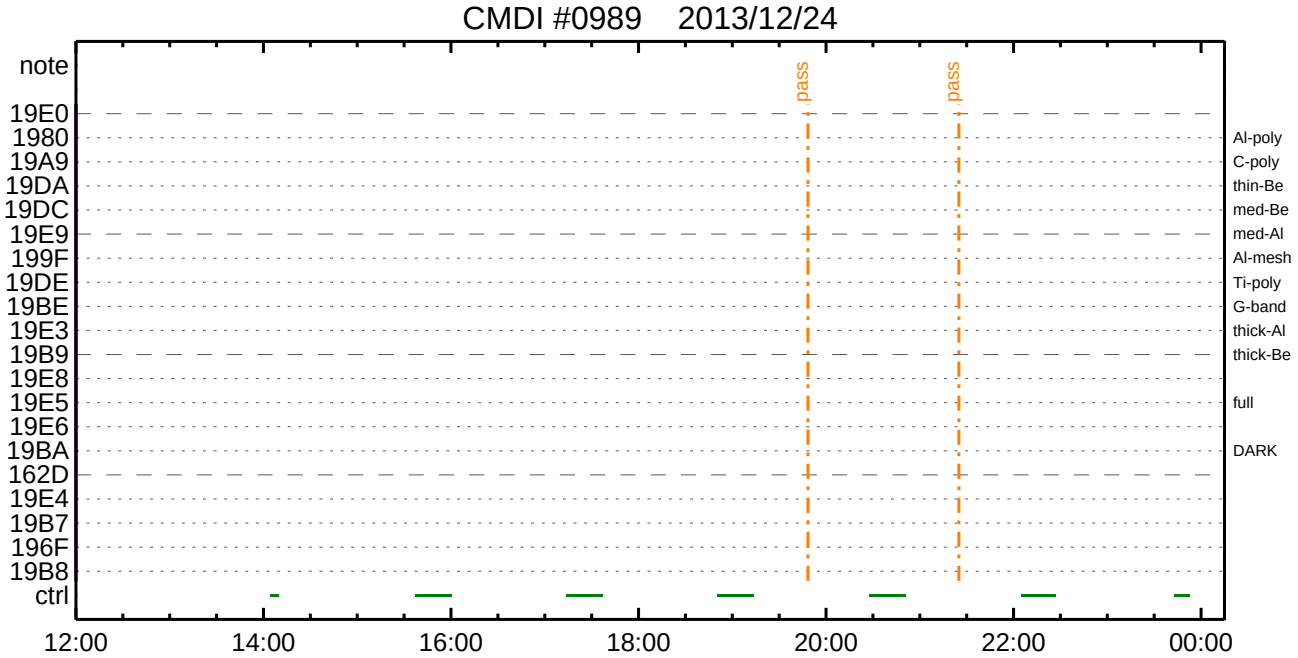
12:00


CMDI #0989 2013/12/24
note
19E0
1980
19A9
19DA
19DC
19E9
199F
19DE
19BE
19E3
19B9
19E8
19E5
19E6
19BA
162D
19E4
19B7
196F
19B8
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
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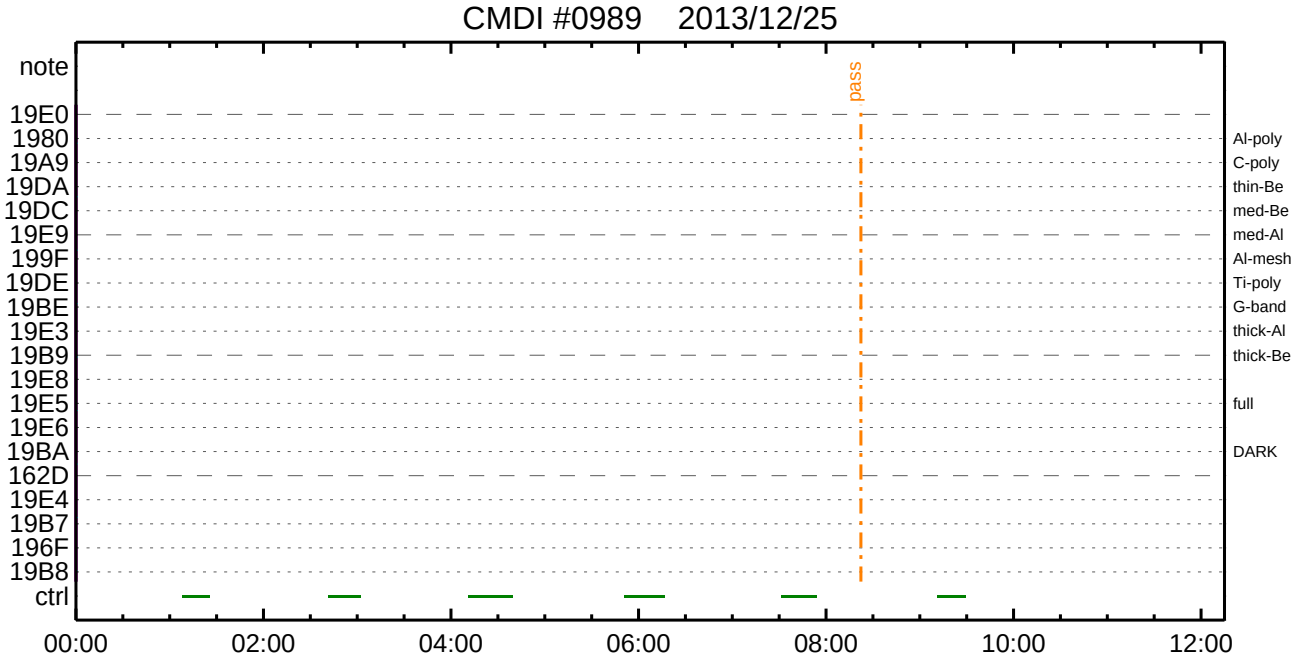
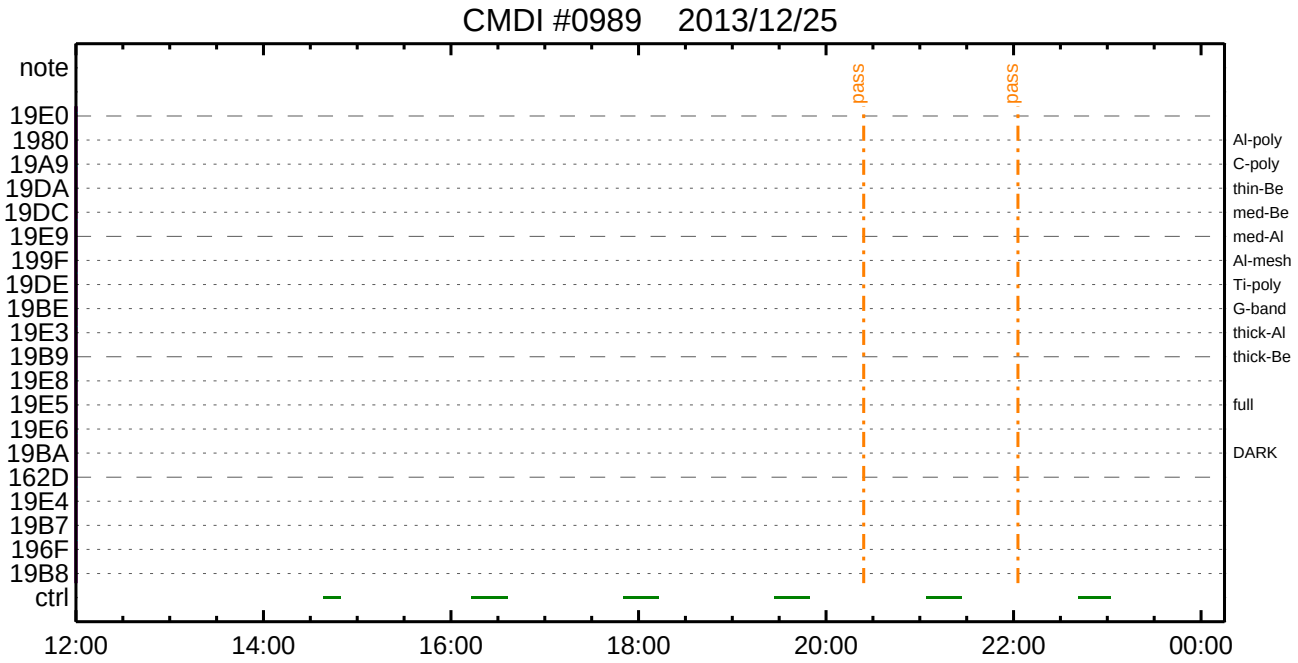
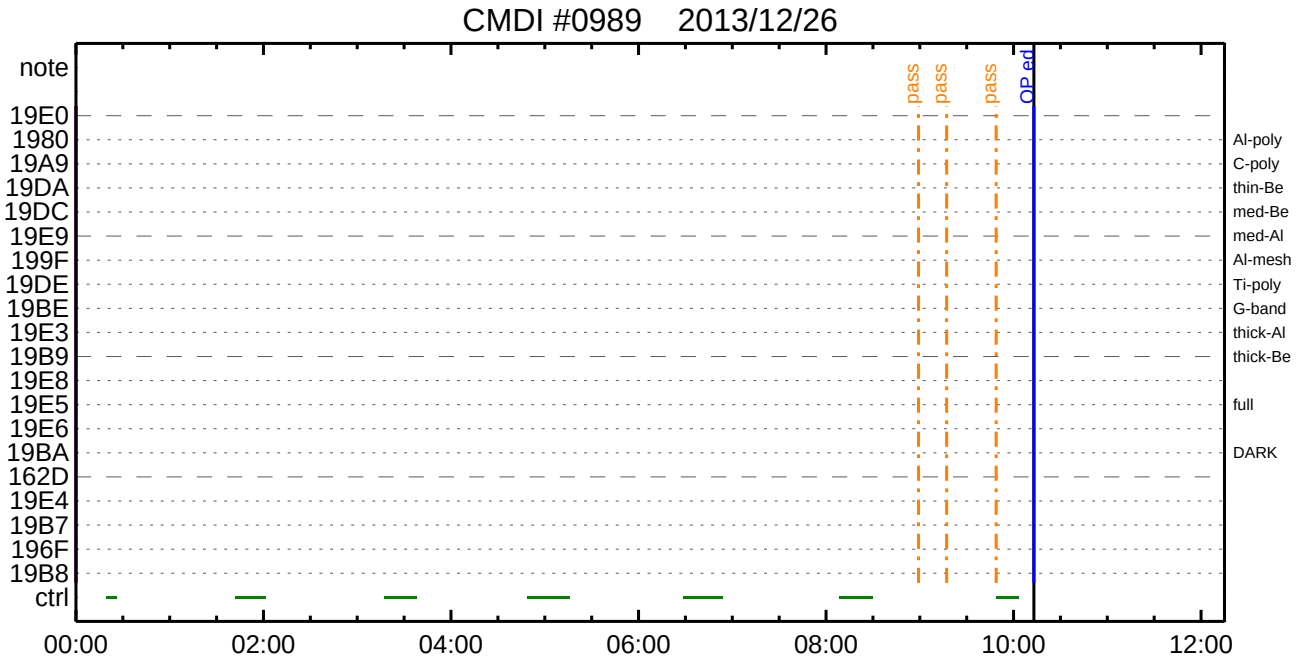
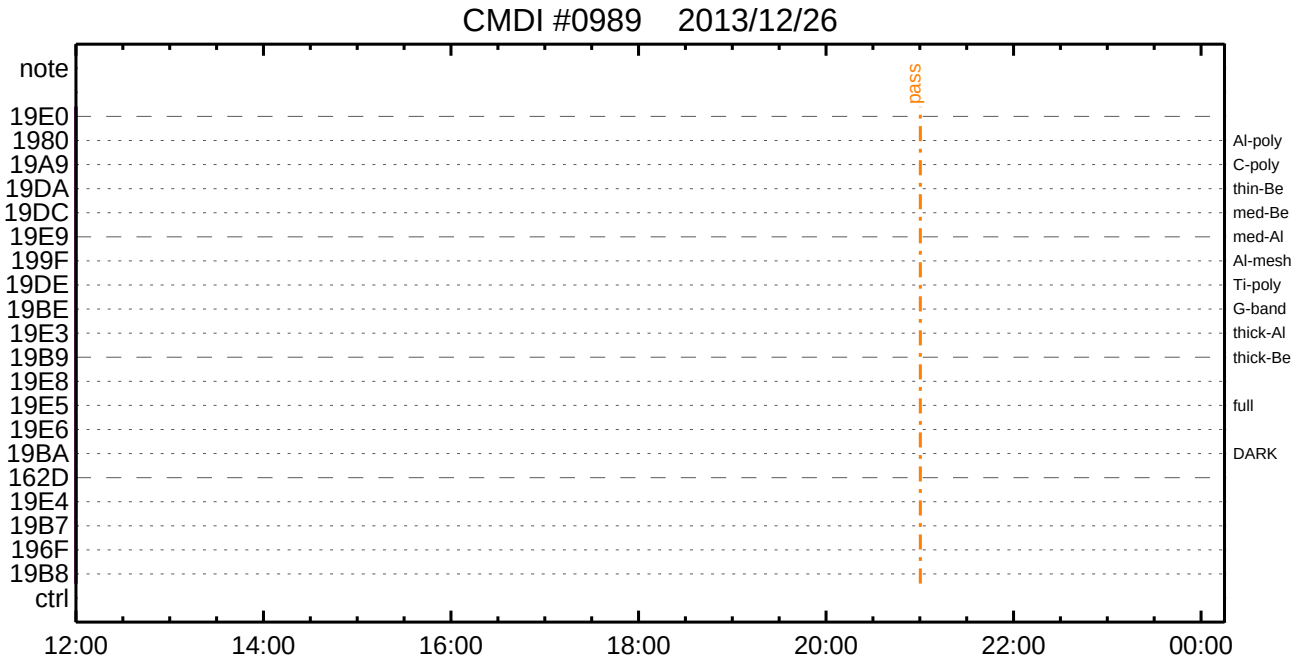
22:00
00:00


CMDI #0989 2013/12/25
note
19E0
1980
19A9
19DA
19DC
19E9
199F
19DE
19BE
19E3
19B9
19E8
19E5
19E6
19BA
162D
19E4
19B7
196F
19B8
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0989 2013/12/25
note
19E0
1980
19A9
19DA
19DC
19E9
199F
19DE
19BE
19E3
19B9
19E8
19E5
19E6
19BA
162D
19E4
19B7
196F
19B8
ctrl
pass
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00


CMDI #0989 2013/12/26
note
19E0
1980
19A9
19DA
19DC
19E9
199F
19DE
19BE
19E3
19B9
19E8
19E5
19E6
19BA
162D
19E4
19B7
196F
19B8
ctrl
pass
pass
pass
OP ed
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
00:00
02:00
04:00
06:00
08:00
10:00
12:00


CMDI #0989 2013/12/26
note
19E0
1980
19A9
19DA
19DC
19E9
199F
19DE
19BE
19E3
19B9
19E8
19E5
19E6
19BA
162D
19E4
19B7
196F
19B8
ctrl
pass
Al-poly
C-poly
thin-Be
med-Be
med-Al
Al-mesh
Ti-poly
G-band
thick-Al
thick-Be
full
DARK
12:00
14:00
16:00
18:00
20:00
22:00
00:00

(a) Spacecraft Operation Procedure (real-commands)

```
main-112 2013-12-21 13:29:49 289 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥ÃÃ¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011  +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇÇÎ»þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019  +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021  +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023  +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                    ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                    ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                    ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                    ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥ÃÃ¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037  +. DC 01-29 DHU_S/X_VC4_OFF
0038  +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040  +  DC 06-B3 DR_REP_START
0041  +  DC 01-32 DHU_X_VC4_ON
0042     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸ ¡¾Ú)
0043     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸ ¡¾Ú)
0044     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸ ¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸ å¤ÎºÆÀ¸ ºÆ³«¡ä
0047  +. DC 06-B3 DR_REP_START
0048  +  DC 01-32 DHU_X_VC4_ON
0049     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸ ¡¾Ú)
0050     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸ ¡¾Ú)
0051     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸ ¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸ å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063  +. DC 01-29 DHU_S/X_VC4_OFF
0064  +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066  +  DC 06-B3 DR_REP_START
0067  +  DC 01-32 DHU_X_VC4_ON
0068     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸ ¡¾Ú)
0069     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸ ¡¾Ú)
0070     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸ ¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸ å¤ÎºÆÀ¸ ºÆ³«¡ä
0073  +. DC 06-B3 DR_REP_START
0074  +  DC 01-32 DHU_X_VC4_ON
0075     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸ ¡¾Ú)
0076     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸ ¡¾Ú)
0077     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸ ¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084  +. DC 06-B4 DR_REP_STOP
0085  +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                    ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                    ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091  +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093  +  DC 03-B5 TCIA_XPA_OFF
0094     C.                    ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                    ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. ****************************
0099     C. OP/OG¥í¡¼¥É¡¦¥À¥ó¥×
0100     C. ****************************
0101     C.
0102   . C. ¡ãOP/OG¥í¡¼¥É¡ä
0103   . S. OP    op-112:OP
0104     ()
0105   . S. OG    og-112:OG
0106     ()
0107     C.
0108   . C. ¡ãNMOG&OPÎÎ°è¥À¥ó¥×¡ä
0109     C. NMOG(0x200000-0x207FFF¡§ 32 kbyte)
0110  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111     BC       (20 00 7f 01 02)
0112     C.                    ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     40
0113     C.                    ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0114     C.                    ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0115     C.                    ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0116     C.                    ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0117  +. DC 01-22 DHU_MODE_CHNG
0118     BC       (07 0b f8)
0119     C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     7
0120     C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0121     C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0122     C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0123     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0124   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0125     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0126   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0127     C.
0128     C. NMOG(0x208000-0x20FFFF¡§ 32 kbyte)
0129  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130     BC       (20 80 7f 01 02)
0131     C.                    ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     41
0132     C.                    ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0133     C.                    ¢¢[HK1_DMP_BLOCK_NUM]           EQ     127
0134     C.                    ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0135     C.                    ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0136  +. DC 01-22 DHU_MODE_CHNG
0137     BC       (07 0b f8)
0138     C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     7
0139     C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0140     C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0141     C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0142     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0143   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0144     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0145   . C. RAM ID=NMOG¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0146     C.
0147     C. NMOG(0x210000-0x2100FF¡§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149     BC       (21 00 41 01 02)
0150     C.                    ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     42
0151     C.                    ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     0
0152     C.                    ¢¢[HK1_DMP_BLOCK_NUM]           EQ     65
0153     C.                    ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0154     C.                    ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0155  +. DC 01-22 DHU_MODE_CHNG
0156     BC       (07 0b f8)
0157     C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     7
0158     C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0159     C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0160     C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0161     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0162   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0163     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0164   . C. RAM ID=NMOG,RAM ID=OP¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0165     C.
0166   . C. ***** °Ê²¼¤Ï¾Ã´¶Á°¤ËÉ¬¤ºÁ÷¿® (¾åµ-¥À¥ó¥×¼ê½ç¤òÅÓÃæ¤Ç½ª¤¨¤ë¾ì¹ç¤Ç¤â) *****
0167     C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0168  +. DC 01-22 DHU_MODE_CHNG
0169     BC       (02 0a f8)
0170     C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     2
0171     C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0172     C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0173     C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0174     C.
0175   . C. *********************************
0176     C. TI-CMD SET (OPOG STOP/COPY/START)
0177     C. *********************************
0178     C.
0179   . C. NOTICE ¡§ OPOG UPLOAD¤¬Á÷¿®NG¤Î¾ì¹ç¡¢°Ê²¼¤ÎTI-CMDÁ÷¿®¤Ï¼Â¹Ô¤•¤Ê¤¤¤³¤È¡£
0180     C.           ¤Þ¤¿¡¢SET¤ÈDUMP¤ÏÆ±°ì¥Ñ¥¹¤Ç¹Ô¤¦¤³¤È¡£
0181     C.
0182   . C. TI¥³¥Þ¥ó¥É¤òÅÐÏ¿(UT)
0183  +. TI 2013-12-21 09:48:00.0
0184     DC 01-B3 DHU_OP_STOP
0185     C.                    ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0186     C.
0187  +. TI 2013-12-21 09:48:01.0
0188     DC 01-B4 DHU_OP_COPY
0189     C.                    ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0190     C.
0191  +. TI 2013-12-21 09:48:01.0
0192     DC 01-B5 DHU_OPOG_COPY
0193     C.                    ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
```

```
0194     C.
0195  +. TI 2013-12-21 09:52:59.5
0196     DC 01-B2 DHU_OP_START
0197     C.                    ¢¢[HK1_TI_CMD_NUM]              EQ     1COUNTUP
0198     C.
0199     C. °Ê²¼¤ÏÄê¾ïÍÑ¤Î¥Á¥§¥Ã¥¯¹àÌÜ
0200     C.                    ¢¢[HK1_TI_CMD_ENA/DIS]          EQ     ENA
0201     C.                    ¢¢[HK1_TI_CMD_NUM]              EQ     4
0202     C.                    ¢¢[HK1_NEXT_EXEC_PIM]           EQ     DHU
0203     C.                    ¢¢[HK1_NEXT_EXEC_DC]            EQ     0xB3
0204     C.
0205   . C. ****************************
0206     C. TIÎÎ°è¥À¥ó¥×
0207     C. ****************************
0208     C.
0209     C. TI_TBL(0x03AB00-0x03AEFF¡§ 1024byte)
0210  +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211     BC       (03 ab 03 01 02)
0212     C.                    ¢¢[HK1_DMP_TOP_ADRS_1]          EQ     07
0213     C.                    ¢¢[HK1_DMP_TOP_ADRS_0]          EQ     2B
0214     C.                    ¢¢[HK1_DMP_BLOCK_NUM]           EQ     3
0215     C.                    ¢¢[HK1_DMP_REPEAT_NUM]          EQ     0
0216     C.                    ¢¢[HK1_DMA_DMP_PIM]             EQ     DHU
0217  +. DC 01-22 DHU_MODE_CHNG
0218     BC       (07 0b f8)
0219     C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     7
0220     C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.25 s
0221     C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32k
0222     C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0223     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     EXEC
0224     C.
0225   . C. ¥À¥ó¥×½ªÎ»¤ò³ÎÇ§
0226     C.                    ¢¢[HK1_DMP_CHK_FLG]             EQ     NON
0227     C.
0228   . C. RAM ID=TI_TBL¤Î¾È¹ç•ë²ÌOK¤ò³ÎÇ§
0229     C.
0230   . C. DHU¥â¡¼¥É¡Ê¼ý½¸¥ì¡¼¥È¡Ë¤òÌá¤¹
0231  +. DC 01-22 DHU_MODE_CHNG
0232     BC       (02 0a f8)
0233     C.                    ¢¢[HK1_PKT_FORM_NO]             EQ     2
0234     C.                    ¢¢[HK1_PKT_GEN_TIME]            EQ     0.5S
0235     C.                    ¢¢[HK1_S_TLM_BIT_RATE]          EQ     32K
0236     C.                    ¢¢[HK1_X_TLM_BIT_RATE]          EQ     4M
0237     C.
0238     C. *********************************
0239     C.  SOT TI command set
0240     C. *********************************
0241     C. Execute, after the success of OP upload.
0242  +. TI 2013-12-21 09:52:16.0
0243     DC 07-F0 MDP_SOT_MODE_STBY
0244     BC       (41)
0245   . C. ------------------------------------
0246     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0247     C. ------------------------------------
0248     C. ************ SOT END ***********
0249   . C. Stop EIS observation and temporarily disable EIS mode changes
0250     C.
0251     C.
0252     C. ********** Start EIS operation (TI set) **********
0253     C. Execute, after the success of OP upload.
0254     C. Set EIS TI-commands
0255  +. TI 2013-12-21 09:52:30.0
0256     DC 07-FC EIS_MODE_MANU
0257     BC       (21 02)
0258  +. TI 2013-12-21 09:52:40.0
0259     DC 07-FC EIS_MODE_CHG_DIS
0260     BC       (22)
0261   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    2 COUNTUP
0262     C. *********** End EIS operation (TI set) ***********
0263     C.
0264     C.
0265     C.
0266     C. ************ XRT START ***********
0267     C. Execute, after the success of OP upload.
0268  +. TI 2013-12-21 09:52:00.0
0269     DC 07-F0 MDP_XRT_MODE_STBY
0270     BC       (c3)
0271   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1COUNTUP
0272     C.
0273     C. ************ XRT END ***********
0274     C.
0275   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0276     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0277   . S. DC-BC dcbc-402:DCBC
0278     (MDP_known_event)
0279     C.
0280     C.
0281   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0282   . S. DC-BC dcbc-153:DCBC
0283     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284     C.
0285     C.
0286   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0287     C.
0288   . C. *********** LOS ***********
0289     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-113 2013-12-21 13:29:49 82 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011  +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ***** AOCS Commands (Tracking Curve Upload) ******
0015     C. Upload the Orbit Element and the Target Attitude
0016     C. RAM-ID:TARGET_ATT
0017   . S. RAM   ram-150:TARGET_ATT
0018     ()
0019     C.
0020     C.
0021     C. Set the dump memory area of TARGET_ATT
0022  +. DC 02-48 AOCU_DUMP_SET
0023     BC       (07 00 00 00 18 00)
0024     C.
0025     C.    <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026     C.
0027     C.
0028     C. Change the TLMFormatNo for the AOCS Dump Format
0029  +. DC 01-22 DHU_MODE_CHNG
0030     BC       (04 0b f8)
0031     C.
0032     C. Wait for AOCSDUMP to end
0033     C.
0034   . C. Check the dump memory
0035     C.
0036     C.                                      Result = OK [ ]
0037     C.
0038  +. DC 01-22 DHU_MODE_CHNG
0039     BC       (02 0a f8)
0040     C.
0041     C.                        <A_***>[TLM STS] FMT = 2 [ ]
0042     C.
0043  +. DC 02-8E AOCU_ORB_UPD
0044   . C.
0045   . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046  +. DC 07-FC EIS_MODE_MANU
0047     BC       (21 02)
0048   . C. Verify EIS in MANUAL mode
0049   . C. Estimated OBSTBL upload time is 29s
0050     C. ********************************
0051     C. EIS START OBSTBL LOAD
0052     C. ********************************
0053   . S. RAM   ram-820:EIS_OBSTBL
0054     ()
0055  +. DC 07-FC EIS_DUMP_OBSTBL
0056     BC       (07 07 07 00 00 70 00)
0057     C.
0058     C. Execute, after the success of OBSTBL upload.
0059     C. Set EIS TI-commands
0060  +. TI 2013-12-21 09:52:50.0
0061     DC 07-FC EIS_MODE_CHG_ENA
0062     BC       (20)
0063   . C.                      [   ]  [HK1_TI_CMD_NUM]     EQ    1 COUNTUP
0064     C. ********************************
0065     C. EIS END OBSTBL LOAD
0066     C. ********************************
0067     C.
0068   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0069     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0070   . S. DC-BC dcbc-402:DCBC
0071     (MDP_known_event)
0072     C.
0073     C.
0074   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0075   . S. DC-BC dcbc-153:DCBC
0076     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077     C.
0078     C.
0079   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0080     C.
0081   . C. *********** LOS ***********
0082     C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-114 2013-12-21 13:29:49 232 33 SOLAR-B MAIN //
0001     C.
0002   . C. *********** AOS ***********
0003     C.
0004   . C. ¡ãAOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0005     C.
0006     C. ¥À¥ß¡¼¥³¥Þ¥ó¥ÉÁ÷¿®
0007  +. DC 00-00 NULL_DUMMY_CMD
0008     C.
0009   . C. ***** AOCS : Reload orbital element (send every contact) *****
0010     C.  Ãí¡Ë¤¿¤À¤•µ°Æ»Í×ÁÇ¤Î¥¢¥Ã¥×¥í¡¼¥É¡ÊËè½µ•îÍË¡Ë¤Ë¼ºÇÔ¤•¤¿¾ì¹ç¤Ï¡¢À®¸ù¤¹¤ë¤Þ¤ÇÁ÷¿®¤•¤Ê¤¤¤³¤È¡£
0011  +. DC 02-8E AOCU_ORB_UPD
0012     C.
0013     C.
0014   . C. ****************************
0015     C. XÁ÷¿®µ¡ON
0016     C. ****************************
0017     C. ¢¨ ºÆÀ¸ Í×ÈÝ¤äLOS¤Þ¤ÇÇÎ»þ´Ö¤ò¹ÍÎ¸ ¤•¡¢ÉÔÍ×¤ÊXÂÓON¤Ï¹Ô¤Ê¤ï¤Ê¤¤¤³¤È¡£
0018     C.
0019  +. DC 03-B4 TCIA_XPA_ON/HI
0020     M. WAIT_SEC   1
0021  +  DC 03-84 TCIA_XMOD_ON
0022     M. WAIT_SEC   1
0023  +  DC 03-95 TCIA_XMOD_QPSK
0024     C.                    ¢¢[HK1_XPA_ON/OFF]              EQ     ON
0025     C.                    ¢¢[HK1_XPA_PWR_HI/LO]           EQ     HI
0026     C.                    ¢¢[HK1_XMOD_ON/OFF]             EQ     ON
0027     C.                    ¢¢[HK1_XMOD_QPSK/PM]            EQ     QPSK
0028     C.
0029   . C. X¥Ð¥ó¥É¥í¥Ã¥¯¾õÂÖ¤¬°ÂÄê¤•¤¿¤é¡¢°Ê²¼¤ÎºÆÀ¸ ¼ê½ç¤ò¼Â¹Ô¤¹¤ë¡£
0030     C.
0031   . C. ***************
0032     C. DR PT1 ÄÌ¾ïºÆÀ¸
0033     C. ***************
0034     C. ¢¨ RESTART¡ÊPT1¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-150¤Ø¿Ê¤à¡£
0035     C.
0036   . C. ¡ãPT1ºÆÀ¸ ³«»Ï¡ä
0037  +. DC 01-29 DHU_S/X_VC4_OFF
0038  +  DC 06-C8 DR_PT1_REP_SEL
0039     BC       (01 00)
0040  +  DC 06-B3 DR_REP_START
0041  +  DC 01-32 DHU_X_VC4_ON
0042     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸ ¡¾Ú)
0043     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸ ¡¾Ú)
0044     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸ ¡¾Ú)
0045     C.
0046   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸ å¤ÎºÆÀ¸ ºÆ³«¡ä
0047  +. DC 06-B3 DR_REP_START
0048  +  DC 01-32 DHU_X_VC4_ON
0049     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT1    (¼Â¹Ô¸ ¡¾Ú)
0050     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸ ¡¾Ú)
0051     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸ ¡¾Ú)
0052     C.
0053     C.
0054   . C. PT1ºÆÀ¸ ¤¬¼«Æ°Ää»ß¤•¤¿¸ å¡¢°Ê²¼¤ò¼Â¹Ô¤¹¤ë¡£
0055     C. ¥¢¥ó¥Æ¥ÊÀÚÂØ¤äÅ•Äº²óÈò¤¬¶á¤¤¾ì¹ç¤Ï´°Î»¤¹¤ë¤Þ¤ÇÂÔ¤Ä¡£
0056     C.
0057   . C. ***************
0058     C. DR PT2 ÄÌ¾ïºÆÀ¸
0059     C. ***************
0060     C. ¢¨ RESTART¡ÊPT2¡Ë¤•¤¿¤¤¾ì¹ç¤Ï¡¢°Ê²¼¤Ï¼Â¹Ô¤»¤º¡¢DCBC-151¤Ø¿Ê¤à¡£
0061     C.
0062   . C. ¡ãPT2ºÆÀ¸ ³«»Ï¡ä
0063  +. DC 01-29 DHU_S/X_VC4_OFF
0064  +  DC 06-C8 DR_PT2_REP_SEL
0065     BC       (02 00)
0066  +  DC 06-B3 DR_REP_START
0067  +  DC 01-32 DHU_X_VC4_ON
0068     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸ ¡¾Ú)
0069     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸ ¡¾Ú)
0070     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸ ¡¾Ú)
0071     C.
0072   . C. ¡ã¥¢¥ó¥Æ¥ÊÀÚÂØ¡ÊÅ•Äº²óÈò¡Ë¸ å¤ÎºÆÀ¸ ºÆ³«¡ä
0073  +. DC 06-B3 DR_REP_START
0074  +  DC 01-32 DHU_X_VC4_ON
0075     C.                    ¢¢[HK1_REP_PT_1/2]              EQ     PT2    (¼Â¹Ô¸ ¡¾Ú)
0076     C.                    ¢¢[HK1_REP_STA/STP]             EQ     START  (¼Â¹Ô¸ ¡¾Ú)
0077     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     ON     (¼Â¹Ô¸ ¡¾Ú)
0078     C.
0079   . C. ****************************
0080     C. DRºÆÀ¸ Ää»ß¡¢XÁ÷¿®µ¡OFF
0081     C. ****************************
0082     C.
0083   . C. ¡ãDRºÆÀ¸ Ää»ß¡ä
0084  +. DC 06-B4 DR_REP_STOP
0085  +  DC 01-29 DHU_S/X_VC4_OFF
0086     C.                    ¢¢[HK1_REP_STA/STP]             EQ     STOP
0087     C.                    ¢¢[HK1_S_VC4_ON/OFF]            EQ     OFF
0088     C.                    ¢¢[HK1_X_VC4_ON/OFF]            EQ     OFF
0089     C.
0090   . C. ¡ãXÁ÷¿®µ¡OFF¡ä
0091  +. DC 03-85 TCIA_XMOD_OFF
0092     M. WAIT_SEC   1
0093  +  DC 03-B5 TCIA_XPA_OFF
0094     C.                    ¢¢[HK1_XMOD_ON/OFF]             EQ     OFF
0095     C.                    ¢¢[HK1_XPA_ON/OFF]              EQ     OFF
```

```
0096     C.
0097     C.
0098   . C. *********************************
0099     C.  SOT table upload
0100     C. *********************************
0101   . C. < Stop FG table >
0102  +. DC 07-F0 MDP_FG_CTRL_MANU
0103     BC       (51)
0104   . C. ------------------------------------
0105     C.  MDP_FG_CTRL_MODE      = MANU    [ ]
0106     C. ------------------------------------
0107     C.
0108   . C. <Upload FG Observation Table>
0109   . S. RAM   ram-266:MDP_OBS_F
0110     ()
0111     C.
0112   . C. < Dump RAMID=MDP_OBS_F >
0113  +. DC 07-F0 MDP_DUMP_FGTBL
0114     BC       (82 07 00 00 00 38 b8)
0115     C. ------------------------------------
0116     C.  MDP_OBS_F verify = OK/NG        [ ]
0117     C. ------------------------------------
0118     C.
0119   . C. < Stop SP table >
0120  +. DC 07-F0 MDP_SP_CTRL_MANU
0121     BC       (61)
0122     C. ------------------------------------
0123     C.  MDP_SP_CTRL_MODE      = MANU    [ ]
0124     C. ------------------------------------
0125     C.
0126   . C. <Upload SP Observation Table>
0127   . S. RAM   ram-283:MDP_OBS_S
0128     ()
0129     C.
0130   . C. < Dump RAMID=MDP_OBS_S >
0131  +. DC 07-F0 MDP_DUMP_SPTBL
0132     BC       (83 07 00 00 00 38 b8)
0133     C. ------------------------------------
0134     C.  MDP_OBS_S verify = OK/NG        [ ]
0135     C. ------------------------------------
0136     C.
0137     C. *********************************
0138     C.  SOT TI command set
0139     C. *********************************
0140     C. Execute, after the success of TBL upload.
0141  +. TI 2013-12-21 09:52:18.0
0142     DC 07-F0 MDP_SOT_MODE_OBSV
0143     BC       (40)
0144   . C. ------------------------------------
0145     C.   HK1_TI_CMD_NUM       = 1 CNTUP [ ]
0146     C. ------------------------------------
0147     C.
0148     C.
0149     C. ************ XRT START ************
0150     C.
0151  +. DC 07-F0 MDP_XRT_CTRL_MANU
0152     BC       (c1)
0153  +  DC 07-F0 MDP_XRT_MODE_STBY
0154     BC       (c3)
0155   . C. ----------- Success Verify ?              OK / NG____
0156     C.
0157     C. XRT Obs. Table Upload
0158   . S. RAM   ram-291:MDP_OBS_X
0159     ()
0160     C.
0161  +. DC 07-F0 MDP_DUMP_XRTTBL
0162     BC       (84 07 00 00 00 3a d4)
0163   . C. ----------- Comparison Check ?                       OK / ERR ____
0164     C.
0165     C.
0166  +. DC 07-F0 MDP_XRT_ROI_SET
0167     BC       (cd 01 b1 b1 04 04)
0168  +  DC 07-F0 MDP_XRT_ROI_SET
0169     BC       (cd 02 b1 b1 08 08)
0170  +  DC 07-F0 MDP_XRT_ROI_SET
0171     BC       (cd 03 b1 b1 08 08)
0172  +  DC 07-F0 MDP_XRT_ROI_SET
0173     BC       (cd 04 b1 b1 06 06)
0174  +  DC 07-F0 MDP_XRT_ROI_SET
0175     BC       (cd 05 85 83 06 06)
0176  +  DC 07-F0 MDP_XRT_ROI_SET
0177     BC       (cd 06 85 83 06 06)
0178  +  DC 07-F0 MDP_XRT_ROI_SET
0179     BC       (cd 07 85 83 08 08)
0180  +  DC 07-F0 MDP_XRT_ROI_SET
0181     BC       (cd 08 80 80 20 20)
0182  +  DC 07-F0 MDP_XRT_ROI_SET
0183     BC       (cd 09 80 80 20 08)
0184  +  DC 07-F0 MDP_XRT_ROI_SET
0185     BC       (cd 0a 80 80 08 20)
0186  +  DC 07-F0 MDP_XRT_ROI_SET
0187     BC       (cd 0f 80 80 06 06)
0188  +  DC 07-F0 MDP_XRT_ROI_SET
0189     BC       (cd 10 80 80 08 08)
0190  +  DC 07-F0 MDP_XRT_FLD_ENA
0191     BC       (d8)
0192  +  DC 07-F0 MDP_XRT_FLRCTRL_ENA
0193     BC       (c8)
```

```
0194  +  DC 07-F0 MDP_XRT_AEC_RESET
0195     BC       (d0)
0196  +  DC 07-F0 MDP_XRT_ARS_DIS
0197     BC       (d5)
0198  +  DC 07-F0 MDP_XRT_FLD_RESET
0199     BC       (da)
0200  +. DC 07-F0 MDP_XRT_QT_PROG_SET
0201     BC       (c4 08)
0202  +. DC 07-F0 MDP_XRT_FL_PROG_SET
0203     BC       (c5 13)
0204   . C. ---------- Success Verify ?            OK / NG ____
0205     C.
0206     C.
0207   . C. All OK?   Yes--> Please Proceed. / No --> Stop here.
0208     C.
0209  +. DC 07-F0 MDP_XRT_MODE_OBSV
0210     BC       (c2)
0211  +. TI 2013-12-21 09:52:02.0
0212     DC 07-F0 MDP_XRT_MODE_OBSV
0213     BC       (c2)
0214   . C. ---------- Success Verify ?            OK / NG ____
0215     C.
0216     C. ************ XRT END ***********
0217     C.
0218   . C. ***** MDP ´ûÃÎ¤Î»ö¾Ý¤ËÂÐ¤¹¤ëDCBC•×²è *****
0219     C. (¾å°Ì¥Ó¥Ã¥È¥Þ¥Ë¥å¥¢¥ë¤Ë½¾¤¤¼Â»Ü¤¹¤ë)
0220   . S. DC-BC dcbc-402:DCBC
0221     (MDP_known_event)
0222     C.
0223     C.
0224   . C. ***** ¥Ð¥¹•Ï Daily±¿ÍÑ¤Ë´Ø¤¹¤ëDCBC•×²è *****
0225   . S. DC-BC dcbc-153:DCBC
0226     (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0227     C.
0228     C.
0229   . C. ¡ãLOS¥Á¥§¥Ã¥¯¼Â»Ü¡ä
0230     C.
0231   . C. *********** LOS ***********
0232     C.
```

```
*** OP Sequence for XRT ***

2013/12/21  10:02:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/21  10:02:56.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2013/12/21  10:03:00.0  AOCS_ORe-point_Start_1_OG [0x097]
                                        AOCU_NM              5 02-76 01 00 00 00 00
2013/12/21  10:03:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2013/12/21  10:03:18.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2013/12/21  10:03:20.0  XRT_AEC_RESET_413_OG [0x19d]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2013/12/21  10:03:22.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2013/12/21  10:03:24.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/21  10:03:26.0  XRT_QT_PROG_SET_435_OG [0x1b3]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 0f
2013/12/21  10:05:58.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0d
2013/12/21  10:14:30.0  XRT_Custom_430_OG [0x1ae]
2013/12/21  10:15:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/21  15:25:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/21  15:25:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/21  15:25:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/21  15:28:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/21  15:48:30.0  XRT_Custom_430_OG [0x1ae]
2013/12/21  15:49:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/21  17:01:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/21  17:01:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/21  17:01:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/21  17:04:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/21  17:25:30.0  XRT_Custom_430_OG [0x1ae]
2013/12/21  17:26:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/21  17:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/21  17:59:56.0  XRT_FOCUS_RECALIBRATE_433_OG [0x1b1]
                                        XRT_FOCUS_RECAL      2 07-F8 78 00
2013/12/21  18:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2013/12/21  18:03:56.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2013/12/21  18:04:16.0  XRT_FLD_DIS_434_OG [0x1b2]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2013/12/21  18:06:54.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2013/12/21  18:06:56.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2013/12/21  18:06:58.0  XRT_QT_PROG_SET_446_OG [0x1be]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2013/12/21  18:07:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/21  18:09:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/21  18:09:56.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2013/12/21  18:10:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 04 00 00 00 00
2013/12/21  18:10:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2013/12/21  18:10:18.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2013/12/21  18:10:20.0  XRT_AEC_RESET_413_OG [0x19d]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2013/12/21  18:10:22.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2013/12/21  18:10:24.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/21  18:10:26.0  XRT_QT_PROG_SET_414_OG [0x19e]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2013/12/21  18:12:58.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0d
2013/12/21  18:13:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/21  18:38:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/21  18:38:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/21  18:38:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
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2013/12/21  18:41:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/21  19:02:01.0  XRT_Custom_430_OG [0x1ae]
2013/12/21  19:03:01.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/21  20:16:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/21  20:16:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/21  20:16:04.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/21  20:19:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/21  20:39:30.0  XRT_Custom_430_OG [0x1ae]
2013/12/21  20:40:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/21  21:53:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/21  21:53:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/21  21:53:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/21  21:56:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/21  22:15:30.0  XRT_Custom_430_OG [0x1ae]
2013/12/21  22:16:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/21  23:30:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/21  23:30:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/21  23:30:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/21  23:33:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/21  23:42:00.0  XRT_Custom_430_OG [0x1ae]
2013/12/21  23:43:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/22  00:58:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/22  00:58:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/22  00:58:04.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/22  01:01:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/22  01:14:00.0  XRT_Custom_430_OG [0x1ae]
2013/12/22  01:15:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/22  02:29:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/22  02:29:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/22  02:29:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/22  02:32:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/22  02:50:30.0  XRT_Custom_430_OG [0x1ae]
2013/12/22  02:51:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/22  03:58:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/22  03:58:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/22  03:58:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/22  04:01:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/22  04:27:30.0  XRT_Custom_430_OG [0x1ae]
2013/12/22  04:28:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/22  05:39:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/22  05:39:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/22  05:39:04.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/22  05:42:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/22  06:05:00.0  XRT_Custom_430_OG [0x1ae]
2013/12/22  06:06:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/22  06:14:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/22  06:14:56.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2013/12/22  06:15:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2013/12/22  06:15:16.0  XRT_FLD_DIS_434_OG [0x1b2]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2013/12/22  06:17:54.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2013/12/22  06:17:56.0  XRT_ARS_DIS_423_OG [0x1a7]
```

```
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2013/12/22  06:17:58.0  XRT_QT_PROG_SET_446_OG [0x1be]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2013/12/22  06:18:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/22  06:24:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/22  06:24:56.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2013/12/22  06:25:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 04 00 00 00 00
2013/12/22  06:25:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2013/12/22  06:25:18.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2013/12/22  06:25:20.0  XRT_AEC_RESET_413_OG [0x19d]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2013/12/22  06:25:22.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2013/12/22  06:25:24.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/22  06:25:26.0  XRT_QT_PROG_SET_414_OG [0x19e]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2013/12/22  06:27:58.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0d
2013/12/22  06:28:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/22  07:19:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/22  07:19:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/22  07:19:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/22  07:22:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/22  07:42:30.0  XRT_Custom_430_OG [0x1ae]
2013/12/22  07:43:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/22  08:59:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/22  08:59:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/22  08:59:04.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/22  09:02:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/22  09:18:00.0  XRT_Custom_430_OG [0x1ae]
2013/12/22  09:19:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/22  10:42:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/22  10:42:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/22  10:42:04.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/22  10:44:30.0  XRT_Custom_430_OG [0x1ae]
2013/12/22  10:45:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/22  10:45:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/22  14:26:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/22  14:26:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/22  14:26:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/22  14:29:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/22  14:37:00.0  XRT_Custom_430_OG [0x1ae]
2013/12/22  14:38:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/22  16:01:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/22  16:01:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/22  16:01:04.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/22  16:04:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/22  16:24:30.0  XRT_Custom_430_OG [0x1ae]
2013/12/22  16:25:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/22  17:38:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/22  17:38:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/22  17:38:04.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/22  17:41:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/22  18:01:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/22  18:01:26.0  XRT_FOCUS_POSITION_403_OG [0x193]
```

```
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2013/12/22  18:01:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2013/12/22  18:01:46.0  XRT_FLD_DIS_434_OG [0x1b2]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2013/12/22  18:04:24.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2013/12/22  18:04:26.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2013/12/22  18:04:28.0  XRT_QT_PROG_SET_446_OG [0x1be]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2013/12/22  18:04:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/22  18:11:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/22  18:11:26.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2013/12/22  18:11:30.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 04 00 00 00 00
2013/12/22  18:11:46.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2013/12/22  18:11:48.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2013/12/22  18:11:50.0  XRT_AEC_RESET_413_OG [0x19d]
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2013/12/22  18:11:52.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2013/12/22  18:11:54.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/22  18:11:56.0  XRT_QT_PROG_SET_414_OG [0x19e]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2013/12/22  18:14:28.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0d
2013/12/22  18:14:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/22  19:15:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/22  19:15:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/22  19:15:04.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/22  19:18:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/22  19:38:00.0  XRT_Custom_430_OG [0x1ae]
2013/12/22  19:39:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/22  20:52:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/22  20:52:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/22  20:52:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/22  20:55:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/22  21:15:30.0  XRT_Custom_430_OG [0x1ae]
2013/12/22  21:16:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/22  22:30:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/22  22:30:02.0  XRT_FLD_RESET_415_OG [0x19f]
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2013/12/22  22:30:04.0  XRT_PREFLR_STRT_418_OG [0x1a2]
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2013/12/22  22:33:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/22  22:50:30.0  XRT_Custom_430_OG [0x1ae]
2013/12/22  22:51:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
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2013/12/23  00:07:00.0  XRT_CTRL_MANU_400_OG [0x190]
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2013/12/23  00:07:02.0  XRT_FLD_RESET_415_OG [0x19f]
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2013/12/23  00:07:04.0  XRT_PREFLR_STRT_418_OG [0x1a2]
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2013/12/23  00:10:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
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2013/12/23  00:15:00.0  XRT_Custom_430_OG [0x1ae]
2013/12/23  00:16:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/23  01:31:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/23  01:31:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/23  01:31:04.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/23  01:34:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/23  01:49:30.0  XRT_Custom_430_OG [0x1ae]
2013/12/23  01:50:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/23  03:06:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/23  03:06:02.0  XRT_FLD_RESET_415_OG [0x19f]
```

```
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/23  03:06:04.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/23  03:09:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/23  03:26:30.0  XRT_Custom_430_OG [0x1ae]
2013/12/23  03:27:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/23  04:36:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/23  04:36:02.0  XRT_FLD_RESET_415_OG [0x19f]
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2013/12/23  04:36:04.0  XRT_PREFLR_STRT_418_OG [0x1a2]
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2013/12/23  04:39:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/23  05:04:00.0  XRT_Custom_430_OG [0x1ae]
2013/12/23  05:05:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/23  05:48:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/23  05:48:26.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2013/12/23  05:48:30.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2013/12/23  05:48:46.0  XRT_FLD_DIS_434_OG [0x1b2]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2013/12/23  05:51:24.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2013/12/23  05:51:26.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2013/12/23  05:51:28.0  XRT_QT_PROG_SET_446_OG [0x1be]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2013/12/23  05:51:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/23  05:58:24.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/23  05:58:26.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2013/12/23  05:58:30.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 04 00 00 00 00
2013/12/23  05:58:46.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2013/12/23  05:58:48.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2013/12/23  05:58:50.0  XRT_AEC_RESET_413_OG [0x19d]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2013/12/23  05:58:52.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2013/12/23  05:58:54.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/23  05:58:56.0  XRT_QT_PROG_SET_414_OG [0x19e]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2013/12/23  06:01:28.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0d
2013/12/23  06:01:30.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/23  06:16:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/23  06:16:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/23  06:16:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/23  06:19:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/23  06:41:30.0  XRT_Custom_430_OG [0x1ae]
2013/12/23  06:42:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/23  07:56:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/23  07:56:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/23  07:56:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/23  07:59:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/23  08:18:30.0  XRT_Custom_430_OG [0x1ae]
2013/12/23  08:19:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/23  09:37:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/23  09:37:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/23  09:37:04.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/23  09:40:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/23  09:52:30.0  XRT_Custom_430_OG [0x1ae]
2013/12/23  09:53:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/23  15:01:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/23  15:01:32.0  XRT_FLD_RESET_415_OG [0x19f]
```

```
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/23  15:01:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/23  15:04:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/23  15:14:30.0  XRT_Custom_430_OG [0x1ae]
2013/12/23  15:15:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/23  15:22:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/23  15:22:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/23  15:22:04.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/23  15:25:00.0  XRT_Custom_430_OG [0x1ae]
2013/12/23  15:25:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/23  15:26:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/23  16:37:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/23  16:37:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/23  16:37:04.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/23  16:40:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/23  17:01:00.0  XRT_Custom_430_OG [0x1ae]
2013/12/23  17:02:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/23  17:50:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/23  17:50:56.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2013/12/23  17:51:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2013/12/23  17:51:16.0  XRT_FLD_DIS_434_OG [0x1b2]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
2013/12/23  17:53:54.0  XRT_FLRCTRL_DIS_405_OG [0x195]
                                        MDP_XRT_FLRCTRL_DIS  1 07-F0 c9
2013/12/23  17:53:56.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2013/12/23  17:53:58.0  XRT_QT_PROG_SET_446_OG [0x1be]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2013/12/23  17:54:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/23  18:00:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/23  18:00:56.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2013/12/23  18:01:00.0  AOCS_ORe-point_Start_4_OG [0x09a]
                                        AOCU_NM              5 02-76 03 00 00 00 00
2013/12/23  18:01:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2013/12/23  18:01:18.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2013/12/23  18:01:20.0  XRT_AEC_RESET_413_OG [0x19d]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2013/12/23  18:01:22.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2013/12/23  18:01:24.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/23  18:01:26.0  XRT_QT_PROG_SET_414_OG [0x19e]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2013/12/23  18:03:58.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0d
2013/12/23  18:04:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/23  18:14:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/23  18:14:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/23  18:14:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/23  18:17:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/23  18:37:30.0  XRT_Custom_430_OG [0x1ae]
2013/12/23  18:38:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/23  19:51:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/23  19:51:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/23  19:51:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/23  19:54:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/23  20:15:00.0  XRT_Custom_430_OG [0x1ae]
2013/12/23  20:16:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/23  21:29:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/23  21:29:02.0  XRT_FLD_RESET_415_OG [0x19f]
```

```
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/23  21:29:04.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/23  21:32:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/23  21:51:30.0  XRT_Custom_430_OG [0x1ae]
2013/12/23  21:52:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/23  23:06:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/23  23:06:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/23  23:06:04.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/23  23:09:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/23  23:22:00.0  XRT_Custom_430_OG [0x1ae]
2013/12/23  23:23:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/23  23:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/23  23:59:56.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2013/12/24  00:00:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 04 00 00 00 00
2013/12/24  00:00:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2013/12/24  00:00:18.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2013/12/24  00:00:20.0  XRT_AEC_RESET_413_OG [0x19d]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2013/12/24  00:00:22.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2013/12/24  00:00:24.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/24  00:00:26.0  XRT_QT_PROG_SET_414_OG [0x19e]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2013/12/24  00:02:58.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0d
2013/12/24  00:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/24  00:39:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/24  00:39:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/24  00:39:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/24  00:42:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/24  00:50:00.0  XRT_Custom_430_OG [0x1ae]
2013/12/24  00:51:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/24  02:05:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/24  02:05:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/24  02:05:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/24  02:08:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/24  02:26:00.0  XRT_Custom_430_OG [0x1ae]
2013/12/24  02:27:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/24  03:34:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/24  03:34:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/24  03:34:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/24  03:37:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/24  04:03:00.5  XRT_Custom_430_OG [0x1ae]
2013/12/24  04:04:00.5  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/24  05:13:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/24  05:13:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/24  05:13:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/24  05:16:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/24  05:40:30.0  XRT_Custom_430_OG [0x1ae]
2013/12/24  05:41:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/24  05:59:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/24  05:59:56.0  XRT_FOCUS_POSITION_403_OG [0x193]
                                        XRT_FOCUS_POSITION   4 07-F8 22 ff aa 00
2013/12/24  06:00:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
2013/12/24  06:00:16.0  XRT_FLD_DIS_434_OG [0x1b2]
                                        MDP_XRT_FLD_DIS      1 07-F0 d9
```

```
2013/12/24  06:02:54.0  XRT_FLRCTRL_DIS_405_OG [0x195]
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2013/12/24  06:02:56.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2013/12/24  06:02:58.0  XRT_QT_PROG_SET_446_OG [0x1be]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 11
2013/12/24  06:03:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/24  06:09:54.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/24  06:09:56.0  XRT_FOCUS_POSITION_410_OG [0x19a]
                                        XRT_FOCUS_POSITION   4 07-F8 22 fe 97 00
2013/12/24  06:10:00.0  AOCS_ORe-point_Start_3_OG [0x099]
                                        AOCU_NM              5 02-76 04 00 00 00 00
2013/12/24  06:10:16.0  XRT_FLD_ENA_411_OG [0x19b]
                                        MDP_XRT_FLD_ENA      1 07-F0 d8
2013/12/24  06:10:18.0  XRT_FLRCTRL_ENA_412_OG [0x19c]
                                        MDP_XRT_FLRCTRL_ENA  1 07-F0 c8
2013/12/24  06:10:20.0  XRT_AEC_RESET_413_OG [0x19d]
                                        MDP_XRT_AEC_RESET    1 07-F0 d0
2013/12/24  06:10:22.0  XRT_ARS_DIS_423_OG [0x1a7]
                                        MDP_XRT_ARS_DIS      1 07-F0 d5
2013/12/24  06:10:24.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/24  06:10:26.0  XRT_QT_PROG_SET_414_OG [0x19e]
                                        MDP_XRT_QT_PROG_SET  2 07-F0 c4 04
2013/12/24  06:12:58.0  XRT_FL_PROG_SET_421_OG [0x1a5]
                                        MDP_XRT_FL_PROG_SET  2 07-F0 c5 0d
2013/12/24  06:13:00.0  XRT_CTRL_AUTO_408_OG [0x198]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/24  06:54:00.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/24  06:54:02.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/24  06:54:04.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/24  06:57:14.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/24  07:18:00.0  XRT_Custom_430_OG [0x1ae]
2013/12/24  07:19:00.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/24  08:33:30.0  XRT_CTRL_MANU_400_OG [0x190]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/24  08:33:32.0  XRT_FLD_RESET_415_OG [0x19f]
                                        MDP_XRT_FLD_RESET    1 07-F0 da
2013/12/24  08:33:34.0  XRT_PREFLR_STRT_418_OG [0x1a2]
                                        MDP_XRT_PREFLR_STRT  1 07-F0 e8
2013/12/24  08:36:44.0  XRT_PREFLR_STOP_419_OG [0x1a3]
                                        MDP_XRT_PREFLR_STOP  1 07-F0 e9
2013/12/24  08:54:30.0  XRT_Custom_430_OG [0x1ae]
2013/12/24  08:55:30.0  XRT_CTRL_AUTO_424_OG [0x1a8]
                                        MDP_XRT_CTRL_AUTO    1 07-F0 c0
2013/12/24  09:24:00.0  XRT_CTRL_MANU_402_OG [0x192]
                                        MDP_XRT_CTRL_MANU    1 07-F0 c1
2013/12/24  10:48:00.0  AOCS_ORe-point_Start_2_OG [0x098]
                                        AOCU_NM              5 02-76 00 00 00 00 00
```