

XRT Timeline to be uploaded on 2013/12/26

Period: 2013/12/26 10:13:00 - 2013/12/31 10:00:00

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Normal mode

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XOB #19E3: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)													
Term	Pointing (x, y)								Comment				
12/26 10:26:00 - 12/26 17:59:54	Track (35.4, -265.5) [Ⓢ] 12/26 10:23:00	# OP start + 10min, EIS focus mode (10-15UT, 5hour), AR11934											
12/26 18:13:00 - 12/27 00:02:24	Track (104.4, -265.0) [Ⓢ] 12/26 18:10:00	# AR11934											
12/27 13:25:00 - 12/27 18:04:54	Track (271.2, -264.9) [Ⓢ] 12/27 13:22:00	# AR11934											
12/27 18:18:00 - 12/28 05:35:24	Track (312.4, -265.1) [Ⓢ] 12/27 18:15:00	# cont.											
12/28 05:48:30 - 12/28 09:23:30	Track (406.5, -265.9) [Ⓢ] 12/28 05:45:30	# cont.											
PROG= 11 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─┬─ Seqn= 8 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs 1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec	
└─ Subr= 2 2-time(s) 2.0sec													
└─┬─ Seqn= 24 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs 1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec	
└─┬─ Seqn= 42 4-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs 1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec	
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
└─ Seqn= 62 15-time(s) 120.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec	
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec	
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec	
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs 1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec	
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs 1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec	
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #19DA: Synoptic Q95 2x2 - Al/mesh(5/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/362/1443) + Thin													
Term	Pointing (x, y)								Comment				
12/26 18:03:00 - 12/26 18:09:54	Fixed (0.0, 0.0)	synoptic											
12/27 18:08:00 - 12/27 18:14:54	Fixed (0.0, 0.0)	synoptic, shifted 5.0 min											
12/28 05:38:30 - 12/28 05:45:24	Fixed (0.0, 0.0)	synoptic, shifted -24.5 min											
PROG= 17 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─┬─ Seqn= 84 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─┬─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec	
└─┬─ Seqn= 86 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─┬─ Seqn= 95 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─ Seqn= 6 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs 2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec	
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs 1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #19DC: Synoptic 9 Filter w/ Triplet Exposures - 2x2 Q98 - Gband(8ms)													
Term	Pointing (x, y)								Comment				
12/27 06:41:30 - 12/27 07:06:00	Fixed (-6.0, 19.0)	18/23, synoptic											
PROG= 16 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─┬─ Seqn= 84 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs 2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec	
└─┬─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec	
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs 1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec	

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 86 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 95 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 2-time(s) 12.0sec												
Seqn= 56 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 22 1-time(s) 2.0sec												
C-poly/Open	C-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
C-poly/Open	C-poly/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 43 1-time(s) 2.0sec												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 38 1-time(s) 4.0sec												
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 46 1-time(s) 4.0sec												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19A9: G-Band Alignment with North Pole Q90 2x2(G-band only) - 8msec - 5min cadence - Partial Sun-wNGT-2												
Term			Pointing (x, y)					Comment				
12/27 09:37:00 - 12/27 11:21:54			Fixed (0.0, 945.0)					# Co-alignment N-limb				
PROG= 05 1-time(s)												
Subr= 1 1-time(s) 360.0sec												
Seqn= 93 24-time(s) 300.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19AA: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8msec- 8 min cadence-wNGT-2												
Term			Pointing (x, y)					Comment				
12/27 11:37:00 - 12/27 13:21:54			Fixed (-945.0, 0.0)					# Co-alignment E-limb				
PROG= 02 1-time(s)												
Subr= 1 1-time(s) 360.0sec												
Seqn= 94 15-time(s) 480.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #19DE: Flare obs. dynamics - thin-Be high cadence long/short pairs + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loc												
Term			Pointing (x, y)					Comment				
12/26 10:26:00 - 12/26 17:59:54			Track (35.4, -265.5) ^{Ⓢ 12/26 10:23:00}					# OP start + 10min, EIS focus mode (10-15UT, 5hour), AR11934				
12/26 18:13:00 - 12/27 00:02:24			Track (104.4, -265.0) ^{Ⓢ 12/26 18:10:00}					# AR11934				
12/27 13:25:00 - 12/27 18:04:54			Track (271.2, -264.9) ^{Ⓢ 12/27 13:22:00}					# AR11934				
12/27 18:18:00 - 12/28 05:35:24			Track (312.4, -265.1) ^{Ⓢ 12/27 18:15:00}					# cont.				
12/28 05:48:30 - 12/28 09:23:30			Track (406.5, -265.9) ^{Ⓢ 12/28 05:45:30}					# cont.				
PROG= 13 15-time(s)												
Subr= 1 45-time(s) 2.0sec												
Seqn= 44 1-time(s) 8.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 15 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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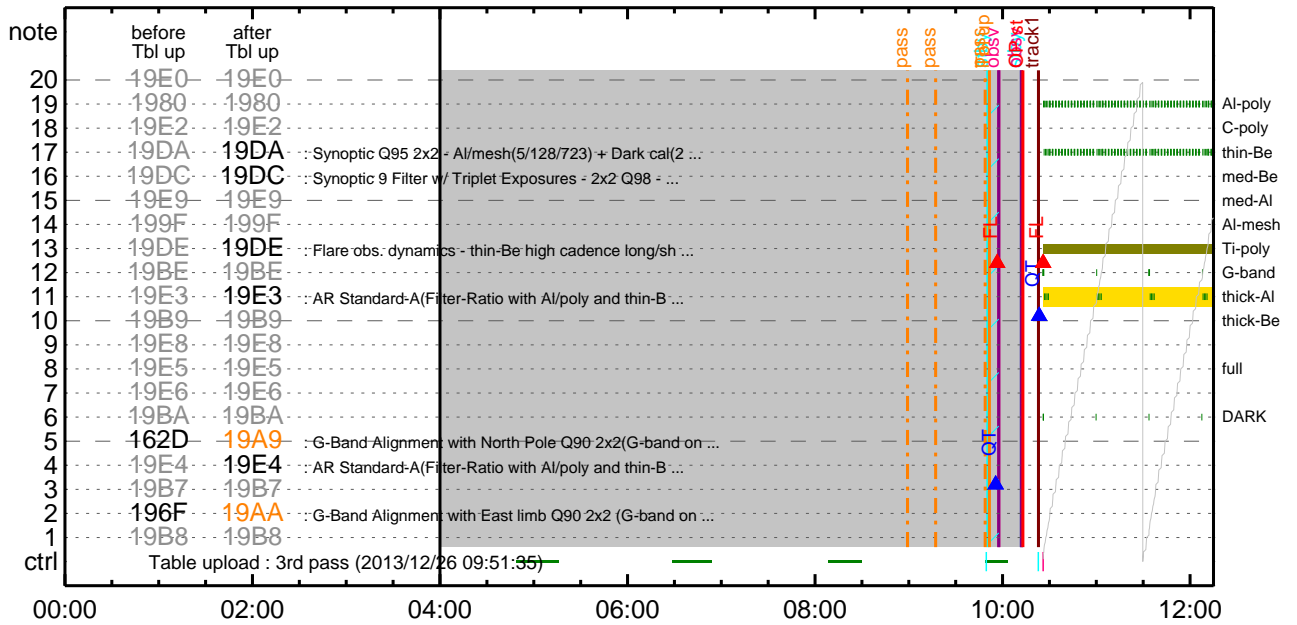
Flare Detection

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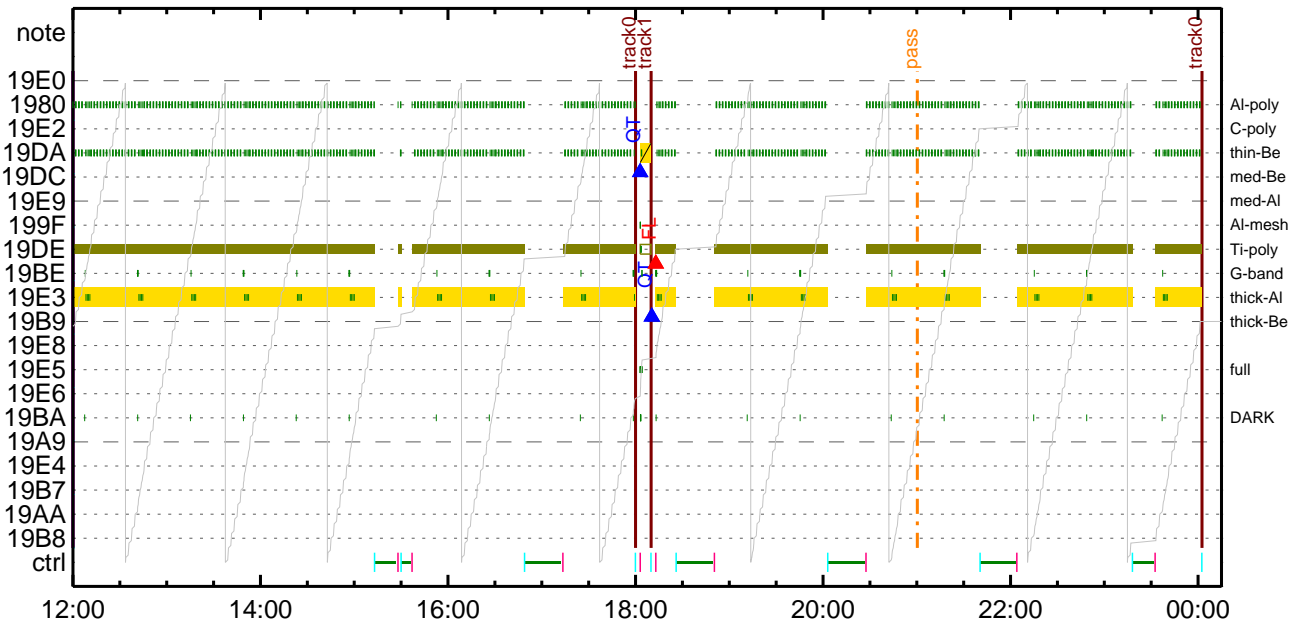
FLD Patrol

Term	Pointing (x, y)	Comment
12/26 18:10:16 - 12/27 06:38:46	Track (104.4, -265.0) ^{@ 12/26 18:10:00}	# AR11934
12/27 13:22:16 - 12/27 18:05:16	Track (271.2, -264.9) ^{@ 12/27 13:22:00}	# AR11934
12/27 18:15:16 - 12/28 05:35:46	Track (312.4, -265.1) ^{@ 12/27 18:15:00}	# cont.
12/28 05:45:46 - 12/31 10:00:00	Track (406.5, -265.9) ^{@ 12/28 05:45:30}	# cont.
Open/Ti-poly	Open/thick-Al	close Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

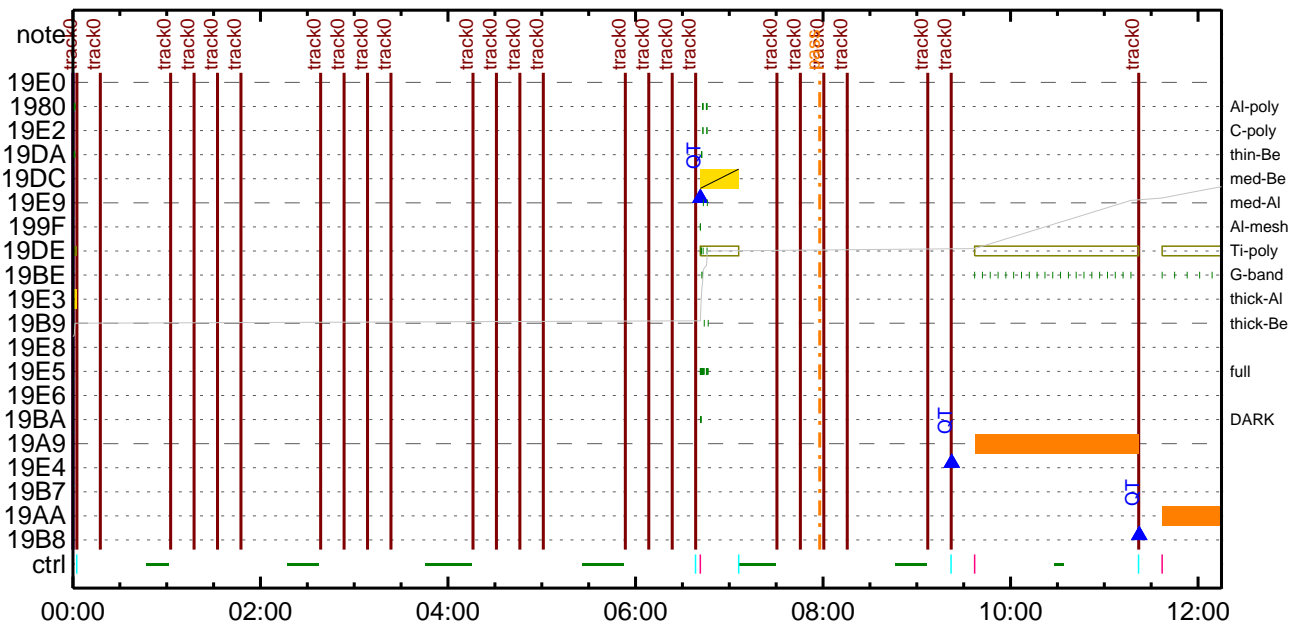
CMDI #0998 2013/12/26



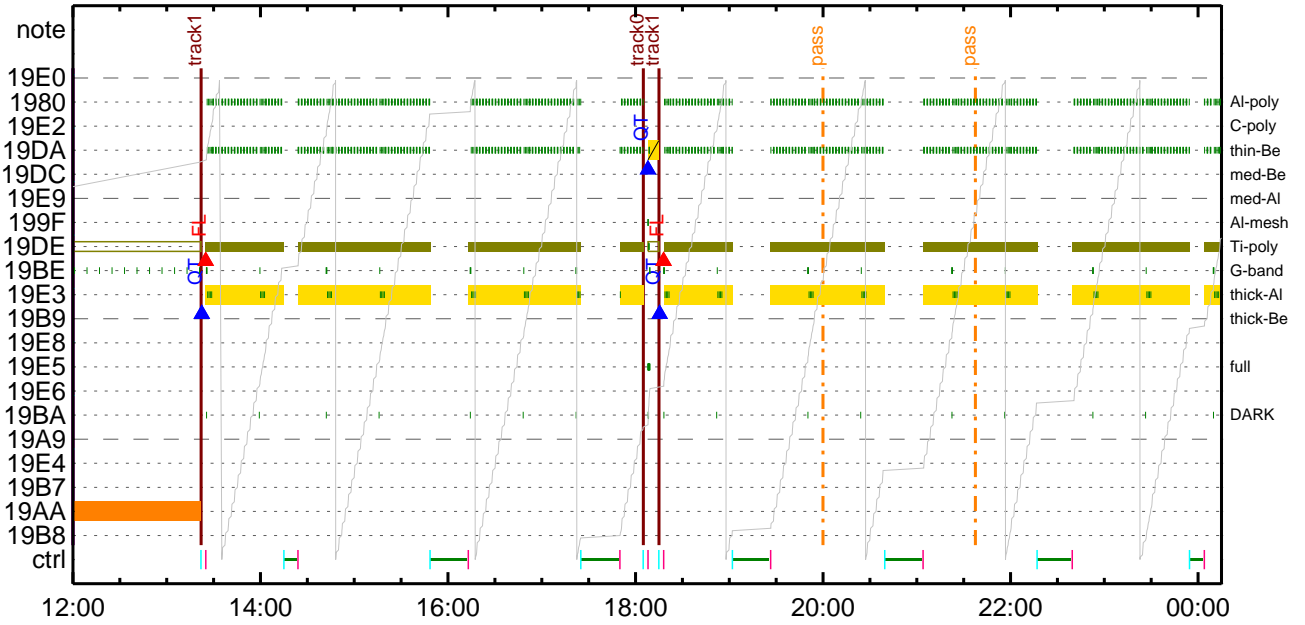
CMDI #0998 2013/12/26



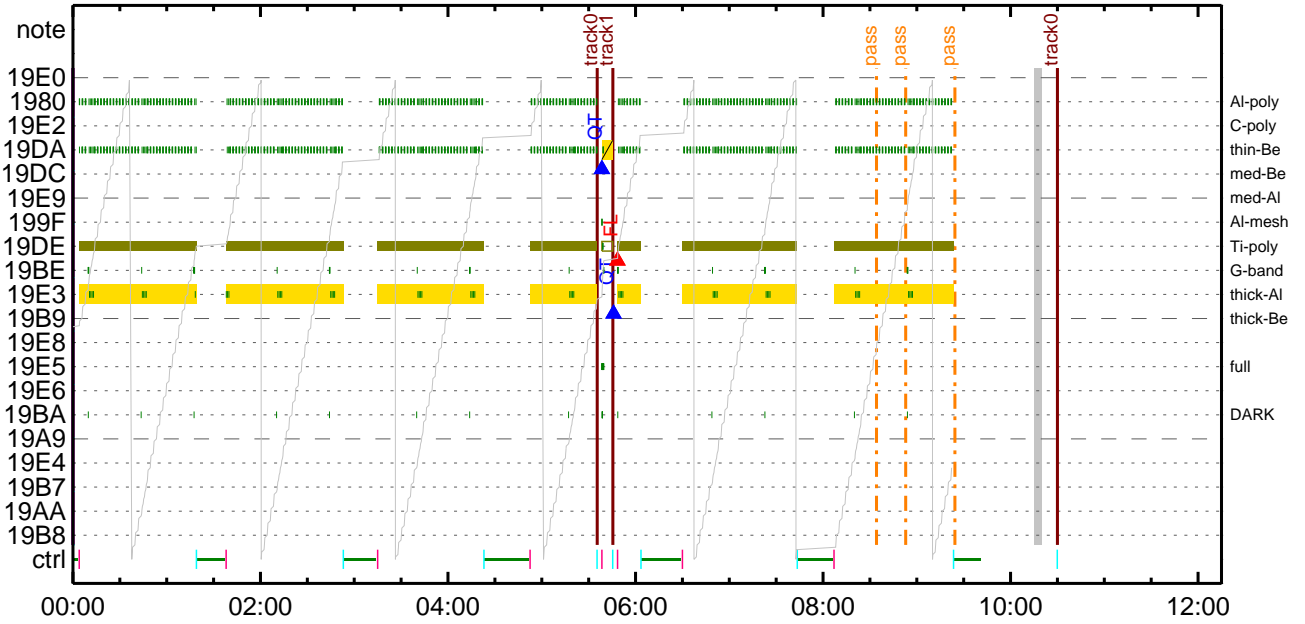
CMDI #0998 2013/12/27



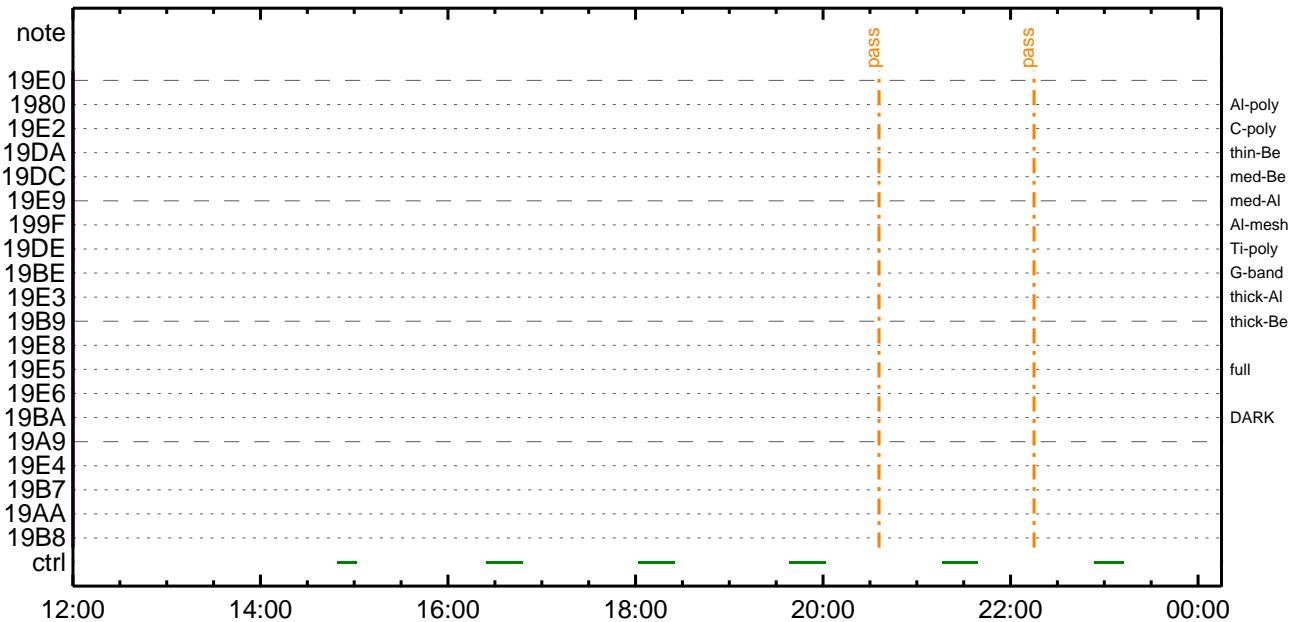
CMDI #0998 2013/12/27



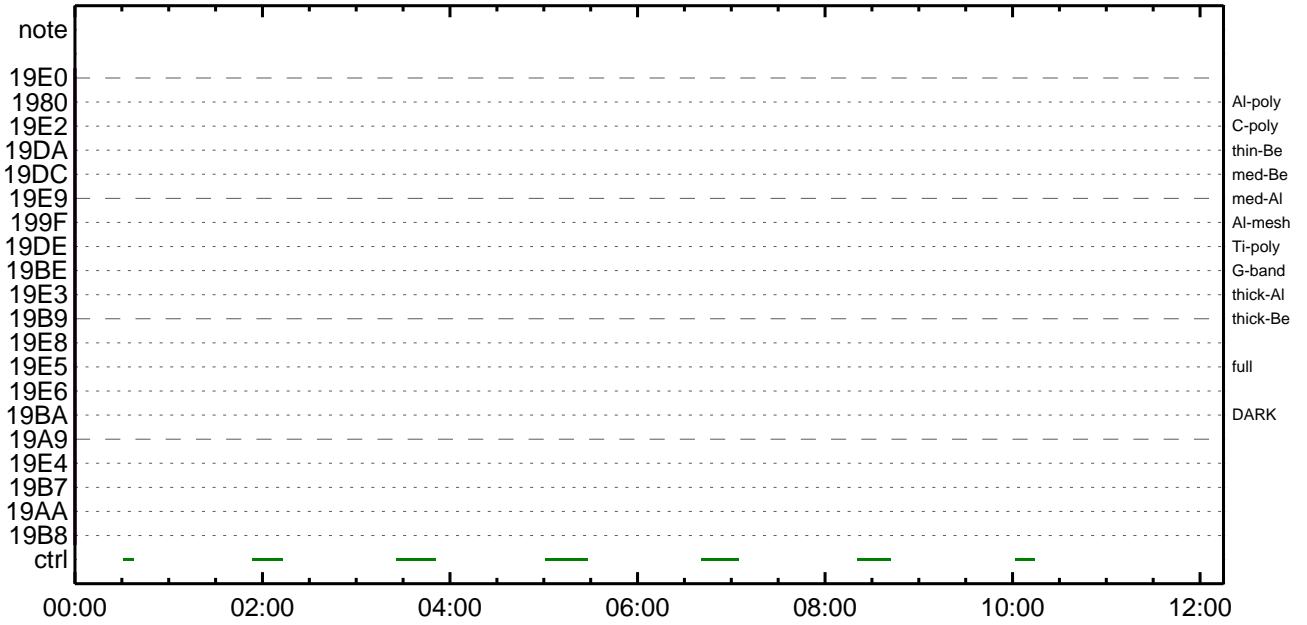
CMDI #0998 2013/12/28



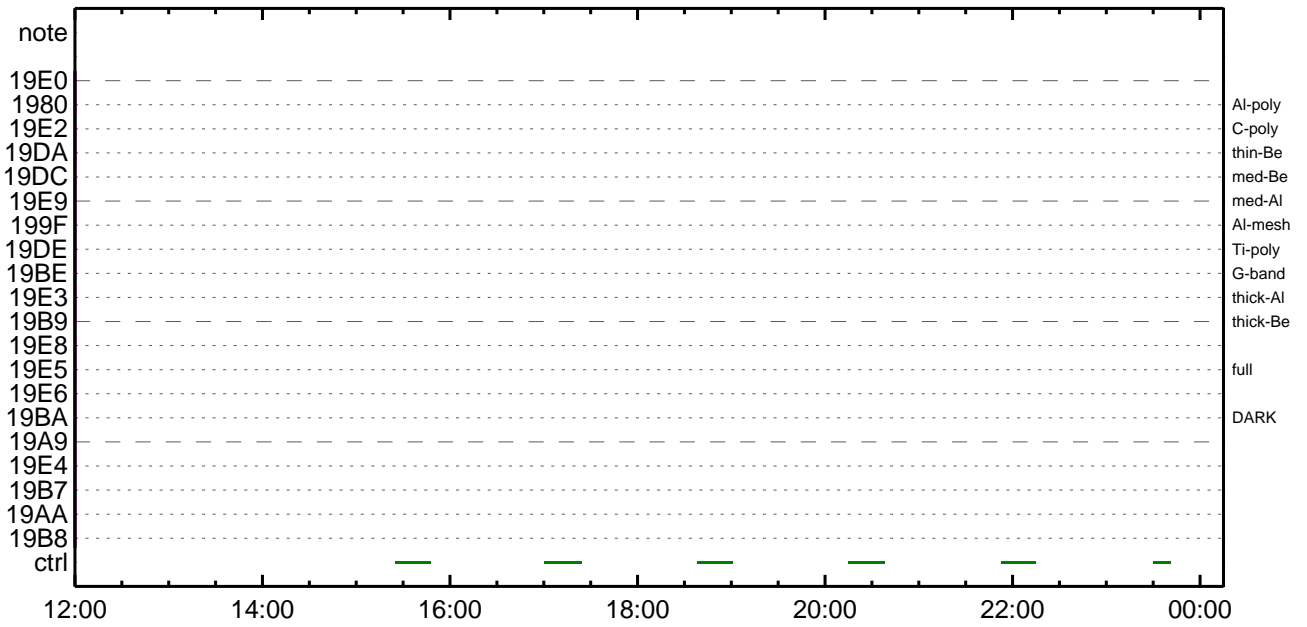
CMDI #0998 2013/12/28



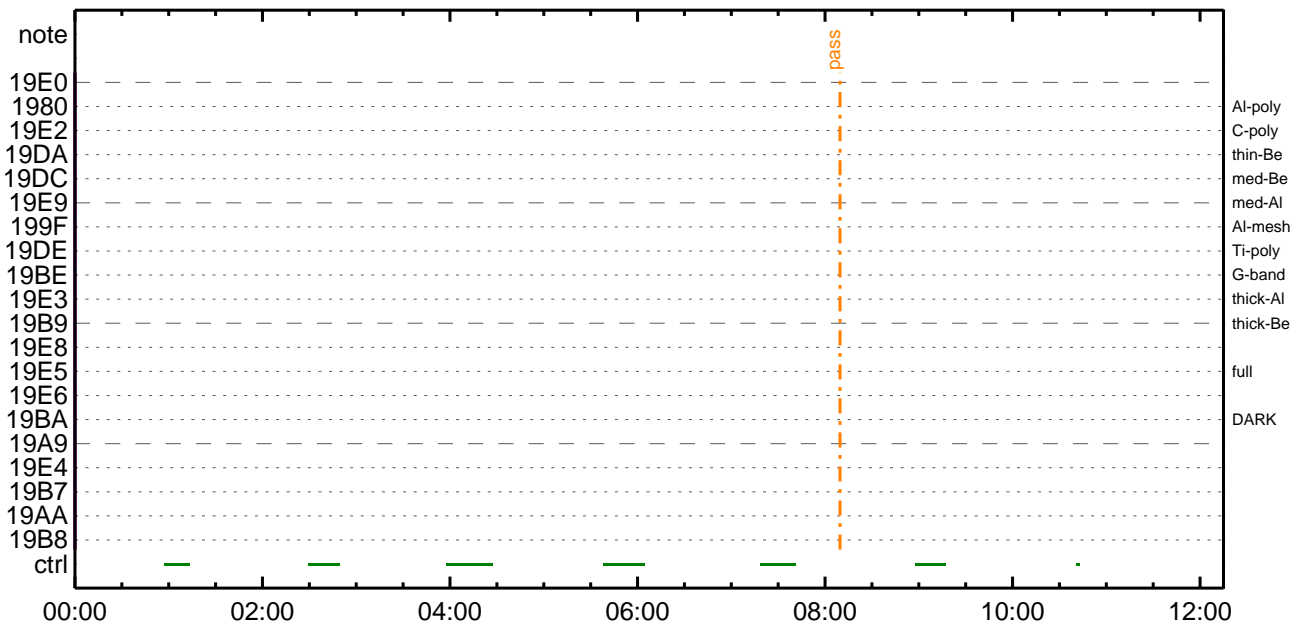
CMDI #0998 2013/12/29



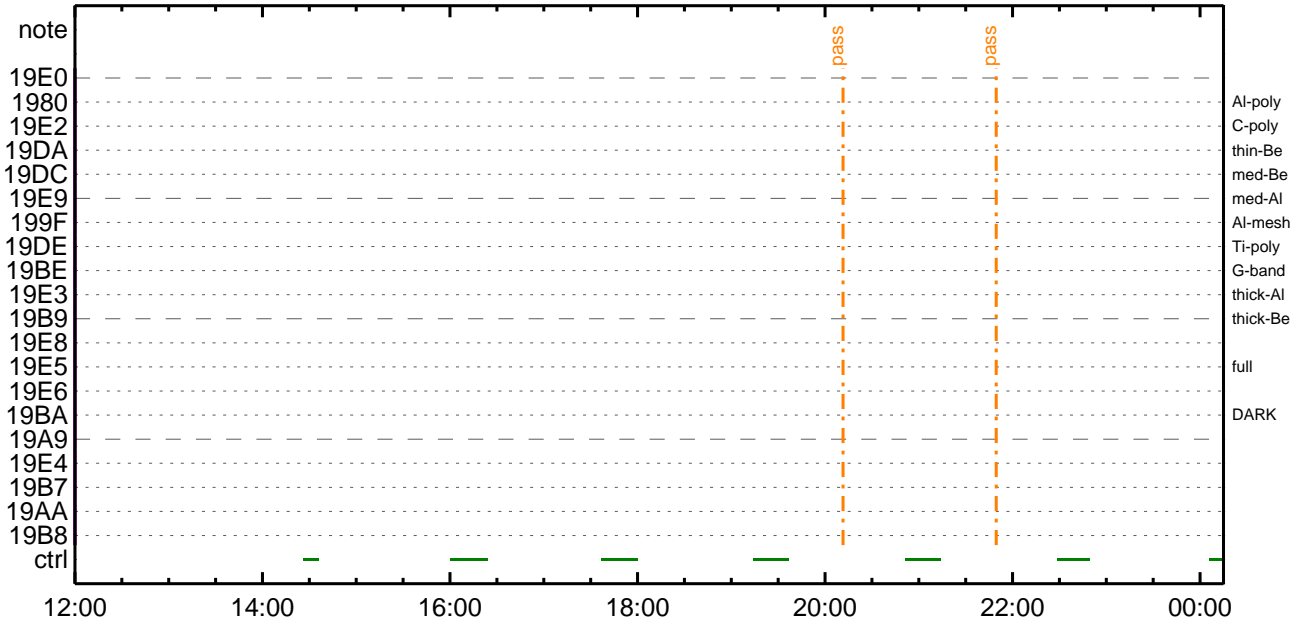
CMDI #0998 2013/12/29



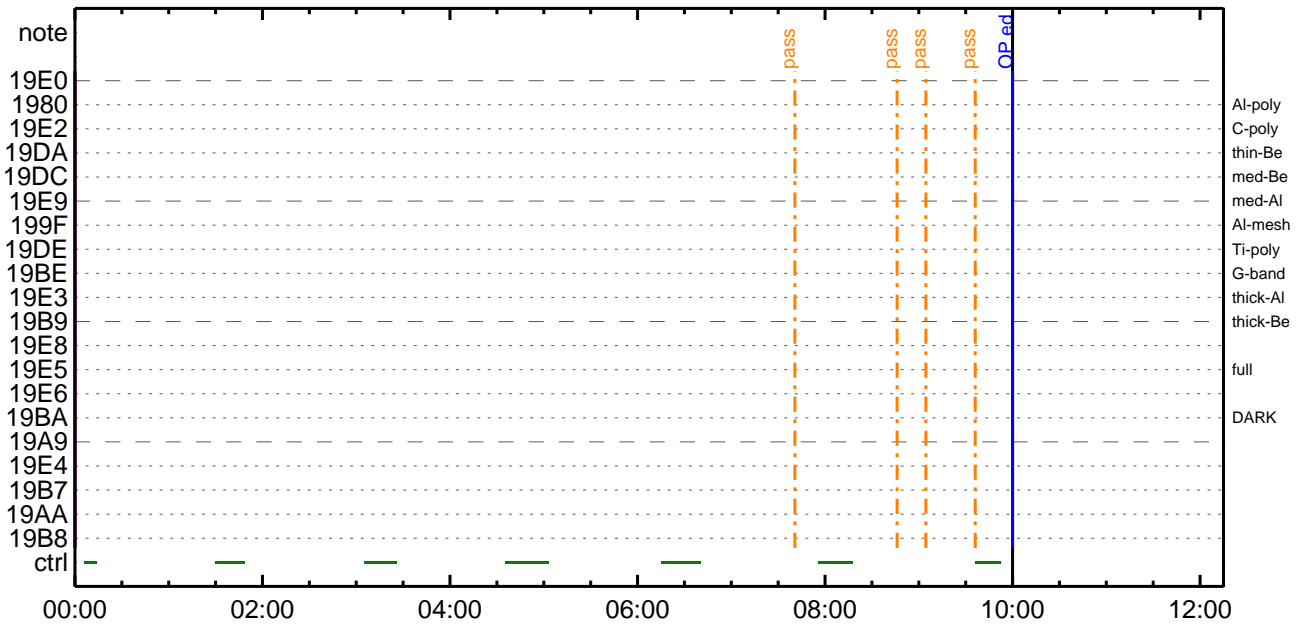
CMDI #0998 2013/12/30



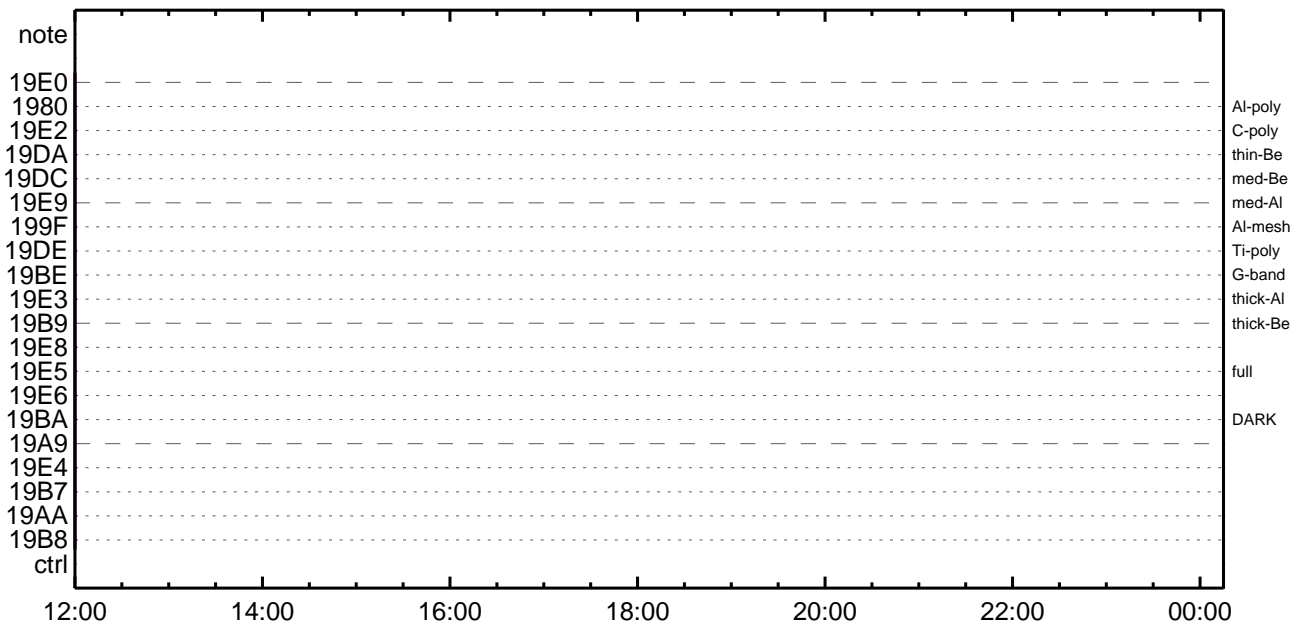
CMDI #0998 2013/12/30



CMDI #0998 2013/12/31



CMDI #0998 2013/12/31




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-120:OP
0104 ( )
0105 S. OG og-120:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ä´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òî½TI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²²ò¿;çSET²EEDUMP²î½±°îYNY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²YóYEòðÁDî¿(UT)
0183 +. TI 2013-12-26 10:08:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2013-12-26 10:08:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2013-12-26 10:08:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-121 2013-12-26 12:29:12 82 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY~¼Ä»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFÝÖYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Ë¿µÁµ°Ë»Í×ÁÇ¿ÍYçYÁY×YÍ;¼YÉ;ËË%µ•íÉ;ËµE¼°ÇÖµ•µ¿¼l¹çµÍ;çÀ®, ùµ¹µëµµµÇÁ+¿®µ•µËµµµ³µË;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 . C. Verify EIS in MANUAL mode
0049 . C. Estimated OBSTBL upload time is 1m27s
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-820:EIS_OBSTBL
0054 ( )
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2013-12-26 10:12:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 . C. ***** MDP `ûÁîîî»ó%YµËÄµ¹µëDCBC•x²è *****
0069 C. (¼á°íYÓYÁYËYËYÉYáYçYëµE¼µµ¼Ä»Ûµ¹µë)
0070 . S. DC-BC dcbc-402:DCBC
0071 (MDP_known_event)
0072 C.
0073 C.
0074 . C. ***** YDÝ¹.Ï Daily±¿ÍÑµË´Øµ¹µëDCBC•x²è *****
0075 . S. DC-BC dcbc-153:DCBC
0076 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077 C.
0078 C.
0079 . C. ;ãLOS YÁYŞYÁY~¼Ä»Û;ã
0080 C.
0081 . C. ***** LOS *****
0082 C.
```



```
0096 + DC 07-F0 MDP_XRT_AEC_RESET
0097 BC (d0)
0098 + DC 07-F0 MDP_XRT_ARS_DIS
0099 BC (d5)
0100 + DC 07-F0 MDP_XRT_FLD_RESET
0101 BC (da)
0102 + DC 07-F0 MDP_XRT_QT_PROG_SET
0103 BC (c4 04)
0104 + DC 07-F0 MDP_XRT_FL_PROG_SET
0105 BC (c5 0d)
0106 . C. ----- Success Verify ? OK / NG ____
0107 C.
0108 C.
0109 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0110 C.
0111 + DC 07-F0 MDP_XRT_MODE_OBSV
0112 BC (c2)
0113 + TI 2013-12-26 10:12:02.0
0114 DC 07-F0 MDP_XRT_MODE_OBSV
0115 BC (c2)
0116 . C. ----- Success Verify ? OK / NG ____
0117 C.
0118 C. ***** XRT END *****
0119 C.
0120 . C. ***** MDP `úÃîñî»ö%ÝñÊÃðñ¹ñèDCBC•x²è *****
0121 C. (%á°îÝÓÝÃÝÈÝÞÝËÝáÝçÝèñÊ¼ññ¼Ã»Ûñ¹ñè)
0122 . S. DC-BC dcbc-402:DCBC
0123 (MDP_known_event)
0124 C.
0125 C.
0126 . C. ***** ÝDÝ¹•Ï Daily±¿îññÊ´Øñ¹ñèDCBC•x²è *****
0127 . S. DC-BC dcbc-153:DCBC
0128 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0129 C.
0130 C.
0131 . C. ¡ãLOSÝÁÝ$ÝÃÝ¬¼Ã»Ûñ¹ñè
0132 C.
0133 . C. ***** LOS *****
0134 C.
```

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*** OP Sequence for XRT ***

2013/12/26	10:22:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/26	10:22:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/12/26	10:23:00.0	AOCS_Orе-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2013/12/26	10:23:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/12/26	10:23:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/12/26	10:23:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/12/26	10:23:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/12/26	10:23:24.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/26	10:23:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2013/12/26	10:25:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d
2013/12/26	10:26:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/26	15:13:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/26	15:13:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/26	15:13:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/26	15:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/26	15:27:00.0	XRT_Custom_430_OG [0x1ae]			
2013/12/26	15:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/26	15:30:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/26	15:30:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/26	15:30:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/26	15:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/26	15:36:00.0	XRT_Custom_430_OG [0x1ae]			
2013/12/26	15:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/26	16:49:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/26	16:49:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/26	16:49:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2013/12/26	16:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2013/12/26	17:12:30.0	XRT_Custom_430_OG [0x1ae]			
2013/12/26	17:13:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/26	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/26	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2013/12/26	18:00:00.0	AOCS_Orе-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2013/12/26	18:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2013/12/26	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2013/12/26	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/12/26	18:02:58.0	XRT_QT_PROG_SET_446_OG [0x1be]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2013/12/26	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2013/12/26	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2013/12/26	18:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2013/12/26	18:10:00.0	AOCS_Orе-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2013/12/26	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2013/12/26	18:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2013/12/26	18:10:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2013/12/26	18:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2013/12/26	18:10:24.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2013/12/26	18:10:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
2013/12/26	18:12:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d

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2013/12/26	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/26	18:26:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/26	18:26:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/12/26	18:26:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/12/26	18:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/12/26	18:49:30.0	XRT_Custom_430_OG [0x1ae]							
2013/12/26	18:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/26	20:03:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/26	20:03:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/12/26	20:03:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/12/26	20:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/12/26	20:26:30.0	XRT_Custom_430_OG [0x1ae]							
2013/12/26	20:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/26	21:40:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/26	21:40:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/12/26	21:40:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/12/26	21:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/12/26	22:03:00.0	XRT_Custom_430_OG [0x1ae]							
2013/12/26	22:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/26	23:18:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/26	23:18:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2013/12/26	23:18:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/12/26	23:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/12/26	23:31:30.0	XRT_Custom_430_OG [0x1ae]							
2013/12/26	23:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/27	00:02:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/27	00:02:30.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 ed 9a 52 d9				
2013/12/27	00:17:30.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 e9 27 42 35				
2013/12/27	01:02:30.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00 e8 8c 31 db				
2013/12/27	01:17:30.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 e8 32 21 58				
2013/12/27	01:32:30.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 e8 01 10 5a				
2013/12/27	01:47:30.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00 e7 e8 ff 03				
2013/12/27	02:38:30.5	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 e8 01 f2 4e				
2013/12/27	02:53:30.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00 e8 32 e1 59				
2013/12/27	03:08:30.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00 e8 8c d0 cd				
2013/12/27	03:23:30.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00 e9 27 c0 73				
2013/12/27	04:16:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00 ed 9a af cf				
2013/12/27	04:31:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00 fe a8 54 e5				
2013/12/27	04:46:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00 ff a6 45 26				
2013/12/27	05:01:00.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	00 ff 03 34 e5				
2013/12/27	05:53:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	00 fe 98 24 5a				
2013/12/27	06:08:30.0	AOCS_ORe-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	00 fe 67 13 74				
2013/12/27	06:23:30.0	AOCS_ORe-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00 fe 4e 02 24				
2013/12/27	06:38:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/27	06:38:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2013/12/27	06:38:30.0	AOCS_ORe-point_Start_20_OG [0x0aa]							
		AOCU_NM	5	02-76	00 fe 4e 00 8b				
2013/12/27	06:38:46.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/12/27	06:41:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/12/27	06:41:26.0	XRT_ARS_DIS_423_OG [0x1a7]							

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2013/12/27	06:41:28.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_ARS_DIS	1	07-F0	d5				
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2013/12/27	06:41:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/27	07:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/27	07:06:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/12/27	07:06:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/12/27	07:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/12/27	07:30:30.0	AOCS_ORe-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00	fe	67	ef	34
2013/12/27	07:45:30.0	AOCS_ORe-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	00	fe	98	de	4e
2013/12/27	08:00:30.0	AOCS_ORe-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00	ff	03	cd	cb
2013/12/27	08:15:30.0	AOCS_ORe-point_Start_24_OG [0x0ae]	AOCU_NM	5	02-76	00	ff	a6	bd	82
2013/12/27	09:07:00.0	AOCS_ORe-point_Start_25_OG [0x0af]	AOCU_NM	5	02-76	00	fe	a8	ad	cb
2013/12/27	09:21:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/27	09:21:56.0	XRT_FOCUS_POSITION_449_OG [0x1c1]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/12/27	09:22:00.0	AOCS_ORe-point_Start_26_OG [0x0b0]	AOCU_NM	5	02-76	00	ac	00	00	00
2013/12/27	09:22:16.0	XRT_FLD_DIS_438_OG [0x1b6]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/12/27	09:22:18.0	XRT_FLRCTRL_DIS_406_OG [0x196]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/12/27	09:22:20.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/12/27	09:22:22.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2013/12/27	09:37:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/27	11:21:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/27	11:21:56.0	XRT_FOCUS_POSITION_449_OG [0x1c1]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/12/27	11:22:00.0	AOCS_ORe-point_Start_27_OG [0x0b1]	AOCU_NM	5	02-76	00	00	00	54	00
2013/12/27	11:22:16.0	XRT_FLD_DIS_438_OG [0x1b6]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2013/12/27	11:22:18.0	XRT_FLRCTRL_DIS_406_OG [0x196]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2013/12/27	11:22:20.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/12/27	11:22:22.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2013/12/27	11:37:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/27	13:21:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/27	13:21:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2013/12/27	13:22:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01	00	00	00	00
2013/12/27	13:22:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2013/12/27	13:22:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2013/12/27	13:22:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2013/12/27	13:22:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2013/12/27	13:22:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/12/27	13:22:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b			
2013/12/27	13:24:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	0d			
2013/12/27	13:25:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/27	14:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/27	14:15:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/12/27	14:15:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/12/27	14:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/12/27	14:23:00.0	XRT_Custom_430_OG [0x1ae]								
2013/12/27	14:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/27	15:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/27	15:48:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/12/27	15:48:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]								

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2013/12/27	15:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/27	16:12:00.0	XRT_Custom_430_OG [0x1ae]					
2013/12/27	16:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/27	17:25:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/27	17:25:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/27	17:25:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/27	17:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/27	17:49:00.0	XRT_Custom_430_OG [0x1ae]					
2013/12/27	17:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/27	18:04:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/27	18:04:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/12/27	18:05:00.0	AOCS_OrE-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2013/12/27	18:05:16.0	XRT_FLD_DIS_434_OG [0x1b2]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/12/27	18:07:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/12/27	18:07:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/12/27	18:07:58.0	XRT_QT_PROG_SET_446_OG [0x1be]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2013/12/27	18:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/27	18:14:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/27	18:14:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/12/27	18:15:00.0	AOCS_OrE-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	01 00 00 00 00	
2013/12/27	18:15:16.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/12/27	18:15:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/12/27	18:15:20.0	XRT_AEC_RESET_413_OG [0x19d]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/12/27	18:15:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/12/27	18:15:24.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/27	18:15:26.0	XRT_QT_PROG_SET_422_OG [0x1a6]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2013/12/27	18:17:58.0	XRT_FL_PROG_SET_421_OG [0x1a5]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2013/12/27	18:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/27	19:02:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/27	19:02:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/27	19:02:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/27	19:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/27	19:25:30.0	XRT_Custom_430_OG [0x1ae]					
2013/12/27	19:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/27	20:39:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/27	20:39:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/27	20:39:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/27	20:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/27	21:03:00.0	XRT_Custom_430_OG [0x1ae]					
2013/12/27	21:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/27	22:17:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/27	22:17:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/27	22:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/27	22:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/27	22:38:30.0	XRT_Custom_430_OG [0x1ae]					
2013/12/27	22:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/27	23:54:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/27	23:54:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/27	23:54:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					

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2013/12/27	23:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/28	00:03:00.0	XRT_Custom_430_OG [0x1ae]					
2013/12/28	00:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/28	01:19:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/28	01:19:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/28	01:19:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/28	01:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/28	01:37:00.0	XRT_Custom_430_OG [0x1ae]					
2013/12/28	01:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/28	02:53:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/28	02:53:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/28	02:53:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/28	02:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/28	03:14:00.0	XRT_Custom_430_OG [0x1ae]					
2013/12/28	03:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/28	04:23:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/28	04:23:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/28	04:23:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/28	04:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/28	04:51:30.0	XRT_Custom_430_OG [0x1ae]					
2013/12/28	04:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/28	05:35:24.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/28	05:35:26.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2013/12/28	05:35:30.5	AOCS_Orе-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2013/12/28	05:35:46.0	XRT_FLD_DIS_434_OG [0x1b2]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2013/12/28	05:38:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2013/12/28	05:38:26.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/12/28	05:38:28.0	XRT_QT_PROG_SET_446_OG [0x1be]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2013/12/28	05:38:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/28	05:45:24.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/28	05:45:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2013/12/28	05:45:30.0	AOCS_Orе-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	01 00 00 00 00	
2013/12/28	05:45:46.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2013/12/28	05:45:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2013/12/28	05:45:50.0	XRT_AEC_RESET_413_OG [0x19d]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2013/12/28	05:45:52.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2013/12/28	05:45:54.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/28	05:45:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b	
2013/12/28	05:48:28.0	XRT_FL_PROG_SET_421_OG [0x1a5]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 0d	
2013/12/28	05:48:30.5	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/28	06:03:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/28	06:03:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/28	06:03:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2013/12/28	06:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2013/12/28	06:29:00.0	XRT_Custom_430_OG [0x1ae]					
2013/12/28	06:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2013/12/28	07:43:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2013/12/28	07:43:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2013/12/28	07:43:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					

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2013/12/28	07:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/12/28	08:06:00.0	XRT_Custom_430_OG [0x1ae]								
2013/12/28	08:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2013/12/28	09:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/28	09:23:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2013/12/28	09:23:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2013/12/28	09:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2013/12/28	10:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2013/12/28	10:30:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				