

XRT Timeline to be uploaded on 2014/01/11

Period: 2014/01/11 10:11:00 - 2014/01/24 09:24:00

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Normal mode

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XOB #19ED: AR Standard-B(Morphology), Be/thin 104s-cad, multifilter PFB (Al/poly, Be/thin, Be/med), multifilter context, 384x384, 1064x1048, G-band (33m)

Term	Pointing (x, y)	Comment
01/11 10:24:00 - 01/12 05:23:30	Track (624.1, -136.2) @ 01/11 10:21:00	# OP start + 10min, AR11944
01/12 06:23:00 - 01/13 05:33:24	Track (751.8, -144.9) @ 01/12 06:20:00	AR11944
01/13 17:00:30 - 01/14 05:54:24	Track (865.9, -157.7) @ 01/13 05:53:30	AR11944
01/14 06:17:30 - 01/15 06:11:54	Fixed (934.0, -172.0)	AR11944 fixed
01/15 06:35:00 - 01/16 05:45:24	Fixed (934.0, -172.0)	AR11944 fixed
01/16 06:39:30 - 01/17 05:59:54	Track (311.4, -224.3) @ 01/16 06:05:30	AR11949
01/17 06:23:00 - 01/17 08:30:30	Track (504.6, -230.5) @ 01/17 06:20:00	AR11949

PROG= 20 Inf.-time(s)

Subr=	1-time(s)	2.0sec										
Seqn= 8	1-time(s)	2.0sec										
Open/G-band	Open/G-band close	Safe Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec		
Seqn= 24	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al close	Safe Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec		
Open/G-band	Open/G-band open	Safe Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec		
Subr= 2	2-time(s)	2.0sec										
Seqn= 16	2-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al close	Safe Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
Open/thick-Al	Open/thick-Al close	Safe Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
Al-poly/Open	thin-Be/Open close	Safe Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
C-poly/Open	C-poly/thick-Al close	Safe Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
med-Be/Open	med-Be/Open close	Safe Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
med-Al/Open	Open/thick-Al close	Safe Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec		
Seqn= 20	20-time(s)	2.0sec										
thin-Be/Open	med-Be/Open close	Safe Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	13.0sec		
Al-poly/Open	thin-Be/Open close	Safe Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	13.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	13.0sec		
med-Be/Open	Open/thick-Al close	Safe Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	13.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	13.0sec		
Al-poly/Open	thin-Be/Open close	Safe Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	13.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	13.0sec		
med-Be/Open	Open/thick-Al close	Safe Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	13.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

XOB #19F1: Synoptic Q95 2x2 - Al/mesh(5/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/362/1443) + Thin.

Term	Pointing (x, y)	Comment
01/12 06:03:00 - 01/12 06:19:54	Fixed (0.0, 0.0)	synoptic
01/13 05:36:30 - 01/13 05:53:24	Fixed (0.0, 0.0)	synoptic, shifted -26.5 min
01/14 05:57:30 - 01/14 06:14:24	Fixed (0.0, 0.0)	synoptic, shifted -5.5 min
01/15 06:15:00 - 01/15 06:31:54	Fixed (0.0, 0.0)	synoptic, shifted 12.0 min
01/16 05:48:30 - 01/16 06:05:24	Fixed (0.0, 0.0)	synoptic, shifted -14.5 min
01/17 06:03:00 - 01/17 06:19:54	Fixed (0.0, 0.0)	synoptic
01/18 06:27:00 - 01/18 06:44:54	Fixed (0.0, 0.0)	synoptic, shifted 24.0 min

PROG= 04 1-time(s)

Subr=	1-time(s)	12.0sec										
Seqn= 84	2-time(s)	4.0sec										
Open/Al-mesh	Open/Al-mesh close	Safe Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al close	Safe Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec		
Seqn= 86	2-time(s)	4.0sec										
Open/Ti-poly	Open/Ti-poly close	Safe Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
Open/Ti-poly	Open/Ti-poly close	Safe Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
Open/Ti-poly	Open/Ti-poly close	Safe Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 95	2-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open close	Safe Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
thin-Be/Open	thin-Be/Open close	Safe Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
thin-Be/Open	thin-Be/Open close	Safe Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 6	2-time(s)	2.0sec										
Open/G-band	Open/G-band open	Safe Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec		
Open/G-band	Open/G-band close	Safe Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval

XOB #19EF: AR Standard-B(Morphology), Be/thin 24s-cad, multifilter PFB (Al/poly, Be/thin, Be/med), multifilter context, 384x384, 1064x1048, G-band (33m)

Term	Pointing (x, y)	Comment
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PROG= 12 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 8 1-time(s) 2.0sec													
	└─ Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Seqn= 24 1-time(s) 2.0sec													
	└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	└─ Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec													
└─ Seqn= 16 2-time(s) 2.0sec													
	└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	└─ Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	└─ C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	└─ med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	└─ med-Al/Open	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 52 100-time(s) 2.0sec													
	└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
	└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
	└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
	└─ med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #19D1: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 240s cadence, G-band - 384x384 45ms

Term	Pointing (x, y)	Comment
01/17 09:03:00 - 01/17 16:28:24	Fixed (-15.0, 910.0)	HOP240 N-pole

PROG= 05 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 13 2-time(s) 2.0sec													
	└─ Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 30 1-time(s) 30.0sec													
	└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Subr= 3 15-time(s) 2.0sec													
└─ Seqn= 57 1-time(s) 240.0sec													
	└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	└─ Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #19F3: CME watch - 4x4 - AEC 2 - Be-thin (Be-Med) - 240s cad

Term	Pointing (x, y)	Comment
01/17 16:31:30 - 01/17 17:10:30	Fixed (-15.0, -965.0)	HOP79 #1
01/17 17:38:00 - 01/17 18:04:54	Fixed (-15.0, -865.0)	HOP79 #2
01/17 18:08:00 - 01/17 18:47:30	Fixed (-15.0, -765.0)	HOP79 #3
01/17 19:15:00 - 01/17 19:41:54	Fixed (-15.0, -665.0)	HOP79 #4
01/17 19:45:00 - 01/17 20:24:30	Fixed (-15.0, -565.0)	HOP79 #5
01/17 20:52:00 - 01/17 21:18:54	Fixed (-15.0, -465.0)	HOP79 #6
01/17 21:22:00 - 01/17 22:02:00	Fixed (-15.0, -365.0)	HOP79 #7
01/17 22:28:00 - 01/17 22:54:54	Fixed (-15.0, -265.0)	HOP79 #7
01/17 22:58:00 - 01/17 23:39:30	Fixed (-15.0, -165.0)	HOP79 #8
01/17 23:54:00 - 01/18 00:20:54	Fixed (-15.0, -65.0)	HOP79 #9
01/18 00:24:00 - 01/18 01:05:30	Fixed (-15.0, 25.0)	HOP79 #10
01/18 01:26:30 - 01/18 01:53:24	Fixed (-15.0, 125.0)	HOP79 #11
01/18 01:56:30 - 01/18 02:38:30	Fixed (-15.0, 225.0)	HOP79 #12
01/18 03:03:00 - 01/18 03:29:54	Fixed (-15.0, 325.0)	HOP79 #13
01/18 03:33:00 - 01/18 04:07:30	Fixed (-15.0, 425.0)	HOP79 #14
01/18 04:40:30 - 01/18 05:07:24	Fixed (-15.0, 525.0)	HOP79 #15
01/18 05:10:30 - 01/18 05:48:00	Fixed (-15.0, 625.0)	HOP79 #16
01/18 06:48:00 - 01/18 07:28:30	Fixed (-15.0, 725.0)	HOP79 #17
01/18 07:55:00 - 01/18 08:21:54	Fixed (-15.0, 825.0)	HOP79 #18
01/18 08:25:00 - 01/18 09:08:00	Fixed (-15.0, 925.0)	HOP79 #19

PROG= 13 Inf.-time(s)

Subr= 1 15-time(s) 240.0sec													
└─ Seqn= 19 1-time(s) 4.0sec													
	└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

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Flare mode

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XOB #19F0: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops-lowered-gband

Term	Pointing (x, y)	Comment
01/11 10:24:00 - 01/12 05:23:30	Track (624.1, -136.2) @ 01/11 10:21:00	# OP start + 10min, AR11944
01/12 06:23:00 - 01/13 05:33:24	Track (751.8, -144.9) @ 01/12 06:20:00	AR11944
01/13 06:28:00 - 01/13 16:59:54	Track (865.9, -157.7) @ 01/13 05:53:30	AR11944
01/13 17:00:30 - 01/14 05:54:24	Track (865.9, -157.7) @ 01/13 05:53:30	AR11944
01/14 06:17:30 - 01/15 06:11:54	Fixed (934.0, -172.0)	AR11944 fixed

01/15 06:35:00 - 01/16 05:45:24	Fixed (934.0, -172.0)	AR11944 fixed
01/16 06:39:30 - 01/17 05:59:54	Track (311.4, -224.3) @ 01/16 06:05:30	AR11949
01/17 06:23:00 - 01/17 08:30:30	Track (504.6, -230.5) @ 01/17 06:20:00	AR11949
01/17 09:03:00 - 01/17 16:28:24	Fixed (-15.0, 910.0)	HOP79 N-pole
01/17 16:31:30 - 01/17 17:10:30	Fixed (-15.0, -965.0)	HOP79 #1
01/17 17:38:00 - 01/17 18:04:54	Fixed (-15.0, -865.0)	HOP79 #2
01/17 18:08:00 - 01/17 18:47:30	Fixed (-15.0, -765.0)	HOP79 #3
01/17 19:15:00 - 01/17 19:41:54	Fixed (-15.0, -665.0)	HOP79 #4
01/17 19:45:00 - 01/17 20:24:30	Fixed (-15.0, -565.0)	HOP79 #5
01/17 20:52:00 - 01/17 21:18:54	Fixed (-15.0, -465.0)	HOP79 #6
01/17 21:22:00 - 01/17 22:02:00	Fixed (-15.0, -365.0)	HOP79 #7
01/17 22:28:00 - 01/17 22:54:54	Fixed (-15.0, -265.0)	HOP79 #7
01/17 22:58:00 - 01/17 23:39:30	Fixed (-15.0, -165.0)	HOP79 #8
01/17 23:54:00 - 01/18 00:20:54	Fixed (-15.0, -65.0)	HOP79 #9
01/18 00:24:00 - 01/18 01:05:30	Fixed (-15.0, 25.0)	HOP79 #10
01/18 01:26:30 - 01/18 01:53:24	Fixed (-15.0, 125.0)	HOP79 #11
01/18 01:56:30 - 01/18 02:38:30	Fixed (-15.0, 225.0)	HOP79 #12
01/18 03:03:00 - 01/18 03:29:54	Fixed (-15.0, 325.0)	HOP79 #13
01/18 03:33:00 - 01/18 04:07:30	Fixed (-15.0, 425.0)	HOP79 #14
01/18 04:40:30 - 01/18 05:07:24	Fixed (-15.0, 525.0)	HOP79 #15
01/18 05:10:30 - 01/18 05:48:00	Fixed (-15.0, 625.0)	HOP79 #16
01/18 06:48:00 - 01/18 07:28:30	Fixed (-15.0, 725.0)	HOP79 #17
01/18 07:55:00 - 01/18 08:21:54	Fixed (-15.0, 825.0)	HOP79 #18
01/18 08:25:00 - 01/18 09:08:00	Fixed (-15.0, 925.0)	HOP79 #19

PROG= 02 12-time(s)

Subr= 1 45-time(s) 10.0sec												
Seqn= 9 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 10.0sec												
Seqn= 17 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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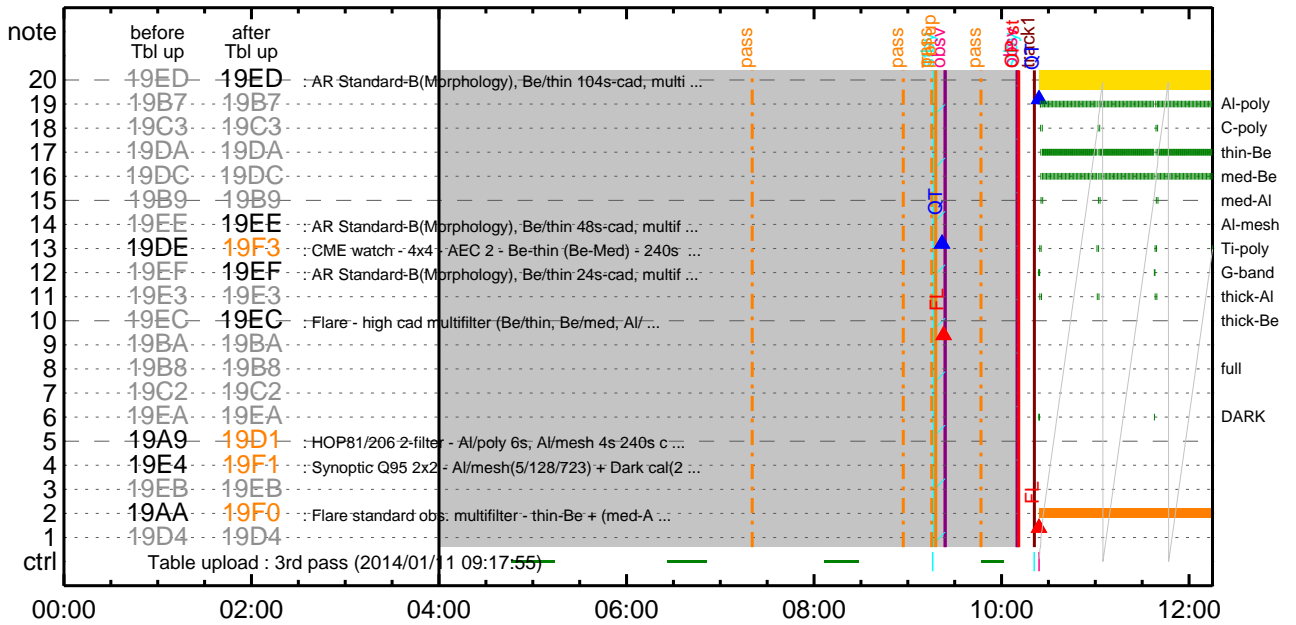
Flare Detection

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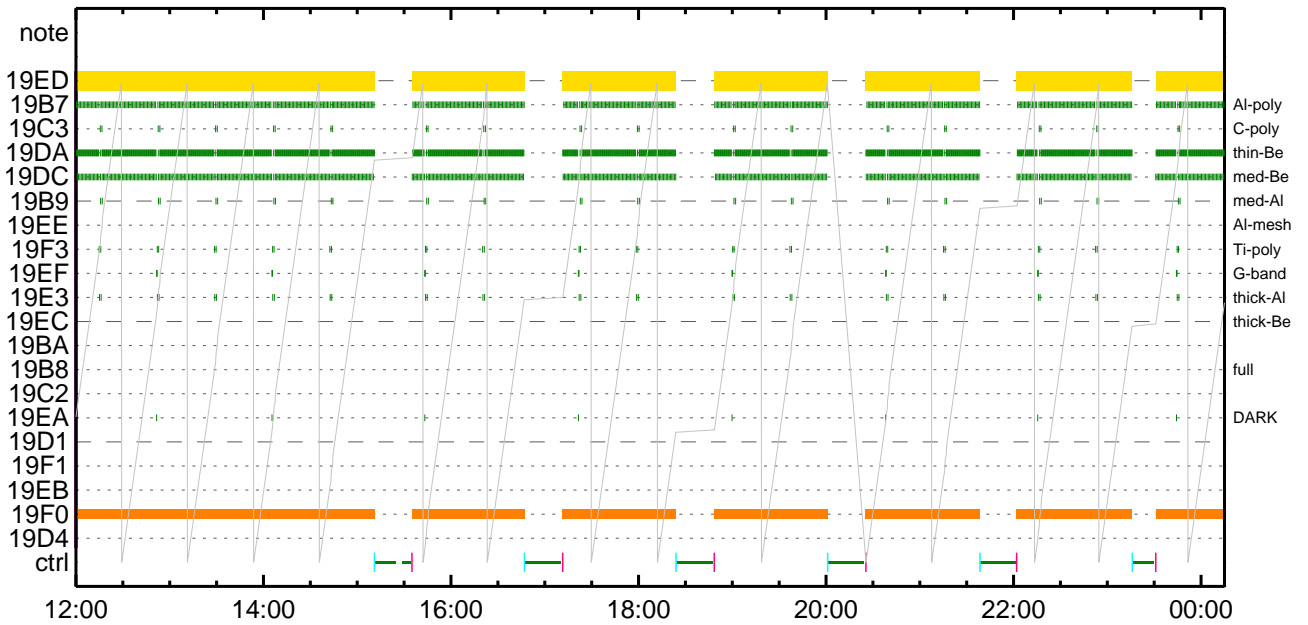
FLD Patrol

Term	Pointing (x, y)	Comment
01/12 06:20:16 - 01/13 05:33:46	Track (751.8, -144.9) @ 01/12 06:20:00	AR11944
01/13 05:57:46 - 01/14 05:54:46	Track (865.9, -157.7) @ 01/13 05:53:30	AR11944
01/14 06:14:46 - 01/15 06:12:16	Fixed (934.0, -172.0)	AR11944 fixed
01/15 06:32:16 - 01/16 05:45:46	Fixed (934.0, -172.0)	AR11944 fixed
01/16 06:05:46 - 01/17 06:00:16	Track (311.4, -224.3) @ 01/16 06:05:30	AR11949
01/17 06:20:16 - 01/18 06:24:16	Track (504.6, -230.5) @ 01/17 06:20:00	AR11949
01/18 06:45:16 - 01/24 09:24:00	Fixed (-15.0, 725.0)	HOP79 #17
Open/Ti-poly	Open/thick-Al close	Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

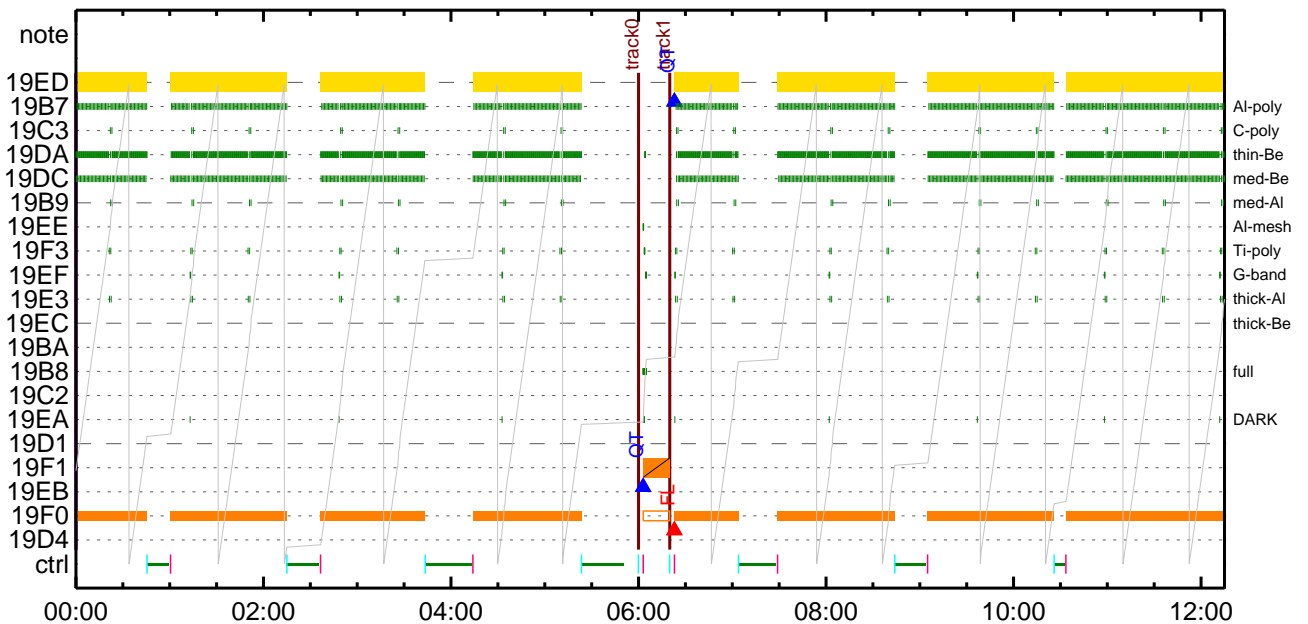
CMDI #0037 2014/01/11



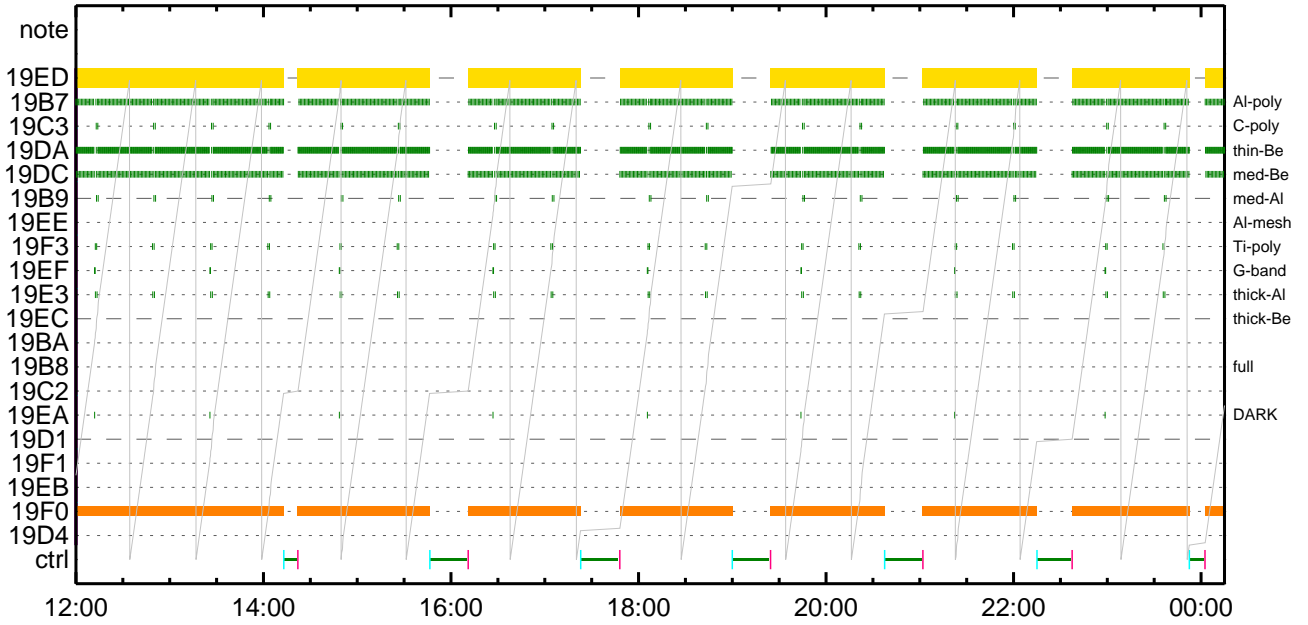
CMDI #0037 2014/01/11



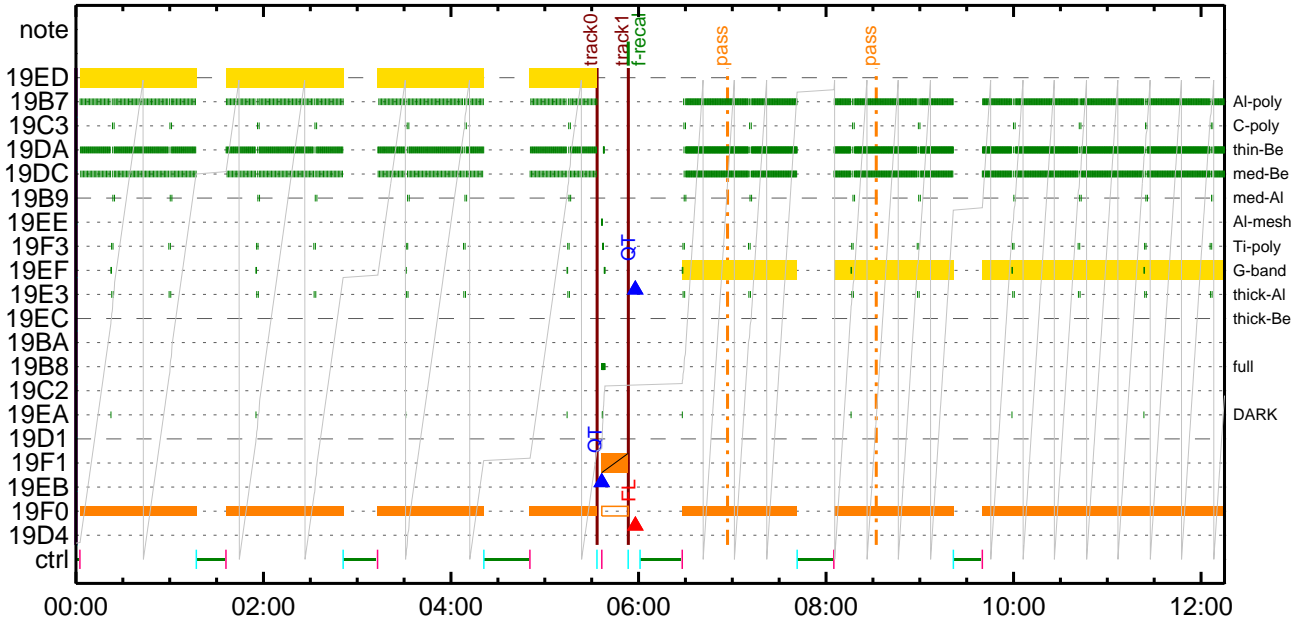
CMDI #0037 2014/01/12



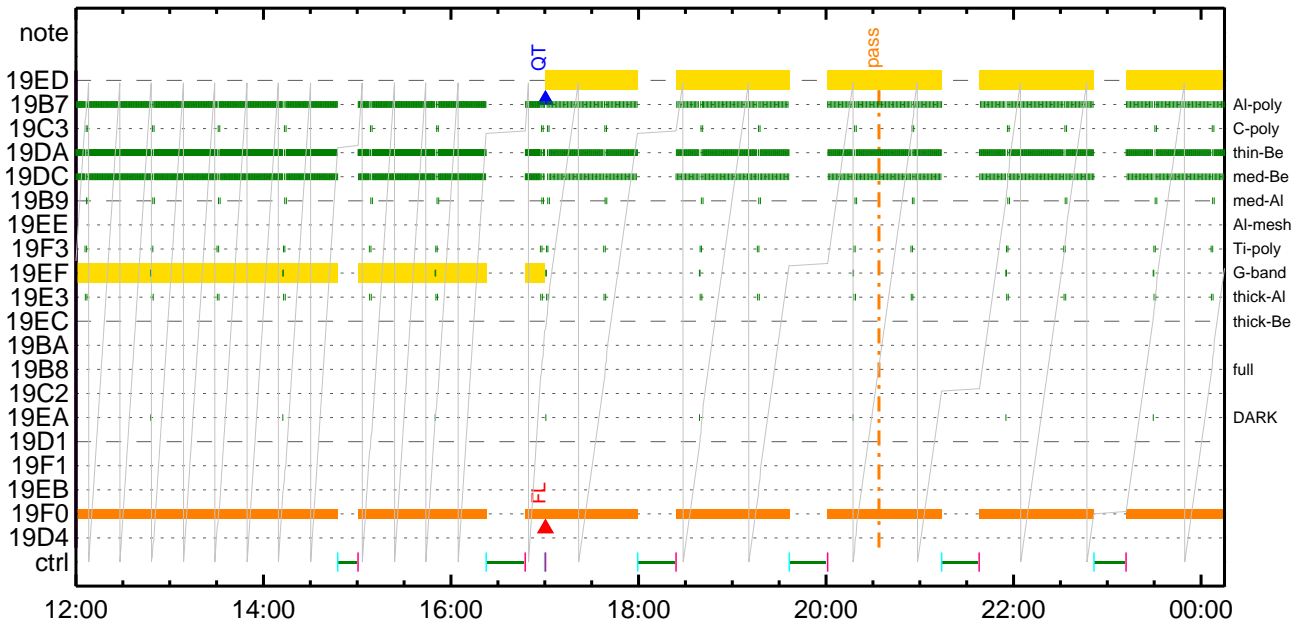
CMDI #0037 2014/01/12



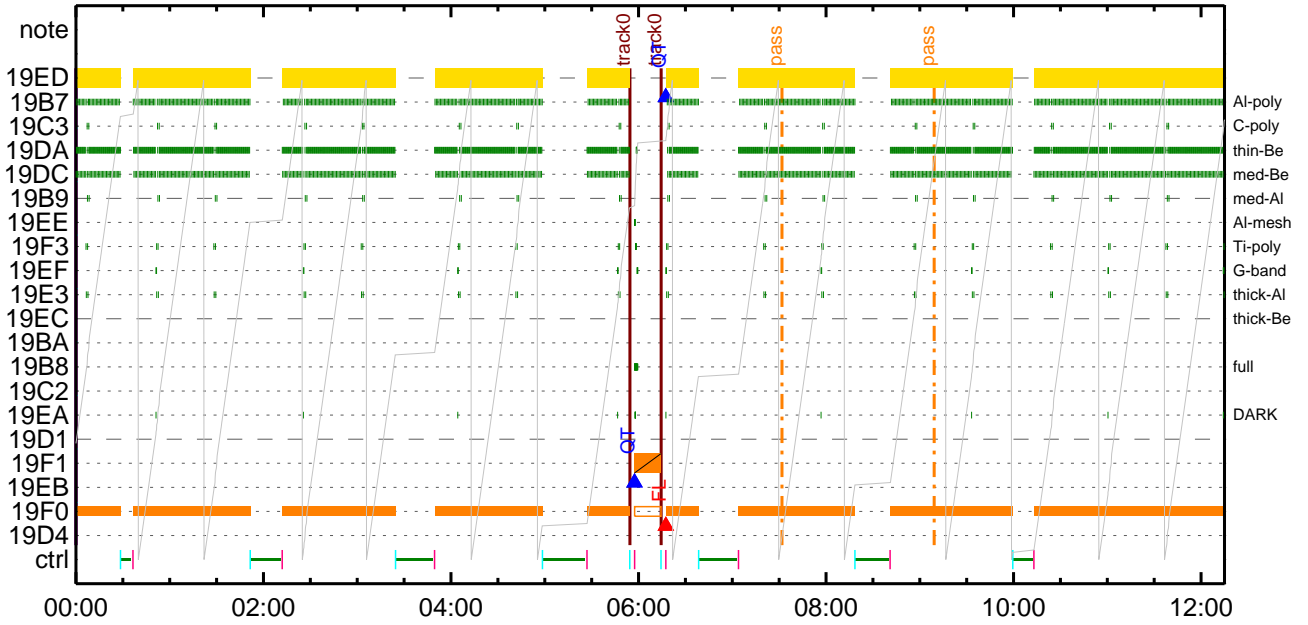
CMDI #0037 2014/01/13



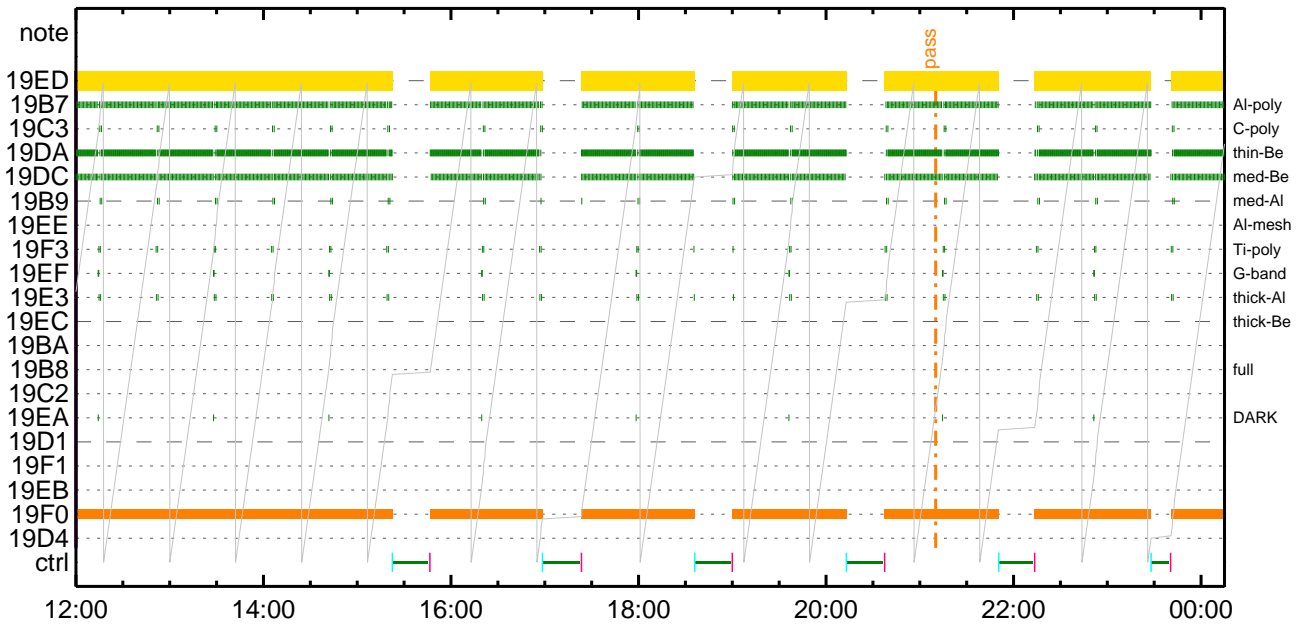
CMDI #0037 2014/01/13



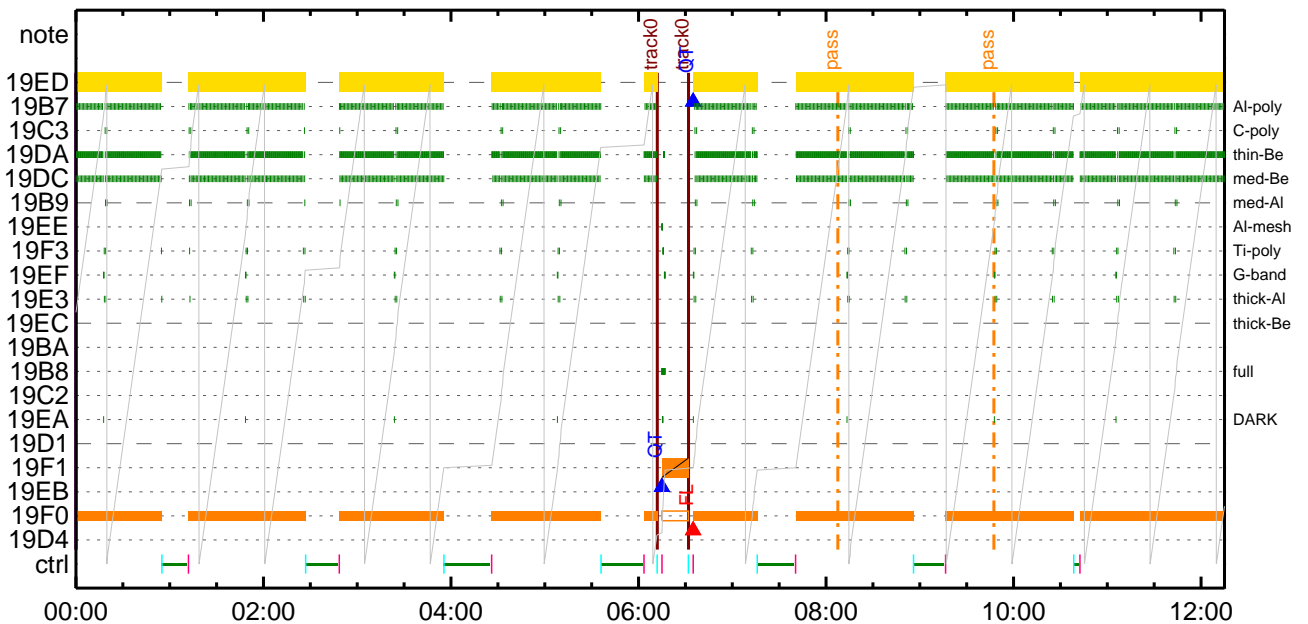
CMDI #0037 2014/01/14



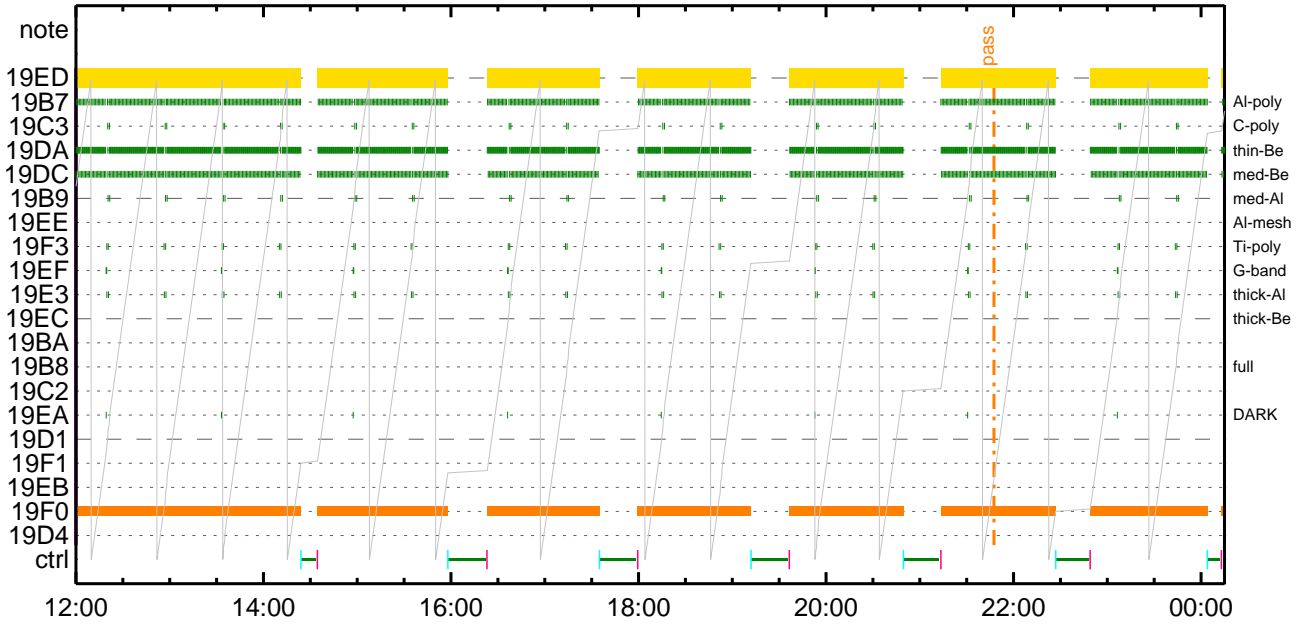
CMDI #0037 2014/01/14



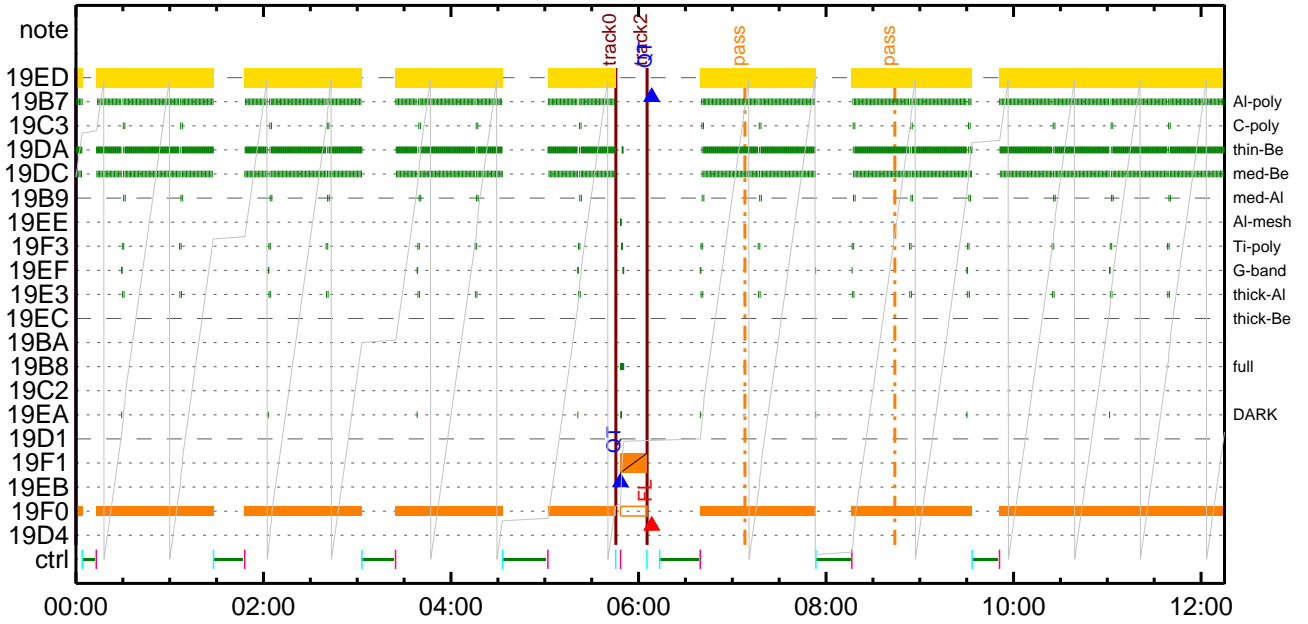
CMDI #0037 2014/01/15



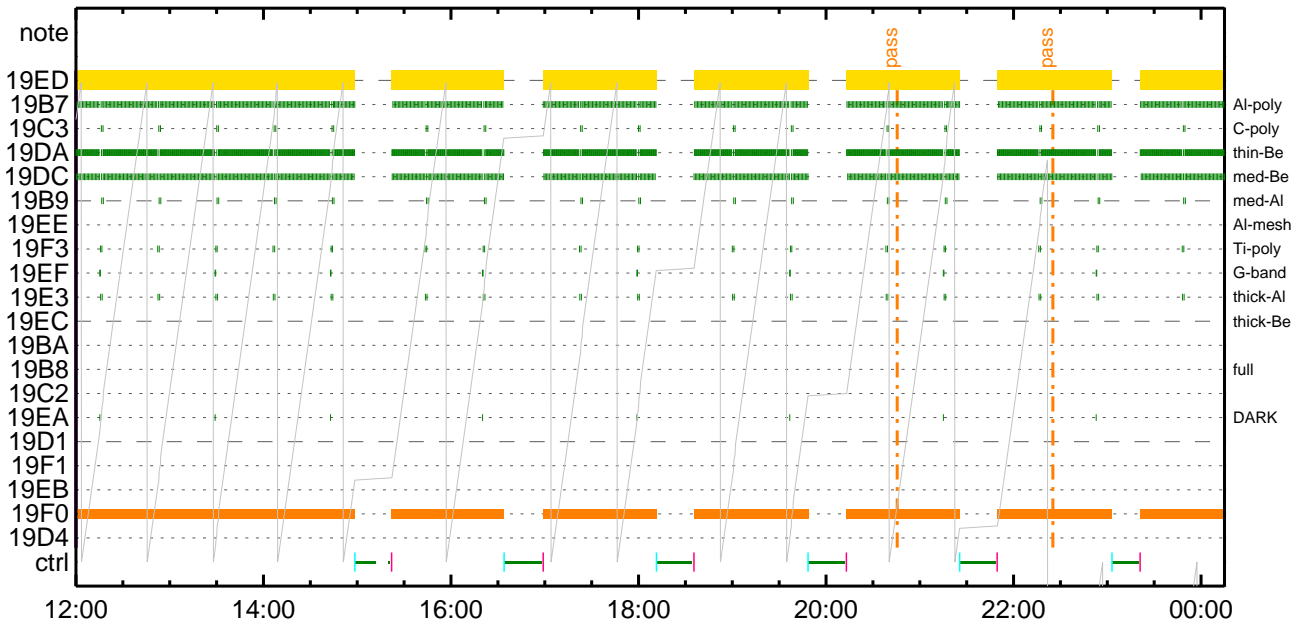
CMDI #0037 2014/01/15



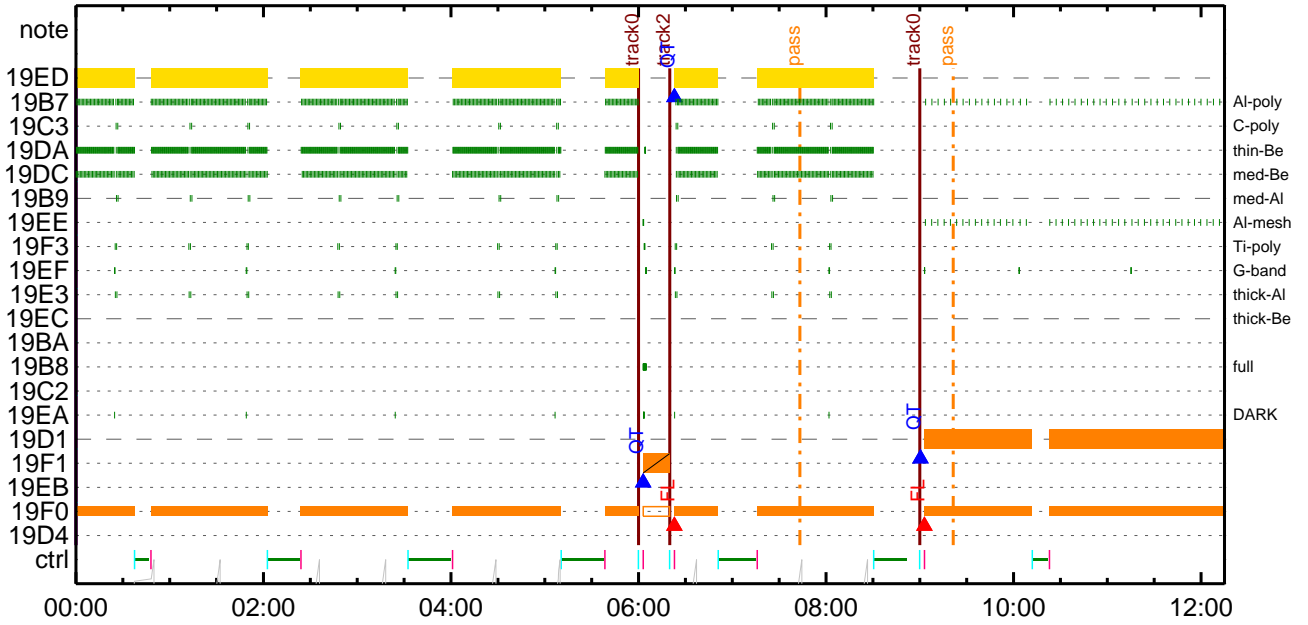
CMDI #0037 2014/01/16



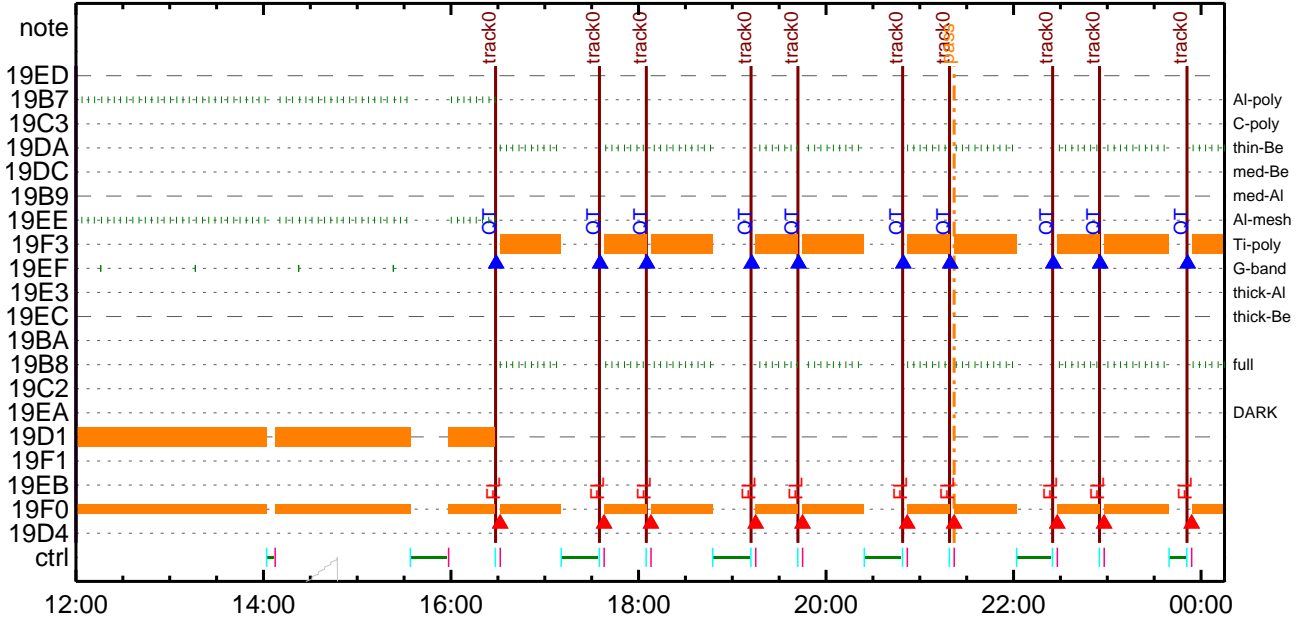
CMDI #0037 2014/01/16



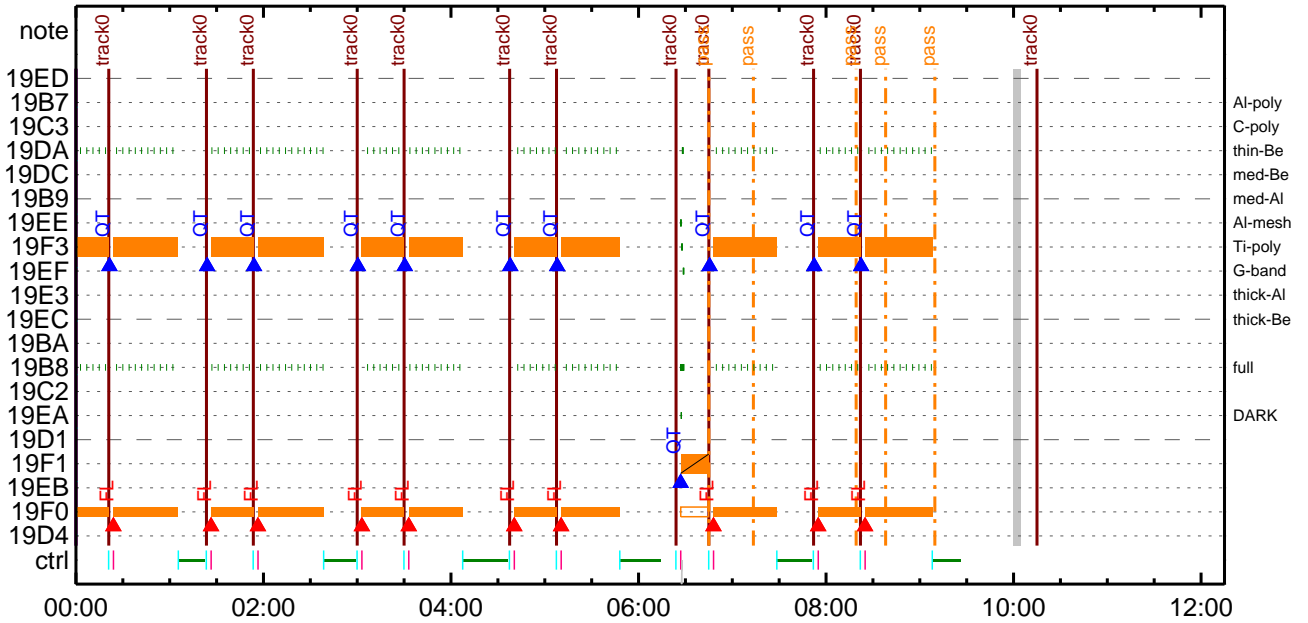
CMDI #0037 2014/01/17



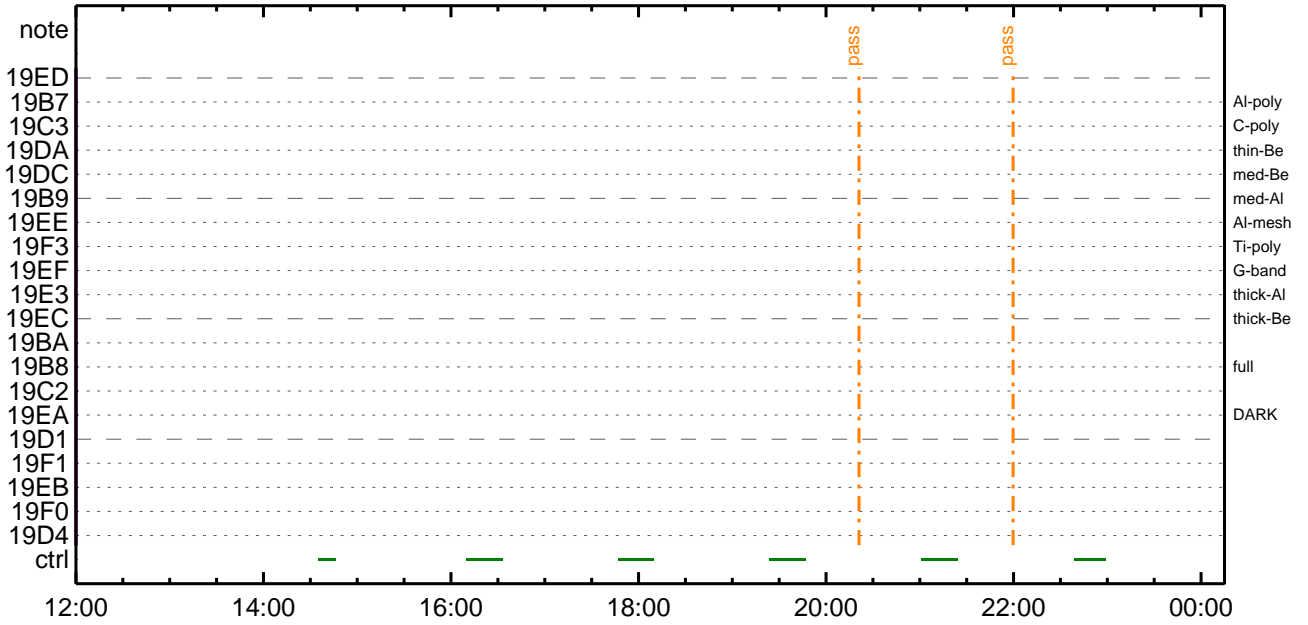
CMDI #0037 2014/01/17



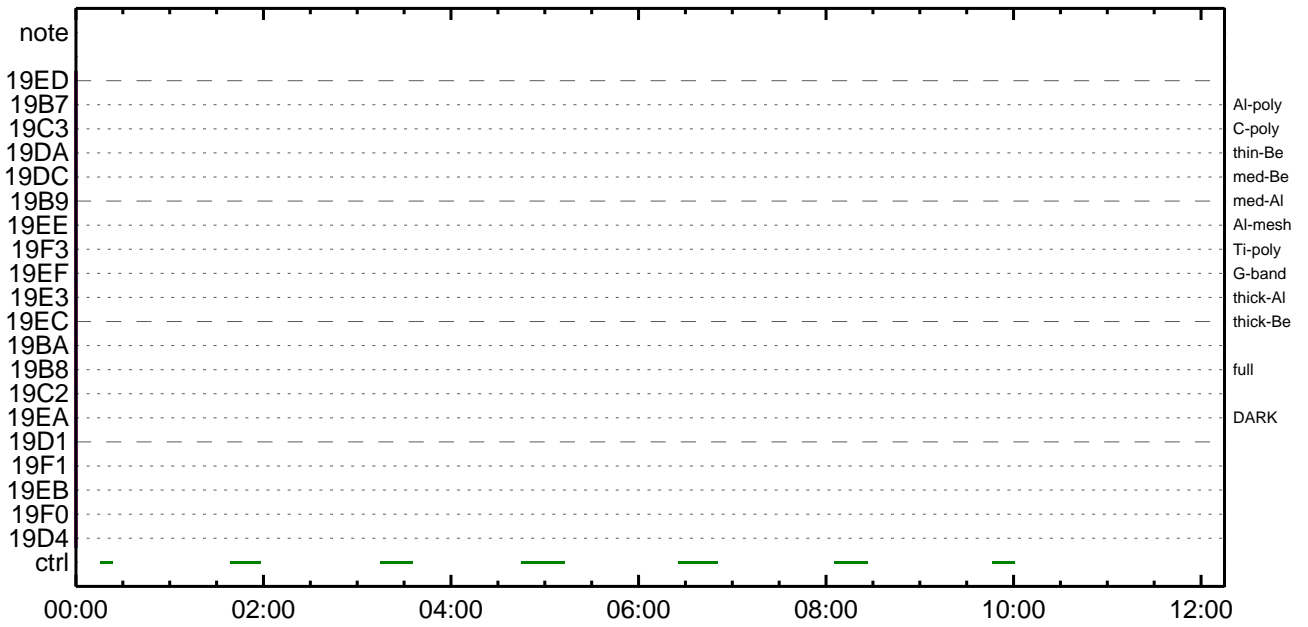
CMDI #0037 2014/01/18



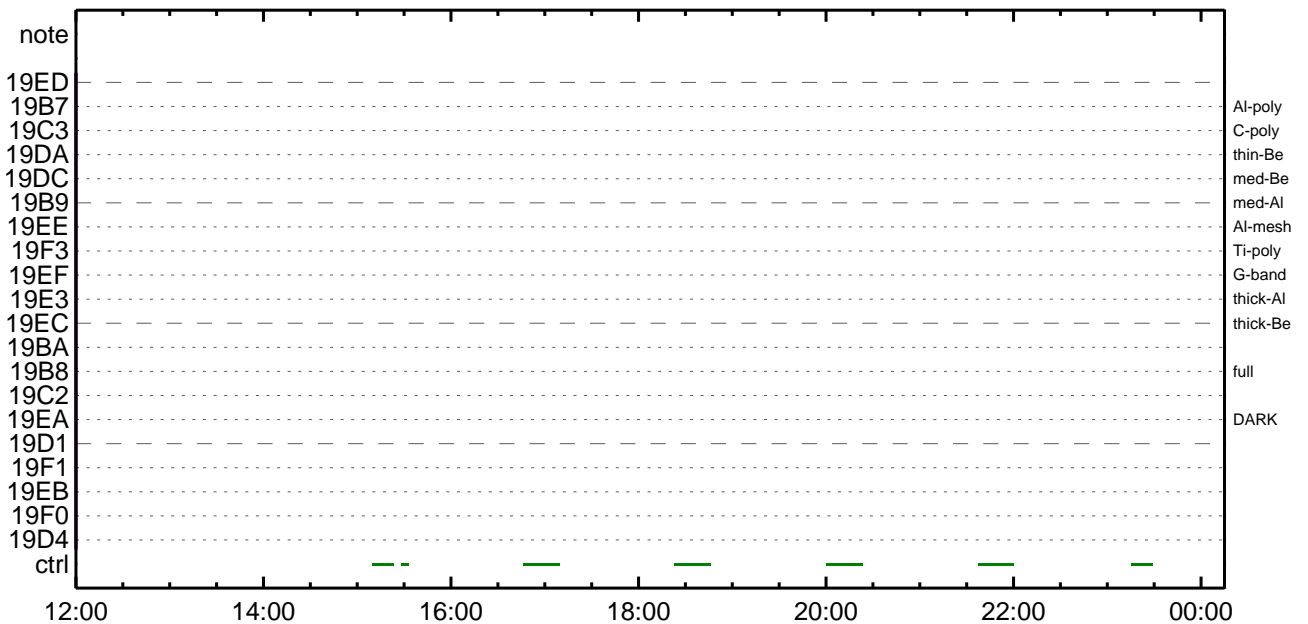
CMDI #0037 2014/01/18



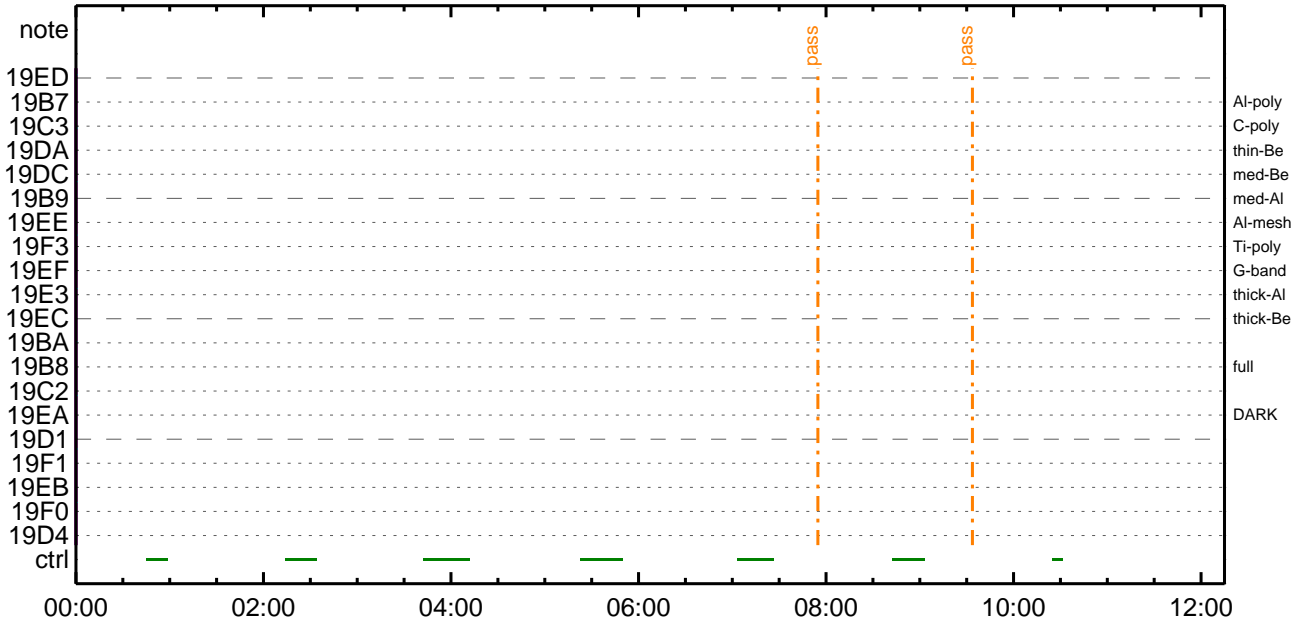
CMDI #0037 2014/01/19



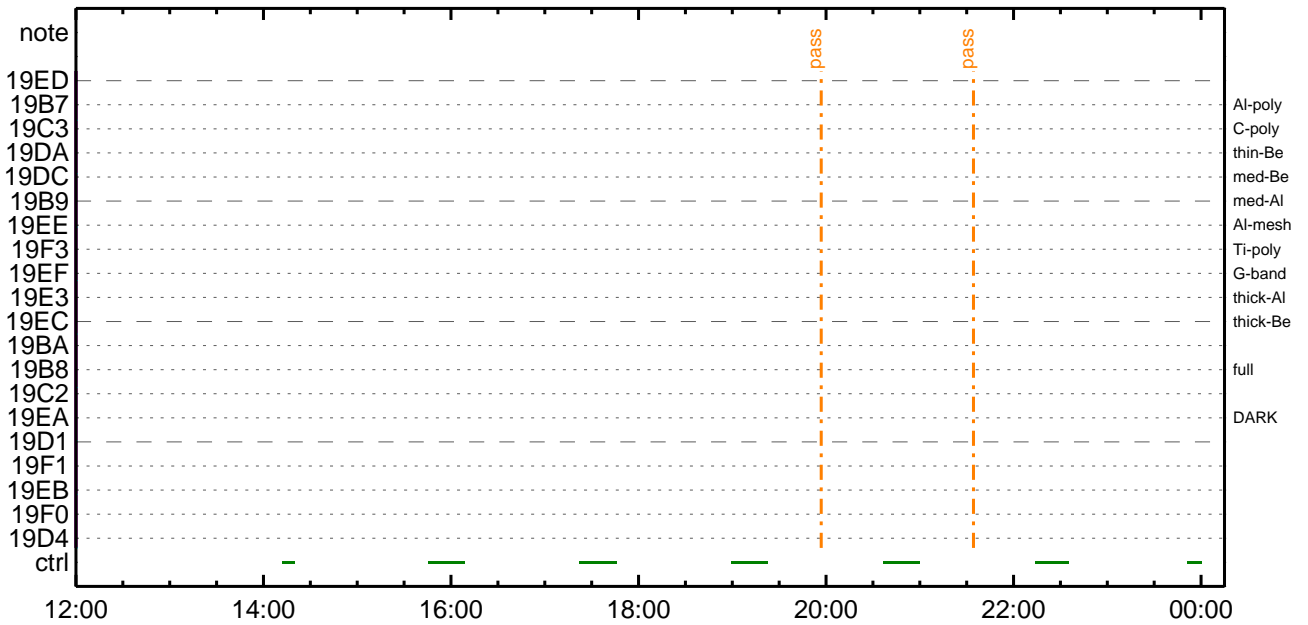
CMDI #0037 2014/01/19



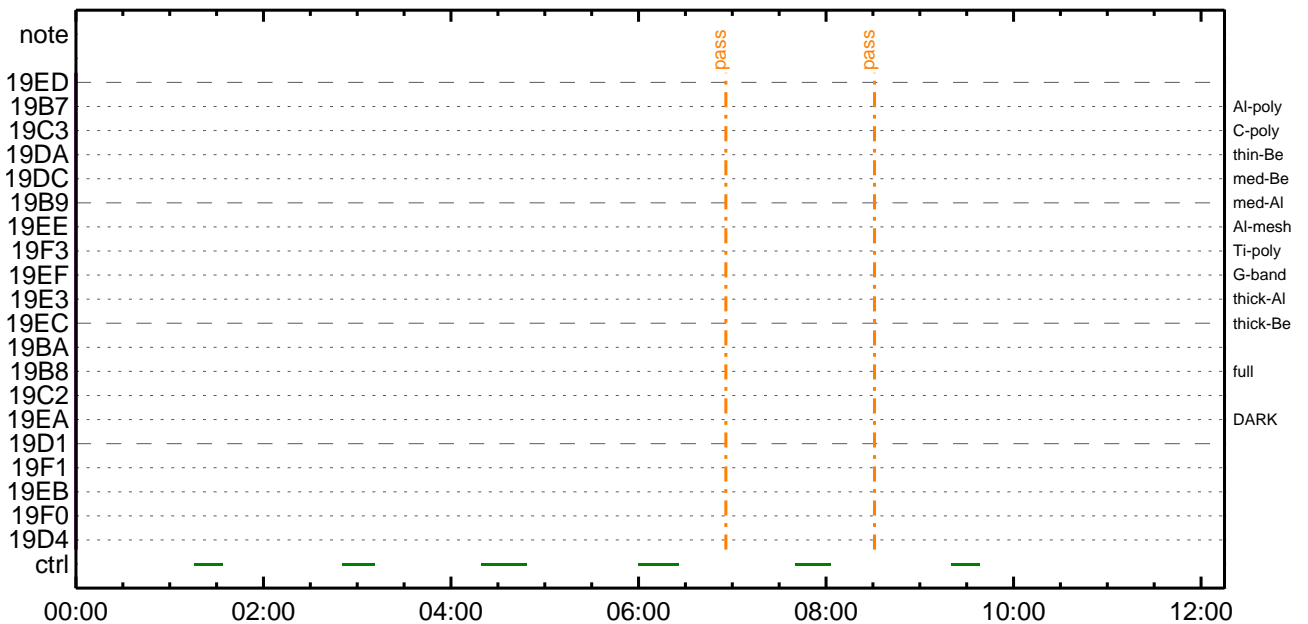
CMDI #0037 2014/01/20



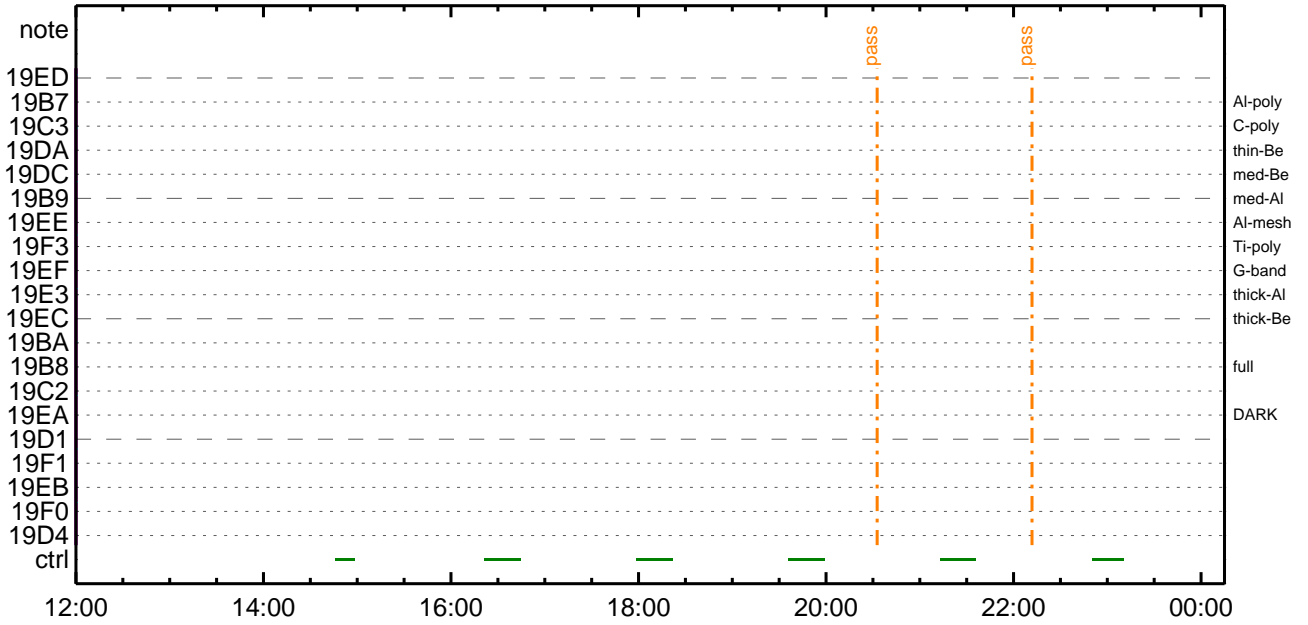
CMDI #0037 2014/01/20



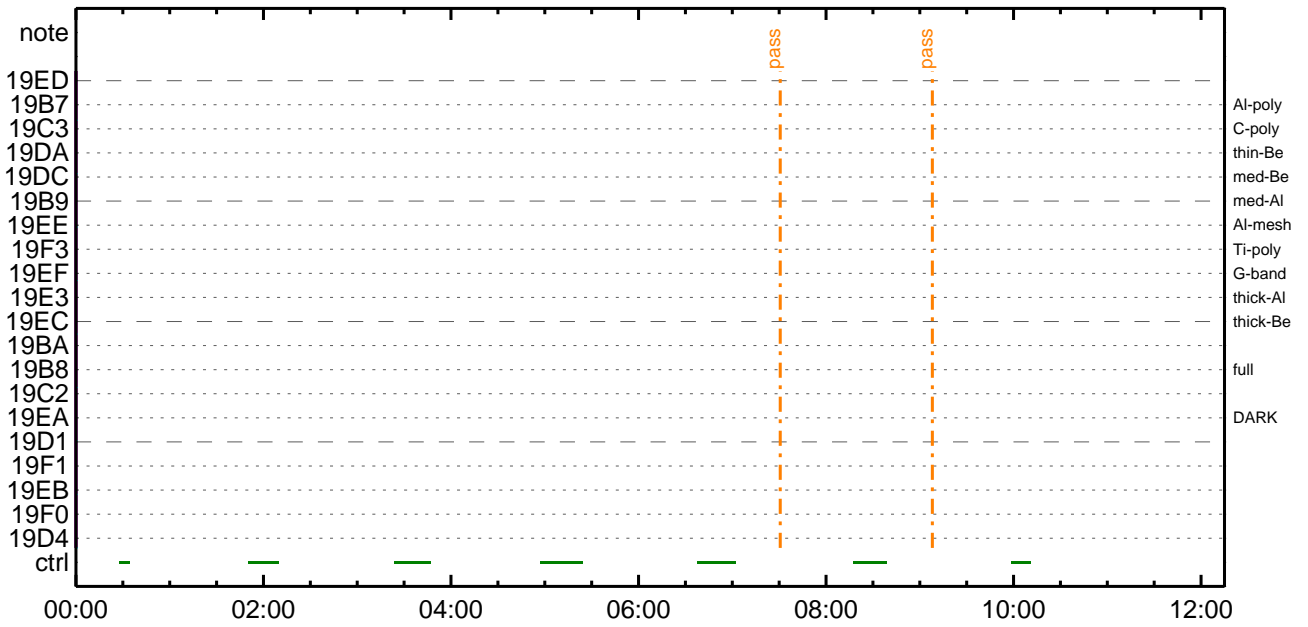
CMDI #0037 2014/01/21



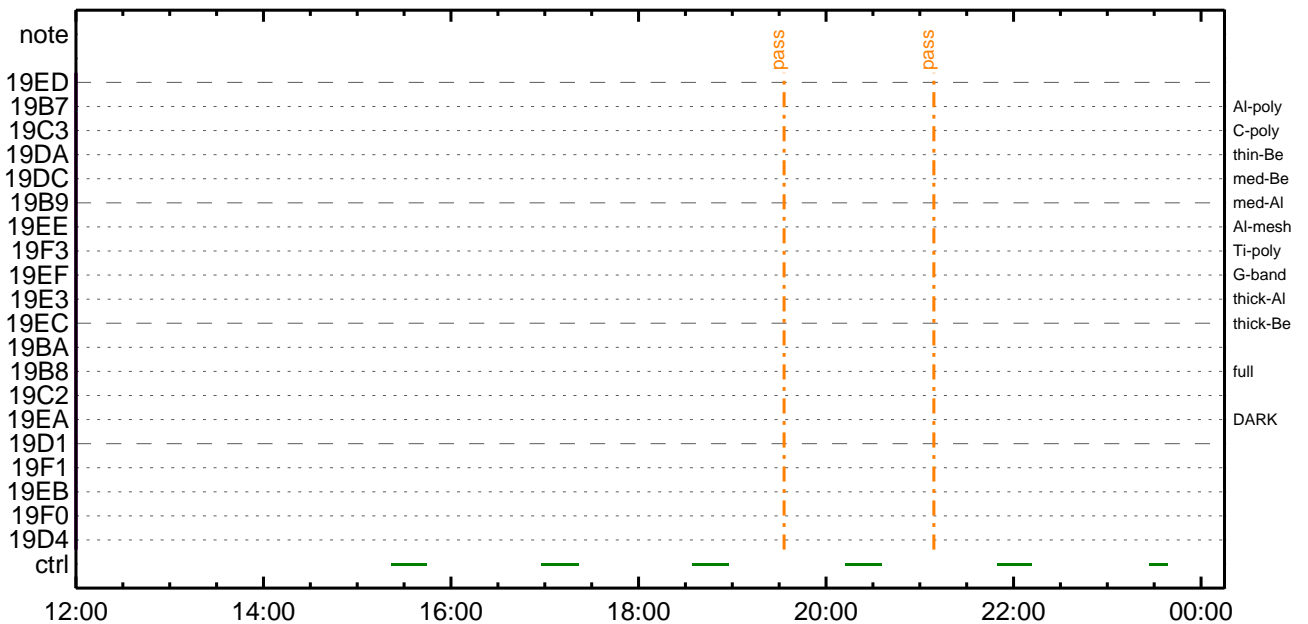
CMDI #0037 2014/01/21



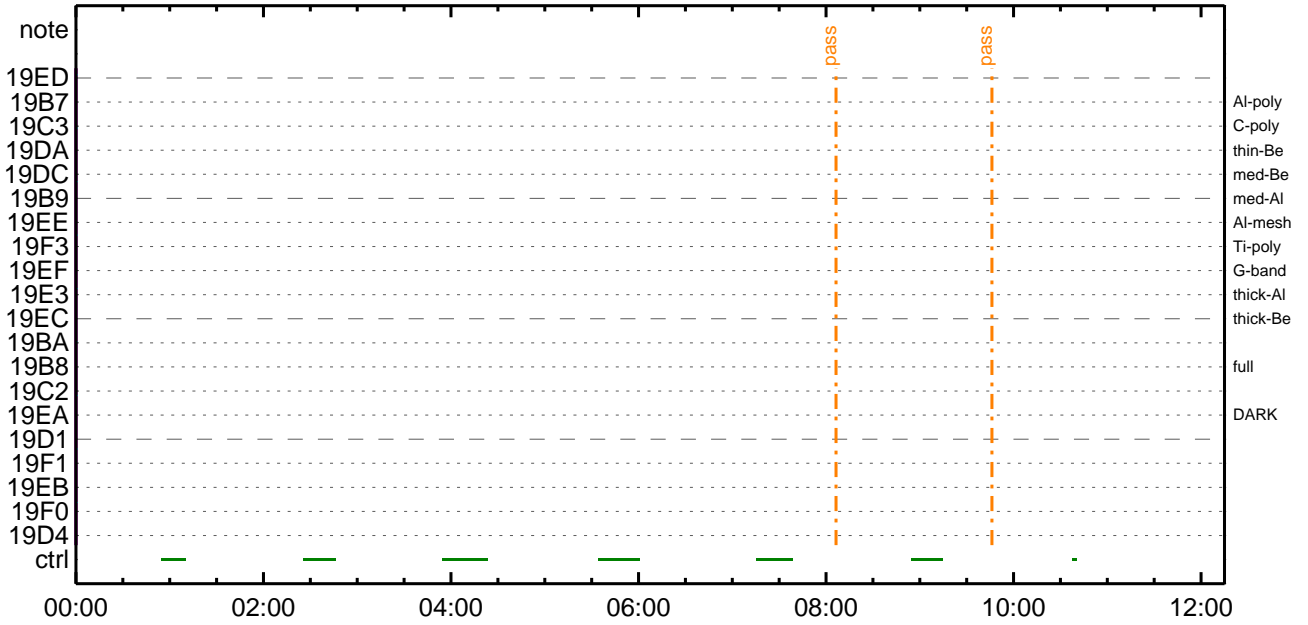
CMDI #0037 2014/01/22



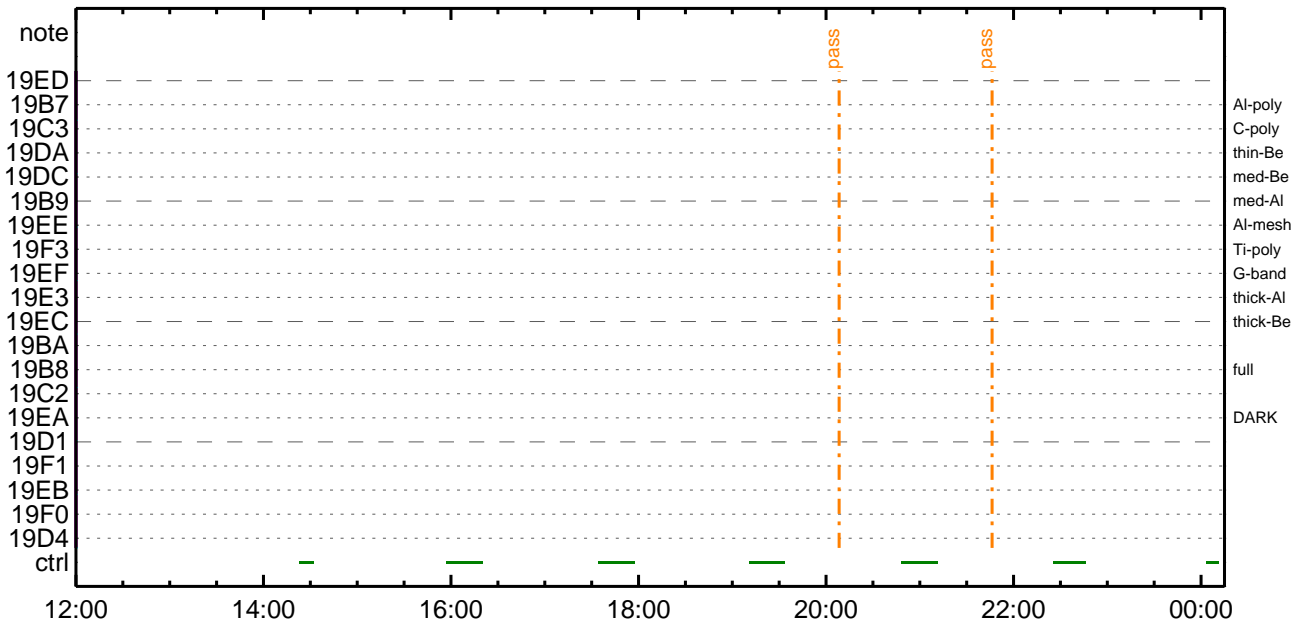
CMDI #0037 2014/01/22



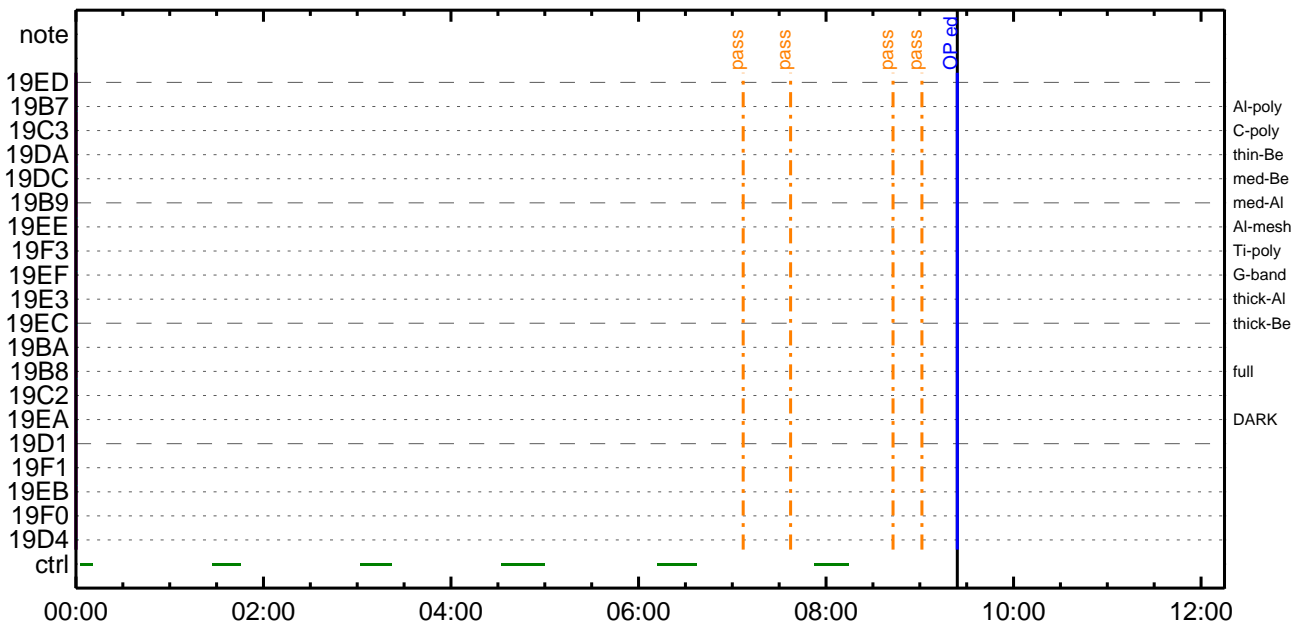
CMDI #0037 2014/01/23



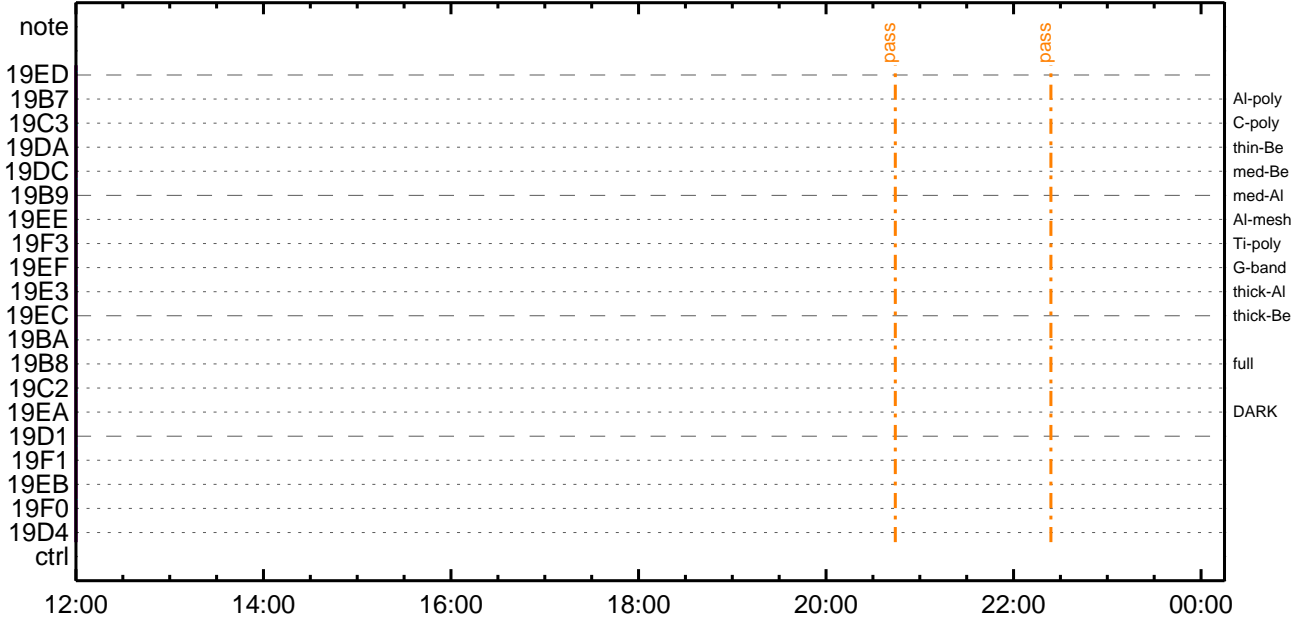
CMDI #0037 2014/01/23



CMDI #0037 2014/01/24



CMDI #0037 2014/01/24




```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ä
0100 C. *****
0101 C.
0102 C. äOP/OGY1;4YE;ä
0103 S. OP op-147:OP
0104 ( )
0105 S. OG og-147:OG
0106 ( )
0107 C.
0108 C. äNMOG&OPfî°èYÄYóYx;ä
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YÄYóYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGñî¼Ë¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YÄYóYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGñî¼Ë¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YÄYóYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPñî¼Ë¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** òË²¼òî¼Ä´¶Á°òËË-ò°Á÷¿@ (¼âµ-YÄYóYx½ªî»ò³îÇ§òðÁÓÄæòÇ¼ª°"òè¼i¹çòçòâ) *****
0167 C. DHUYâ;¼YE;Ë¼Y½, Yî;¼YE;Ëòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADñ-Á÷¿@NGñî¼Ë¹ç;ç°Ë²¼òî¼TI-CMDÁ÷¿@ñî¼Ä¹Òª•òËòò³òË;f
0180 C. òËò¿;çSETòËDUMPAË±òîYÑY¹ç¹Òª|ò³òË;f
0181 C.
0182 C. TIY³YËYóYËòðÁî¿¿(UT)
0183 +. TI 2014-01-11 10:06:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2014-01-11 10:06:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2014-01-11 10:06:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

```
0194 C.
0195 +. TI 2014-01-11 10:10:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄê%îíñαîŷÄŷ§ŷÄŷ-¹àîŮ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîŷ°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]       EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»αò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαîŷÈ¹ç•è²îOKαò³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÈ;Êŷŷ¼. ŷî;¼ŷÈ;Êαòîãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2014-01-11 10:10:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC      (21 02)
0247 +. TI 2014-01-11 10:10:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC      (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2014-01-11 10:10:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC      (c3)
0260 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP ´ûÃîαî»ò¼ŷαÈÄα¹αèDCBC•×²è *****
0265 C. (¼ª°îŷÖŷÄŷÈŷŷ¼ŷÈŷâŷçŷèαÈ¼αα¼Ä»Ůα¹αè)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŷDŷ¹.İ Daily±;îñαÈ´Øα¹αèDCBC•×²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. îãLOSŷÄŷŷÄŷŷÄŷ-¼Ä»Ů;ã
0276 C.
0277 C. ***** LOS *****
0278 C.
```


(a) Spacecraft Operation Procedure (real-commands)

```
main-149 2014-01-11 14:49:56 183 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YF¥óYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãã•µ°È»Í×ÁÇãÍYçYÁY×Yí;¼YÉ;ÈÈ¿µ•íÉ;ÈÈ¿°ÇÔã•¿¿¼í¹çãÍ;çÀ®, ùã¹ãÈãÈãÇÁ+¿®ã•ãÈããã³ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C. *****
0015 C. SOT TI command set
0016 C. *****
0017 C. Execute, after the success of OP upload.
0018 +. TI 2014-01-11 10:10:16.0
0019 DC 07-F0 MDP_SOT_MODE_STBY
0020 BC (41)
0021 C. -----
0022 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0023 C. -----
0024 C. ***** SOT END *****
0025 C. *****
0026 C. SOT table upload
0027 C. *****
0028 . C. < Stop FG table >
0029 +. DC 07-F0 MDP_FG_CTRL_MANU
0030 BC (51)
0031 C. -----
0032 C. MDP_FG_CTRL_MODE = MANU [ ]
0033 C. -----
0034 C.
0035 . C. <Upload FG Observation Table>
0036 . S. RAM ram-263:MDP_OBS_F
0037 ( )
0038 C.
0039 . C. < Dump RAMID=MDP_OBS_F >
0040 +. DC 07-F0 MDP_DUMP_FGTBL
0041 BC (82 07 00 00 00 38 b8)
0042 C. -----
0043 C. MDP_OBS_F verify = OK/NG [ ]
0044 C. -----
0045 C.
0046 . C. < Stop SP table >
0047 +. DC 07-F0 MDP_SP_CTRL_MANU
0048 BC (61)
0049 C. -----
0050 C. MDP_SP_CTRL_MODE = MANU [ ]
0051 C. -----
0052 C.
0053 . C. <Upload SP Observation Table>
0054 . S. RAM ram-287:MDP_OBS_S
0055 ( )
0056 C.
0057 . C. < Dump RAMID=MDP_OBS_S >
0058 +. DC 07-F0 MDP_DUMP_SPTBL
0059 BC (83 07 00 00 00 38 b8)
0060 C. -----
0061 C. MDP_OBS_S verify = OK/NG [ ]
0062 C. -----
0063 C.
0064 . C. < Upload DPL table >
0065 C.
0066 C. YçYÁY×Yí;¼YÉãÍÁ°ãÈSTS_CHKãðOFFãÈã¹ãÈ
0067 C.
0068 . S. RAM ram-271:MDP_DPL
0069 ( )
0070 C.
0071 . C. < Dump RAMID=MDP_DPL >
0072 +. DC 07-F0 MDP_DUMP_FGTBL
0073 BC (82 07 00 38 b8 00 40)
0074 C. -----
0075 C. MDP_DPL verify = OK [ ]
0076 C. -----
0077 C.
0078 C. STS_CHKãðONãÈã¹ãÈ
0079 C.
0080 . C. < Update MDP DSC PAR1 >
0081 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0082 BC (4c)
0083 C. MDP_CMD_CODE = F04C0700 [ ]
0084 C. MDP_CMD_CNT (count-up 1) [ ]
0085 C. -----
0086 C.
0087 C.
0088 C. *****
0089 C. SOT TI command set
0090 C. *****
0091 C. Execute, after the success of TBL upload.
0092 +. TI 2014-01-11 10:10:18.0
0093 DC 07-F0 MDP_SOT_MODE_OBSV
0094 BC (40)
0095 C. -----
```

```

0096 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0097 C. -----
0098 C.
0099 C.
0100 C. ***** XRT START *****
0101 C.
0102 +. DC 07-F0 MDP_XRT_CTRL_MANU
0103 BC (c1)
0104 +. DC 07-F0 MDP_XRT_MODE_STBY
0105 BC (c3)
0106 . C. ----- Success Verify ? OK / NG____
0107 C.
0108 C. XRT Obs. Table Upload
0109 . S. RAM ram-291:MDP_OBS_X
0110 ( )
0111 C.
0112 +. DC 07-F0 MDP_DUMP_XRTTBL
0113 BC (84 07 00 00 00 3a d4)
0114 . C. ----- Comparison Check ? OK / ERR ____
0115 C.
0116 C.
0117 +. DC 07-F0 MDP_XRT_ROI_SET
0118 BC (cd 01 b1 b1 04 04)
0119 +. DC 07-F0 MDP_XRT_ROI_SET
0120 BC (cd 02 b1 b1 08 08)
0121 +. DC 07-F0 MDP_XRT_ROI_SET
0122 BC (cd 03 b1 b1 08 08)
0123 +. DC 07-F0 MDP_XRT_ROI_SET
0124 BC (cd 04 b1 b1 06 06)
0125 +. DC 07-F0 MDP_XRT_ROI_SET
0126 BC (cd 05 85 83 06 06)
0127 +. DC 07-F0 MDP_XRT_ROI_SET
0128 BC (cd 06 85 83 06 06)
0129 +. DC 07-F0 MDP_XRT_ROI_SET
0130 BC (cd 07 80 80 20 20)
0131 +. DC 07-F0 MDP_XRT_ROI_SET
0132 BC (cd 08 80 80 20 08)
0133 +. DC 07-F0 MDP_XRT_ROI_SET
0134 BC (cd 09 80 80 08 20)
0135 +. DC 07-F0 MDP_XRT_ROI_SET
0136 BC (cd 0a 85 83 08 08)
0137 +. DC 07-F0 MDP_XRT_ROI_SET
0138 BC (cd 0f 80 80 06 06)
0139 +. DC 07-F0 MDP_XRT_ROI_SET
0140 BC (cd 10 80 80 08 08)
0141 +. DC 07-F0 MDP_XRT_FLD_ENA
0142 BC (d8)
0143 +. DC 07-F0 MDP_XRT_FLRCTRL_ENA
0144 BC (c8)
0145 +. DC 07-F0 MDP_XRT_AEC_RESET
0146 BC (d0)
0147 +. DC 07-F0 MDP_XRT_ARS_DIS
0148 BC (d5)
0149 +. DC 07-F0 MDP_XRT_FLD_RESET
0150 BC (da)
0151 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0152 BC (c4 0e)
0153 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0154 BC (c5 0a)
0155 . C. ----- Success Verify ? OK / NG ____
0156 C.
0157 C.
0158 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0159 C.
0160 +. DC 07-F0 MDP_XRT_MODE_OBSV
0161 BC (c2)
0162 +. TI 2014-01-11 10:10:02.0
0163 DC 07-F0 MDP_XRT_MODE_OBSV
0164 BC (c2)
0165 . C. ----- Success Verify ? OK / NG ____
0166 C.
0167 C. ***** XRT END *****
0168 C.
0169 . C. ***** MDP `ûÃîôî»ô%ÿòÈÃð¹òèDCBC•x²è *****
0170 C. (%ã°îÿÓÿÃÿÈÿËÿËÿáÿçÿèòÈ%¼ò¼Ã»Û¹òè)
0171 . S. DC-BC dcbc-402:DCBC
0172 (MDP_known_event)
0173 C.
0174 C.
0175 . C. ***** ÿÐÿ!•Ï Daily±;îÑòÈ`Ø¹òèDCBC•x²è *****
0176 . S. DC-BC dcbc-153:DCBC
0177 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0178 C.
0179 C.
0180 . C. ;ãLOSÿÁÿSÿÿÿÿ¼Ã»Û;ã
0181 C.
0182 . C. ***** LOS *****
0183 C.

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*** OP Sequence for XRT ***

2014/01/11	10:20:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/11	10:20:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/01/11	10:21:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2014/01/11	10:21:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/01/11	10:21:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/01/11	10:21:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/01/11	10:21:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/01/11	10:21:24.0	XRT_FLD_RESET_431_OG [0x1af]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/11	10:23:56.0	XRT_QT_PROG_SET_407_OG [0x197]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2014/01/11	10:23:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2014/01/11	10:24:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/11	15:11:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/11	15:11:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/11	15:11:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/11	15:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/11	15:34:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/11	15:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/11	16:47:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/11	16:47:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/11	16:47:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/11	16:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/11	17:10:30.0	XRT_Custom_430_OG [0x1ae]			
2014/01/11	17:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/11	18:24:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/11	18:24:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/11	18:24:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/11	18:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/11	18:47:30.0	XRT_Custom_430_OG [0x1ae]			
2014/01/11	18:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/11	20:01:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/11	20:01:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/11	20:01:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/11	20:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/11	20:24:30.0	XRT_Custom_430_OG [0x1ae]			
2014/01/11	20:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/11	21:38:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/11	21:38:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/11	21:38:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/11	21:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/11	22:01:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/11	22:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/11	23:16:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/11	23:16:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/11	23:16:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/11	23:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/11	23:30:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/11	23:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/12	00:45:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/12	00:45:32.0	XRT_FLD_RESET_415_OG [0x19f]			

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2014/01/12	00:45:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/01/12	00:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/01/12	00:59:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/01/12	01:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/01/12	02:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/01/12	02:15:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/01/12	02:15:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/01/12	02:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/01/12	02:35:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/01/12	02:36:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/01/12	03:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/01/12	03:43:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/01/12	03:43:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/01/12	03:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/01/12	04:13:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/01/12	04:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/01/12	05:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/01/12	05:23:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/01/12	05:23:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/01/12	05:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/01/12	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/01/12	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/01/12	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2014/01/12	06:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/01/12	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/01/12	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/01/12	06:02:58.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2014/01/12	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/01/12	06:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/01/12	06:19:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2014/01/12	06:20:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00	
2014/01/12	06:20:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/01/12	06:20:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/01/12	06:20:20.0	XRT_AEC_RESET_448_OG [0x1c0]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/01/12	06:22:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/01/12	06:22:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/01/12	06:22:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14	
2014/01/12	06:22:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2014/01/12	06:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/01/12	07:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/01/12	07:04:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/01/12	07:04:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/01/12	07:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/01/12	07:28:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/01/12	07:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/01/12	08:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/01/12	08:44:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/01/12	08:44:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	

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2014/01/12	08:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/12	09:04:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/12	09:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/12	10:26:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/12	10:26:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/12	10:26:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/12	10:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/12	10:32:30.0	XRT_Custom_430_OG [0x1ae]			
2014/01/12	10:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/12	14:13:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/12	14:13:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/12	14:13:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/12	14:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/12	14:21:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/12	14:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/12	15:46:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/12	15:46:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/12	15:46:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/12	15:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/12	16:10:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/12	16:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/12	17:23:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/12	17:23:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/12	17:23:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/12	17:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/12	17:47:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/12	17:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/12	19:00:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/12	19:00:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/12	19:00:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/12	19:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/12	19:23:30.0	XRT_Custom_430_OG [0x1ae]			
2014/01/12	19:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/12	20:37:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/12	20:37:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/12	20:37:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/12	20:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/12	21:01:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/12	21:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/12	22:15:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/12	22:15:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/12	22:15:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/12	22:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/12	22:36:30.0	XRT_Custom_430_OG [0x1ae]			
2014/01/12	22:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/12	23:52:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/12	23:52:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/12	23:52:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/12	23:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/13	00:01:30.0	XRT_Custom_430_OG [0x1ae]			
2014/01/13	00:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2014/01/13	01:17:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/13	01:17:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/13	01:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/01/13	01:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/01/13	01:35:00.0	XRT_Custom_430_OG [0x1ae]							
2014/01/13	01:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/13	02:51:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/13	02:51:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/13	02:51:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/01/13	02:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/01/13	03:12:00.0	XRT_Custom_430_OG [0x1ae]							
2014/01/13	03:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/13	04:21:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/13	04:21:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/13	04:21:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/01/13	04:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/01/13	04:49:30.0	XRT_Custom_430_OG [0x1ae]							
2014/01/13	04:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/13	05:33:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/13	05:33:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/01/13	05:33:30.5	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/01/13	05:33:46.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/01/13	05:36:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/01/13	05:36:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/01/13	05:36:28.0	XRT_QT_PROG_SET_414_OG [0x19e]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2014/01/13	05:36:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/13	05:53:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/13	05:53:26.0	XRT_FOCUS_RECALIBRATE_433_OG [0x1b1]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2014/01/13	05:53:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2014/01/13	05:57:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/01/13	05:57:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/01/13	05:57:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/01/13	05:57:50.0	XRT_AEC_RESET_420_OG [0x1a4]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/01/13	05:57:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/01/13	05:57:54.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/13	05:57:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2014/01/13	05:57:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/01/13	06:01:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/13	06:01:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/13	06:01:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/01/13	06:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/01/13	06:27:00.0	XRT_Custom_430_OG [0x1ae]							
2014/01/13	06:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/13	07:41:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/13	07:41:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/13	07:41:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/01/13	07:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/01/13	08:04:00.0	XRT_Custom_430_OG [0x1ae]							
2014/01/13	08:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2014/01/13	09:21:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/13	09:21:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/13	09:21:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/01/13	09:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/01/13	09:39:00.0	XRT_Custom_430_OG [0x1ae]						
2014/01/13	09:40:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/13	14:47:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/13	14:47:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/13	14:47:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/01/13	14:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/01/13	14:59:30.0	XRT_Custom_430_OG [0x1ae]						
2014/01/13	15:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/13	16:22:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/13	16:22:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/13	16:22:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/01/13	16:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/01/13	16:46:30.0	XRT_Custom_430_OG [0x1ae]						
2014/01/13	16:47:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/13	16:59:54.0	XRT_CTRL_MANU_402_OG [0x192]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/13	16:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2014/01/13	17:00:16.0	XRT_FLD_ENA_411_OG [0x19b]						
		MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/01/13	17:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/01/13	17:00:20.0	XRT_AEC_RESET_413_OG [0x19d]						
		MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/01/13	17:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]						
		MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/01/13	17:00:24.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/13	17:00:26.0	XRT_QT_PROG_SET_407_OG [0x197]						
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14			
2014/01/13	17:00:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]						
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02			
2014/01/13	17:00:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/13	17:59:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/13	17:59:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/13	17:59:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/01/13	18:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/01/13	18:23:00.0	XRT_Custom_430_OG [0x1ae]						
2014/01/13	18:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/13	19:36:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/13	19:36:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/13	19:36:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/01/13	19:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/01/13	20:00:00.0	XRT_Custom_430_OG [0x1ae]						
2014/01/13	20:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/13	21:14:00.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/13	21:14:02.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/13	21:14:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/01/13	21:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
		MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/01/13	21:37:00.0	XRT_Custom_430_OG [0x1ae]						
2014/01/13	21:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
		MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/13	22:51:30.0	XRT_CTRL_MANU_400_OG [0x190]						
		MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/13	22:51:32.0	XRT_FLD_RESET_415_OG [0x19f]						
		MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/13	22:51:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
		MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/01/13	22:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						

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2014/01/13	23:11:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/01/13	23:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2014/01/14	00:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/01/14	00:28:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/01/14	00:28:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/01/14	00:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/01/14	00:35:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/01/14	00:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2014/01/14	01:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/01/14	01:51:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/01/14	01:51:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/01/14	01:51:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/01/14	01:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/01/14	02:11:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/01/14	02:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2014/01/14	03:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/01/14	03:24:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/01/14	03:24:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/01/14	03:24:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/01/14	03:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/01/14	03:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/01/14	03:48:30.0	XRT_Custom_430_OG [0x1ae]						
2014/01/14	03:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2014/01/14	04:58:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/01/14	04:58:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/01/14	04:58:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/01/14	04:58:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/01/14	04:58:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/01/14	05:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/01/14	05:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/01/14	05:26:00.0	XRT_Custom_430_OG [0x1ae]						
2014/01/14	05:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
2014/01/14	05:54:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/01/14	05:54:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/01/14	05:54:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2014/01/14	05:54:30.0	AOCS_Orе-point_Start_2_OG [0x098]						
2014/01/14	05:54:46.0	XRT_FLD_DIS_434_OG [0x1b2]	AOCU_NM	5	02-76	00 00 00 00 00		
2014/01/14	05:57:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2014/01/14	05:57:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2014/01/14	05:57:28.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/01/14	05:57:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04		
2014/01/14	06:14:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/01/14	06:14:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/01/14	06:14:30.0	AOCS_Orе-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/01/14	06:14:46.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 0f 4b ac fe		
2014/01/14	06:14:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/01/14	06:14:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/01/14	06:14:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/01/14	06:14:54.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/01/14	06:17:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/01/14	06:17:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14		
2014/01/14	06:17:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
2014/01/14	06:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/01/14	06:38:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/01/14	06:38:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/01/14	06:38:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/01/14	06:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						

2014/01/14	07:03:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/14	07:04:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2014/01/14	08:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/14	08:18:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/14	08:18:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/14	08:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/14	08:40:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/14	08:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2014/01/14	09:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/14	09:59:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/14	09:59:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/14	09:59:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	da
2014/01/14	10:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/14	10:12:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/14	10:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2014/01/14	15:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/14	15:22:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/14	15:22:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/14	15:22:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	da
2014/01/14	15:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/14	15:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/14	15:45:30.0	XRT_Custom_430_OG [0x1ae]				
2014/01/14	15:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2014/01/14	16:58:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/14	16:58:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/14	16:58:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/14	16:58:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	da
2014/01/14	17:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/14	17:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/14	17:22:30.0	XRT_Custom_430_OG [0x1ae]				
2014/01/14	17:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2014/01/14	18:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/14	18:36:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/14	18:36:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/14	18:36:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	da
2014/01/14	18:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/14	18:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/14	18:59:00.0	XRT_Custom_430_OG [0x1ae]				
2014/01/14	19:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2014/01/14	20:13:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/14	20:13:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/14	20:13:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/14	20:13:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	da
2014/01/14	20:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/14	20:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/14	20:36:30.0	XRT_Custom_430_OG [0x1ae]				
2014/01/14	20:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2014/01/14	21:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/14	21:50:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/14	21:50:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/14	21:50:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	da
2014/01/14	21:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/14	21:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/14	22:12:30.0	XRT_Custom_430_OG [0x1ae]				
2014/01/14	22:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2014/01/14	23:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/14	23:28:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/14	23:28:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/14	23:28:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	da
2014/01/14	23:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/14	23:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/14	23:39:30.0	XRT_Custom_430_OG [0x1ae]				
2014/01/14	23:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2014/01/15	00:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0

2014/01/15	00:55:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/15	00:55:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/15	00:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/01/15	01:11:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/01/15	01:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/15	02:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/15	02:27:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/15	02:27:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/01/15	02:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/01/15	02:47:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/15	02:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/15	03:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/15	03:55:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/15	03:55:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/01/15	03:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/01/15	04:25:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/15	04:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/15	05:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/15	05:36:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/15	05:36:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/01/15	05:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/01/15	06:02:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/15	06:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/15	06:11:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/15	06:11:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2014/01/15	06:12:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2014/01/15	06:12:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2014/01/15	06:14:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2014/01/15	06:14:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/01/15	06:14:58.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04			
2014/01/15	06:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/15	06:31:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/15	06:31:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2014/01/15	06:32:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 0f 4b ac fe			
2014/01/15	06:32:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/01/15	06:32:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/01/15	06:32:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/01/15	06:32:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/01/15	06:32:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/15	06:34:56.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14			
2014/01/15	06:34:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02			
2014/01/15	06:35:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/15	07:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/15	07:16:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/15	07:16:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/01/15	07:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/01/15	07:39:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/15	07:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/15	08:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			

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2014/01/15	08:56:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/15	08:56:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/15	08:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/15	09:15:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/15	09:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/15	10:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/15	10:38:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/15	10:38:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/15	10:41:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/15	10:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/15	10:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/15	14:24:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/15	14:24:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/15	14:24:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/15	14:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/15	14:33:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/15	14:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/15	15:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/15	15:58:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/15	15:58:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/15	16:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/15	16:22:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/15	16:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/15	17:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/15	17:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/15	17:35:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/15	17:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/15	17:58:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/15	17:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/15	19:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/15	19:12:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/15	19:12:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/15	19:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/15	19:35:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/15	19:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/15	20:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/15	20:49:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/15	20:49:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/15	20:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/15	21:12:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/15	21:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/15	22:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/15	22:27:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/15	22:27:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/15	22:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/15	22:48:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/15	22:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/16	00:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/16	00:04:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/16	00:04:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2014/01/16	00:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/01/16	00:12:00.0	XRT_Custom_430_OG [0x1ae]							
2014/01/16	00:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/16	01:28:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/16	01:28:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/16	01:28:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/01/16	01:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/01/16	01:47:00.0	XRT_Custom_430_OG [0x1ae]							
2014/01/16	01:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/16	03:03:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/16	03:03:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/16	03:03:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/01/16	03:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/01/16	03:23:30.5	XRT_Custom_430_OG [0x1ae]							
2014/01/16	03:24:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/16	04:33:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/16	04:33:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/16	04:33:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/01/16	04:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/01/16	05:01:00.5	XRT_Custom_430_OG [0x1ae]							
2014/01/16	05:02:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/16	05:45:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/16	05:45:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/01/16	05:45:30.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/01/16	05:45:46.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/01/16	05:48:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/01/16	05:48:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/01/16	05:48:28.0	XRT_QT_PROG_SET_414_OG [0x19e]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2014/01/16	05:48:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/16	06:05:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/16	06:05:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/01/16	06:05:30.0	AOCS_OrE-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2014/01/16	06:05:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/01/16	06:05:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/01/16	06:05:50.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/01/16	06:05:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/01/16	06:05:54.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/16	06:08:26.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2014/01/16	06:08:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/01/16	06:13:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/16	06:13:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/16	06:13:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/01/16	06:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/01/16	06:38:30.0	XRT_Custom_430_OG [0x1ae]							
2014/01/16	06:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/16	07:53:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/16	07:53:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/16	07:53:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/01/16	07:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				

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2014/01/16	08:15:30.0	XRT_Custom_430_OG [0x1ae]			
2014/01/16	08:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/16	09:33:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/16	09:33:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/16	09:33:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/16	09:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/16	09:50:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/16	09:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/16	14:58:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/16	14:58:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/16	14:58:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/16	15:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/16	15:21:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/16	15:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/16	16:34:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/16	16:34:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/16	16:34:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/16	16:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/16	16:58:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/16	16:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/16	18:11:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/16	18:11:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/16	18:11:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/16	18:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/16	18:34:30.5	XRT_Custom_430_OG [0x1ae]			
2014/01/16	18:35:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/16	19:48:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/16	19:48:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/16	19:48:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/16	19:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/16	20:12:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/16	20:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/16	21:25:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/16	21:25:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/16	21:25:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/16	21:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/16	21:48:30.0	XRT_Custom_430_OG [0x1ae]			
2014/01/16	21:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/16	23:03:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/16	23:03:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/16	23:03:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/16	23:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/16	23:20:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/16	23:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/17	00:37:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/17	00:37:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/17	00:37:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/17	00:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/17	00:47:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/17	00:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/17	02:02:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

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2014/01/17	02:02:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/17	02:02:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/01/17	02:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/01/17	02:23:00.0	XRT_Custom_430_OG [0x1ae]							
2014/01/17	02:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/17	03:32:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/17	03:32:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/17	03:32:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/01/17	03:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/01/17	04:00:00.0	XRT_Custom_430_OG [0x1ae]							
2014/01/17	04:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/17	05:10:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/17	05:10:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/17	05:10:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/01/17	05:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/01/17	05:37:30.0	XRT_Custom_430_OG [0x1ae]							
2014/01/17	05:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/17	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/17	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/01/17	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/01/17	06:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/01/17	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/01/17	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/01/17	06:02:58.0	XRT_QT_PROG_SET_414_OG [0x19e]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2014/01/17	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/17	06:19:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/17	06:19:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/01/17	06:20:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2014/01/17	06:20:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/01/17	06:20:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/01/17	06:20:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/01/17	06:20:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/01/17	06:20:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/17	06:22:56.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2014/01/17	06:22:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/01/17	06:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/17	06:51:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/17	06:51:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/17	06:51:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/01/17	06:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/01/17	07:15:00.0	XRT_Custom_430_OG [0x1ae]							
2014/01/17	07:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/17	08:30:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/17	08:30:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/17	08:30:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/01/17	08:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/01/17	08:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/17	08:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/01/17	09:00:00.5	AOCS_ORe-point_Start_5_OG [0x09b]							

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2014/01/17	09:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00	af	1b	01	58
		MDP_XRT_FLD_ENA		1	07-F0			d8		
2014/01/17	09:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0			c8		
		MDP_XRT_FLRCTRL_ENA		1	07-F0					
2014/01/17	09:00:20.0	XRT_AEC_RESET_413_OG [0x19d]		1	07-F0			d0		
		MDP_XRT_AEC_RESET		1	07-F0					
2014/01/17	09:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0			d5		
		MDP_XRT_ARS_DIS		1	07-F0					
2014/01/17	09:00:24.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0			da		
		MDP_XRT_FLD_RESET		1	07-F0					
2014/01/17	09:00:26.0	XRT_QT_PROG_SET_440_OG [0x1b8]		2	07-F0			c4	05	
		MDP_XRT_QT_PROG_SET		2	07-F0					
2014/01/17	09:02:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]		2	07-F0			c5	02	
		MDP_XRT_FL_PROG_SET		2	07-F0					
2014/01/17	09:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0			c0		
		MDP_XRT_CTRL_AUTO		1	07-F0					
2014/01/17	10:12:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0			c1		
		MDP_XRT_CTRL_MANU		1	07-F0					
2014/01/17	10:12:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0			da		
		MDP_XRT_FLD_RESET		1	07-F0					
2014/01/17	10:12:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0			e8		
		MDP_XRT_PREFLR_STRT		1	07-F0					
2014/01/17	10:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0			e9		
		MDP_XRT_PREFLR_STOP		1	07-F0					
2014/01/17	10:22:00.0	XRT_Custom_430_OG [0x1ae]								
2014/01/17	10:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0			c0		
		MDP_XRT_CTRL_AUTO		1	07-F0					
2014/01/17	14:02:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0			c1		
		MDP_XRT_CTRL_MANU		1	07-F0					
2014/01/17	14:02:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0			da		
		MDP_XRT_FLD_RESET		1	07-F0					
2014/01/17	14:02:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0			e8		
		MDP_XRT_PREFLR_STRT		1	07-F0					
2014/01/17	14:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0			e9		
		MDP_XRT_PREFLR_STOP		1	07-F0					
2014/01/17	14:06:30.0	XRT_Custom_430_OG [0x1ae]								
2014/01/17	14:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0			c0		
		MDP_XRT_CTRL_AUTO		1	07-F0					
2014/01/17	15:34:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0			c1		
		MDP_XRT_CTRL_MANU		1	07-F0					
2014/01/17	15:34:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0			da		
		MDP_XRT_FLD_RESET		1	07-F0					
2014/01/17	15:34:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0			e8		
		MDP_XRT_PREFLR_STRT		1	07-F0					
2014/01/17	15:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0			e9		
		MDP_XRT_PREFLR_STOP		1	07-F0					
2014/01/17	15:57:30.0	XRT_Custom_430_OG [0x1ae]								
2014/01/17	15:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0			c0		
		MDP_XRT_CTRL_AUTO		1	07-F0					
2014/01/17	16:28:24.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0			c1		
		MDP_XRT_CTRL_MANU		1	07-F0					
2014/01/17	16:28:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2014/01/17	16:28:30.0	AOCS_OrE-point_Start_6_OG [0x09c]		5	02-76	00	55	ca	01	58
		AOCU_NM		5	02-76	00	55	ca	01	58
2014/01/17	16:28:46.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0			d8		
		MDP_XRT_FLD_ENA		1	07-F0					
2014/01/17	16:28:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0			c8		
		MDP_XRT_FLRCTRL_ENA		1	07-F0					
2014/01/17	16:28:50.0	XRT_AEC_RESET_413_OG [0x19d]		1	07-F0			d0		
		MDP_XRT_AEC_RESET		1	07-F0					
2014/01/17	16:28:52.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0			d5		
		MDP_XRT_ARS_DIS		1	07-F0					
2014/01/17	16:28:54.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0			da		
		MDP_XRT_FLD_RESET		1	07-F0					
2014/01/17	16:28:56.0	XRT_QT_PROG_SET_406_OG [0x196]		2	07-F0			c4	0d	
		MDP_XRT_QT_PROG_SET		2	07-F0					
2014/01/17	16:31:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]		2	07-F0			c5	02	
		MDP_XRT_FL_PROG_SET		2	07-F0					
2014/01/17	16:31:30.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0			c0		
		MDP_XRT_CTRL_AUTO		1	07-F0					
2014/01/17	17:10:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0			c1		
		MDP_XRT_CTRL_MANU		1	07-F0					
2014/01/17	17:10:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0			da		
		MDP_XRT_FLD_RESET		1	07-F0					
2014/01/17	17:10:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0			e8		
		MDP_XRT_PREFLR_STRT		1	07-F0					
2014/01/17	17:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0			e9		
		MDP_XRT_PREFLR_STOP		1	07-F0					
2014/01/17	17:34:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0			c1		
		MDP_XRT_CTRL_MANU		1	07-F0					
2014/01/17	17:34:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2014/01/17	17:35:00.0	AOCS_OrE-point_Start_7_OG [0x09d]		5	02-76	00	4c	e5	01	58
		AOCU_NM		5	02-76	00	4c	e5	01	58
2014/01/17	17:35:16.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0			d8		
		MDP_XRT_FLD_ENA		1	07-F0					
2014/01/17	17:35:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0			c8		
		MDP_XRT_FLRCTRL_ENA		1	07-F0					
2014/01/17	17:35:20.0	XRT_AEC_RESET_413_OG [0x19d]		1	07-F0			d0		
		MDP_XRT_AEC_RESET		1	07-F0					
2014/01/17	17:35:22.0	XRT_ARS_DIS_423_OG [0x1a7]								

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2014/01/17	17:35:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/17	17:35:26.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d		
2014/01/17	17:37:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02		
2014/01/17	17:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/17	18:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/17	18:04:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2014/01/17	18:05:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00	43	ff	01 58
2014/01/17	18:05:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/01/17	18:05:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/01/17	18:05:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/01/17	18:05:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/01/17	18:05:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/17	18:05:26.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d		
2014/01/17	18:07:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02		
2014/01/17	18:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/17	18:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/17	18:47:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/17	18:47:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/01/17	18:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/01/17	19:11:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/17	19:11:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2014/01/17	19:12:00.0	AOCS_ORe-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00	3b	1a	01 58
2014/01/17	19:12:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/01/17	19:12:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/01/17	19:12:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/01/17	19:12:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/01/17	19:12:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/17	19:12:26.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d		
2014/01/17	19:14:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02		
2014/01/17	19:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/17	19:41:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/17	19:41:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2014/01/17	19:42:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00	32	35	01 58
2014/01/17	19:42:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/01/17	19:42:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/01/17	19:42:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/01/17	19:42:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/01/17	19:42:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/17	19:42:26.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d		
2014/01/17	19:44:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02		
2014/01/17	19:45:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/17	20:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/17	20:24:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/17	20:24:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/01/17	20:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/01/17	20:48:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			

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2014/01/17	20:48:56.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2014/01/17	20:49:00.0	AOCs_OrE-point_Start_11_OG [0x0a1] AOCU_NM	5	02-76	00	29	58	01 58
2014/01/17	20:49:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0		d8		
2014/01/17	20:49:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2014/01/17	20:49:20.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0		d0		
2014/01/17	20:49:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5		
2014/01/17	20:49:24.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2014/01/17	20:49:26.0	XRT_QT_PROG_SET_406_OG [0x196] MDP_XRT_QT_PROG_SET	2	07-F0		c4	0d	
2014/01/17	20:51:58.0	XRT_FL_PROG_SET_417_OG [0x1a1] MDP_XRT_FL_PROG_SET	2	07-F0		c5	02	
2014/01/17	20:52:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2014/01/17	21:18:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2014/01/17	21:18:56.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2014/01/17	21:19:00.0	AOCs_OrE-point_Start_12_OG [0x0a2] AOCU_NM	5	02-76	00	20	72	01 58
2014/01/17	21:19:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0		d8		
2014/01/17	21:19:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2014/01/17	21:19:20.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0		d0		
2014/01/17	21:19:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5		
2014/01/17	21:19:24.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2014/01/17	21:19:26.0	XRT_QT_PROG_SET_406_OG [0x196] MDP_XRT_QT_PROG_SET	2	07-F0		c4	0d	
2014/01/17	21:21:58.0	XRT_FL_PROG_SET_417_OG [0x1a1] MDP_XRT_FL_PROG_SET	2	07-F0		c5	02	
2014/01/17	21:22:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2014/01/17	22:02:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2014/01/17	22:02:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2014/01/17	22:02:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2014/01/17	22:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2014/01/17	22:24:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2014/01/17	22:24:56.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2014/01/17	22:25:00.0	AOCs_OrE-point_Start_13_OG [0x0a3] AOCU_NM	5	02-76	00	17	8d	01 58
2014/01/17	22:25:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0		d8		
2014/01/17	22:25:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2014/01/17	22:25:20.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0		d0		
2014/01/17	22:25:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5		
2014/01/17	22:25:24.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2014/01/17	22:25:26.0	XRT_QT_PROG_SET_406_OG [0x196] MDP_XRT_QT_PROG_SET	2	07-F0		c4	0d	
2014/01/17	22:27:58.0	XRT_FL_PROG_SET_417_OG [0x1a1] MDP_XRT_FL_PROG_SET	2	07-F0		c5	02	
2014/01/17	22:28:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2014/01/17	22:54:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2014/01/17	22:54:56.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2014/01/17	22:55:00.0	AOCs_OrE-point_Start_14_OG [0x0a4] AOCU_NM	5	02-76	00	0e	a7	01 58
2014/01/17	22:55:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0		d8		
2014/01/17	22:55:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2014/01/17	22:55:20.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0		d0		
2014/01/17	22:55:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5		
2014/01/17	22:55:24.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2014/01/17	22:55:26.0	XRT_QT_PROG_SET_406_OG [0x196] MDP_XRT_QT_PROG_SET	2	07-F0		c4	0d	
2014/01/17	22:57:58.0	XRT_FL_PROG_SET_417_OG [0x1a1] MDP_XRT_FL_PROG_SET	2	07-F0		c5	02	
2014/01/17	22:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]						

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2014/01/17	23:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/17	23:39:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/17	23:39:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/01/17	23:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/01/17	23:50:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/17	23:50:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2014/01/17	23:51:00.0	AOCS_OrE-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00 05 ca 01 58			
2014/01/17	23:51:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/01/17	23:51:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/01/17	23:51:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/01/17	23:51:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/01/17	23:51:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/17	23:51:26.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d			
2014/01/17	23:53:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02			
2014/01/17	23:54:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/18	00:20:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/18	00:20:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2014/01/18	00:21:00.0	AOCS_OrE-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	00 fd cb 01 58			
2014/01/18	00:21:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/01/18	00:21:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/01/18	00:21:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/01/18	00:21:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/01/18	00:21:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/18	00:21:26.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d			
2014/01/18	00:23:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02			
2014/01/18	00:24:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/18	01:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/18	01:05:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/18	01:05:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/01/18	01:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/01/18	01:23:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/18	01:23:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2014/01/18	01:23:30.0	AOCS_OrE-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	00 f4 e6 01 58			
2014/01/18	01:23:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/01/18	01:23:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/01/18	01:23:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/01/18	01:23:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/01/18	01:23:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/18	01:23:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d			
2014/01/18	01:26:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02			
2014/01/18	01:26:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/18	01:53:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/18	01:53:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2014/01/18	01:53:30.0	AOCS_OrE-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00 ec 00 01 58			
2014/01/18	01:53:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/01/18	01:53:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			

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2014/01/18	01:53:50.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/01/18	01:53:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/01/18	01:53:54.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/18	01:53:56.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2014/01/18	01:56:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2014/01/18	01:56:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/18	02:38:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/18	02:38:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/18	02:38:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/01/18	02:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/01/18	02:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/18	02:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/01/18	03:00:00.0	AOCS_OrE-point_Start_19_OG [0x0a9]							
		AOCU_NM	5	02-76	00	e3	1b	01	58
2014/01/18	03:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/01/18	03:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/01/18	03:00:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/01/18	03:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/01/18	03:00:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/18	03:00:26.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2014/01/18	03:02:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2014/01/18	03:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/18	03:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/18	03:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/01/18	03:30:00.0	AOCS_OrE-point_Start_20_OG [0x0aa]							
		AOCU_NM	5	02-76	00	da	36	01	58
2014/01/18	03:30:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/01/18	03:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/01/18	03:30:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/01/18	03:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/01/18	03:30:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/18	03:30:26.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2014/01/18	03:32:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2014/01/18	03:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/18	04:07:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/18	04:07:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/18	04:07:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/01/18	04:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/01/18	04:37:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/18	04:37:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/01/18	04:37:30.0	AOCS_OrE-point_Start_21_OG [0x0ab]							
		AOCU_NM	5	02-76	00	d1	59	01	58
2014/01/18	04:37:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/01/18	04:37:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/01/18	04:37:50.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/01/18	04:37:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/01/18	04:37:54.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/18	04:37:56.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2014/01/18	04:40:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2014/01/18	04:40:30.0	XRT_CTRL_AUTO_408_OG [0x198]							

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2014/01/18	05:07:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/18	05:07:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/01/18	05:07:30.0	AOCS_Ore-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	00	c8	73	01	58
2014/01/18	05:07:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/01/18	05:07:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/01/18	05:07:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/01/18	05:07:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/01/18	05:07:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/18	05:07:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2014/01/18	05:10:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2014/01/18	05:10:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/18	05:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/18	05:48:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/18	05:48:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/01/18	05:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/01/18	06:23:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/18	06:23:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/01/18	06:24:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00	00
2014/01/18	06:24:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/01/18	06:26:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/01/18	06:26:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/01/18	06:26:58.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2014/01/18	06:27:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/18	06:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/18	06:44:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/01/18	06:45:00.0	AOCS_Ore-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00	bf	8e	01	58
2014/01/18	06:45:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/01/18	06:45:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/01/18	06:45:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/01/18	06:45:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/01/18	06:45:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/18	06:45:26.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2014/01/18	06:47:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2014/01/18	06:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/18	07:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/18	07:28:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/18	07:28:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/01/18	07:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/01/18	07:51:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/18	07:51:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/01/18	07:52:00.0	AOCS_Ore-point_Start_24_OG [0x0ae]	AOCU_NM	5	02-76	00	b6	a8	01	58
2014/01/18	07:52:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/01/18	07:52:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/01/18	07:52:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/01/18	07:52:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/01/18	07:52:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				

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2014/01/18	07:52:26.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2014/01/18	07:54:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2014/01/18	07:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/18	08:21:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/18	08:21:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2014/01/18	08:22:00.0	AOCS_ORe-point_Start_25_OG [0x0af]							
		AOCU_NM	5	02-76	00	ad cb 01 58			
2014/01/18	08:22:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/01/18	08:22:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/01/18	08:22:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/01/18	08:22:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/01/18	08:22:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/18	08:22:26.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2014/01/18	08:24:58.5	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2014/01/18	08:25:00.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/01/18	09:08:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/01/18	09:08:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/01/18	09:08:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/01/18	09:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/01/18	10:15:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00 00 00 00			