

XRT Timeline to be uploaded on 2014/01/24

Period: 2014/01/24 09:24:00 - 2014/02/03 08:58:00

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Normal mode

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XOB #19A9: G-Band Alignment with North Pole Q90 2x2(G-band only) - 8msec - 5min cadence - Partial Sun-wNGT-2												
Term		Pointing (x, y)						Comment				
01/24 09:49:00 - 01/24 11:48:54		Fixed (-0.0, 945.0)						# OP start + 10min North limb coalignment				
PROG= 01 1-time(s)												
└ Subr= 1 1-time(s) 360.0sec												
└ Seqn= 93 24-time(s) 300.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x1536 (1024, 768)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19AA: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8msec- 8 min cadence-wNGT-2												
Term		Pointing (x, y)						Comment				
01/24 12:04:00 - 01/24 14:03:54		Fixed (-945.0, 0.0)						#East limb coalignment				
PROG= 03 1-time(s)												
└ Subr= 1 1-time(s) 360.0sec												
└ Seqn= 94 15-time(s) 480.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	1536x2048 (1280, 1024)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19EE: AR Standard-B(Morphology), Be/thin 48s-cad, multifilter PFB (Al/poly, Be/thin, Be/med), multifilter context, 384x384, 1064x1048, G-band (33msec)												
Term		Pointing (x, y)						Comment				
01/24 14:07:00 - 01/25 05:59:54		Track (-18.5, -323.2) @ 01/24 14:04:00						#AR 11959 obs.				
01/25 06:23:00 - 01/26 06:22:24		Track (116.9, -322.9) @ 01/25 06:20:00						# AR 11959 obs.				
01/26 06:45:30 - 01/26 23:59:54		Track (313.7, -326.2) @ 01/26 06:42:30						# AR 11959 obs.				
PROG= 14 Inf.-time(s)												
└ Subr= 1 1-time(s) 2.0sec												
└ Seqn= 8 1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└ Seqn= 24 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└ Subr= 2 2-time(s) 2.0sec												
└ Seqn= 16 2-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└ Seqn= 29 50-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19F1: Synoptic Q95 2x2 - Al/mesh(5/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/362/1443) + Thin												
Term		Pointing (x, y)						Comment				
01/25 06:03:00 - 01/25 06:19:54		Fixed (0.0, 0.0)						synoptic				
01/26 06:25:30 - 01/26 06:42:24		Fixed (0.0, 0.0)						synoptic, shifted 22.5 min				
01/30 07:08:00 - 01/30 07:24:54		Fixed (0.0, 0.0)						synoptic				
01/31 06:12:30 - 01/31 06:29:24		Fixed (0.0, 0.0)						synoptic, shifted 9.5 min				
02/01 05:46:00 - 02/01 06:02:54		Fixed (0.0, 0.0)						synoptic, shifted -17.0 min				
PROG= 04 1-time(s)												
└ Subr= 1 1-time(s) 12.0sec												
└ Seqn= 84 2-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└ Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└ Seqn= 86 2-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 95		2-time(s)		2.0sec								
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6		2-time(s)		2.0sec								
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19BA: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment
01/30 05:58:05 - 01/30 06:04:54	Fixed (-528.4, -528.4)	# Quadrant pointing (1/4)
PROG= 09 1-time(s)		
Subr= 1 1-time(s) 12.0sec		
Seqn= 1 1-time(s) 12.0sec		
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 23 2-time(s) 2.0sec		
Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 3 2-time(s) 2.0sec		
Seqn= 12 1-time(s) 2.0sec		
Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 7 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #19B9: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment
01/30 06:08:00 - 01/30 06:14:54	Fixed (528.4, -528.4)	#Quadrant pointing (2/4)
PROG= 15 1-time(s)		
Subr= 1 1-time(s) 12.0sec		
Seqn= 2 1-time(s) 12.0sec		
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 23 2-time(s) 2.0sec		
Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 3 2-time(s) 2.0sec		
Seqn= 12 1-time(s) 2.0sec		
Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 7 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #19B8: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment
01/30 06:18:00 - 01/30 06:24:54	Fixed (528.4, 528.4)	#Quadrant pointing (3/4)
PROG= 08 1-time(s)		
Subr= 1 1-time(s) 12.0sec		
Seqn= 3 1-time(s) 12.0sec		
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
Open/thick-Be	Open/thick-Be close	Safe Dark 44ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 23 2-time(s) 2.0sec		
Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 3 2-time(s) 2.0sec		
Seqn= 12 1-time(s) 2.0sec		
Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 7 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #19B7: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment
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PROG= 19 1-time(s)

- Subr= 1 1-time(s) 12.0sec
 - Seqn= 4 1-time(s) 12.0sec
 - Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 2048x1536 (1024, 768) Q=90 0 0 2.0sec
 - Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 2048x1536 (1024, 768) Q=90 0 0 2.0sec
 - Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 2048x1536 (1024, 768) Q=98 0 0 2.0sec
 - Open/thick-Be Open/thick-Be close Safe Dark 44ms Obs 1x1 2048x1536 (1024, 768) Q=98 0 0 2.0sec
- Subr= 2 1-time(s) 2.0sec
 - Seqn= 23 2-time(s) 2.0sec
 - Open/Al-mesh Open/Ti-poly close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
- Subr= 3 1-time(s) 2.0sec
 - Seqn= 12 1-time(s) 2.0sec
 - Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
 - Seqn= 7 1-time(s) 2.0sec
 - Open/G-band Open/G-band open Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #19EF: AR Standard-B(Morphology), Be/thin 24s-cad, multifilter PFB (Al/poly, Be/thin, Be/med), multifilter context, 384x384, 1064x1048, G-band (33ms)

Term	Pointing (x, y)	Comment
01/30 07:28:00 - 01/31 06:09:24	Fixed (840.0, -380.0)	# AR 11959 obs.
01/31 06:32:30 - 01/31 09:18:54	Fixed (840.0, -400.0)	#AR 11959 obs.
01/31 19:34:00 - 02/01 05:42:54	Fixed (840.0, -415.0)	#AR 11959 obs.
02/01 06:06:00 - 02/01 09:31:00	Fixed (840.0, -415.0)	# AR 11959 obs

PROG= 12 Inf.-time(s)

- Subr= 1 1-time(s) 2.0sec
 - Seqn= 8 1-time(s) 2.0sec
 - Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
 - Seqn= 24 1-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
 - Open/G-band Open/G-band open Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
- Subr= 2 2-time(s) 2.0sec
 - Seqn= 16 2-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - Al-poly/Open thin-Be/Open close Safe Norm 177ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - C-poly/Open C-poly/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - med-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - med-Al/Open Open/thick-Al close Safe Norm 2.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - Seqn= 52 100-time(s) 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 6.0sec
 - Al-poly/Open thin-Be/Open close Safe Norm 177ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 6.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 6.0sec
 - med-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 6.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #199F: HOP130-Synoptic 3 Filter- 2x2 Q95 (Almesh 12/128/723ms; Tipoly 24/362/1443ms; Althick 65536ms) with G-band

Term	Pointing (x, y)	Comment
01/31 09:54:00 - 01/31 10:35:30	Fixed (0.0, 0.0)	#HOP 130 (3/15)

PROG= 16 2-time(s)

- Subr= 1 1-time(s) 180.0sec
 - Seqn= 85 1-time(s) 25.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 86 1-time(s) 25.0sec
 - Open/Ti-poly Open/Ti-poly close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Ti-poly Open/Ti-poly close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 75 1-time(s) 2.0sec
 - Open/thick-Al Open/thick-Al close Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Seqn= 66 1-time(s) 2.0sec
 - Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
 - Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #19F4: Full-Sun Movie Q95 2x2 75min cadence - Al/mesh(5/128/723) + Ti-poly(24/362/1443) + Thin-Be(64/512/3897ms) + G-band + Leak + Darks

Term	Pointing (x, y)	Comment
01/31 14:03:00 - 01/31 19:00:00	Fixed (0.0, 0.0)	#EIS synop 006 for QS

PROG= 11 Inf.-time(s)

- Subr= 1 1-time(s) 12.0sec
 - Seqn= 5 1-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 6		1-time(s)	2.0sec									
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2		2-time(s)	4500.0sec									
Seqn= 84		1-time(s)	2.0sec									
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 86		1-time(s)	2.0sec									
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 95		1-time(s)	2.0sec									
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19F2: Synoptic 9 Filter w/ Triplet Exposures - 2x2 Q98 - Gband(8ms)-double images for focus mode

Term	Pointing (x, y)	Comment
01/31 19:07:06 - 01/31 19:09:30	Fixed (0.0, 0.0)	#EIS synop 006 for QS
PROG= 06		
Subr= 1		1-time(s)
Seqn= 84		2-time(s)
Open/Al-mesh	Open/Al-mesh	close
Open/Al-mesh	Open/Al-mesh	close
Open/Al-mesh	Open/Al-mesh	close
Seqn= 5		1-time(s)
Open/Ti-poly	Open/thick-Al	close
Open/Ti-poly	Open/thick-Al	close
Open/Ti-poly	Open/thick-Al	close
Open/Ti-poly	Open/thick-Al	close
Open/Ti-poly	Open/thick-Al	close
Seqn= 86		2-time(s)
Open/Ti-poly	Open/Ti-poly	close
Open/Ti-poly	Open/Ti-poly	close
Open/Ti-poly	Open/Ti-poly	close
Seqn= 95		2-time(s)
thin-Be/Open	thin-Be/Open	close
thin-Be/Open	thin-Be/Open	close
thin-Be/Open	thin-Be/Open	close
Seqn= 6		2-time(s)
Open/G-band	Open/G-band	open
Open/G-band	Open/G-band	close
Subr= 2		2-time(s)
Seqn= 56		1-time(s)
Al-poly/Open	Al-poly/Open	close
Al-poly/Open	Al-poly/Open	close
Seqn= 22		1-time(s)
C-poly/Open	C-poly/Open	close
C-poly/Open	C-poly/Open	close
Seqn= 43		1-time(s)
Al-poly/Ti-poly	Al-poly/thick-Al	close
Seqn= 38		1-time(s)
med-Al/Open	med-Al/Open	close
Seqn= 46		1-time(s)
Open/thick-Be	Open/thick-Be	close
Default Filter	Thicker Filter	VLS
mode	image	Exp.
CCD	Bin	ROI: size (center)
Comp.	AEC Buffer	Interval

* * * * *

Flare mode

* * * * *

XOB #19F0: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops-lowered-gband

Term	Pointing (x, y)	Comment
01/24 14:07:00 - 01/25 05:59:54	Track (-18.5, -323.2) @ 01/24 14:04:00	#AR 11959 obs.
01/25 06:23:00 - 01/26 06:22:24	Track (116.9, -322.9) @ 01/25 06:20:00	# AR 11959 obs.
01/26 06:45:30 - 01/26 23:59:54	Track (313.7, -326.2) @ 01/26 06:42:30	# AR 11959 obs.
01/30 07:28:00 - 01/31 06:09:24	Fixed (840.0, -380.0)	# AR 11959 obs.
01/31 06:32:30 - 01/31 09:18:54	Fixed (840.0, -400.0)	#AR 11959 obs.
01/31 14:03:00 - 01/31 19:00:00	Fixed (0.0, 0.0)	#EIS synop 006 for QS
01/31 19:34:00 - 02/01 05:42:54	Fixed (840.0, -415.0)	#AR 11959 obs.
02/01 06:06:00 - 02/01 09:31:00	Fixed (840.0, -415.0)	# AR 11959 obs
PROG= 02		
Subr= 1		45-time(s)
Seqn= 9		1-time(s)
thin-Be/Open	med-Be/Open	close
Seqn= 10		1-time(s)

med- <i>Al</i> /Open	med- <i>Al</i> /thick- <i>Al</i>	close	Safe	Norm	500ms	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick- <i>Be</i>	Open/thick- <i>Be</i>	close	Safe	Norm	2.00s	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11		1-time(s)	2.0sec								
<i>Al</i> -poly/Open	<i>Al</i> -poly/thick- <i>Al</i>	close	Safe	Norm	125ms	Obs 2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2		1-time(s)	10.0sec								
Seqn= 17		1-time(s)	2.0sec								
Open/ <i>G</i> -band	Open/ <i>G</i> -band	open	Safe	Norm	32ms	Obs 1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick- <i>Al</i>	Open/thick- <i>Al</i>	close	Safe	Dark	1.00s	Obs 1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick- <i>Al</i>	Open/thick- <i>Al</i>	close	Safe	Dark	1.00s	Obs 2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

* * * * *

Active Region Search

* * * * *

NOT USED

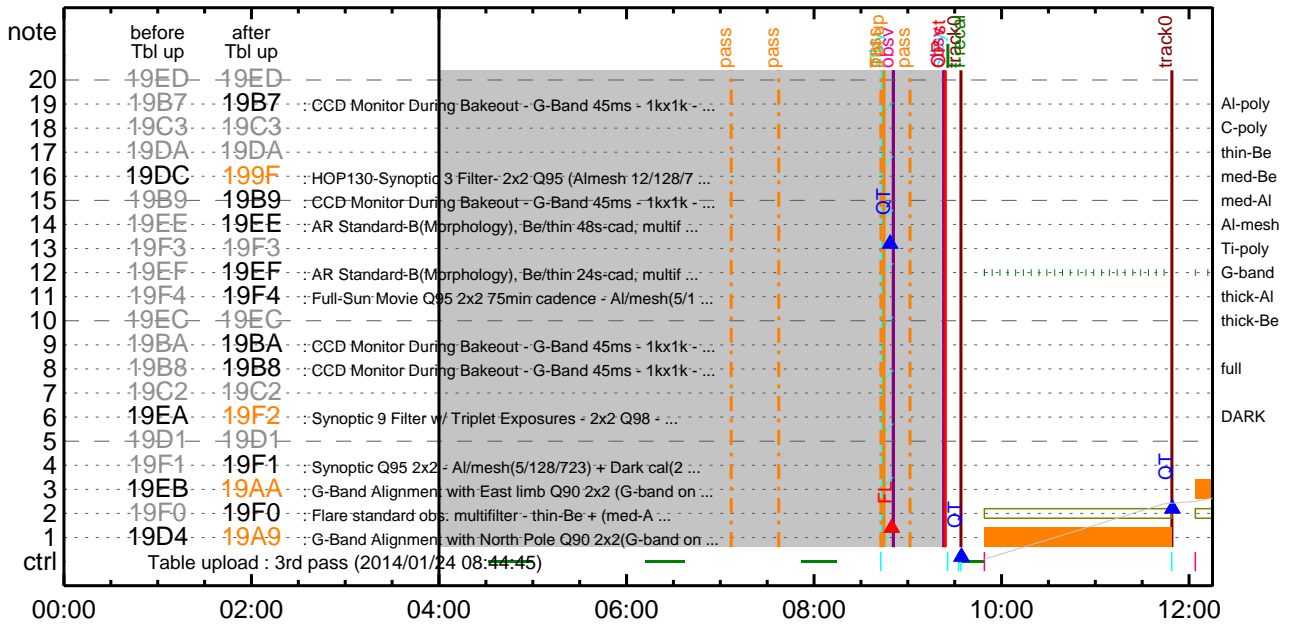
* * * * *

Flare Detection

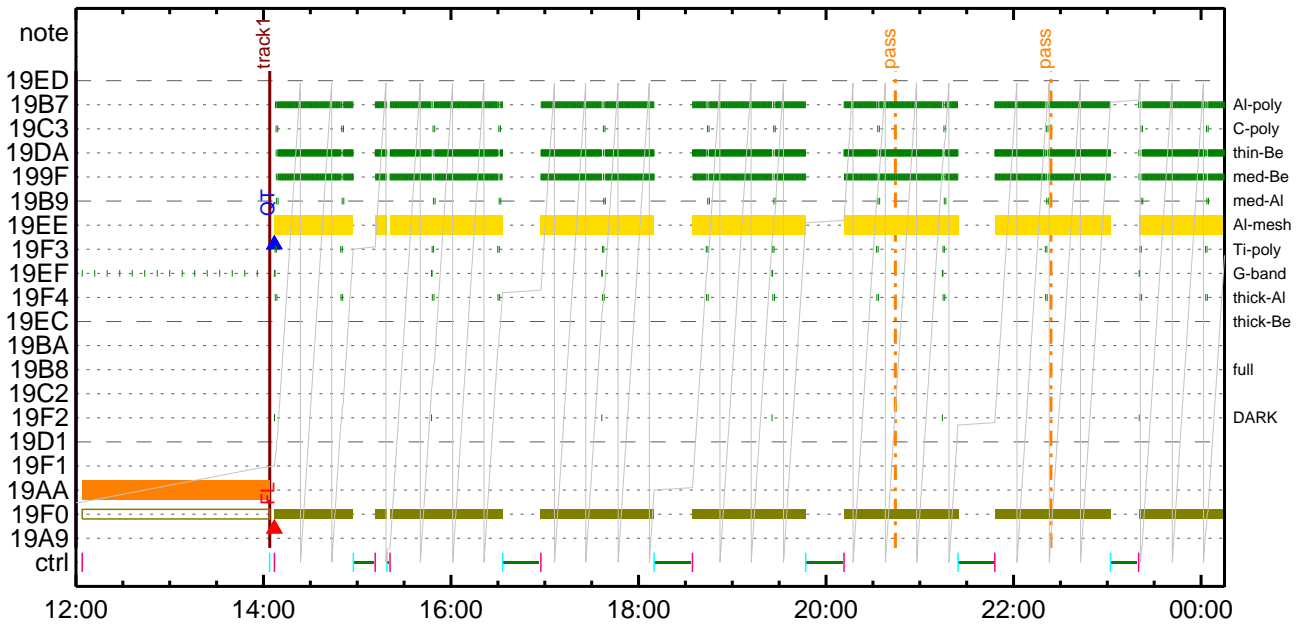
* * * * *

FLD Patrol											
Term	Pointing (x, y)						Comment				
01/24 14:04:16 - 01/25 06:00:16	Track (-18.5,	-323.2)	<small>Ⓢ 01/24 14:04:00</small>	#AR 11959 obs.						
01/25 06:20:16 - 01/26 06:22:46	Track (116.9,	-322.9)	<small>Ⓢ 01/25 06:20:00</small>	# AR 11959 obs.						
01/26 06:42:46 - 01/30 05:57:59	Track (313.7,	-326.2)	<small>Ⓢ 01/26 06:42:30</small>	# AR 11959 obs.						
01/30 07:25:16 - 01/31 06:09:46	Fixed (840.0,	-380.0)	# AR 11959 obs.							
01/31 06:29:46 - 01/31 09:51:16	Fixed (840.0,	-400.0)	#AR 11959 obs.							
01/31 14:00:16 - 01/31 19:04:22	Fixed (0.0,	0.0)	#EIS synop 006 for QS							
01/31 19:30:16 - 02/01 05:43:16	Fixed (840.0,	-415.0)	#AR 11959 obs.							
02/01 06:03:16 - 02/03 08:58:00	Fixed (840.0,	-415.0)	# AR 11959 obs							
Open/ <i>Ti</i> -poly	Open/thick- <i>Al</i>	close	Safe	Norm	8ms	Obs 8x8	Q=50	30sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

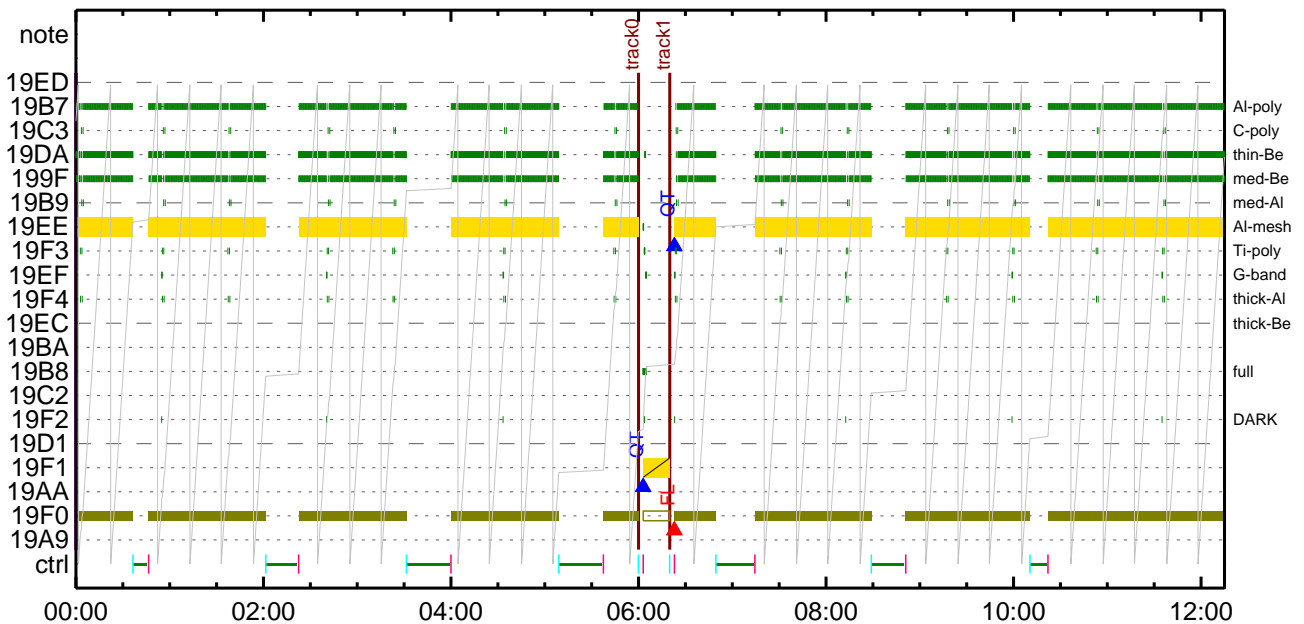
CMDI #0058 2014/01/24



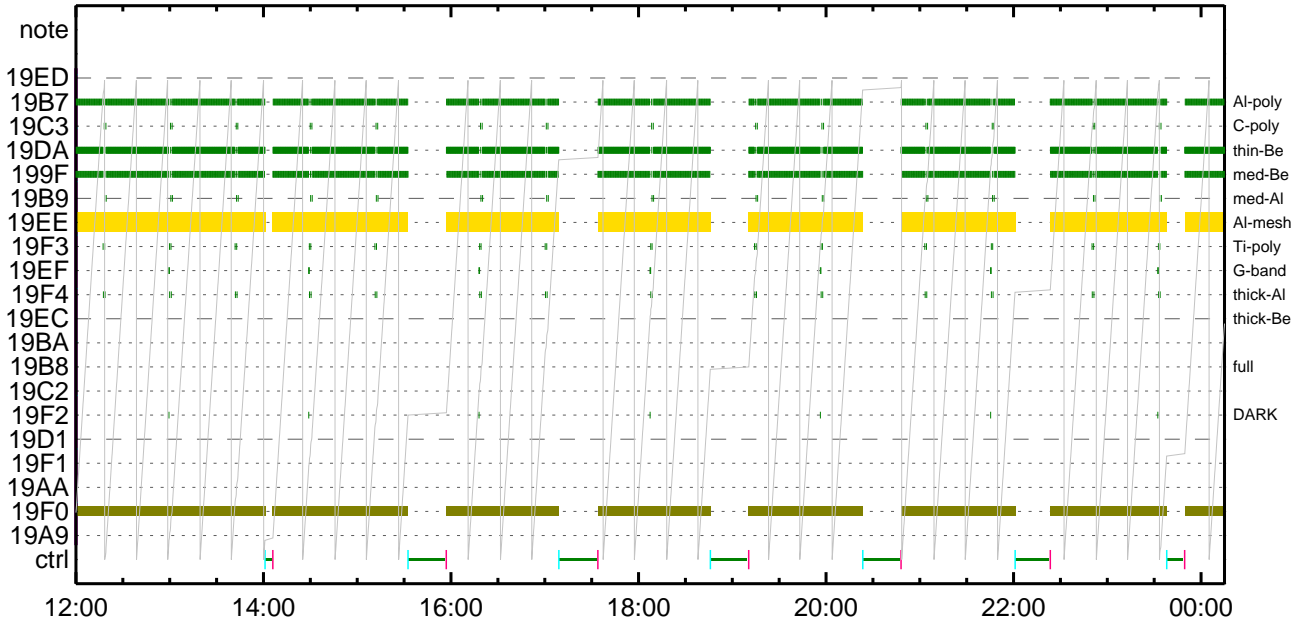
CMDI #0058 2014/01/24



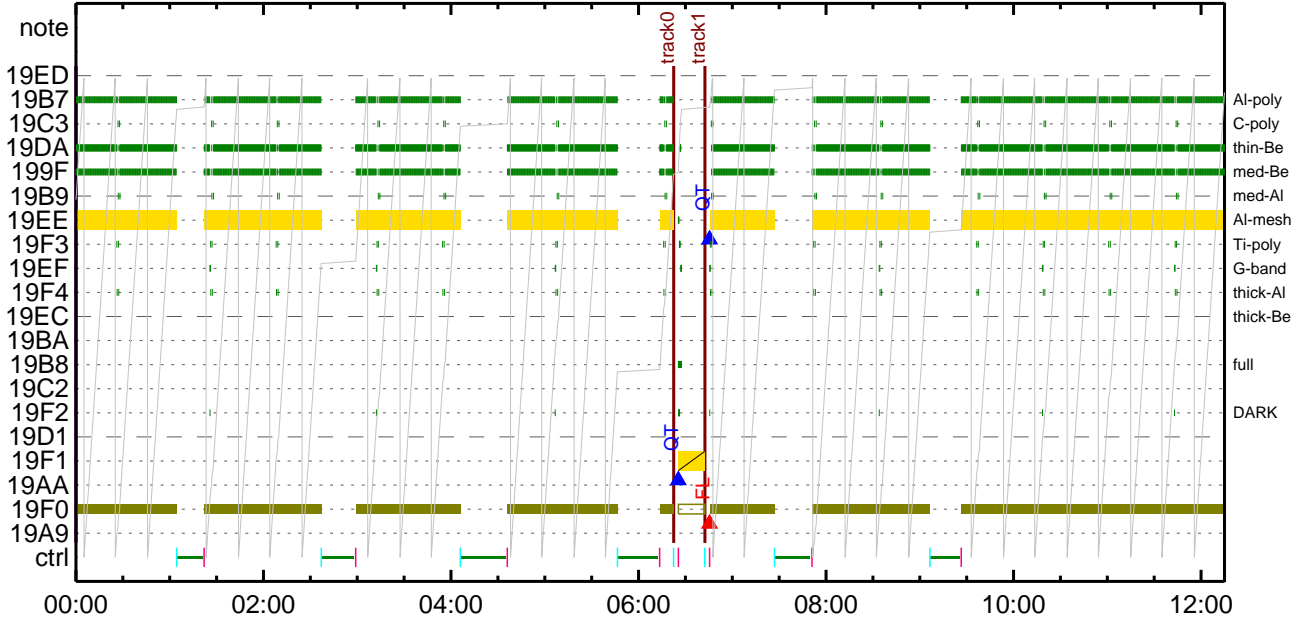
CMDI #0058 2014/01/25



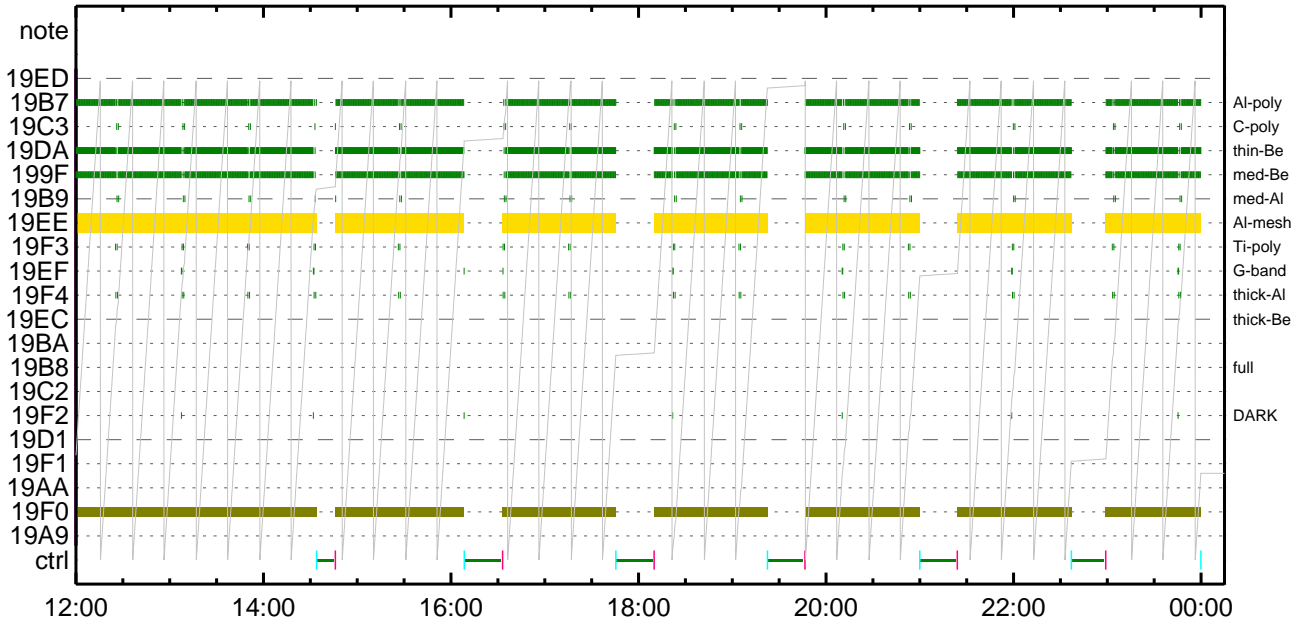
CMDI #0058 2014/01/25



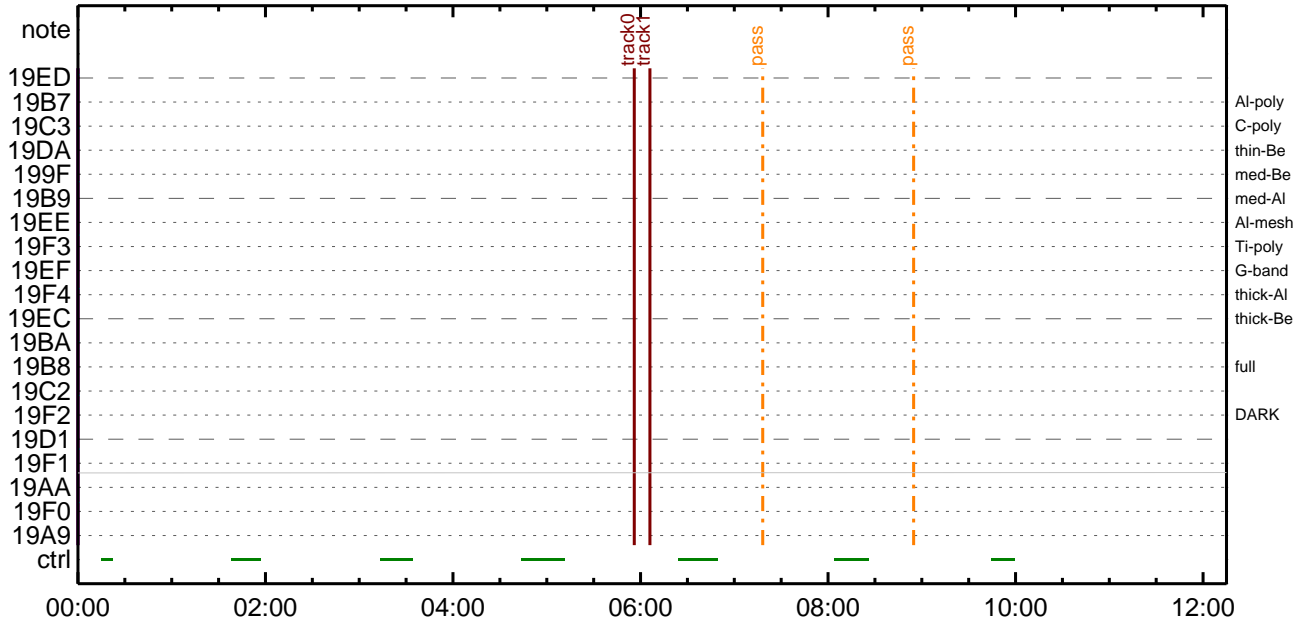
CMDI #0058 2014/01/26



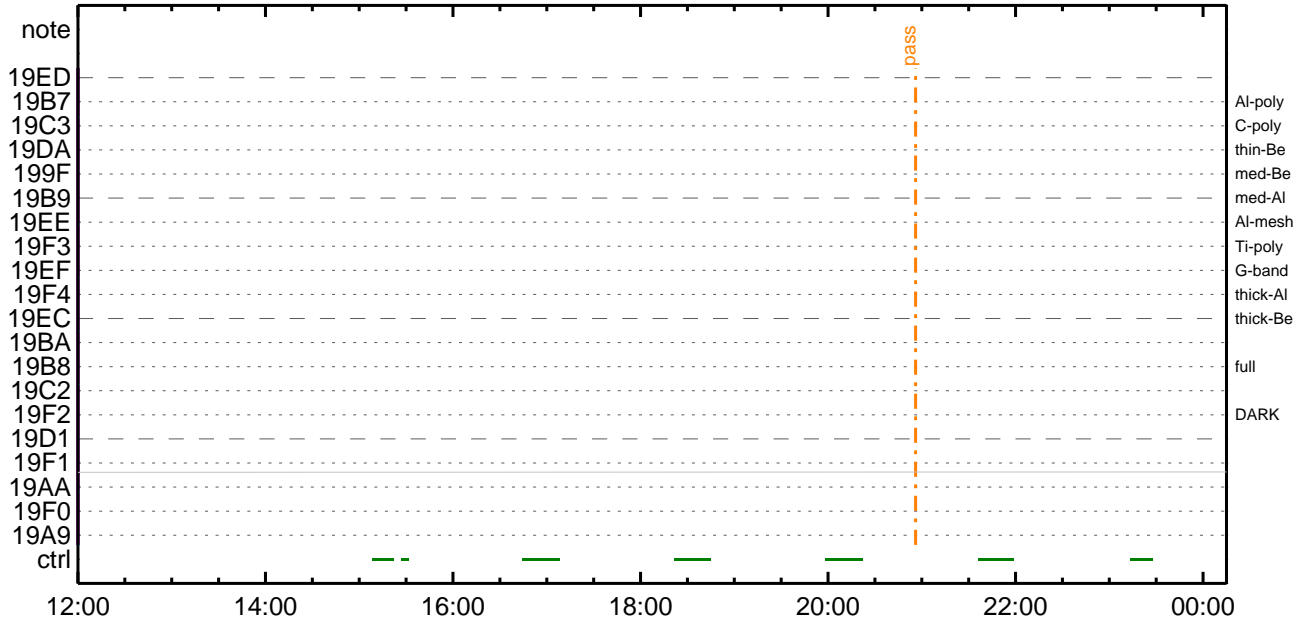
CMDI #0058 2014/01/26



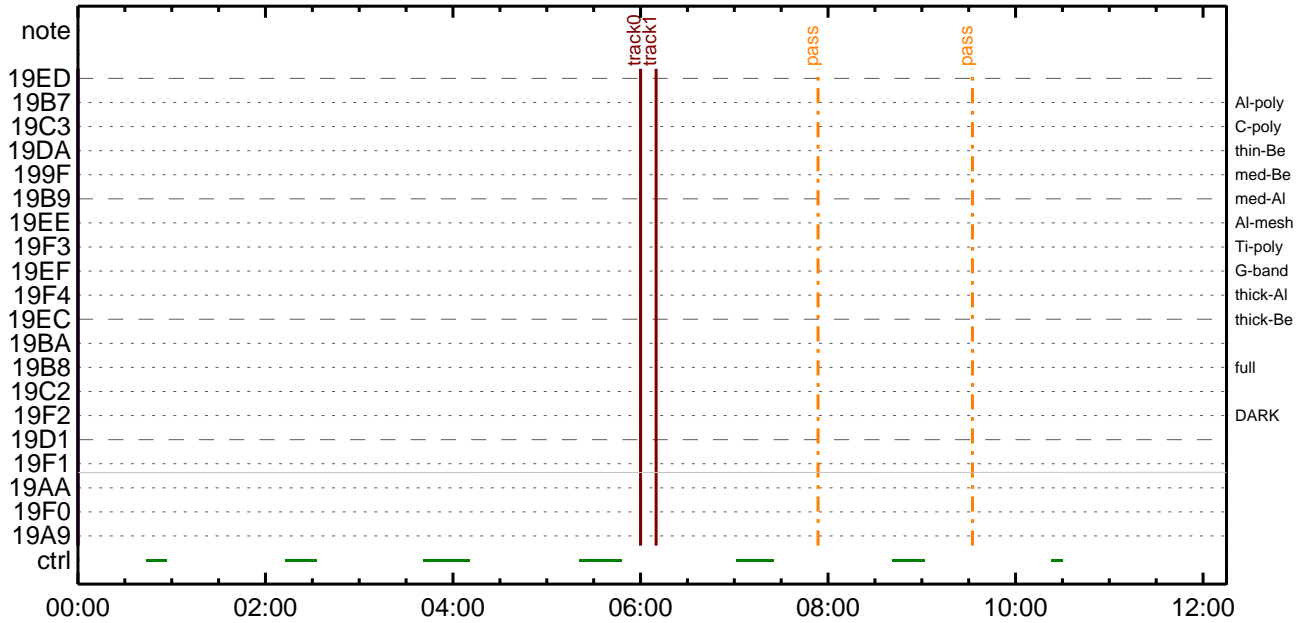
CMDI #0058 2014/01/27



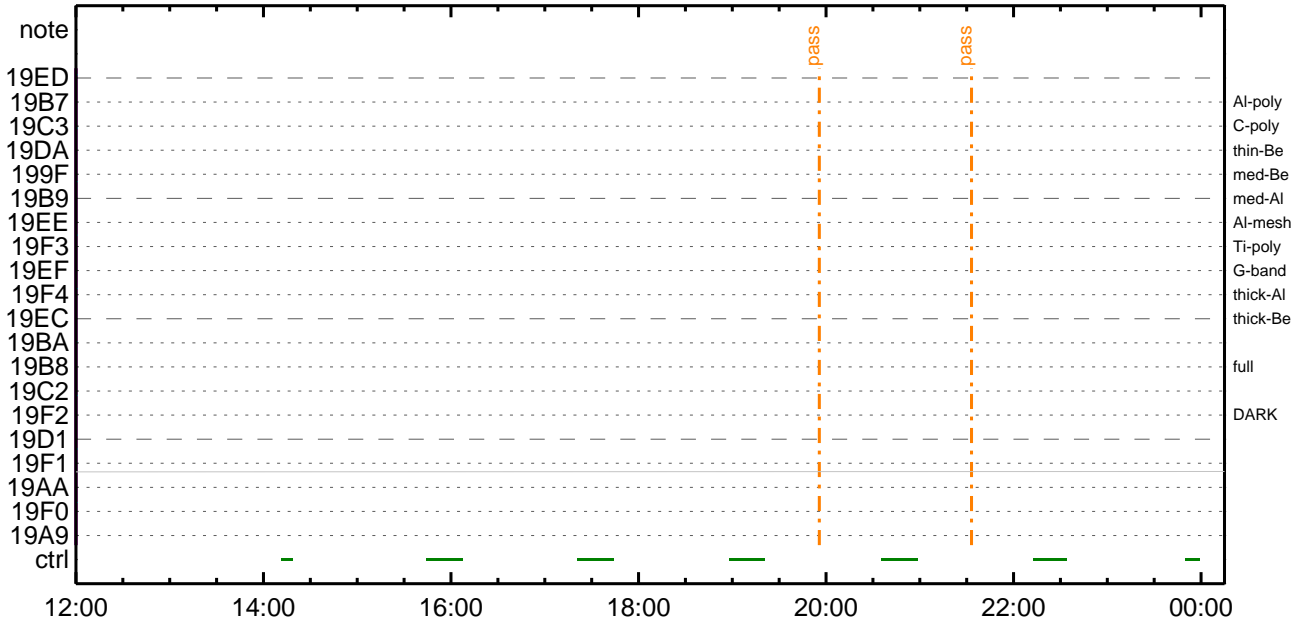
CMDI #0058 2014/01/27



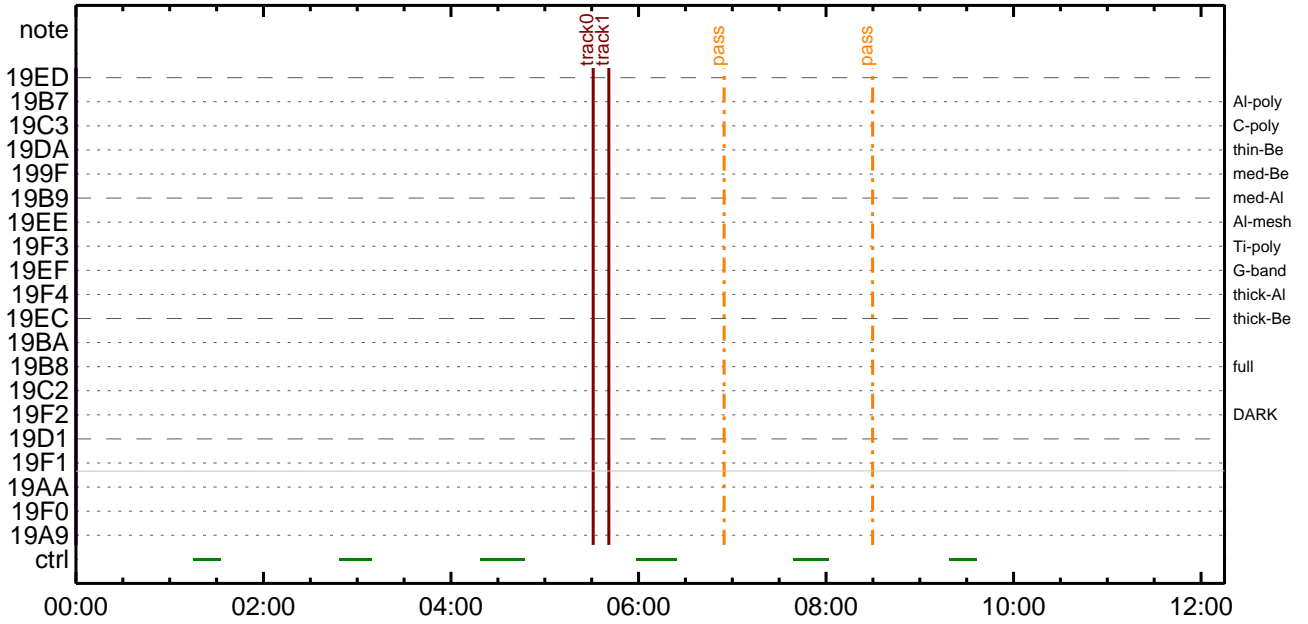
CMDI #0058 2014/01/28



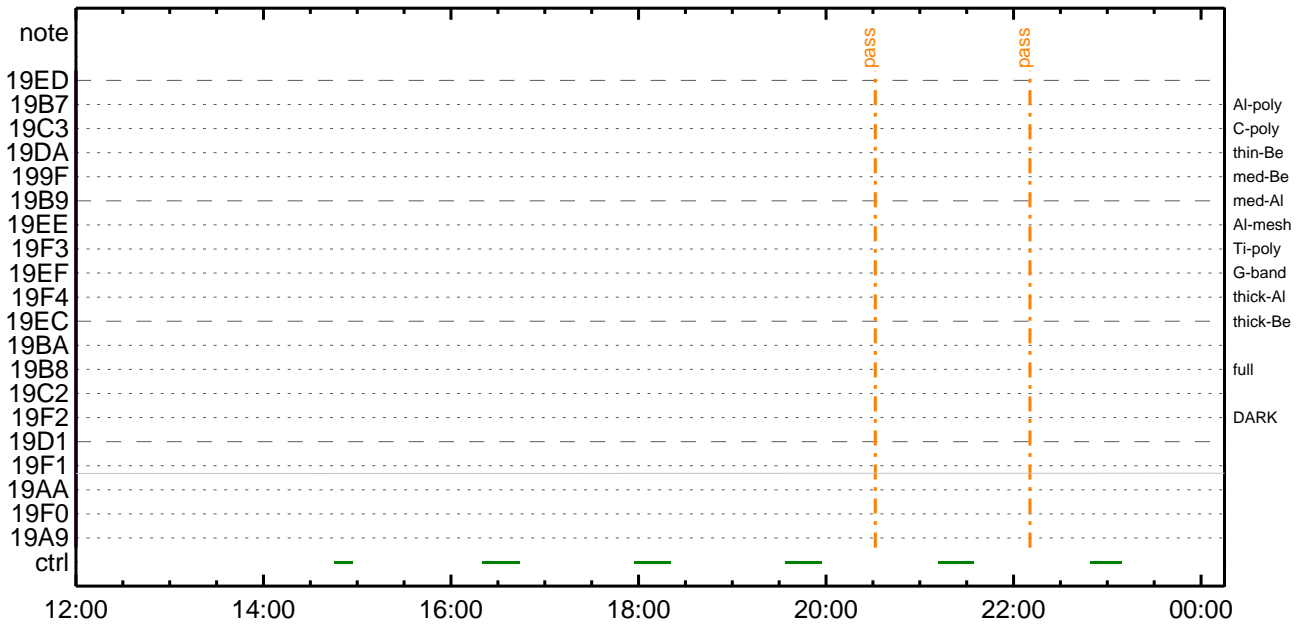
CMDI #0058 2014/01/28



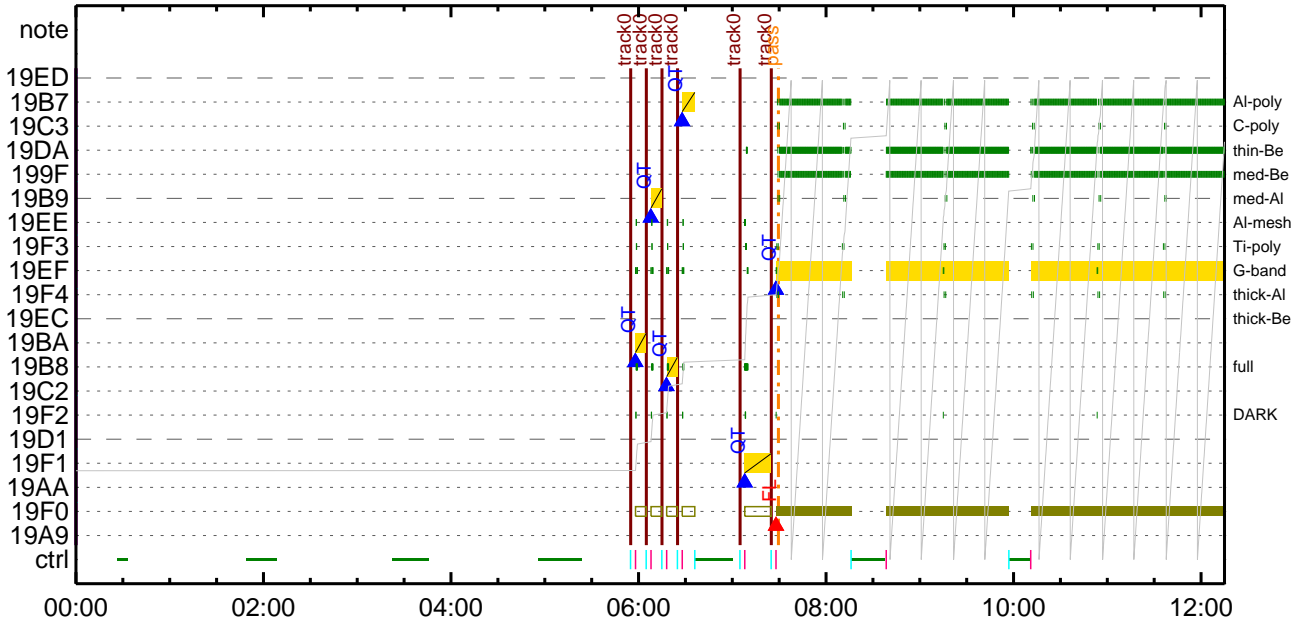
CMDI #0058 2014/01/29



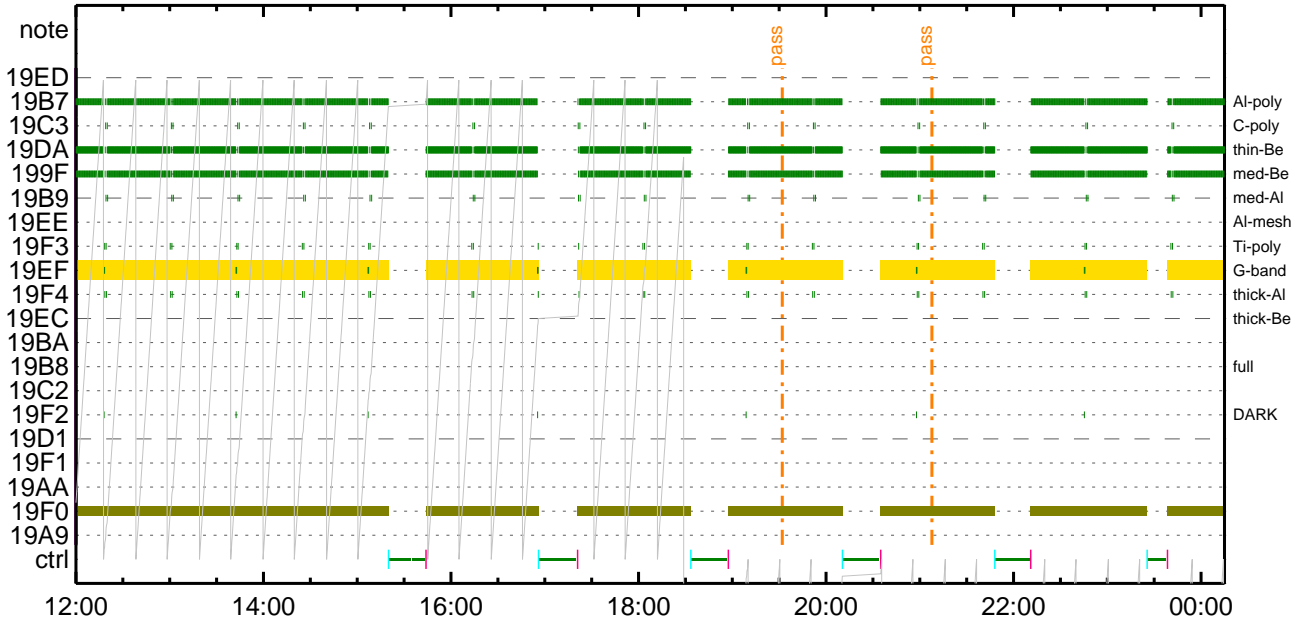
CMDI #0058 2014/01/29



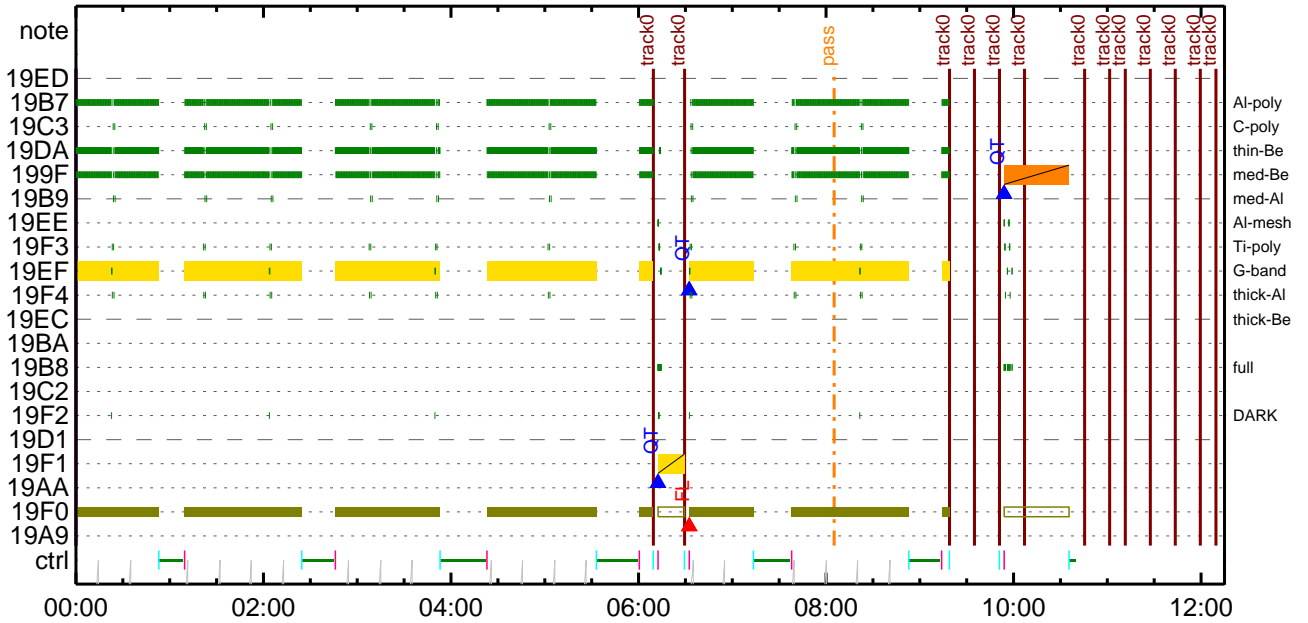
CMDI #0058 2014/01/30



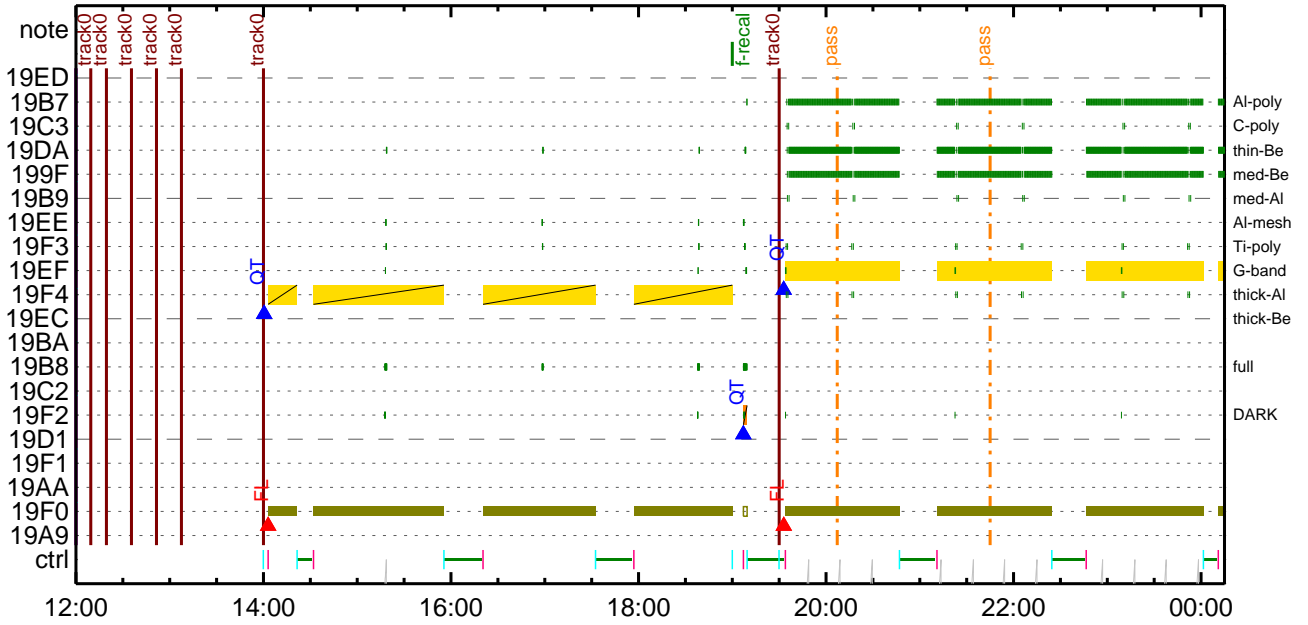
CMDI #0058 2014/01/30



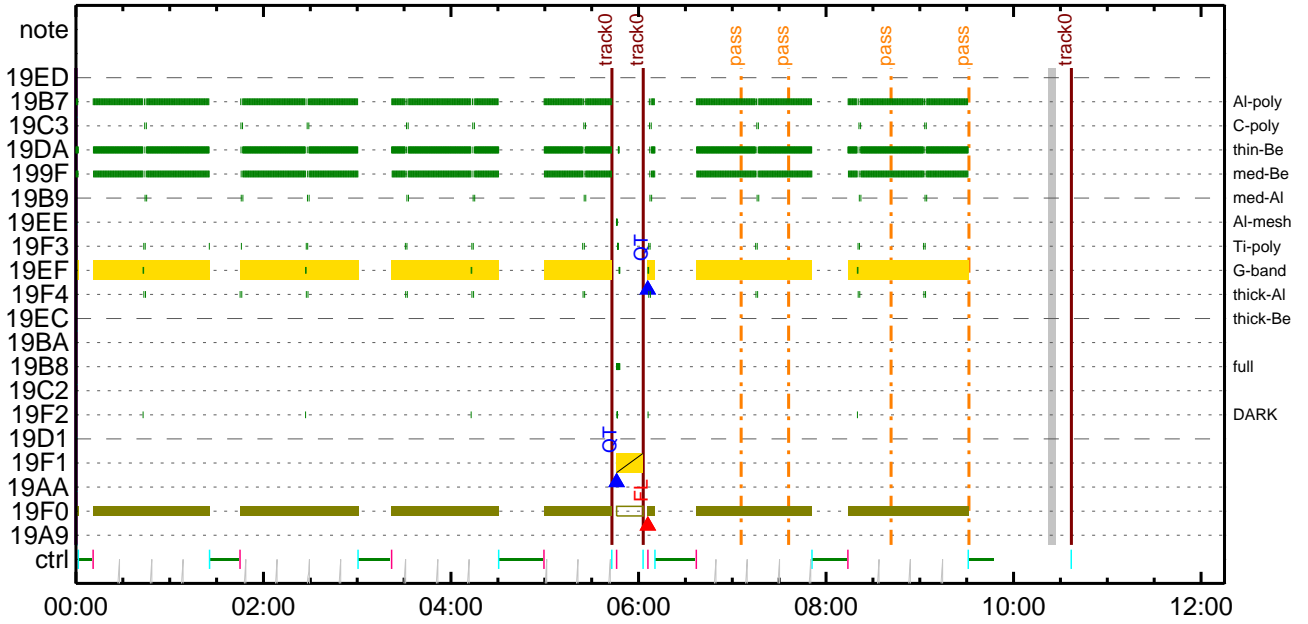
CMDI #0058 2014/01/31



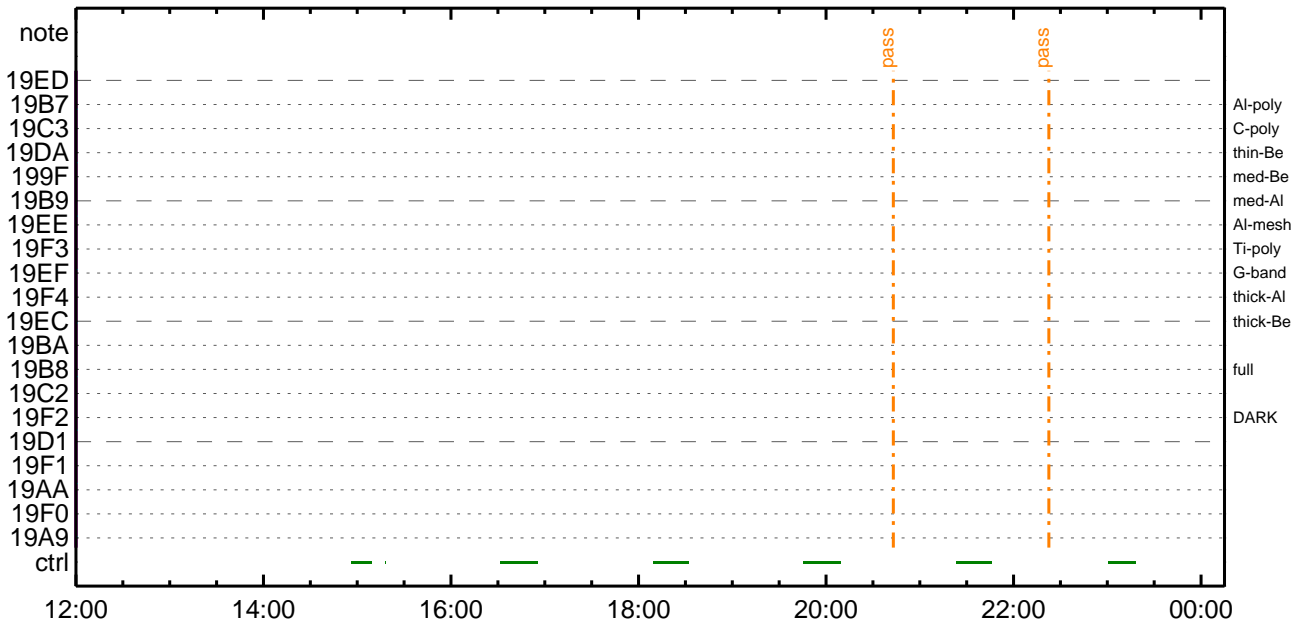
CMDI #0058 2014/01/31



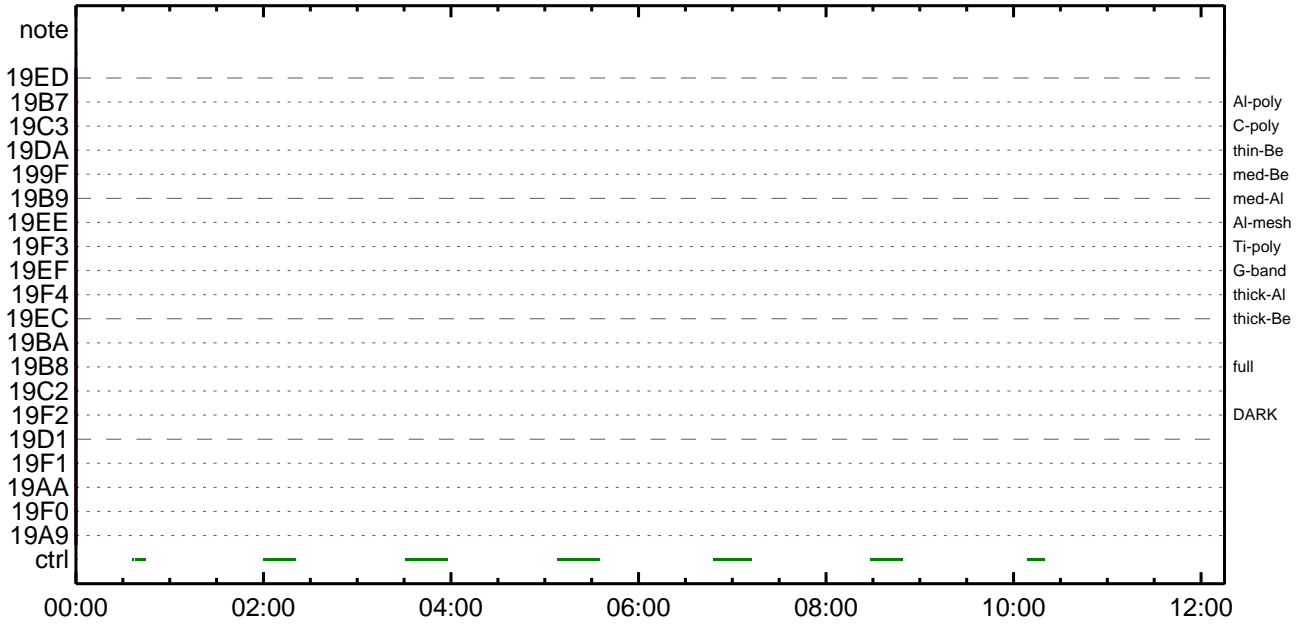
CMDI #0058 2014/02/01



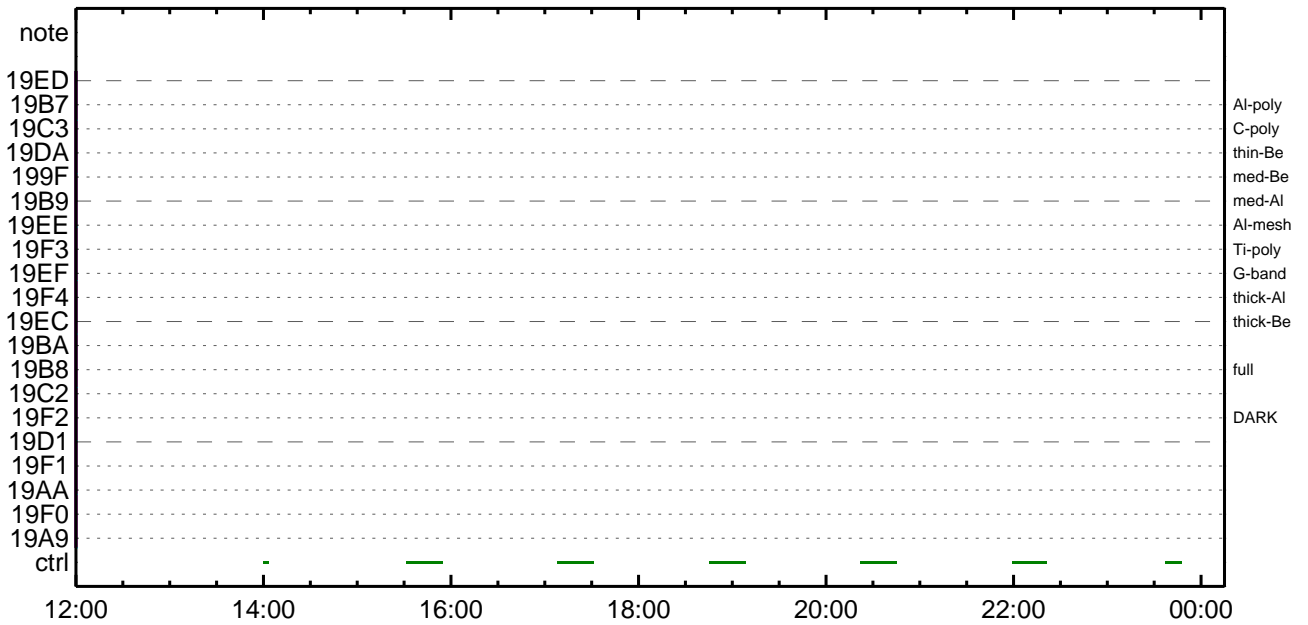
CMDI #0058 2014/02/01



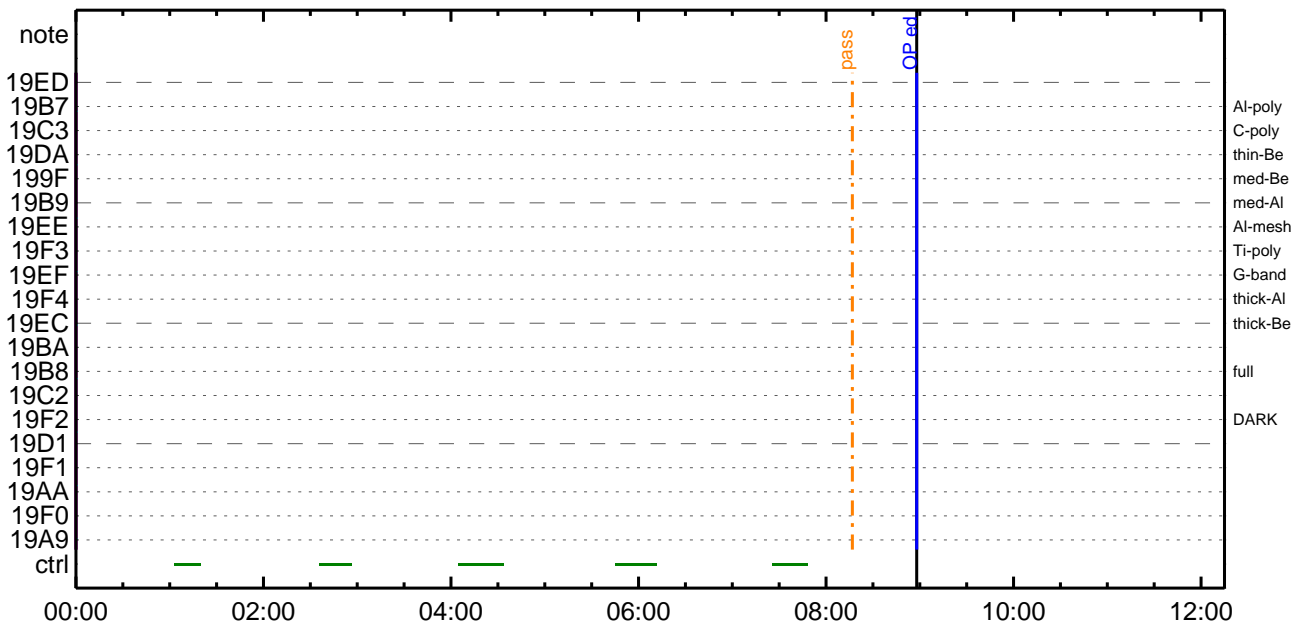
CMDI #0058 2014/02/02



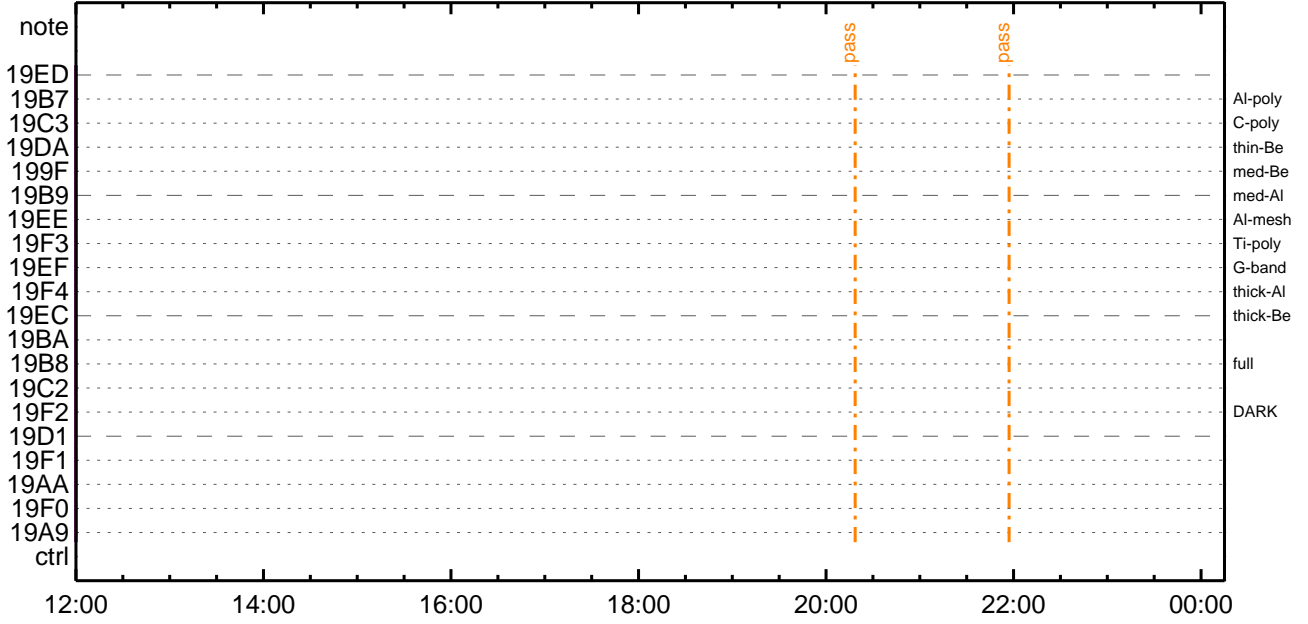
CMDI #0058 2014/02/02



CMDI #0058 2014/02/03



CMDI #0058 2014/02/03




```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 . C. ;ãOP/OGY1;4YE;ã
0103 . S. OP op-172:OP
0104 ( )
0105 . S. OG og-172:OG
0106 ( )
0107 C.
0108 . C. ;ãNMOG&OPîî°èYAYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 . C. YAYôYx½ªî»ð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOG²î½E¹ç•è²îOK²³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 . C. YAYôYx½ªî»ð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOG²î½E¹ç•è²îOK²³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 . C. YAYôYx½ªî»ð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOK²³îÇ§
0165 C.
0166 . C. ***** °E²¼²î½E¹ç•è²îOK²³îÇ§ *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;E²ðîã²
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 . C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE ;§ OPOG UPLOAD²-Á÷ç@NG²î½E¹ç•è²îOK²³îÇ§
0180 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0181 C.
0182 . C. TIY³Y³YôY³E²ðîã²îÇ§ (UT)
0183 +. TI 2014-01-24 09:19:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2014-01-24 09:19:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2014-01-24 09:19:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-173 2014-01-24 13:44:02 82 33 SOLAR-B MAIN //  
0001 C.  
0002 . C. ***** AOS *****  
0003 C.  
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã  
0005 C.  
0006 C. YÁYB;¼Y³YFYOYÉÁ+¿®  
0007 +. DC 00-00 NULL_DUMMY_CMD  
0008 C.  
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****  
0010 C. Áí;Ë¿µÁß•µ°Æ»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ËË¿µ•íÉ;ËßÉ¼°ÇÓß•¿¼l¹çµÍ;çÁ®, ùñ¹ñèñßçÁ+¿®ß•ñÈññ³ñÈ;f  
0011 +. DC 02-8E AOCU_ORB_UPD  
0012 C.  
0013 C.  
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****  
0015 C. Upload the Orbit Element and the Target Attitude  
0016 C. RAM-ID:TARGET_ATT  
0017 . S. RAM ram-150:TARGET_ATT  
0018 ( )  
0019 C.  
0020 C.  
0021 C. Set the dump memory area of TARGET_ATT  
0022 +. DC 02-48 AOCU_DUMP_SET  
0023 BC (07 00 00 00 18 00)  
0024 C.  
0025 C. <A_ST$1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]  
0026 C.  
0027 C.  
0028 C. Change the TLMFormatNo for the AOCS Dump Format  
0029 +. DC 01-22 DHU_MODE_CHNG  
0030 BC (04 0b f8)  
0031 C.  
0032 C. Wait for AOCSDUMP to end  
0033 C.  
0034 . C. Check the dump memory  
0035 C.  
0036 C. Result = OK [ ]  
0037 C.  
0038 +. DC 01-22 DHU_MODE_CHNG  
0039 BC (02 0a f8)  
0040 C.  
0041 C. <A_***>[TLM STS] FMT = 2 [ ]  
0042 C.  
0043 +. DC 02-8E AOCU_ORB_UPD  
0044 . C.  
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes  
0046 +. DC 07-FC EIS_MODE_MANU  
0047 BC (21 02)  
0048 . C. Verify EIS in MANUAL mode  
0049 . C. Estimated OBSTBL upload time is 2m10s  
0050 C. *****  
0051 C. EIS START OBSTBL LOAD  
0052 C. *****  
0053 . S. RAM ram-820:EIS_OBSTBL  
0054 ( )  
0055 +. DC 07-FC EIS_DUMP_OBSTBL  
0056 BC (07 07 07 00 00 70 00)  
0057 C.  
0058 C. Execute, after the success of OBSTBL upload.  
0059 C. Set EIS TI-commands  
0060 +. TI 2014-01-24 09:23:50.0  
0061 DC 07-FC EIS_MODE_CHG_ENA  
0062 BC (20)  
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP  
0064 C. *****  
0065 C. EIS END OBSTBL LOAD  
0066 C. *****  
0067 C.  
0068 . C. ***** MDP `ûÁîóî»ó%YñÉÁßñ¹ñèDCBC•x²è *****  
0069 C. (¼á°íYÓYÁYBYPYÉYáYçYÈñ¼°ñ¼Á»Ûñ¹ñè)  
0070 . S. DC-BC dcbc-402:DCBC  
0071 (MDP_known_event)  
0072 C.  
0073 C.  
0074 . C. ***** YDY¹.İ Daily±;îÑñË`Øñ¹ñèDCBC•x²è *****  
0075 . S. DC-BC dcbc-153:DCBC  
0076 (SPECIAL-CMD_DAILY_OPERATIN_DCB)  
0077 C.  
0078 C.  
0079 . C. ;ãLOSÁY$YÁY-¼Á»Û;ã  
0080 C.  
0081 . C. ***** LOS *****  
0082 C.
```



```

0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-265:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Stop SP table >
0120 +. DC 07-F0 MDP_SP_CTRL_MANU
0121 BC (61)
0122 C. -----
0123 C. MDP_SP_CTRL_MODE = MANU [ ]
0124 C. -----
0125 C.
0126 . C. <Upload SP Observation Table>
0127 . S. RAM ram-289:MDP_OBS_S
0128 ( )
0129 C.
0130 . C. < Dump RAMID=MDP_OBS_S >
0131 +. DC 07-F0 MDP_DUMP_SPTBL
0132 BC (83 07 00 00 00 38 b8)
0133 C. -----
0134 C. MDP_OBS_S verify = OK/NG [ ]
0135 C. -----
0136 C.
0137 . C. < Upload DPL table >
0138 C.
0139 C. ¥ç¥Ã¥×¥í;¼¥É°âÊSTS_CHKðOFFðËð¹ðë
0140 C.
0141 . S. RAM ram-271:MDP_DPL
0142 ( )
0143 C.
0144 . C. < Dump RAMID=MDP_DPL >
0145 +. DC 07-F0 MDP_DUMP_FGTBL
0146 BC (82 07 00 38 b8 00 40)
0147 C. -----
0148 C. MDP_DPL verify = OK [ ]
0149 C. -----
0150 C.
0151 C. STS_CHKðONðËð¹ðë
0152 C.
0153 . C. < Update MDP DSC PAR1 >
0154 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0155 BC (4c)
0156 C. MDP_CMD_CODE = F04C0700[ ]
0157 C. MDP_CMD_CNT (count-up 1) [ ]
0158 C. -----
0159 C.
0160 . C.
0161 C. *****
0162 C. SOT TI command set
0163 C. *****
0164 C. Execute, after the success of TBL upload.
0165 +. TI 2014-01-24 09:23:18.0
0166 DC 07-F0 MDP_SOT_MODE_OBSV
0167 BC (40)
0168 . C. -----
0169 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0170 C. -----
0171 C.
0172 C.
0173 C. ***** XRT START *****
0174 C.
0175 +. DC 07-F0 MDP_XRT_CTRL_MANU
0176 BC (c1)
0177 + DC 07-F0 MDP_XRT_MODE_STBY
0178 BC (c3)
0179 . C. ----- Success Verify ? OK / NG____
0180 C.
0181 C. XRT Obs. Table Upload
0182 . S. RAM ram-291:MDP_OBS_X
0183 ( )
0184 C.
0185 +. DC 07-F0 MDP_DUMP_XRTTBL
0186 BC (84 07 00 00 00 3a d4)
0187 . C. ----- Comparison Check ? OK / ERR ____
0188 C.
0189 C.
0190 +. DC 07-F0 MDP_XRT_ROI_SET
0191 BC (cd 01 b1 b1 04 04)
0192 + DC 07-F0 MDP_XRT_ROI_SET
0193 BC (cd 02 b1 b1 08 08)

```

```

0194 + DC 07-F0 MDP_XRT_ROI_SET
0195 BC (cd 03 b1 b1 08 08)
0196 + DC 07-F0 MDP_XRT_ROI_SET
0197 BC (cd 04 b1 b1 06 06)
0198 + DC 07-F0 MDP_XRT_ROI_SET
0199 BC (cd 05 85 83 06 06)
0200 + DC 07-F0 MDP_XRT_ROI_SET
0201 BC (cd 06 85 83 06 06)
0202 + DC 07-F0 MDP_XRT_ROI_SET
0203 BC (cd 07 80 60 20 18)
0204 + DC 07-F0 MDP_XRT_ROI_SET
0205 BC (cd 08 a0 80 18 20)
0206 + DC 07-F0 MDP_XRT_ROI_SET
0207 BC (cd 09 80 80 20 20)
0208 + DC 07-F0 MDP_XRT_ROI_SET
0209 BC (cd 0a 80 80 20 08)
0210 + DC 07-F0 MDP_XRT_ROI_SET
0211 BC (cd 0b 80 80 08 20)
0212 + DC 07-F0 MDP_XRT_ROI_SET
0213 BC (cd 0c c0 c0 10 10)
0214 + DC 07-F0 MDP_XRT_ROI_SET
0215 BC (cd 0d 40 c0 10 10)
0216 + DC 07-F0 MDP_XRT_ROI_SET
0217 BC (cd 0e 40 40 10 10)
0218 + DC 07-F0 MDP_XRT_ROI_SET
0219 BC (cd 0f 80 80 06 06)
0220 + DC 07-F0 MDP_XRT_ROI_SET
0221 BC (cd 10 80 80 08 08)
0222 + DC 07-F0 MDP_XRT_FLD_ENA
0223 BC (d8)
0224 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0225 BC (c8)
0226 + DC 07-F0 MDP_XRT_AEC_RESET
0227 BC (d0)
0228 + DC 07-F0 MDP_XRT_ARS_DIS
0229 BC (d5)
0230 + DC 07-F0 MDP_XRT_FLD_RESET
0231 BC (da)
0232 + DC 07-F0 MDP_XRT_QT_PROG_SET
0233 BC (c4 0e)
0234 + DC 07-F0 MDP_XRT_FL_PROG_SET
0235 BC (c5 02)
0236 . C. ----- Success Verify ? OK / NG ____
0237 C.
0238 C.
0239 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0240 C.
0241 + DC 07-F0 MDP_XRT_MODE_OBSV
0242 BC (c2)
0243 + TI 2014-01-24 09:23:02.0
0244 DC 07-F0 MDP_XRT_MODE_OBSV
0245 BC (c2)
0246 . C. ----- Success Verify ? OK / NG ____
0247 C.
0248 C. ***** XRT END *****
0249 C.
0250 . C. ***** MDP 'ûÃîâî»ò¼ÝñÊÄð¹ñèDCBC•x²è *****
0251 C. (%ã°îÿÓÿÄÿÈÿÞÿËÿÄÿÇÿÈÿ¼ñ¼Ä»Û¹ñè)
0252 . S. DC-BC dcbc-402:DCBC
0253 (MDP_known_event)
0254 C.
0255 C.
0256 . C. ***** ÿÐÿ¹•ï Daily±¿îññÉ'Ø¹ñèDCBC•x²è *****
0257 . S. DC-BC dcbc-153:DCBC
0258 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0259 C.
0260 C.
0261 . C. ;ãLOSÿÁÿÿÿÄÿ-¼Ä»Û;ã
0262 C.
0263 . C. ***** LOS *****
0264 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-175 2014-01-24 13:44:02 29 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY^-¼Á»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿Àµ°È»Í×ÁÇüÍYçYÁY×Yí;¼YÉ;ÈÈèµ•íÍÉ;ÈÈÈ¼°ÇÖµ•¿¿¼i¹çµÍ;çÀ®,ùµ¹æµµµÇÁ+¿®µ•µÈµµµ³µÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 . C. ***** MDP `úÁíµí»ö¼YµÈÁµ¹µèDCBC•x²è *****
0016 C. (¼á°íYÓYÁYÈYBÝÈYáYçYèµ¼µ¼Á»Û¹µè)
0017 . S. DC-BC dcbc-402:DCBC
0018 (MDP_known_event)
0019 C.
0020 C.
0021 . C. ***** YDÝ¹•Í Daily+¿ÍÑµÈ´Øµ¹µèDCBC•x²è *****
0022 . S. DC-BC dcbc-153:DCBC
0023 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0024 C.
0025 C.
0026 . C. ;ãLOSÁY$YÁY^-¼Á»Û;ä
0027 C.
0028 . C. ***** LOS *****
0029 C.
```

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*** OP Sequence for XRT ***

2014/01/24	09:25:30.0	XRT_CTRL_MANU_416_OG [0x1a0]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/24	09:25:40.0	XRT_FOCUS_RECALIBRATE_433_OG [0x1b1]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2014/01/24	09:29:40.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/01/24	09:32:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/24	09:32:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/24	09:32:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/24	09:33:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/24	09:33:56.0	XRT_FOCUS_POSITION_449_OG [0x1c1]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/01/24	09:34:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	00 ac 00 00 00
2014/01/24	09:34:16.0	XRT_FLD_DIS_438_OG [0x1b6]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2014/01/24	09:34:18.0	XRT_FLRCTRL_DIS_421_OG [0x1a5]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/01/24	09:34:20.0	XRT_ARS_DIS_427_OG [0x1ab]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/01/24	09:34:22.0	XRT_QT_PROG_SET_426_OG [0x1aa]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2014/01/24	09:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/24	09:49:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/24	11:48:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/24	11:48:56.0	XRT_FOCUS_POSITION_449_OG [0x1c1]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/01/24	11:49:00.0	AOCS_Ore-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 54 00
2014/01/24	11:49:16.0	XRT_FLD_DIS_438_OG [0x1b6]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2014/01/24	11:49:18.0	XRT_FLRCTRL_DIS_421_OG [0x1a5]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/01/24	11:49:20.0	XRT_ARS_DIS_427_OG [0x1ab]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/01/24	11:49:22.0	XRT_QT_PROG_SET_422_OG [0x1a6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2014/01/24	12:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/24	14:03:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/24	14:03:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/01/24	14:04:00.0	AOCS_Ore-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	01 00 00 00 00
2014/01/24	14:04:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/01/24	14:04:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/01/24	14:04:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/01/24	14:04:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/01/24	14:04:24.0	XRT_FLD_RESET_431_OG [0x1af]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/24	14:06:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2014/01/24	14:06:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2014/01/24	14:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/24	14:57:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/24	14:57:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/24	14:57:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/24	15:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/24	15:10:30.0	XRT_Custom_430_OG [0x1ae]			
2014/01/24	15:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/24	15:19:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/24	15:19:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/24	15:19:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/24	15:20:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/24	15:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/24	15:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/24	16:33:00.0	XRT_CTRL_MANU_400_OG [0x190]			

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2014/01/24	16:33:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/24	16:33:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/24	16:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/24	16:56:30.0	XRT_Custom_430_OG [0x1ae]				
2014/01/24	16:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/24	18:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/24	18:10:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/24	18:10:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/24	18:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/24	18:33:30.0	XRT_Custom_430_OG [0x1ae]				
2014/01/24	18:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/24	19:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/24	19:47:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/24	19:47:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/24	19:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/24	20:10:30.0	XRT_Custom_430_OG [0x1ae]				
2014/01/24	20:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/24	21:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/24	21:24:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/24	21:24:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/24	21:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/24	21:47:00.0	XRT_Custom_430_OG [0x1ae]				
2014/01/24	21:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/24	23:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/24	23:02:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/24	23:02:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/24	23:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/24	23:19:00.0	XRT_Custom_430_OG [0x1ae]				
2014/01/24	23:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/25	00:36:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/25	00:36:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/25	00:36:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/25	00:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/25	00:45:30.0	XRT_Custom_430_OG [0x1ae]				
2014/01/25	00:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/25	02:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/25	02:01:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/25	02:01:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/25	02:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/25	02:21:30.0	XRT_Custom_430_OG [0x1ae]				
2014/01/25	02:22:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/25	03:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/25	03:31:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/25	03:31:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/25	03:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/25	03:59:00.0	XRT_Custom_430_OG [0x1ae]				
2014/01/25	04:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/25	05:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/25	05:09:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/25	05:09:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2014/01/25	05:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/25	05:36:30.0	XRT_Custom_430_OG [0x1ae]			
2014/01/25	05:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/25	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/25	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/01/25	06:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 00 00 00 00
2014/01/25	06:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2014/01/25	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/01/25	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/01/25	06:02:58.0	XRT_QT_PROG_SET_414_OG [0x19e]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2014/01/25	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/25	06:19:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/25	06:19:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/01/25	06:20:00.0	AOCS_ORe-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	01 00 00 00 00
2014/01/25	06:20:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/01/25	06:20:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/01/25	06:20:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/01/25	06:20:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/01/25	06:20:24.0	XRT_FLD_RESET_431_OG [0x1af]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/25	06:22:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2014/01/25	06:22:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2014/01/25	06:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/25	06:49:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/25	06:49:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/25	06:49:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/25	06:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/25	07:13:30.0	XRT_Custom_430_OG [0x1ae]			
2014/01/25	07:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/25	08:29:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/25	08:29:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/25	08:29:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/25	08:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/25	08:50:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/25	08:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/25	10:10:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/25	10:10:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/25	10:10:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/25	10:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/25	10:21:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/25	10:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/25	14:01:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/25	14:01:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/25	14:01:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/25	14:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/25	14:05:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/25	14:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/25	15:32:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/25	15:32:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/25	15:32:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2014/01/25	15:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/25	15:56:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/25	15:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/25	17:09:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/25	17:09:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/25	17:09:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/25	17:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/25	17:33:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/25	17:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/25	18:46:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/25	18:46:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/25	18:46:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/25	18:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/25	19:09:30.0	XRT_Custom_430_OG [0x1ae]			
2014/01/25	19:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/25	20:23:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/25	20:23:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/25	20:23:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/25	20:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/25	20:47:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/25	20:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/25	22:01:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/25	22:01:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/25	22:01:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/25	22:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/25	22:22:30.0	XRT_Custom_430_OG [0x1ae]			
2014/01/25	22:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/25	23:38:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/25	23:38:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/25	23:38:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/25	23:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/25	23:48:30.0	XRT_Custom_430_OG [0x1ae]			
2014/01/25	23:49:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/26	01:04:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/26	01:04:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/26	01:04:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/26	01:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/26	01:21:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/26	01:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/26	02:37:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/26	02:37:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/26	02:37:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/26	02:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/26	02:58:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/26	02:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/26	04:06:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/26	04:06:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/26	04:06:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/26	04:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/26	04:35:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/26	04:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0

2014/01/26	05:46:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/01/26	05:46:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/01/26	05:46:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/01/26	05:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/01/26	06:12:30.0	XRT_Custom_430_OG [0x1ae]					
2014/01/26	06:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/01/26	06:22:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/01/26	06:22:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/01/26	06:22:30.0	AOCS_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00	
2014/01/26	06:22:46.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/01/26	06:25:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/01/26	06:25:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/01/26	06:25:28.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2014/01/26	06:25:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/01/26	06:42:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/01/26	06:42:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2014/01/26	06:42:30.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 00 00 00 00	
2014/01/26	06:42:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/01/26	06:42:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/01/26	06:42:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/01/26	06:42:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/01/26	06:42:54.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/01/26	06:45:26.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2014/01/26	06:45:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2014/01/26	06:45:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/01/26	07:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/01/26	07:27:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/01/26	07:27:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/01/26	07:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/01/26	07:50:00.0	XRT_Custom_430_OG [0x1ae]					
2014/01/26	07:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/01/26	09:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/01/26	09:06:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/01/26	09:06:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/01/26	09:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/01/26	09:25:30.0	XRT_Custom_430_OG [0x1ae]					
2014/01/26	09:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/01/26	14:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/01/26	14:34:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/01/26	14:34:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/01/26	14:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/01/26	14:45:00.0	XRT_Custom_430_OG [0x1ae]					
2014/01/26	14:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/01/26	16:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/01/26	16:08:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/01/26	16:08:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/01/26	16:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/01/26	16:32:00.0	XRT_Custom_430_OG [0x1ae]					
2014/01/26	16:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2014/01/30	06:05:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			AOCU_NM	5	02-76	00 2e f9 d1	07		
2014/01/30	06:07:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00		
2014/01/30	06:07:52.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f		
2014/01/30	06:07:54.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2014/01/30	06:07:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2014/01/30	06:07:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/01/30	06:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/30	06:14:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/30	06:15:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 d1 07 d1	07		
2014/01/30	06:17:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00		
2014/01/30	06:17:52.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	08		
2014/01/30	06:17:54.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2014/01/30	06:17:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2014/01/30	06:17:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/01/30	06:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/30	06:24:54.0	XRT_CTRL_MANU_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/30	06:25:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 d1 07 2e	f9		
2014/01/30	06:27:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00		
2014/01/30	06:27:52.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13		
2014/01/30	06:27:54.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2014/01/30	06:27:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2014/01/30	06:27:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/01/30	06:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/30	06:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/30	06:36:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/30	06:36:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/01/30	06:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/01/30	07:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/30	07:04:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00		
2014/01/30	07:05:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00	00		
2014/01/30	07:05:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2014/01/30	07:07:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2014/01/30	07:07:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/01/30	07:07:58.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04		
2014/01/30	07:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/30	07:24:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/30	07:24:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00		
2014/01/30	07:25:00.0	AOCS_ORe-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00 21 ca b5	59		
2014/01/30	07:25:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/01/30	07:25:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/01/30	07:25:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/01/30	07:25:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/01/30	07:25:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/30	07:27:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c		
2014/01/30	07:27:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02		
2014/01/30	07:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			

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2014/01/30	08:16:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2014/01/30	08:16:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2014/01/30	08:16:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2014/01/30	08:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2014/01/30	08:37:30.0	XRT_Custom_430_OG [0x1ae]		
2014/01/30	08:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2014/01/30	09:57:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2014/01/30	09:57:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2014/01/30	09:57:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2014/01/30	10:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2014/01/30	10:10:00.0	XRT_Custom_430_OG [0x1ae]		
2014/01/30	10:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2014/01/30	15:20:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2014/01/30	15:20:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2014/01/30	15:20:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2014/01/30	15:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2014/01/30	15:43:00.0	XRT_Custom_430_OG [0x1ae]		
2014/01/30	15:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2014/01/30	16:56:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2014/01/30	16:56:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2014/01/30	16:56:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2014/01/30	16:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2014/01/30	17:20:00.0	XRT_Custom_430_OG [0x1ae]		
2014/01/30	17:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2014/01/30	18:33:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2014/01/30	18:33:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2014/01/30	18:33:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2014/01/30	18:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2014/01/30	18:56:30.0	XRT_Custom_430_OG [0x1ae]		
2014/01/30	18:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2014/01/30	20:10:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2014/01/30	20:10:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2014/01/30	20:10:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2014/01/30	20:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2014/01/30	20:34:00.0	XRT_Custom_430_OG [0x1ae]		
2014/01/30	20:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2014/01/30	21:48:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2014/01/30	21:48:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2014/01/30	21:48:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2014/01/30	21:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2014/01/30	22:10:00.5	XRT_Custom_430_OG [0x1ae]		
2014/01/30	22:11:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2014/01/30	23:25:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2014/01/30	23:25:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2014/01/30	23:25:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2014/01/30	23:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2014/01/30	23:37:30.0	XRT_Custom_430_OG [0x1ae]		
2014/01/30	23:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2014/01/31	00:53:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2014/01/31	00:53:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2014/01/31	00:53:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		

2014/01/31	00:56:14.0	MDP_XRT_PREFLR_STRT	1	07-F0	e8
		XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/31	01:08:30.0	XRT_Custom_430_OG [0x1ae]			
2014/01/31	01:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/31	02:24:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/31	02:24:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/31	02:24:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/31	02:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/31	02:45:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/31	02:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/31	03:53:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/31	03:53:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/31	03:53:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/31	03:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/31	04:22:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/31	04:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/31	05:33:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/31	05:33:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/31	05:33:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/31	05:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/31	05:59:30.0	XRT_Custom_430_OG [0x1ae]			
2014/01/31	06:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/31	06:09:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/31	06:09:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/01/31	06:09:30.0	AOCS_Orе-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	00 00 00 00 00
2014/01/31	06:09:46.0	XRT_FLD_DIS_434_OG [0x1b2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2014/01/31	06:12:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/01/31	06:12:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/01/31	06:12:28.0	XRT_QT_PROG_SET_414_OG [0x19e]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2014/01/31	06:12:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/31	06:29:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/31	06:29:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/01/31	06:29:30.0	AOCS_Orе-point_Start_10_OG [0x0a0]			
		AOCU_NM	5	02-76	00 23 8d b5 59
2014/01/31	06:29:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/01/31	06:29:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/01/31	06:29:50.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/01/31	06:29:52.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/01/31	06:29:54.0	XRT_FLD_RESET_431_OG [0x1af]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/31	06:32:26.0	XRT_QT_PROG_SET_425_OG [0x1a9]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2014/01/31	06:32:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2014/01/31	06:32:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/31	07:13:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/31	07:13:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/31	07:13:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/01/31	07:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/01/31	07:37:00.0	XRT_Custom_430_OG [0x1ae]			
2014/01/31	07:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/01/31	08:53:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/01/31	08:53:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/01/31	08:53:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			

2014/01/31	08:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e8			
2014/01/31	09:13:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/01/31	09:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/31	09:18:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/31	09:19:00.0	AOCS_Ore-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	00 00 00	ac	cd	
2014/01/31	09:35:00.0	AOCS_Ore-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00 00 00	d6	67	
2014/01/31	09:50:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/31	09:50:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff	aa	00	
2014/01/31	09:51:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00	00	00	
2014/01/31	09:51:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2014/01/31	09:53:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2014/01/31	09:53:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/01/31	09:53:58.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	10		
2014/01/31	09:54:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/31	10:07:00.0	AOCS_Ore-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00 00 00	29	99	
2014/01/31	10:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/31	10:35:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/31	10:35:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/01/31	10:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/01/31	10:45:30.0	AOCS_Ore-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00 00 00	53	33	
2014/01/31	11:01:30.0	AOCS_Ore-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00 d6	36	b7 8e	
2014/01/31	11:11:30.0	AOCS_Ore-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	00 b4	b5	db 75	
2014/01/31	11:27:30.0	AOCS_Ore-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	00 ac	5b	00 00	
2014/01/31	11:43:30.0	AOCS_Ore-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00 b4	b5	24 8b	
2014/01/31	11:59:30.0	AOCS_Ore-point_Start_19_OG [0x0a9]	AOCU_NM	5	02-76	00 d6	36	48 72	
2014/01/31	12:09:30.0	AOCS_Ore-point_Start_20_OG [0x0aa]	AOCU_NM	5	02-76	00 29	ca	b7 8e	
2014/01/31	12:19:30.0	AOCS_Ore-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00 4b	4b	db 75	
2014/01/31	12:35:30.0	AOCS_Ore-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	00 53	a5	00 00	
2014/01/31	12:51:30.0	AOCS_Ore-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	00 4b	4b	24 8b	
2014/01/31	13:07:30.0	AOCS_Ore-point_Start_24_OG [0x0ae]	AOCU_NM	5	02-76	00 29	db	48 72	
2014/01/31	13:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/31	13:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff	aa	00	
2014/01/31	14:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00	00	00	
2014/01/31	14:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/01/31	14:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/01/31	14:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/01/31	14:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/01/31	14:00:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/31	14:00:26.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b		
2014/01/31	14:02:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02		
2014/01/31	14:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/01/31	14:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/01/31	14:21:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/01/31	14:21:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/01/31	14:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/01/31	14:31:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/01/31	14:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			

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2014/01/31	15:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/01/31	15:55:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/01/31	15:55:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/01/31	15:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/01/31	16:19:30.0	XRT_Custom_430_OG [0x1ae]						
2014/01/31	16:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/01/31	17:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/01/31	17:32:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/01/31	17:32:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/01/31	17:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/01/31	17:56:00.0	XRT_Custom_430_OG [0x1ae]						
2014/01/31	17:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/01/31	19:00:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/01/31	19:00:02.0	XRT_FOCUS_RECALIBRATE_433_OG [0x1b1]	XRT_FOCUS_RECAL	2	07-F8	78	00	
2014/01/31	19:04:02.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa 00
2014/01/31	19:04:22.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2014/01/31	19:07:00.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2014/01/31	19:07:02.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/01/31	19:07:04.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06	
2014/01/31	19:07:06.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/01/31	19:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/01/31	19:09:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/01/31	19:09:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/01/31	19:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/01/31	19:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/01/31	19:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97 00
2014/01/31	19:30:00.0	AOCS_Ore-point_Start_25_OG [0x0af]	AOCU_NM	5	02-76	00	24	e5 b5 59
2014/01/31	19:30:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/01/31	19:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/01/31	19:30:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/01/31	19:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/01/31	19:30:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/01/31	19:32:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c	
2014/01/31	19:32:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02	
2014/01/31	19:33:00.5	XRT_Custom_430_OG [0x1ae]						
2014/01/31	19:34:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/01/31	20:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/01/31	20:47:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/01/31	20:47:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/01/31	20:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/01/31	21:10:00.0	XRT_Custom_430_OG [0x1ae]						
2014/01/31	21:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/01/31	22:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/01/31	22:24:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/01/31	22:24:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/01/31	22:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/01/31	22:45:30.0	XRT_Custom_430_OG [0x1ae]						
2014/01/31	22:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/01	00:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		

2014/02/01	00:01:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/01	00:01:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/01	00:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/01	00:10:00.0	XRT_Custom_430_OG [0x1ae]				
2014/02/01	00:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/01	01:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/01	01:25:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/01	01:25:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/01	01:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/01	01:44:00.0	XRT_Custom_430_OG [0x1ae]				
2014/02/01	01:45:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/01	03:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/01	03:00:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/01	03:00:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/01	03:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/01	03:21:00.0	XRT_Custom_430_OG [0x1ae]				
2014/02/01	03:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/01	04:30:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/01	04:30:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/01	04:30:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/01	04:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/01	04:58:30.0	XRT_Custom_430_OG [0x1ae]				
2014/02/01	04:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/01	05:42:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/01	05:42:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/02/01	05:43:00.0	AOCs_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 00 00 00 00
2014/02/01	05:43:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9
2014/02/01	05:45:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/02/01	05:45:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/02/01	05:45:58.0	XRT_QT_PROG_SET_414_OG [0x19e]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2014/02/01	05:46:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/01	06:02:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/01	06:02:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/02/01	06:03:00.0	AOCs_OrE-point_Start_25_OG [0x0af]	AOCU_NM	5	02-76	00 24 e5 b5 59
2014/02/01	06:03:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2014/02/01	06:03:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/02/01	06:03:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2014/02/01	06:03:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/02/01	06:03:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/01	06:05:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2014/02/01	06:05:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2014/02/01	06:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/01	06:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/01	06:10:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/01	06:10:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/01	06:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/01	06:36:00.0	XRT_Custom_430_OG [0x1ae]				
2014/02/01	06:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/01	07:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2014/02/01	07:51:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/01	07:51:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/01	07:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/01	08:13:00.0	XRT_Custom_430_OG [0x1ae]							
2014/02/01	08:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/01	09:31:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/01	09:31:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/01	09:31:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/01	09:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/01	10:36:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/01	10:37:00.0	AOCS_Orpoint_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 00 00				