

XRT Timeline to be uploaded on 2014/02/01

Period: 2014/02/01 10:27:00 - 2014/02/10 10:31:00

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Normal mode

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XOB #19F5: AR standard-B (thin-Be) with PFB (384FOV), 512x512 at 1064 1048, thick Al and Al/Poly context, With G-band (33ms/45ms leak), 120s cad

Term	Pointing (x, y)	Comment
02/01 10:40:00 - 02/01 17:45:24	Track (-391.6, -148.8) @ 02/01 10:37:00	# OP start + 10min - AR11967
02/01 18:33:00 - 02/02 05:59:54	Track (-328.7, -145.7) @ 02/01 18:05:30	# AR11967
02/02 06:13:00 - 02/02 18:02:54	Track (-223.8, -141.8) @ 02/02 06:10:00	# AR11967
02/02 18:16:00 - 02/03 06:21:24	Track (-116.0, -139.1) @ 02/02 18:13:00	# AR11967
02/03 06:34:30 - 02/03 17:44:00	Track (-4.3, -137.7) @ 02/03 06:31:30	# AR11967
02/03 18:20:30 - 02/04 05:54:54	Track (102.5, -137.8) @ 02/03 18:17:30	# AR11967
02/04 06:08:00 - 02/04 17:57:24	Track (208.2, -139.1) @ 02/04 06:05:00	# AR11967
02/04 18:10:30 - 02/05 05:59:54	Track (313.4, -141.8) @ 02/04 18:07:30	# AR11967
02/05 06:13:00 - 02/05 17:59:54	Track (414.3, -145.9) @ 02/05 06:10:00	# AR11967
02/05 18:13:00 - 02/06 05:29:54	Track (509.2, -151.3) @ 02/05 18:10:00	# AR11967
02/06 05:43:00 - 02/06 09:17:30	Track (593.8, -157.5) @ 02/06 05:40:00	# AR11967
02/06 15:38:00 - 02/06 17:56:00	Track (660.9, -163.8) @ 02/06 15:35:00	# AR11967

PROG= 10 Inf.-time(s)

Subr=	1-time(s)	2.0sec										
Subr= 1	1-time(s)	2.0sec										
Seqn= 71	2-time(s)	2.0sec										
Open/G-band	Open/G-band close	Safe Norm	44ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec		
Seqn= 65	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al close	Safe Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec		
Open/G-band	Open/G-band open	Safe Norm	32ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec		
Subr= 2	2-time(s)	2.0sec										
Seqn= 77	2-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open close	Safe Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec		
Open/thick-Al	Open/thick-Al close	Safe Norm	16.0s	Obs	1x1	512x512 (1064, 1456)	Q=95	3	0	2.0sec		
Seqn= 27	30-time(s)	120.0sec										
thin-Be/Open	med-Be/Open close	Safe Norm	1.41s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	17.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	17.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	17.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	17.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	17.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	17.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	17.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19DA: Synoptic Q95 2x2 - Al/mesh(5/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/362/1443) + Thin

Term	Pointing (x, y)	Comment
02/01 17:48:30 - 02/01 18:05:24	Fixed (0.0, 0.0)	synoptic, 20 min, shifted -14.5 min
02/02 06:03:00 - 02/02 06:09:54	Fixed (0.0, 0.0)	synoptic
02/02 18:06:00 - 02/02 18:12:54	Fixed (0.0, 0.0)	synoptic, shifted 3.0 min
02/03 06:24:30 - 02/03 06:31:24	Fixed (0.0, 0.0)	synoptic, shifted 21.5 min
02/03 18:10:30 - 02/03 18:17:24	Fixed (0.0, 0.0)	synoptic, shifted 7.5 min
02/04 05:58:00 - 02/04 06:04:54	Fixed (0.0, 0.0)	synoptic, shifted -5.0 min
02/04 18:01:54 - 02/04 18:07:24	Fixed (0.0, 0.0)	synoptic, shifted -2.5 min
02/05 06:03:00 - 02/05 06:09:54	Fixed (0.0, 0.0)	synoptic
02/05 18:03:00 - 02/05 18:09:54	Fixed (0.0, 0.0)	synoptic
02/06 18:22:30 - 02/06 18:29:24	Fixed (0.0, 0.0)	synoptic, shifted 19.5 min
02/07 06:08:00 - 02/07 06:14:54	Fixed (0.0, 0.0)	synoptic, shifted 5.0 min
02/07 18:03:00 - 02/07 18:09:54	Fixed (0.0, 0.0)	synoptic
02/08 06:11:30 - 02/08 06:18:24	Fixed (0.0, 0.0)	synoptic, shifted 8.5 min

PROG= 17 1-time(s)

Subr=	1-time(s)	12.0sec										
Subr= 1	1-time(s)	12.0sec										
Seqn= 84	1-time(s)	4.0sec										
Open/Al-mesh	Open/Al-mesh close	Safe Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al close	Safe Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec		
Seqn= 86	1-time(s)	4.0sec										
Open/Ti-poly	Open/Ti-poly close	Safe Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
Open/Ti-poly	Open/Ti-poly close	Safe Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
Open/Ti-poly	Open/Ti-poly close	Safe Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 95	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open close	Safe Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
thin-Be/Open	thin-Be/Open close	Safe Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
thin-Be/Open	thin-Be/Open close	Safe Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		
Seqn= 6	1-time(s)	2.0sec										
Open/G-band	Open/G-band open	Safe Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec		
Open/G-band	Open/G-band close	Safe Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec		

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #19DC: Synoptic 9 Filter w/ Triplet Exposures - 2x2 Q98 - Gband(8ms)

Term	Pointing (x, y)	Comment
02/06 05:33:00 - 02/06 05:39:54	Fixed (0.0, 0.0)	synoptic, shifted -30.0 min
PROG= 07 1-time(s)		
Subr= 1 1-time(s) 12.0sec		
Seqn= 84 1-time(s) 4.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh close	Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al close	Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 86 1-time(s) 4.0sec		
Open/Ti-poly	Open/Ti-poly close	Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 95 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open close	Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open close	Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 6 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Subr= 2 2-time(s) 12.0sec		
Seqn= 56 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/Open close	Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 22 1-time(s) 2.0sec		
C-poly/Open	C-poly/Open close	Safe Norm 177ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
C-poly/Open	C-poly/Open close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 43 1-time(s) 2.0sec		
Al-poly/Ti-poly	Al-poly/thick-Al close	Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 38 1-time(s) 4.0sec		
med-Al/Open	med-Al/Open close	Safe Norm 22.6s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Seqn= 46 1-time(s) 4.0sec		
Open/thick-Be	Open/thick-Be close	Safe Norm 64.0s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #19C3: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 45ms-2

Term	Pointing (x, y)	Comment
02/06 09:38:00 - 02/06 15:34:54	Fixed (-15.0, 880.0)	# HOP 81 North Pole
02/07 10:10:00 - 02/07 16:04:54	Fixed (-15.0, -940.0)	# HOP 81 South Pole
PROG= 18 Inf.-time(s)		
Subr= 1 1-time(s) 2.0sec		
Seqn= 13 2-time(s) 2.0sec		
Open/G-band	Open/G-band close	Safe Norm 63ms Obs 1x1 512x512 (1064, 1048) DPCM 0 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 30 1-time(s) 30.0sec		
Open/G-band	Open/G-band open	Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
Subr= 3 30-time(s) 2.0sec		
Seqn= 57 1-time(s) 60.0sec		
Open/Al-mesh	Open/Al-mesh close	Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
Al-poly/Open	Al-poly/Open close	Safe Norm 5.66s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #19F6: CME watch - 4x4 - AEC 2 - Be-thin - G-band (2x2,8ms) - Leak (33ms) - 100s cad

Term	Pointing (x, y)	Comment
02/06 18:32:30 - 02/07 06:04:54	Track (679.4, -165.8) [Ⓜ] 02/06 18:29:30	# AR11967
02/07 06:18:00 - 02/07 09:55:30	Track (748.9, -174.4) [Ⓜ] 02/07 06:15:00	# AR11967
02/07 16:08:00 - 02/07 17:59:54	Track (799.5, -182.3) [Ⓜ] 02/07 16:05:00	# AR11967
02/07 18:13:00 - 02/08 06:08:24	Track (809.4, -184.0) [Ⓜ] 02/07 18:10:00	# AR11967
02/08 06:21:30 - 02/08 08:52:00	Track (860.0, -194.6) [Ⓜ] 02/08 06:18:30	# AR11967
PROG= 20 Inf.-time(s)		
Subr= 1 60-time(s) 100.0sec		
Seqn= 19 1-time(s) 4.0sec		
thin-Be/Open	med-Be/Open close	Safe Norm 1.41s Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 6 1-time(s) 2.0sec		
Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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Flare mode

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XOB #19F0: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops-lowered-gband

Term	Pointing (x, y)	Comment
02/01 10:40:00 - 02/01 17:45:24	Track (-391.6, -148.8) @ 02/01 10:37:00	# OP start + 10min - AR11967
02/01 18:33:00 - 02/02 05:59:54	Track (-328.7, -145.7) @ 02/01 18:05:30	# AR11967
02/02 06:13:00 - 02/02 18:02:54	Track (-223.8, -141.8) @ 02/02 06:10:00	# AR11967
02/02 18:16:00 - 02/03 06:21:24	Track (-116.0, -139.1) @ 02/02 18:13:00	# AR11967
02/03 06:34:30 - 02/03 17:44:00	Track (-4.3, -137.7) @ 02/03 06:31:30	# AR11967
02/03 18:20:30 - 02/04 05:54:54	Track (102.5, -137.8) @ 02/03 18:17:30	# AR11967
02/04 06:08:00 - 02/04 17:57:24	Track (208.2, -139.1) @ 02/04 06:05:00	# AR11967
02/04 18:10:30 - 02/05 05:59:54	Track (313.4, -141.8) @ 02/04 18:07:30	# AR11967
02/05 06:13:00 - 02/05 17:59:54	Track (414.3, -145.9) @ 02/05 06:10:00	# AR11967
02/05 18:13:00 - 02/06 05:29:54	Track (509.2, -151.3) @ 02/05 18:10:00	# AR11967
02/06 05:43:00 - 02/06 09:17:30	Track (593.8, -157.5) @ 02/06 05:40:00	# AR11967
02/06 09:38:00 - 02/06 15:34:54	Fixed (-15.0, 880.0)	# HOP 81 North Pole
02/06 15:38:00 - 02/06 17:56:00	Track (660.9, -163.8) @ 02/06 15:35:00	# AR11967
02/06 18:32:30 - 02/07 06:04:54	Track (679.4, -165.8) @ 02/06 18:29:30	# AR11967
02/07 06:18:00 - 02/07 09:55:30	Track (748.9, -174.4) @ 02/07 06:15:00	# AR11967
02/07 10:10:00 - 02/07 16:04:54	Fixed (-15.0, -940.0)	# HOP 81 South Pole
02/07 16:08:00 - 02/07 17:59:54	Track (799.5, -182.3) @ 02/07 16:05:00	# AR11967
02/07 18:13:00 - 02/08 06:08:24	Track (809.4, -184.0) @ 02/07 18:10:00	# AR11967
02/08 06:21:30 - 02/08 08:52:00	Track (860.0, -194.6) @ 02/08 06:18:30	# AR11967

PROG= 02 12-time(s)

Subr=	1	45-time(s)	10.0sec													
Seqn=	9	1-time(s)	2.0sec	thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn=	10	1-time(s)	2.0sec	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
				Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn=	11	1-time(s)	2.0sec	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr=	2	1-time(s)	10.0sec													
Seqn=	17	1-time(s)	2.0sec	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
				Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
				Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval					

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Active Region Search

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NOT USED

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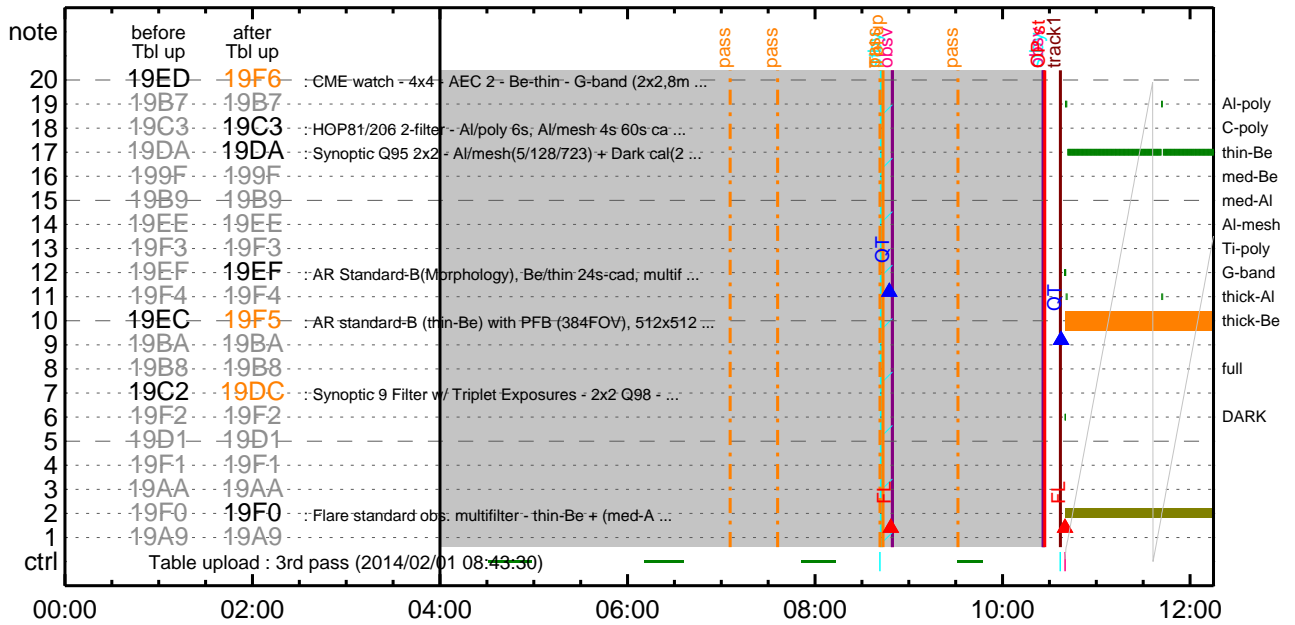
Flare Detection

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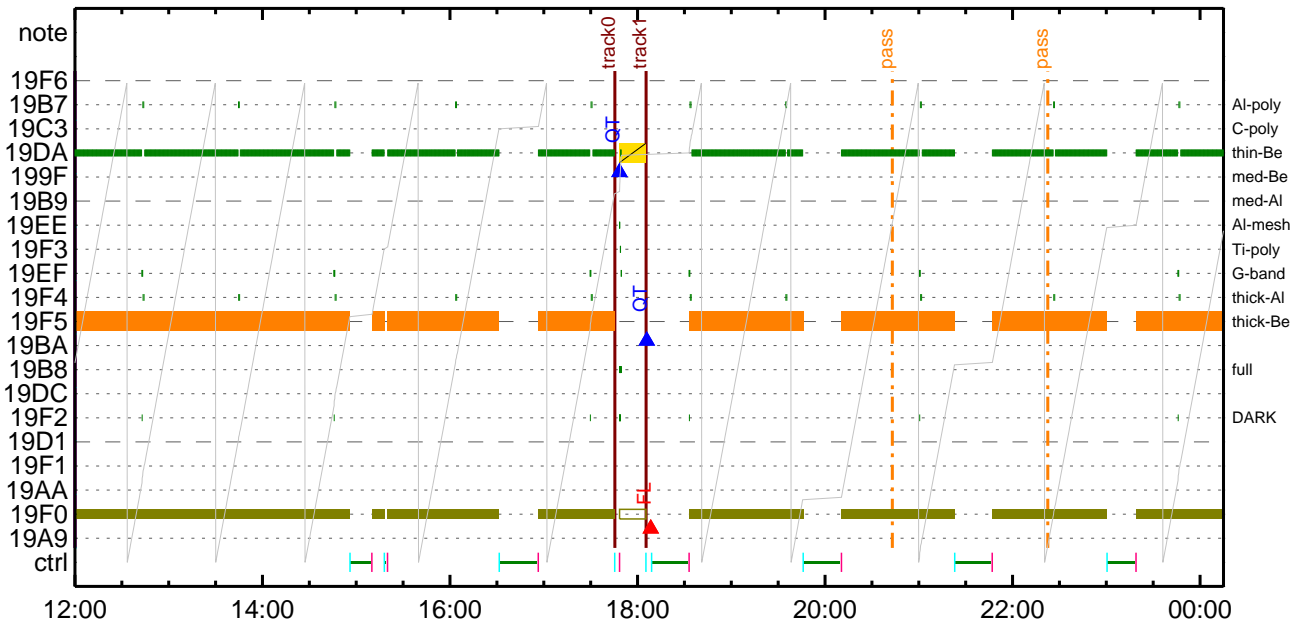
FLD Patrol

Term	Pointing (x, y)	Comment														
02/01 18:05:46 - 02/02 06:00:16	Track (-328.7, -145.7) @ 02/01 18:05:30	# AR11967														
02/02 06:10:16 - 02/02 18:03:16	Track (-223.8, -141.8) @ 02/02 06:10:00	# AR11967														
02/02 18:13:16 - 02/03 06:21:46	Track (-116.0, -139.1) @ 02/02 18:13:00	# AR11967														
02/03 06:31:46 - 02/03 18:07:46	Track (-4.3, -137.7) @ 02/03 06:31:30	# AR11967														
02/03 18:17:46 - 02/04 05:55:16	Track (102.5, -137.8) @ 02/03 18:17:30	# AR11967														
02/04 06:05:16 - 02/04 18:01:46	Track (208.2, -139.1) @ 02/04 06:05:00	# AR11967														
02/04 18:07:46 - 02/05 06:00:16	Track (313.4, -141.8) @ 02/04 18:07:30	# AR11967														
02/05 06:10:16 - 02/05 18:00:16	Track (414.3, -145.9) @ 02/05 06:10:00	# AR11967														
02/05 18:10:16 - 02/06 05:30:16	Track (509.2, -151.3) @ 02/05 18:10:00	# AR11967														
02/06 05:40:16 - 02/06 18:19:46	Track (593.8, -157.5) @ 02/06 05:40:00	# AR11967														
02/06 18:29:46 - 02/07 06:05:16	Track (679.4, -165.8) @ 02/06 18:29:30	# AR11967														
02/07 06:15:16 - 02/07 18:00:16	Track (748.9, -174.4) @ 02/07 06:15:00	# AR11967														
02/07 18:10:16 - 02/08 06:08:46	Track (809.4, -184.0) @ 02/07 18:10:00	# AR11967														
02/08 06:18:46 - 02/10 10:31:00	Track (860.0, -194.6) @ 02/08 06:18:30	# AR11967														
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec							
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval					

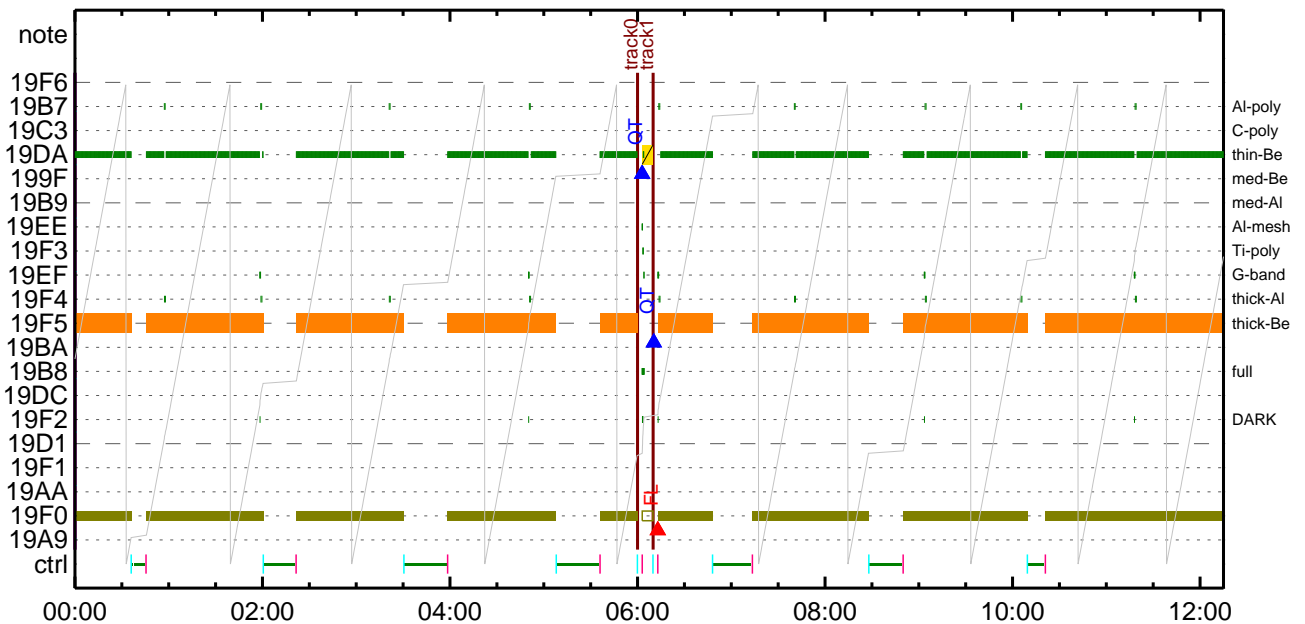
CMDI #0072 2014/02/01



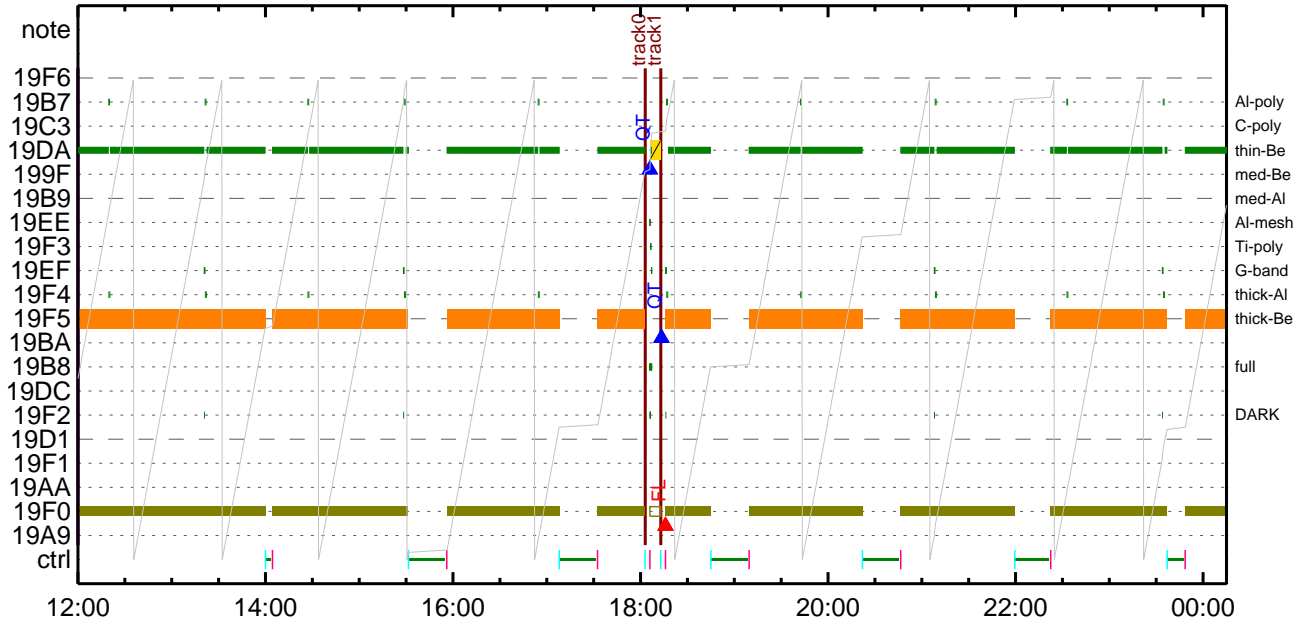
CMDI #0072 2014/02/01



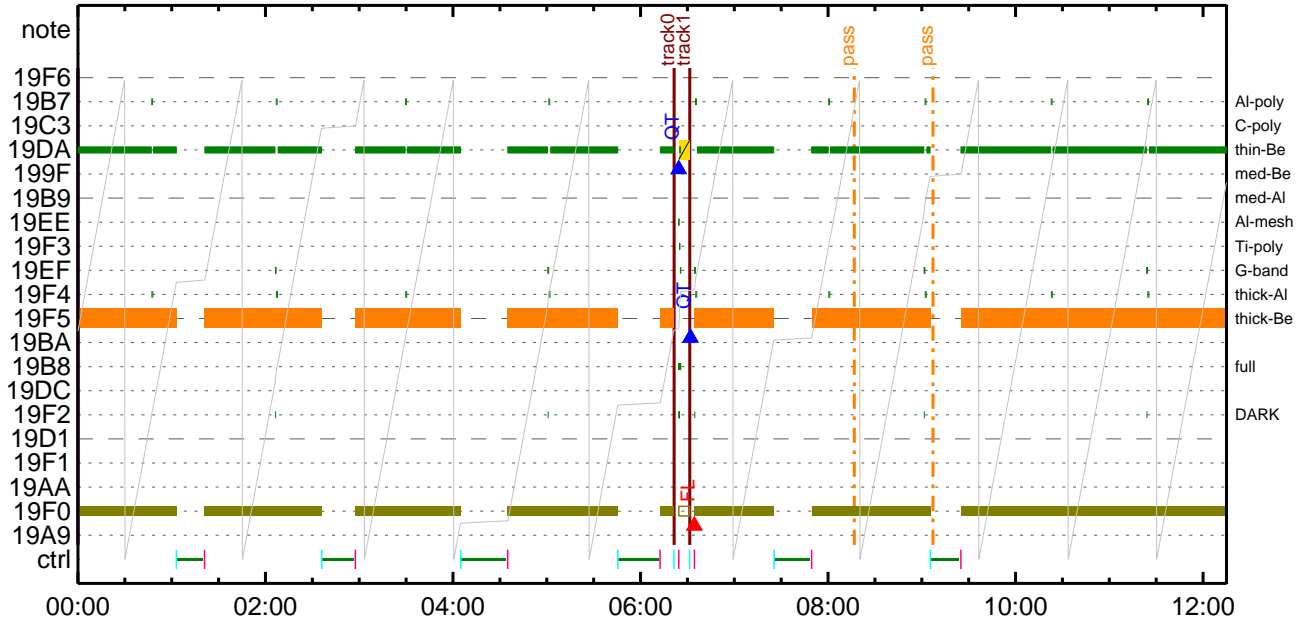
CMDI #0072 2014/02/02



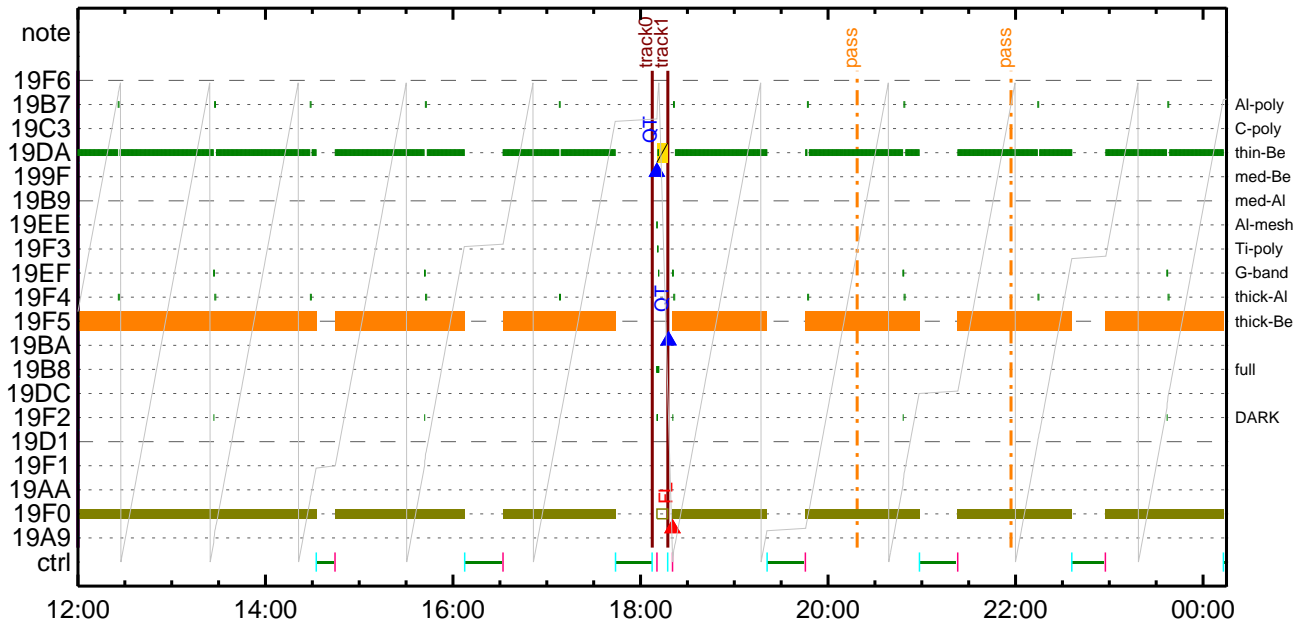
CMDI #0072 2014/02/02



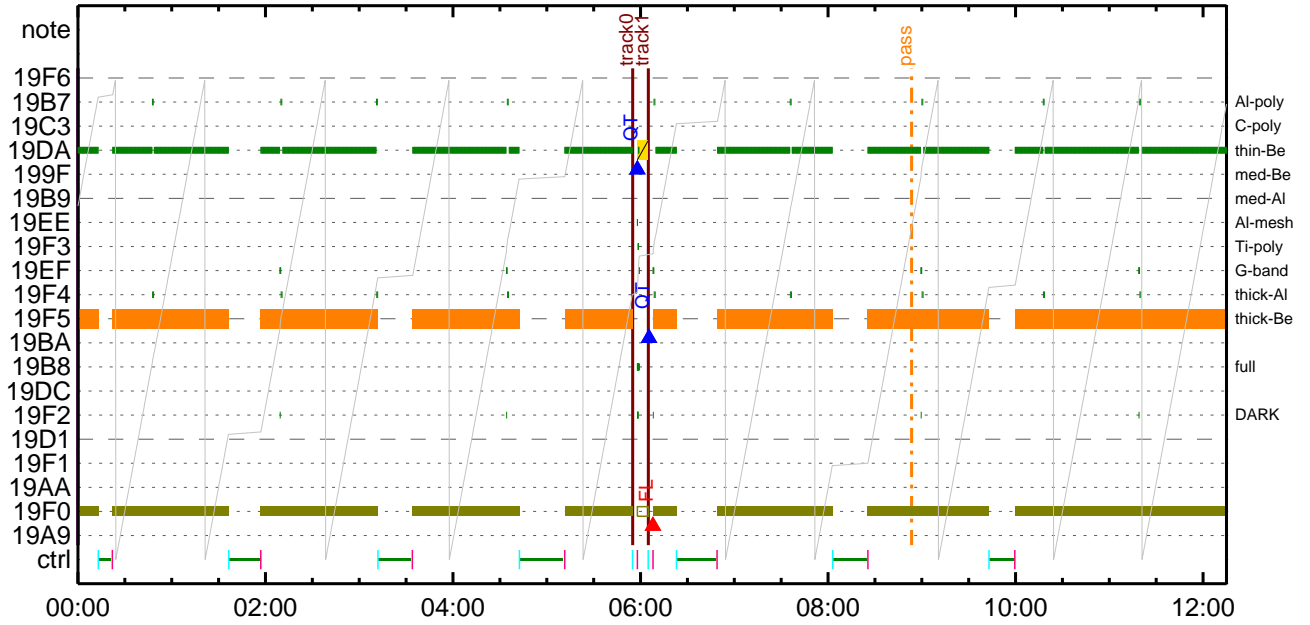
CMDI #0072 2014/02/03



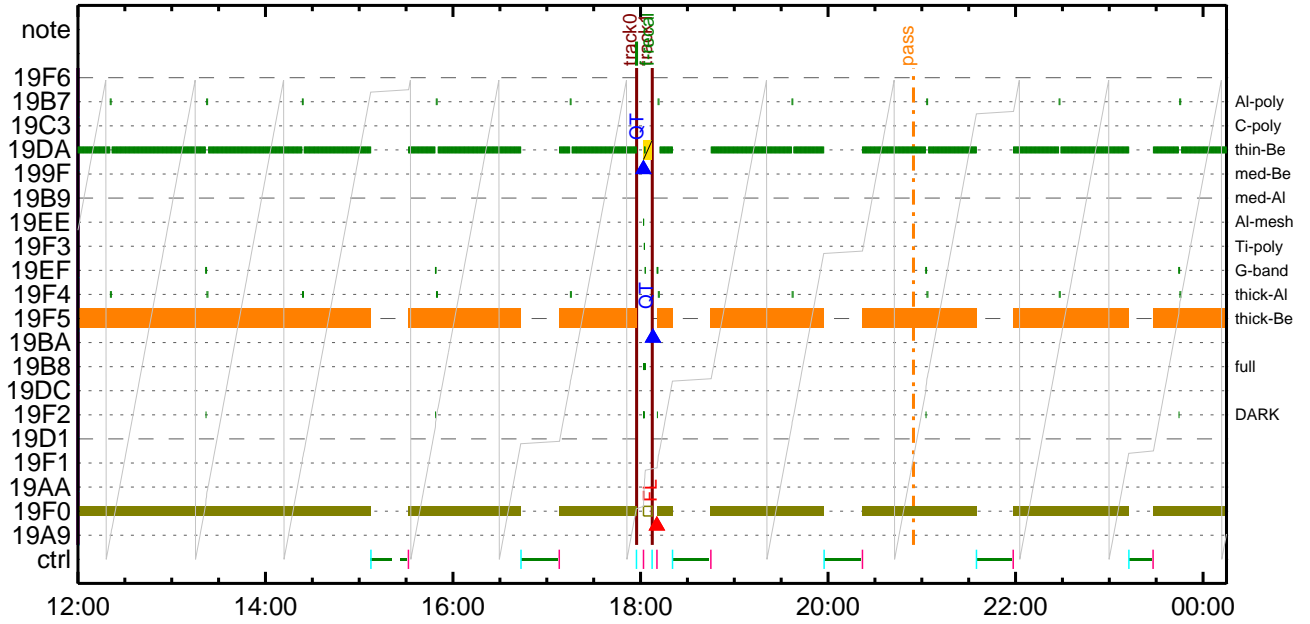
CMDI #0072 2014/02/03



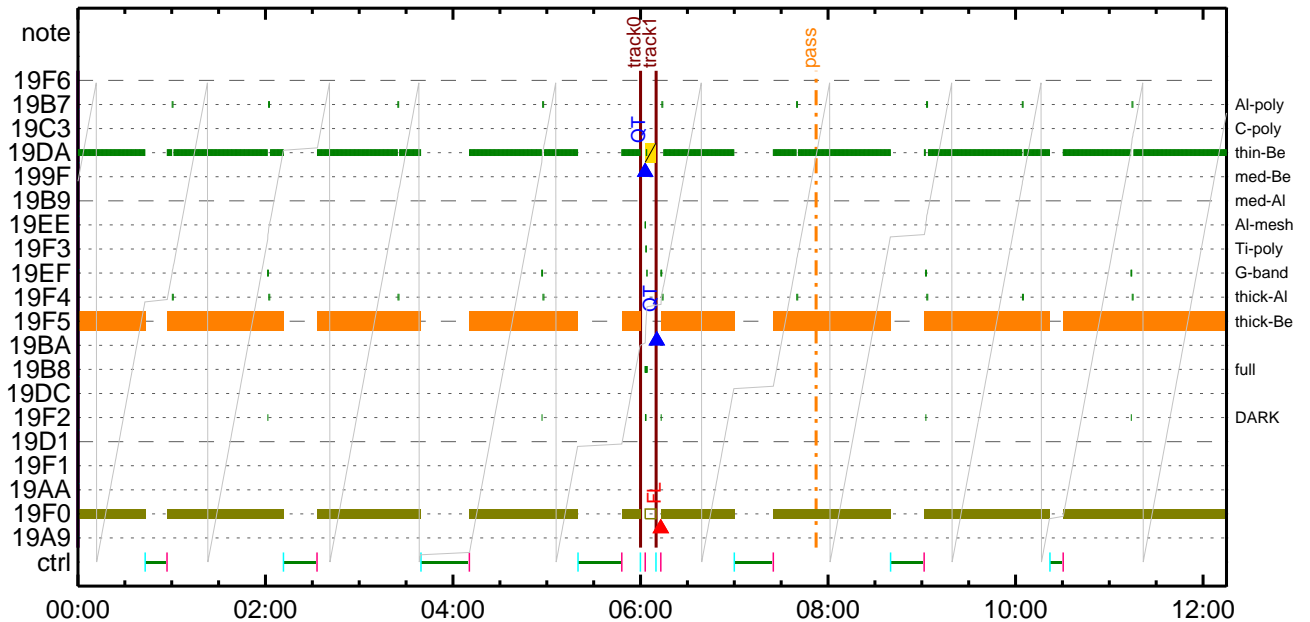
CMDI #0072 2014/02/04



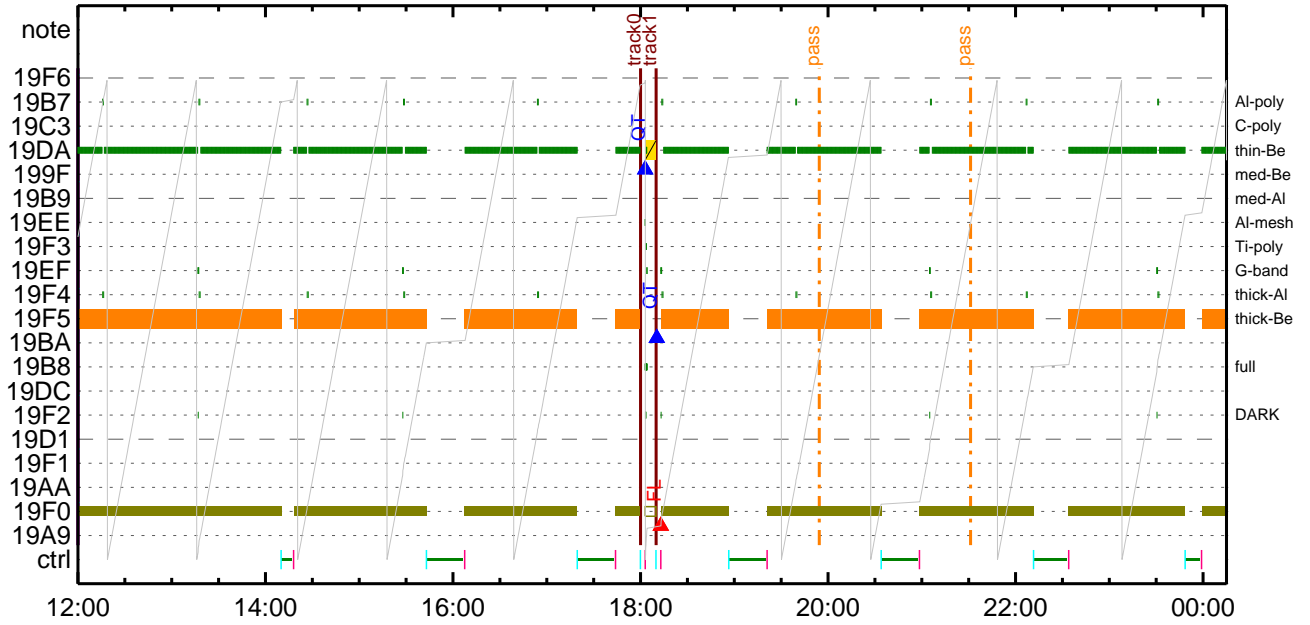
CMDI #0072 2014/02/04



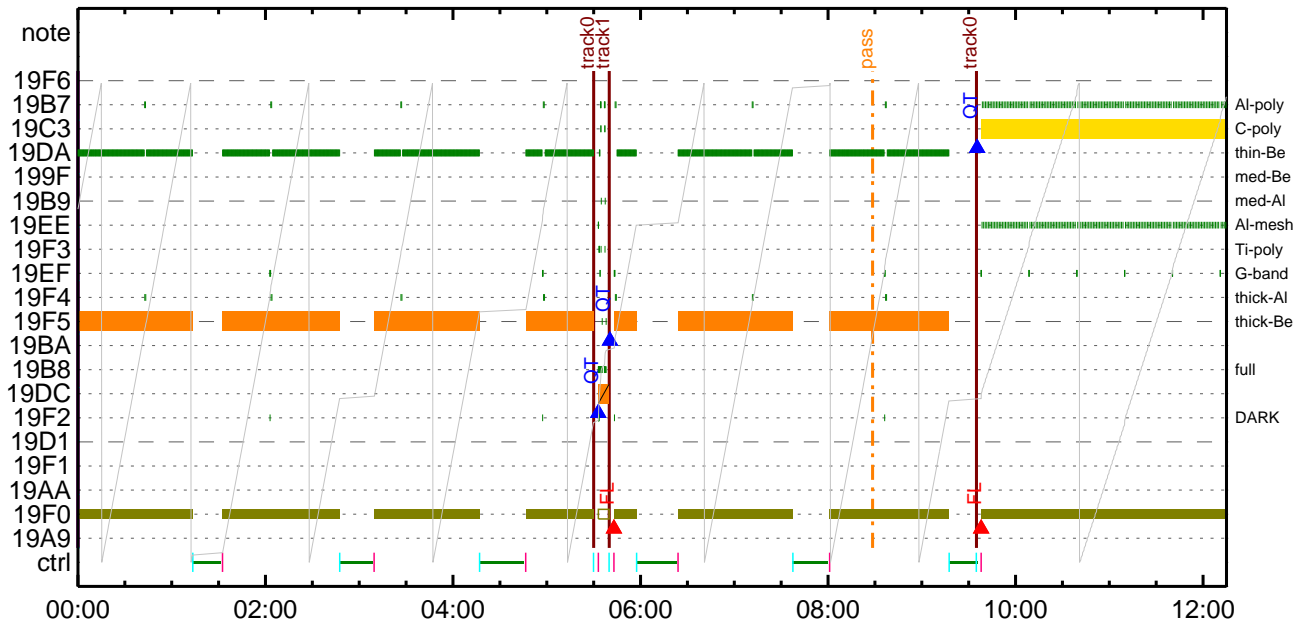
CMDI #0072 2014/02/05



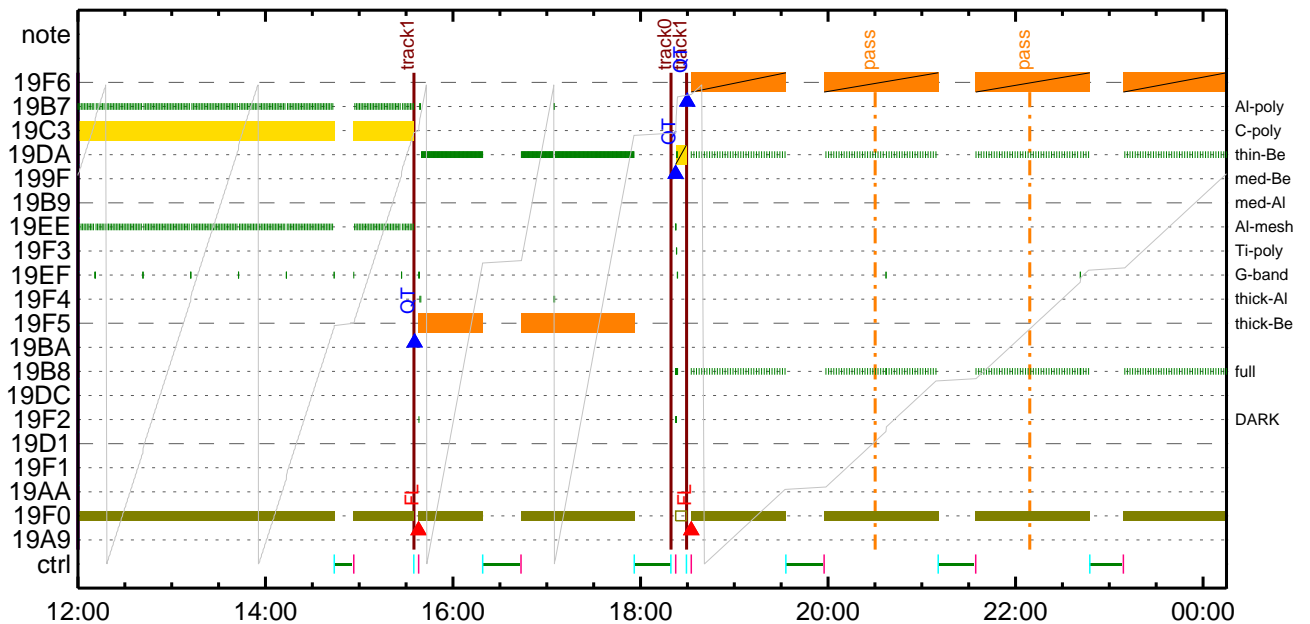
CMDI #0072 2014/02/05



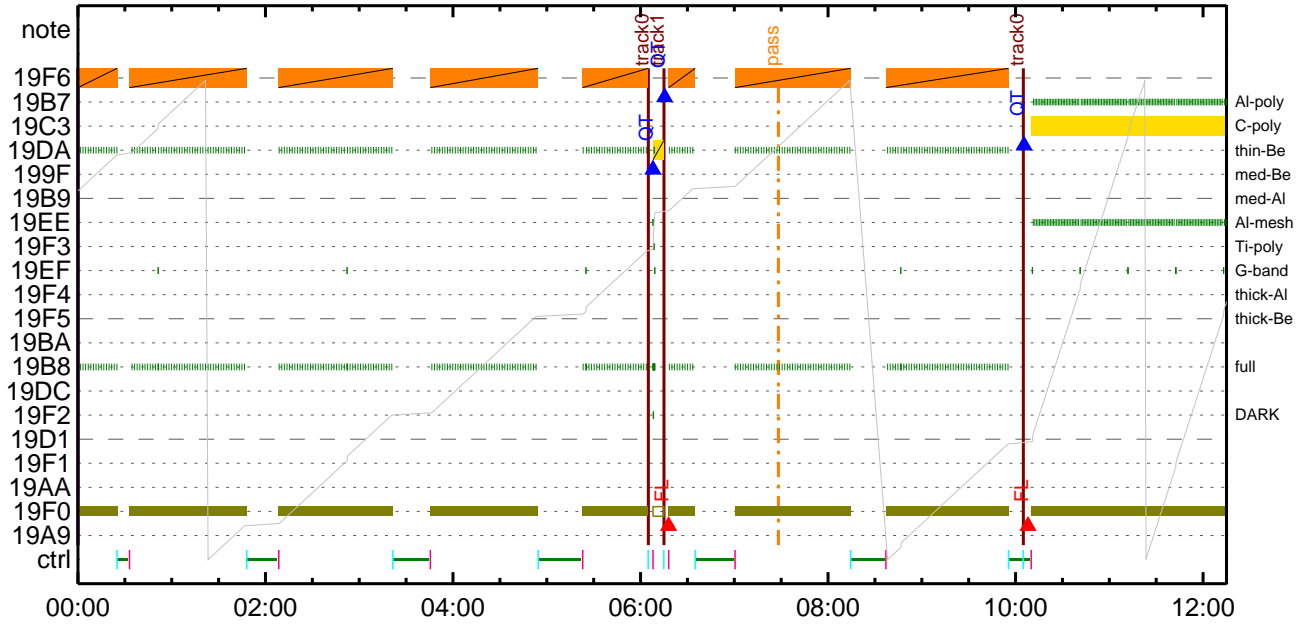
CMDI #0072 2014/02/06



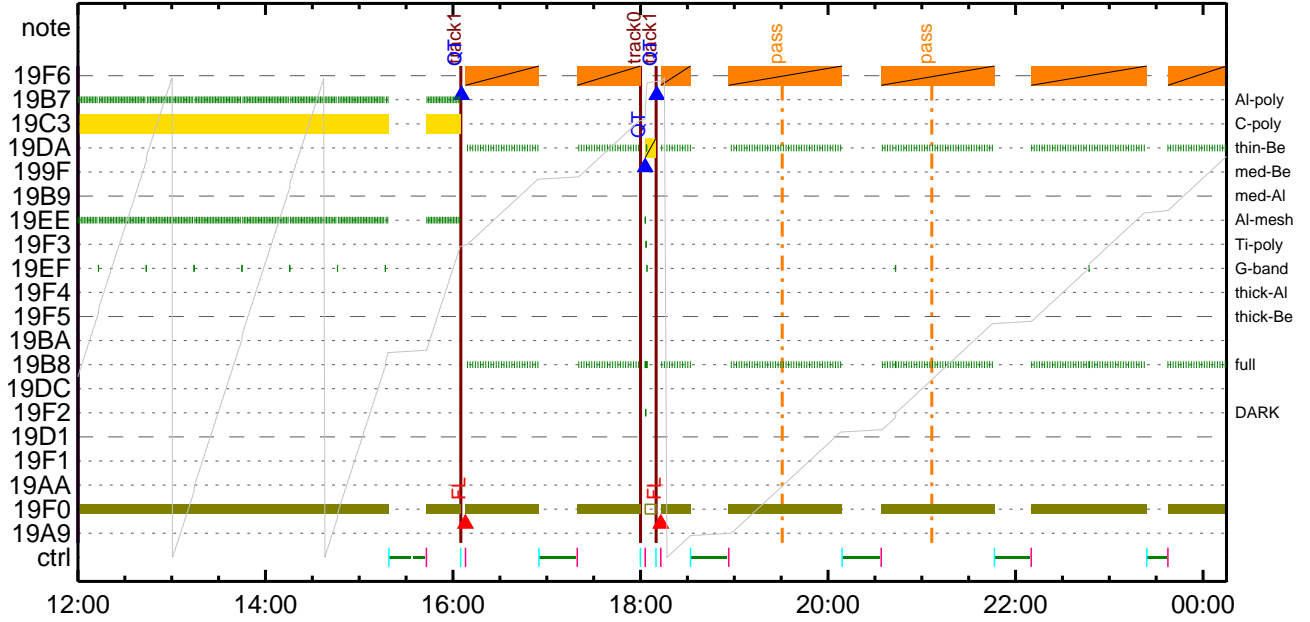
CMDI #0072 2014/02/06



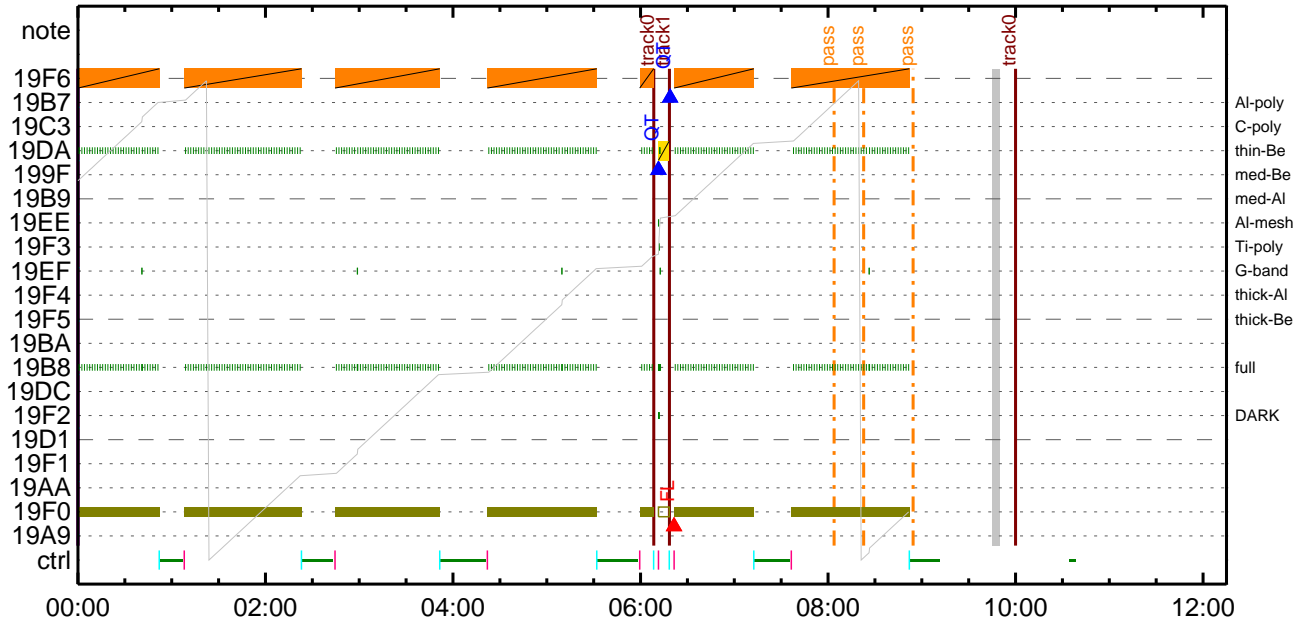
CMDI #0072 2014/02/07



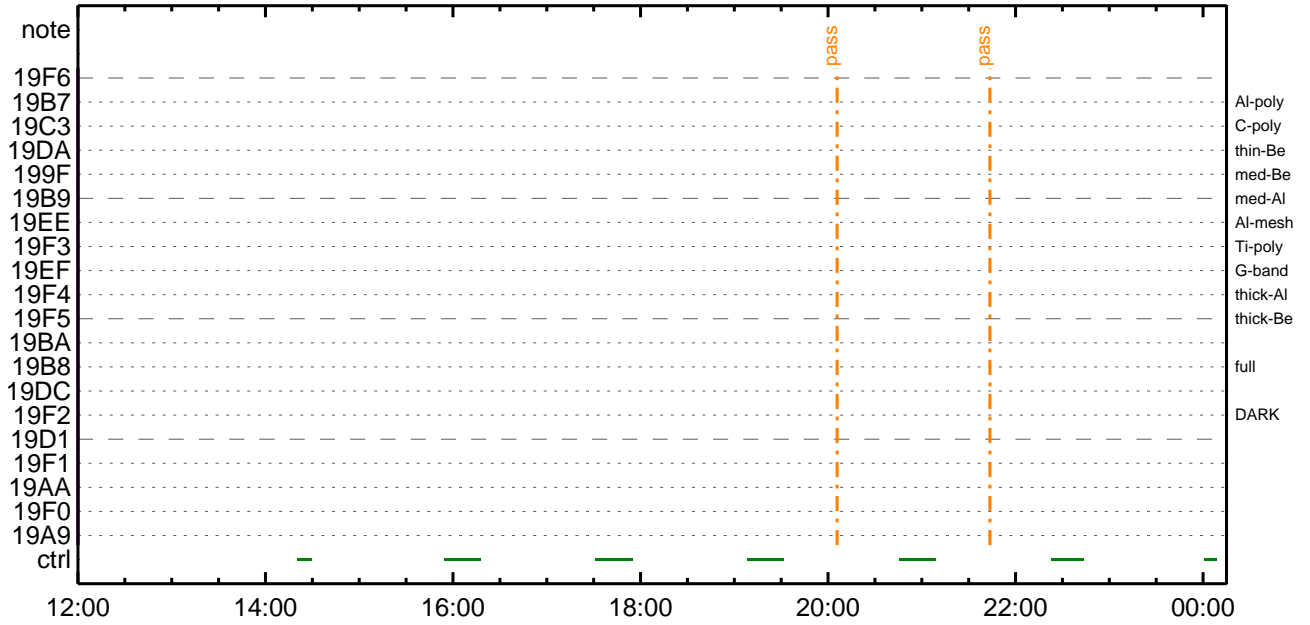
CMDI #0072 2014/02/07



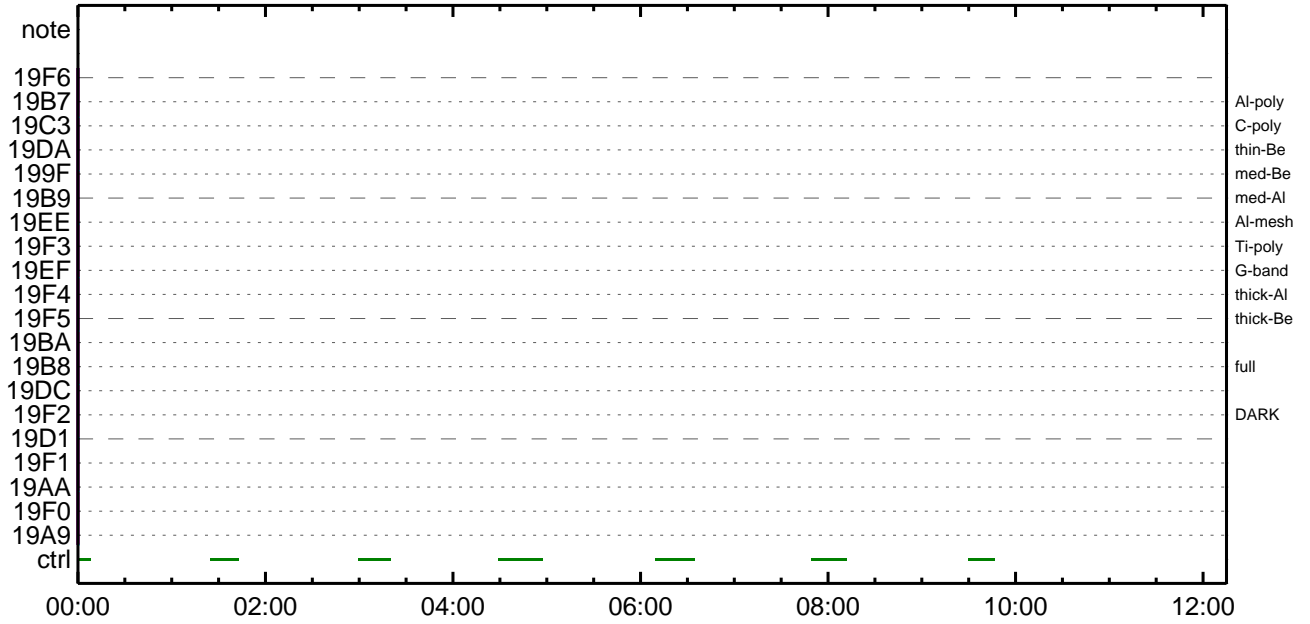
CMDI #0072 2014/02/08



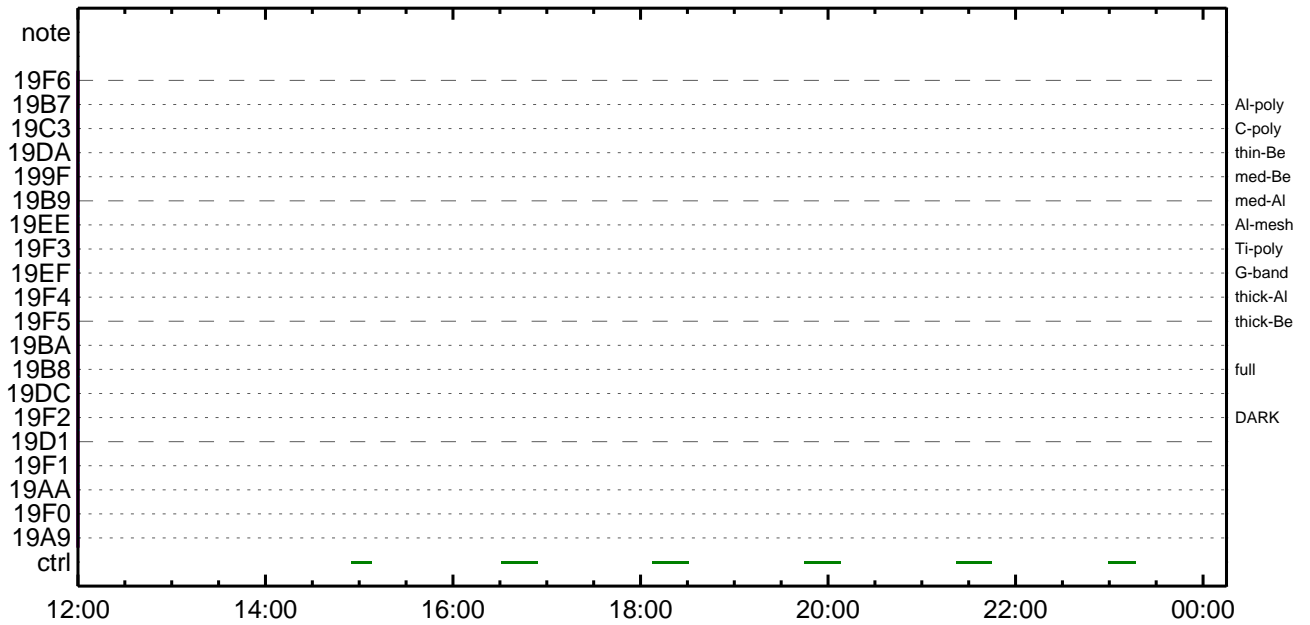
CMDI #0072 2014/02/08



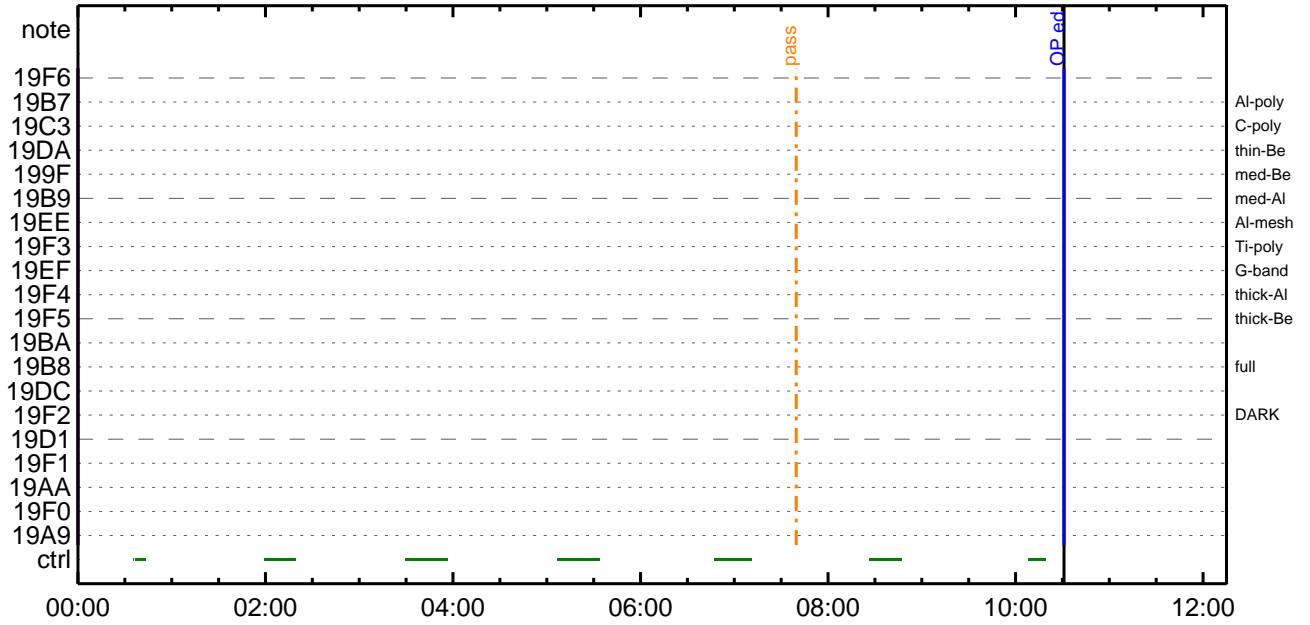
CMDI #0072 2014/02/09



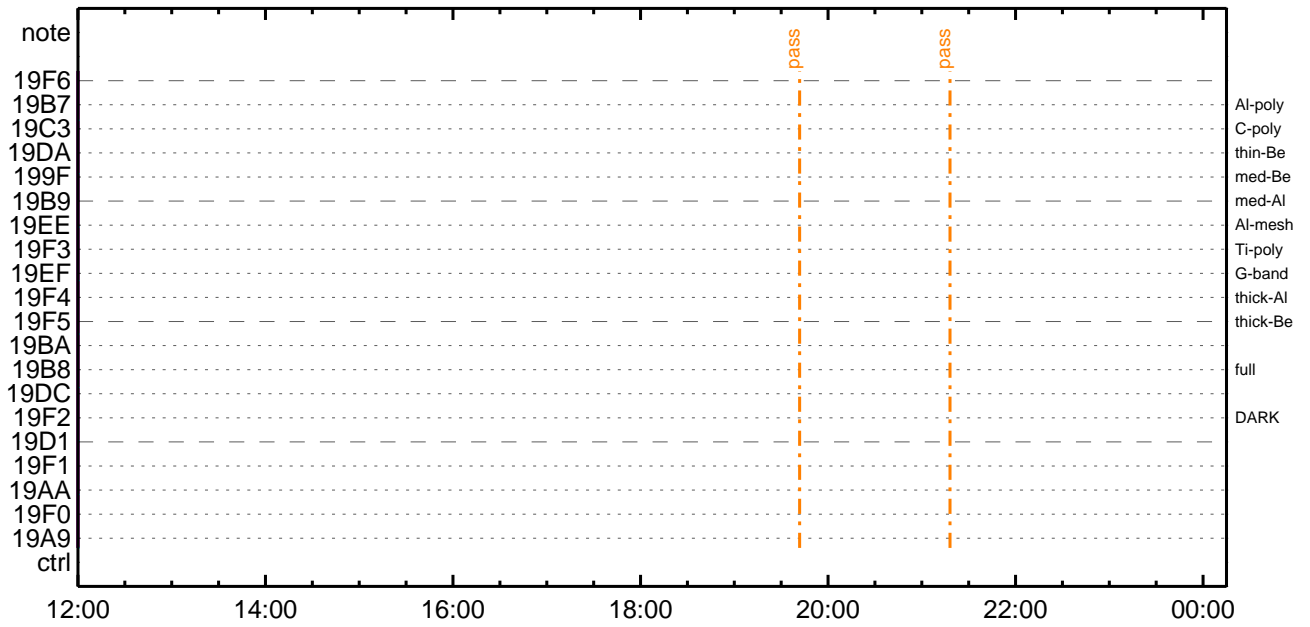
CMDI #0072 2014/02/09



CMDI #0072 2014/02/10



CMDI #0072 2014/02/10




```
0096 C.                0303; SET 03EDUMP 03E±0iYNY¹0Ç¹0a|0³0E; E
0097 C.
0098 . C. TIY³YFYYÖYÉ00dÁDİ¿(UT)
0099 +. TI 2014-02-01 10:22:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0102 C.
0103 +. TI 2014-02-01 10:22:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0106 C.
0107 +. TI 2014-02-01 10:22:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0110 C.
0111 +. TI 2014-02-01 10:26:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        1COUNTUP
0114 C.
0115 C.                °E²¼0İÄê%îÍÑ0İYÁY§YÁY-¹àİÜ
0116 C.                ÇÇ[HK1_TI_CMD_ENA/DIS]              EQ        ENA
0117 C.                ÇÇ[HK1_TI_CMD_NUM]                EQ        4
0118 C.                ÇÇ[HK1_NEXT_EXEC_PIM]              EQ        DHU
0119 C.                ÇÇ[HK1_NEXT_EXEC_DC]              EQ        0xB3
0120 C.
0121 . C. *****
0122 C. TIİİ°èYÁYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                ÇÇ[HK1_DMP_TOP_ADRS_1]              EQ        07
0129 C.                ÇÇ[HK1_DMP_TOP_ADRS_0]              EQ        2B
0130 C.                ÇÇ[HK1_DMP_BLOCK_NUM]              EQ        3
0131 C.                ÇÇ[HK1_DMP_REPEAT_NUM]             EQ        0
0132 C.                ÇÇ[HK1_DMA_DMP_PIM]               EQ        DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                ÇÇ[HK1_PKT_FORM_NO]                EQ        7
0136 C.                ÇÇ[HK1_PKT_GEN_TIME]               EQ        0.25 s
0137 C.                ÇÇ[HK1_S_TLM_BIT_RATE]             EQ        32k
0138 C.                ÇÇ[HK1_X_TLM_BIT_RATE]            EQ        4M
0139 C.                ÇÇ[HK1_DMP_CHK_FLG]               EQ        EXEC
0140 C.
0141 . C. YÁYÖY×½ªİ»0d³İÇ§
0142 C.                ÇÇ[HK1_DMP_CHK_FLG]               EQ        NON
0143 C.
0144 . C. RAM ID=TI_TBL0İÈ¹Ç•è²İOK0d³İÇ§
0145 C.
0146 . C. DHUYâ;¼YÉ;È¼Y¼, Yİ;¼YÈ;È0dİá0¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                ÇÇ[HK1_PKT_FORM_NO]                EQ        2
0150 C.                ÇÇ[HK1_PKT_GEN_TIME]               EQ        0.5S
0151 C.                ÇÇ[HK1_S_TLM_BIT_RATE]             EQ        32K
0152 C.                ÇÇ[HK1_X_TLM_BIT_RATE]            EQ        4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2014-02-01 10:26:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 . C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 . C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2014-02-01 10:26:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2014-02-01 10:26:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 . C. [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2014-02-01 10:26:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 . C. [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP `úÄİ0İ»ö¼Y0ÈÄ0¹0èDCBC•×²è *****
0192 C. (¼0İYÖYÁYÈYBYYÁYÇYè0E¼00¼Ä»Ü0¹0é)
0193 . S. DC-BC dcbc-402:DCBC
```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥D¥!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```

0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop SP table >
0102 +. DC 07-F0 MDP_SP_CTRL_MANU
0103 BC (61)
0104 C. -----
0105 C. MDP_SP_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload SP Observation Table>
0109 . S. RAM ram-281:MDP_OBS_S
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_S >
0113 +. DC 07-F0 MDP_DUMP_SPTBL
0114 BC (83 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_S verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Upload DPL table >
0120 +. DC 07-F0 MDP_FG_CTRL_MANU
0121 BC (51)
0122 C. -----
0123 C. MDP_FG_CTRL_MODE = MANU [ ]
0124 C. -----
0125 C.
0126 C. ¥ç¥Ã¥×¥í;¼¥É°îÁ°ãËSTS_CHKãðOFFãËã¹ãè
0127 C.
0128 . S. RAM ram-271:MDP_DPL
0129 ( )
0130 C.
0131 . C. < Dump RAMID=MDP_DPL >
0132 +. DC 07-F0 MDP_DUMP_FGTBL
0133 BC (82 07 00 38 b8 00 40)
0134 C. -----
0135 C. MDP_DPL verify = OK [ ]
0136 C. -----
0137 C.
0138 C. STS_CHKãðONãËã¹ãè
0139 C.
0140 . C. < Update MDP DSC PAR1 >
0141 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0142 BC (4c)
0143 C. MDP_CMD_CODE = F04C0700[ ]
0144 C. MDP_CMD_CNT (count-up 1) [ ]
0145 C. -----
0146 C.
0147 . C.
0148 C. *****
0149 C. SOT TI command set
0150 C. *****
0151 C. Execute, after the success of TBL upload.
0152 +. TI 2014-02-01 10:26:18.0
0153 DC 07-F0 MDP_SOT_MODE_OBSV
0154 BC (40)
0155 C. -----
0156 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0157 C. -----
0158 C.
0159 C.
0160 C. ***** XRT START *****
0161 C.
0162 +. DC 07-F0 MDP_XRT_CTRL_MANU
0163 BC (c1)
0164 + DC 07-F0 MDP_XRT_MODE_STBY
0165 BC (c3)
0166 . C. ----- Success Verify ? OK / NG_____
0167 C.
0168 C. XRT Obs. Table Upload
0169 . S. RAM ram-291:MDP_OBS_X
0170 ( )
0171 C.
0172 +. DC 07-F0 MDP_DUMP_XRTTBL
0173 BC (84 07 00 00 00 3a d4)
0174 . C. ----- Comparison Check ? OK / ERR _____
0175 C.
0176 C.
0177 +. DC 07-F0 MDP_XRT_ROI_SET
0178 BC (cd 01 b1 b1 04 04)
0179 + DC 07-F0 MDP_XRT_ROI_SET
0180 BC (cd 02 b1 b1 08 08)
0181 + DC 07-F0 MDP_XRT_ROI_SET
0182 BC (cd 03 b1 b1 08 08)
0183 + DC 07-F0 MDP_XRT_ROI_SET
0184 BC (cd 04 b1 b1 06 06)
0185 + DC 07-F0 MDP_XRT_ROI_SET
0186 BC (cd 05 85 83 06 06)
0187 + DC 07-F0 MDP_XRT_ROI_SET
0188 BC (cd 06 85 83 06 06)
0189 + DC 07-F0 MDP_XRT_ROI_SET
0190 BC (cd 07 85 83 08 08)
0191 + DC 07-F0 MDP_XRT_ROI_SET
0192 BC (cd 08 85 b6 08 08)
0193 + DC 07-F0 MDP_XRT_ROI_SET

```



```
0194 BC (cd 09 80 80 20 20)
0195 + DC 07-F0 MDP_XRT_ROI_SET
0196 BC (cd 0a 80 80 20 08)
0197 + DC 07-F0 MDP_XRT_ROI_SET
0198 BC (cd 0b 80 80 08 20)
0199 + DC 07-F0 MDP_XRT_ROI_SET
0200 BC (cd 0f 80 80 06 06)
0201 + DC 07-F0 MDP_XRT_ROI_SET
0202 BC (cd 10 80 80 08 08)
0203 + DC 07-F0 MDP_XRT_FLD_ENA
0204 BC (d8)
0205 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0206 BC (c8)
0207 + DC 07-F0 MDP_XRT_AEC_RESET
0208 BC (d0)
0209 + DC 07-F0 MDP_XRT_ARS_DIS
0210 BC (d5)
0211 + DC 07-F0 MDP_XRT_FLD_RESET
0212 BC (da)
0213 + DC 07-F0 MDP_XRT_QT_PROG_SET
0214 BC (c4 0c)
0215 + DC 07-F0 MDP_XRT_FL_PROG_SET
0216 BC (c5 02)
0217 . C. ----- Success Verify ? OK / NG ____
0218 C.
0219 C.
0220 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0221 C.
0222 + DC 07-F0 MDP_XRT_MODE_OBSV
0223 BC (c2)
0224 + TI 2014-02-01 10:26:02.0
0225 DC 07-F0 MDP_XRT_MODE_OBSV
0226 BC (c2)
0227 . C. ----- Success Verify ? OK / NG ____
0228 C.
0229 C. ***** XRT END *****
0230 C.
0231 . C. ***** MDP 'uAInI»0%YnEAdn1nEdCBC•x2è *****
0232 C. (%a°iY0YAYEYpYEYfYcYèaE%¼a¼A»Üa1nè)
0233 . S. DC-BC dcbc-402:DCBC
0234 (MDP_known_event)
0235 C.
0236 C.
0237 . C. ***** YDY1•I Daily±;iNnE'0a1nèDCBC•x2è *****
0238 . S. DC-BC dcbc-153:DCBC
0239 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0240 C.
0241 C.
0242 . C. ;ãLOS¥Á¥S¥Ã¥~¼A»Ü;ã
0243 C.
0244 . C. ***** LOS *****
0245 C.
```


Feb 01, 14 12:05

XRT_OGLIST_0072.chk

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*** OP Sequence for XRT ***

2014/02/01	10:36:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/01	10:36:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/02/01	10:37:00.0	AOCS_Orе-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	01 00 00 00 00		
2014/02/01	10:37:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/02/01	10:37:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/02/01	10:37:20.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/02/01	10:37:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/02/01	10:37:24.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/01	10:37:26.0	XRT_QT_PROG_SET_446_OG [0x1be]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a		
2014/02/01	10:39:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
2014/02/01	10:40:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/01	14:56:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/01	14:56:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/01	14:56:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/02/01	14:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/01	15:09:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/01	15:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/01	15:18:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/01	15:18:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/01	15:18:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/02/01	15:19:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/01	15:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/01	15:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/01	16:31:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/01	16:31:32.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/01	16:31:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/02/01	16:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/01	16:55:30.0	XRT_Custom_430_OG [0x1ae]					
2014/02/01	16:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/01	17:45:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/01	17:45:26.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2014/02/01	17:45:30.0	AOCS_Orе-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2014/02/01	17:45:46.0	XRT_FLD_DIS_434_OG [0x1b2]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2014/02/01	17:48:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2014/02/01	17:48:26.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/02/01	17:48:28.0	XRT_QT_PROG_SET_447_OG [0x1bf]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2014/02/01	17:48:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/01	18:05:24.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/01	18:05:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/02/01	18:05:30.0	AOCS_Orе-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	01 00 00 00 00		
2014/02/01	18:05:46.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/02/01	18:05:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/02/01	18:05:50.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/02/01	18:05:52.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/02/01	18:05:54.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/01	18:05:56.0	XRT_QT_PROG_SET_446_OG [0x1be]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a		
2014/02/01	18:08:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		

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2014/02/01	18:09:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/01	18:09:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/01	18:09:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/01	18:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/01	18:32:00.0	XRT_Custom_430_OG [0x1ae]			
2014/02/01	18:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/01	19:46:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/01	19:46:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/01	19:46:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/01	19:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/01	20:09:30.0	XRT_Custom_430_OG [0x1ae]			
2014/02/01	20:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/01	21:23:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/01	21:23:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/01	21:23:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/01	21:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/01	21:46:00.0	XRT_Custom_430_OG [0x1ae]			
2014/02/01	21:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/01	23:00:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/01	23:00:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/01	23:00:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/01	23:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/01	23:18:00.5	XRT_Custom_430_OG [0x1ae]			
2014/02/01	23:19:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/02	00:36:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/02	00:36:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/02	00:36:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/02	00:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/02	00:44:30.0	XRT_Custom_430_OG [0x1ae]			
2014/02/02	00:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/02	02:00:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/02	02:00:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/02	02:00:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/02	02:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/02	02:20:30.0	XRT_Custom_430_OG [0x1ae]			
2014/02/02	02:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/02	03:30:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/02	03:30:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/02	03:30:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/02	03:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/02	03:57:30.5	XRT_Custom_430_OG [0x1ae]			
2014/02/02	03:58:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/02	05:08:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/02	05:08:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/02	05:08:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/02	05:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/02	05:35:00.0	XRT_Custom_430_OG [0x1ae]			
2014/02/02	05:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/02	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/02	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/02/02	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]			

2014/02/02	06:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]	AOCU_NM	5	02-76	00	00	00	00	00
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/02/02	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]		1	07-F0	c9				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/02/02	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/02	06:02:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]		2	07-F0	c4	11			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2014/02/02	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/02	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/02	06:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]		4	07-F8	22	fe	97	00	
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/02/02	06:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]		5	02-76	01	00	00	00	00
			AOCU_NM	5	02-76	01	00	00	00	00
2014/02/02	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]		1	07-F0	d8				
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/02	06:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]		1	07-F0	c8				
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/02	06:10:20.0	XRT_AEC_RESET_413_OG [0x19d]		1	07-F0	d0				
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/02	06:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/02	06:10:24.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/02	06:10:26.0	XRT_QT_PROG_SET_446_OG [0x1be]		2	07-F0	c4	0a			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0a			
2014/02/02	06:12:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]		2	07-F0	c5	02			
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2014/02/02	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/02	06:48:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/02	06:48:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/02	06:48:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/02	06:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/02	07:12:30.0	XRT_Custom_430_OG [0x1ae]								
2014/02/02	07:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/02	08:28:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/02	08:28:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/02	08:28:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/02	08:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/02	08:49:00.0	XRT_Custom_430_OG [0x1ae]								
2014/02/02	08:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/02	10:09:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/02	10:09:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/02	10:09:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/02	10:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/02	10:20:01.0	XRT_Custom_430_OG [0x1ae]								
2014/02/02	10:21:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/02	14:00:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/02	14:00:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/02	14:00:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/02	14:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/02	14:03:30.0	XRT_Custom_430_OG [0x1ae]								
2014/02/02	14:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/02	15:31:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/02	15:31:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/02	15:31:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/02	15:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/02	15:55:00.0	XRT_Custom_430_OG [0x1ae]								
2014/02/02	15:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/02	17:08:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/02	17:08:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/02	17:08:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								

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2014/02/02	17:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/02/02	17:31:30.0	XRT_Custom_430_OG [0x1ae]							
2014/02/02	17:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/02/02	18:02:54.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/02	18:02:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2014/02/02	18:03:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
			AOCU_NM	5	02-76	00 00 00 00 00			
2014/02/02	18:03:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
			MDP_XRT_FLD_DIS	1	07-F0	d9			
2014/02/02	18:05:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2014/02/02	18:05:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/02/02	18:05:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11			
2014/02/02	18:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/02/02	18:12:54.0	XRT_CTRL_MANU_402_OG [0x192]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/02	18:12:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2014/02/02	18:13:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
			AOCU_NM	5	02-76	01 00 00 00 00			
2014/02/02	18:13:16.0	XRT_FLD_ENA_411_OG [0x19b]							
			MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/02/02	18:13:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/02/02	18:13:20.0	XRT_AEC_RESET_413_OG [0x19d]							
			MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/02/02	18:13:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/02/02	18:13:24.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/02/02	18:13:26.0	XRT_QT_PROG_SET_446_OG [0x1be]							
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a			
2014/02/02	18:15:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 02			
2014/02/02	18:16:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/02/02	18:45:00.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/02	18:45:02.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/02/02	18:45:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/02/02	18:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/02/02	19:08:30.0	XRT_Custom_430_OG [0x1ae]							
2014/02/02	19:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/02/02	20:22:00.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/02	20:22:02.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/02/02	20:22:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/02/02	20:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/02/02	20:45:30.0	XRT_Custom_430_OG [0x1ae]							
2014/02/02	20:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/02/02	21:59:30.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/02	21:59:32.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/02/02	21:59:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/02/02	22:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/02/02	22:21:30.0	XRT_Custom_430_OG [0x1ae]							
2014/02/02	22:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/02/02	23:37:00.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/02	23:37:02.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/02/02	23:37:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/02/02	23:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/02/02	23:47:30.0	XRT_Custom_430_OG [0x1ae]							
2014/02/02	23:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/02/03	01:03:00.0	XRT_CTRL_MANU_400_OG [0x190]							
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/03	01:03:02.0	XRT_FLD_RESET_415_OG [0x19f]							
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/02/03	01:03:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							

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2014/02/03	01:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/03	01:20:00.0	XRT_Custom_430_OG [0x1ae]						
2014/02/03	01:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/03	02:36:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/03	02:36:02.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/03	02:36:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/02/03	02:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/03	02:56:30.0	XRT_Custom_430_OG [0x1ae]						
2014/02/03	02:57:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/03	04:05:00.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/03	04:05:02.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/03	04:05:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/02/03	04:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/03	04:34:00.0	XRT_Custom_430_OG [0x1ae]						
2014/02/03	04:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/03	05:45:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/03	05:45:32.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/03	05:45:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/02/03	05:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/03	06:11:30.0	XRT_Custom_430_OG [0x1ae]						
2014/02/03	06:12:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/03	06:21:24.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/03	06:21:26.0	XRT_FOCUS_POSITION_403_OG [0x193]						
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2014/02/03	06:21:30.0	AOCS_Orе-point_Start_2_OG [0x098]						
			AOCU_NM	5	02-76	00 00 00 00 00		
2014/02/03	06:21:46.0	XRT_FLD_DIS_434_OG [0x1b2]						
			MDP_XRT_FLD_DIS	1	07-F0	d9		
2014/02/03	06:24:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]						
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2014/02/03	06:24:26.0	XRT_ARS_DIS_423_OG [0x1a7]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/02/03	06:24:28.0	XRT_QT_PROG_SET_447_OG [0x1bf]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2014/02/03	06:24:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/03	06:31:24.0	XRT_CTRL_MANU_402_OG [0x192]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/03	06:31:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]						
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/02/03	06:31:30.0	AOCS_Orе-point_Start_1_OG [0x097]						
			AOCU_NM	5	02-76	01 00 00 00 00		
2014/02/03	06:31:46.0	XRT_FLD_ENA_411_OG [0x19b]						
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/02/03	06:31:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]						
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/02/03	06:31:50.0	XRT_AEC_RESET_413_OG [0x19d]						
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/02/03	06:31:52.0	XRT_ARS_DIS_423_OG [0x1a7]						
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/02/03	06:31:54.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/03	06:31:56.0	XRT_QT_PROG_SET_446_OG [0x1be]						
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a		
2014/02/03	06:34:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]						
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
2014/02/03	06:34:30.0	XRT_CTRL_AUTO_408_OG [0x198]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/03	07:25:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/03	07:25:32.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/03	07:25:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						
			MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/02/03	07:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]						
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/03	07:48:30.0	XRT_Custom_430_OG [0x1ae]						
2014/02/03	07:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]						
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/03	09:05:30.0	XRT_CTRL_MANU_400_OG [0x190]						
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/03	09:05:32.0	XRT_FLD_RESET_415_OG [0x19f]						
			MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/03	09:05:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]						

2014/02/03	09:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/03	09:24:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/03	09:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/03	14:32:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/03	14:32:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/03	14:32:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/03	14:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/03	14:43:30.0	XRT_Custom_430_OG [0x1ae]					
2014/02/03	14:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/03	16:07:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/03	16:07:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/03	16:07:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/03	16:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/03	16:31:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/03	16:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/03	17:44:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/03	17:44:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/03	17:44:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/03	17:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/03	18:07:24.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/03	18:07:26.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/02/03	18:07:30.0	AOCS_Or-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2014/02/03	18:07:46.0	XRT_FLD_DIS_434_OG [0x1b2]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/02/03	18:10:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/02/03	18:10:26.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/03	18:10:28.0	XRT_QT_PROG_SET_447_OG [0x1bf]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2014/02/03	18:10:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/03	18:17:24.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/03	18:17:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2014/02/03	18:17:30.0	AOCS_Or-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	01 00 00 00 00	
2014/02/03	18:17:46.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/02/03	18:17:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/02/03	18:17:50.0	XRT_AEC_RESET_413_OG [0x19d]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/02/03	18:17:52.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/03	18:17:54.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/03	18:17:56.0	XRT_QT_PROG_SET_446_OG [0x1be]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2014/02/03	18:20:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2014/02/03	18:20:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/03	19:21:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/03	19:21:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/03	19:21:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/03	19:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/03	19:44:30.0	XRT_Custom_430_OG [0x1ae]					
2014/02/03	19:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/03	20:58:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/03	20:58:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/03	20:58:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/03	21:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	

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2014/02/03	21:22:00.0	XRT_Custom_430_OG [0x1ae]							
2014/02/03	21:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/03	22:36:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/03	22:36:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/03	22:36:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/03	22:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/03	22:56:30.0	XRT_Custom_430_OG [0x1ae]							
2014/02/03	22:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/04	00:13:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/04	00:13:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/04	00:13:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/04	00:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/04	00:21:00.0	XRT_Custom_430_OG [0x1ae]							
2014/02/04	00:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/04	01:36:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/04	01:36:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/04	01:36:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/04	01:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/04	01:56:00.0	XRT_Custom_430_OG [0x1ae]							
2014/02/04	01:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/04	03:12:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/04	03:12:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/04	03:12:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/04	03:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/04	03:33:00.0	XRT_Custom_430_OG [0x1ae]							
2014/02/04	03:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/04	04:42:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/04	04:42:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/04	04:42:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/04	04:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/04	05:10:30.0	XRT_Custom_430_OG [0x1ae]							
2014/02/04	05:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/04	05:54:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/04	05:54:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/02/04	05:55:00.0	AOCs_Orе-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/02/04	05:55:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/02/04	05:57:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/02/04	05:57:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/04	05:57:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2014/02/04	05:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/04	06:04:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/04	06:04:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/02/04	06:05:00.0	AOCs_Orе-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2014/02/04	06:05:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/04	06:05:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/04	06:05:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/04	06:05:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/04	06:05:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/04	06:05:26.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2014/02/04	06:07:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]							

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2014/02/04	06:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/04	06:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/04	06:23:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/04	06:23:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/04	06:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/04	06:48:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/04	06:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/04	08:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/04	08:03:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/04	08:03:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/04	08:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/04	08:24:30.0	XRT_Custom_430_OG [0x1ae]					
2014/02/04	08:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/04	09:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/04	09:43:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/04	09:43:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/04	09:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/04	09:58:30.0	XRT_Custom_430_OG [0x1ae]					
2014/02/04	09:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/04	15:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/04	15:07:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/04	15:07:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/04	15:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/04	15:30:30.0	XRT_Custom_430_OG [0x1ae]					
2014/02/04	15:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/04	16:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/04	16:43:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/04	16:43:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/04	16:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/04	17:07:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/04	17:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/04	17:57:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/04	17:57:26.0	XRT_FOCUS_RECALIBRATE_433_OG [0x1b1]	XRT_FOCUS_RECAL	2	07-F8	78	00
2014/02/04	17:57:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00
2014/02/04	18:01:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2014/02/04	18:01:46.0	XRT_FLD_DIS_404_OG [0x194]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/02/04	18:01:48.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/02/04	18:01:50.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/04	18:01:52.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2014/02/04	18:01:54.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/04	18:07:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/04	18:07:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2014/02/04	18:07:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01	00 00 00 00
2014/02/04	18:07:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/02/04	18:07:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/02/04	18:07:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/02/04	18:07:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/04	18:07:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/04	18:07:56.0	XRT_QT_PROG_SET_446_OG [0x1be]					

2014/02/04	18:10:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_OT_PROG_SET	2	07-F0	c4	0a
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	02
2014/02/04	18:10:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/04	18:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/04	18:20:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/04	18:20:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/04	18:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/04	18:44:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/04	18:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/04	19:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/04	19:57:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/04	19:57:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/04	20:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/04	20:21:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/04	20:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/04	21:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/04	21:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/04	21:35:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/04	21:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/04	21:57:30.0	XRT_Custom_430_OG [0x1ae]					
2014/02/04	21:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/04	23:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/04	23:12:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/04	23:12:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/04	23:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/04	23:27:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/04	23:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/05	00:43:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/05	00:43:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/05	00:43:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/05	00:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/05	00:56:00.5	XRT_Custom_430_OG [0x1ae]					
2014/02/05	00:57:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/05	02:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/05	02:11:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/05	02:11:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/05	02:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/05	02:32:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/05	02:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/05	03:39:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/05	03:39:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/05	03:39:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/05	03:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/05	04:09:30.0	XRT_Custom_430_OG [0x1ae]					
2014/02/05	04:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/05	05:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/05	05:20:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/05	05:20:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/05	05:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/05	05:47:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/05	05:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2014/02/05	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/05	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/02/05	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00 00 00 00 00
2014/02/05	06:00:16.0	XRT_FLD_DIS_434_OG [0x1b2] MDP_XRT_FLD_DIS	1	07-F0	d9
2014/02/05	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/02/05	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2014/02/05	06:02:58.0	XRT_QT_PROG_SET_447_OG [0x1bf] MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2014/02/05	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/05	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/05	06:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/02/05	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	01 00 00 00 00
2014/02/05	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2014/02/05	06:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/02/05	06:10:20.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0	d0
2014/02/05	06:10:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2014/02/05	06:10:24.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/05	06:10:26.0	XRT_QT_PROG_SET_446_OG [0x1be] MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a
2014/02/05	06:12:58.0	XRT_FL_PROG_SET_417_OG [0x1a1] MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2014/02/05	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/05	07:00:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/05	07:00:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/05	07:00:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/05	07:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/05	07:24:00.0	XRT_Custom_430_OG [0x1ae] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/05	07:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/05	08:40:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/05	08:40:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/05	08:40:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/05	08:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/05	09:00:30.0	XRT_Custom_430_OG [0x1ae] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/05	09:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/05	10:22:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/05	10:22:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/05	10:22:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/05	10:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/05	10:29:30.0	XRT_Custom_430_OG [0x1ae] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/05	10:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/05	14:10:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/05	14:10:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/05	14:10:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/05	14:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/05	14:17:00.0	XRT_Custom_430_OG [0x1ae] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/05	14:18:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/05	15:43:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/05	15:43:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/05	15:43:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/05	15:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/05	16:06:30.0	XRT_Custom_430_OG [0x1ae] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/05	16:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2014/02/05	17:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/05	17:19:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/05	17:19:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/05	17:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/05	17:43:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/05	17:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/05	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/05	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/02/05	18:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2014/02/05	18:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/02/05	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/02/05	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/05	18:02:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2014/02/05	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/05	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/05	18:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2014/02/05	18:10:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00	
2014/02/05	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/02/05	18:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/02/05	18:10:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/02/05	18:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/05	18:10:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/05	18:10:26.0	XRT_QT_PROG_SET_446_OG [0x1be]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a	
2014/02/05	18:12:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2014/02/05	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/05	18:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/05	18:56:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/05	18:56:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/05	18:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/05	19:20:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/05	19:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/05	20:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/05	20:34:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/05	20:34:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/05	20:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/05	20:57:30.0	XRT_Custom_430_OG [0x1ae]					
2014/02/05	20:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/05	22:11:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/05	22:11:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/05	22:11:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/05	22:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/05	22:33:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/05	22:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/05	23:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/05	23:48:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/05	23:48:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/05	23:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/05	23:58:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/05	23:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

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2014/02/06	01:13:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/06	01:13:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/06	01:13:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/06	01:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/06	01:31:30.0	XRT_Custom_430_OG [0x1ae]							
2014/02/06	01:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/06	02:47:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/06	02:47:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/06	02:47:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/06	02:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/06	03:08:30.0	XRT_Custom_430_OG [0x1ae]							
2014/02/06	03:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/06	04:17:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/06	04:17:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/06	04:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/06	04:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/06	04:45:30.0	XRT_Custom_430_OG [0x1ae]							
2014/02/06	04:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/06	05:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/06	05:29:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/02/06	05:30:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/02/06	05:30:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/02/06	05:32:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/02/06	05:32:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/06	05:32:58.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2014/02/06	05:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/06	05:39:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/06	05:39:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/02/06	05:40:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2014/02/06	05:40:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/06	05:40:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/06	05:40:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/06	05:40:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/06	05:40:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/06	05:40:26.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2014/02/06	05:42:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/02/06	05:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/06	05:57:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/06	05:57:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/06	05:57:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/06	06:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/06	06:23:00.0	XRT_Custom_430_OG [0x1ae]							
2014/02/06	06:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/06	07:37:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/06	07:37:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/06	07:37:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/06	07:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/06	08:00:00.0	XRT_Custom_430_OG [0x1ae]							
2014/02/06	08:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2014/02/06	09:17:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/06	09:17:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/06	09:17:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/06	09:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/06	09:34:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/06	09:34:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/02/06	09:35:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 b1 cb 01 58				
2014/02/06	09:35:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/06	09:35:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/06	09:35:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/06	09:35:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/06	09:35:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/06	09:35:26.0	XRT_QT_PROG_SET_407_OG [0x197]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2014/02/06	09:37:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/02/06	09:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/06	14:44:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/06	14:44:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/06	14:44:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/06	14:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/06	14:55:30.0	XRT_Custom_430_OG [0x1ae]							
2014/02/06	14:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/06	15:34:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/06	15:34:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/02/06	15:35:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2014/02/06	15:35:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/06	15:35:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/06	15:35:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/06	15:35:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/06	15:35:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/06	15:35:26.0	XRT_QT_PROG_SET_446_OG [0x1be]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2014/02/06	15:37:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/02/06	15:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/06	16:19:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/06	16:19:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/06	16:19:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/06	16:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/06	16:42:30.0	XRT_Custom_430_OG [0x1ae]							
2014/02/06	16:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/06	17:56:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/06	17:56:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/06	17:56:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/06	17:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/06	18:19:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/06	18:19:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/02/06	18:19:30.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/02/06	18:19:46.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/02/06	18:22:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/02/06	18:22:26.0	XRT_ARS_DIS_423_OG [0x1a7]							

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2014/02/06	18:22:28.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2014/02/06	18:22:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/06	18:29:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/06	18:29:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/02/06	18:29:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00
2014/02/06	18:29:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2014/02/06	18:29:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/02/06	18:29:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2014/02/06	18:29:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/02/06	18:29:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/06	18:29:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2014/02/06	18:32:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2014/02/06	18:32:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/06	19:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/06	19:33:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/06	19:33:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/06	19:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/06	19:56:30.0	XRT_Custom_430_OG [0x1ae]				
2014/02/06	19:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/06	21:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/06	21:10:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/06	21:10:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/06	21:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/06	21:33:30.0	XRT_Custom_430_OG [0x1ae]				
2014/02/06	21:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/06	22:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/06	22:47:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/06	22:47:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/06	22:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/06	23:08:00.0	XRT_Custom_430_OG [0x1ae]				
2014/02/06	23:09:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/07	00:25:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/07	00:25:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/07	00:25:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/07	00:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/07	00:32:00.0	XRT_Custom_430_OG [0x1ae]				
2014/02/07	00:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/07	01:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/07	01:48:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/07	01:48:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/07	01:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/07	02:07:30.0	XRT_Custom_430_OG [0x1ae]				
2014/02/07	02:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/07	03:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/07	03:21:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/07	03:21:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/07	03:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/07	03:44:30.5	XRT_Custom_430_OG [0x1ae]				
2014/02/07	03:45:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2014/02/07	04:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/07	04:54:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/07	04:54:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/02/07	04:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/07	05:22:00.0	XRT_Custom_430_OG [0x1ae]						
2014/02/07	05:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/07	06:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/07	06:04:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2014/02/07	06:05:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2014/02/07	06:05:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2014/02/07	06:07:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2014/02/07	06:07:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/02/07	06:07:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2014/02/07	06:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/07	06:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/07	06:14:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2014/02/07	06:15:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00		
2014/02/07	06:15:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/02/07	06:15:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/02/07	06:15:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/02/07	06:15:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/02/07	06:15:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/07	06:15:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14		
2014/02/07	06:17:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
2014/02/07	06:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/07	06:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/07	06:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/07	06:35:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/02/07	06:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/07	06:59:30.0	XRT_Custom_430_OG [0x1ae]						
2014/02/07	07:00:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/07	08:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/07	08:14:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/07	08:14:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/02/07	08:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/07	08:36:00.0	XRT_Custom_430_OG [0x1ae]						
2014/02/07	08:37:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/07	09:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/07	09:55:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/07	09:55:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/02/07	09:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/07	10:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/07	10:04:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/02/07	10:05:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 53 8d 01 58		
2014/02/07	10:05:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/02/07	10:05:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/02/07	10:05:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/02/07	10:05:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		

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2014/02/07	10:05:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/07	10:05:26.0	XRT_QT_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12
2014/02/07	10:07:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02
2014/02/07	10:09:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/07	10:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/07	15:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/07	15:19:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/07	15:19:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/07	15:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/07	15:42:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/07	15:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/07	16:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/07	16:04:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2014/02/07	16:05:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00	00
2014/02/07	16:05:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/02/07	16:05:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/02/07	16:05:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/02/07	16:05:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/07	16:05:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/07	16:05:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14
2014/02/07	16:07:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02
2014/02/07	16:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/07	16:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/07	16:55:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/07	16:55:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/07	16:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/07	17:18:30.0	XRT_Custom_430_OG [0x1ae]					
2014/02/07	17:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/07	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/07	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2014/02/07	18:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00	00
2014/02/07	18:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/02/07	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/02/07	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/07	18:02:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2014/02/07	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/07	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/07	18:09:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00
2014/02/07	18:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00	00
2014/02/07	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/02/07	18:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/02/07	18:10:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/02/07	18:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/07	18:10:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/07	18:10:26.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14
2014/02/07	18:12:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02
2014/02/07	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/07	18:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2014/02/07	18:32:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/07	18:32:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/07	18:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/07	18:55:30.0	XRT_Custom_430_OG [0x1ae]							
2014/02/07	18:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/07	20:09:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/07	20:09:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/07	20:09:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/07	20:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/07	20:33:00.0	XRT_Custom_430_OG [0x1ae]							
2014/02/07	20:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/07	21:46:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/07	21:46:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/07	21:46:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/07	21:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/07	22:09:00.0	XRT_Custom_430_OG [0x1ae]							
2014/02/07	22:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/07	23:24:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/07	23:24:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/07	23:24:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/07	23:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/07	23:36:30.0	XRT_Custom_430_OG [0x1ae]							
2014/02/07	23:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/08	00:52:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/08	00:52:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/08	00:52:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/08	00:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/08	01:07:00.0	XRT_Custom_430_OG [0x1ae]							
2014/02/08	01:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/08	02:23:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/08	02:23:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/08	02:23:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/08	02:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/08	02:43:30.0	XRT_Custom_430_OG [0x1ae]							
2014/02/08	02:44:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/08	03:51:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/08	03:51:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/08	03:51:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/08	03:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/08	04:21:00.0	XRT_Custom_430_OG [0x1ae]							
2014/02/08	04:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/08	05:32:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/08	05:32:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/08	05:32:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/08	05:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/08	05:58:30.0	XRT_Custom_430_OG [0x1ae]							
2014/02/08	05:59:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/08	06:08:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/08	06:08:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/02/08	06:08:30.0	AOCs_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/02/08	06:08:46.0	XRT_FLD_DIS_434_OG [0x1b2]							

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2014/02/08	06:11:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2014/02/08	06:11:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/02/08	06:11:28.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/02/08	06:11:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2014/02/08	06:18:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/08	06:18:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/08	06:18:30.0	AOCS_Or-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/02/08	06:18:46.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00
2014/02/08	06:18:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2014/02/08	06:18:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/02/08	06:18:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2014/02/08	06:18:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/02/08	06:18:56.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/08	06:21:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2014/02/08	06:21:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2014/02/08	07:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/08	07:12:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/08	07:12:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/08	07:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/08	07:35:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/08	07:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2014/02/08	08:52:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/08	08:52:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/08	08:52:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/08	08:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/08	10:00:00.0	AOCS_Or-point_Start_2_OG [0x098]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
		AOCU_NM		5	02-76	00 00 00 00 00