

XRT Timeline to be uploaded on 2014/02/11

Period: 2014/02/11 11:06:00 - 2014/02/15 10:18:00

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Normal mode

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XOB #19F9: multifilter 10min cadence for EIS sensitivity support, 384x384, 1064x1048, G-band (33ms/45ms leak)													
Term		Pointing (x, y)				Comment							
02/11 11:19:00 - 02/11 17:43:00		Track (-922.3, 238.0) ^{02/11 11:16:00}				# OP start + 10min, Returning AR on NE limb for EIS.							
PROG= 16 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 8 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Seqn= 24 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Subr= 2 3-time(s) 2.0sec													
└─ Seqn= 16 1-time(s) 600.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Al/Open	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19DA: Synoptic Q95 2x2 - Al/mesh(5/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/362/1443) + Thin													
Term		Pointing (x, y)				Comment							
02/11 18:09:30 - 02/11 18:16:24		Fixed (0.0, 0.0)				synoptic, shifted 6.5 min							
02/12 05:56:30 - 02/12 06:03:24		Fixed (0.0, 0.0)				synoptic, shifted -6.5 min							
02/12 17:28:00 - 02/12 17:34:54		Fixed (0.0, 0.0)				synoptic, shifted -31.0 min							
02/13 06:19:30 - 02/13 06:26:24		Fixed (0.0, 0.0)				synoptic, shifted 16.5 mins							
PROG= 17 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 84 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 86 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 95 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 6 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19E2: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)													
Term		Pointing (x, y)				Comment							
02/11 18:19:30 - 02/12 05:53:24		Track (-692.0, -193.6) ^{02/11 18:16:30}				# AR 11976 observations.							
02/12 06:06:30 - 02/12 17:24:54		Track (-616.6, -184.8) ^{02/12 06:03:30}				# AR cont.							
PROG= 01 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 8 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2 2-time(s) 2.0sec													
└─ Seqn= 24 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 42 4-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 62 20-time(s) 90.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec

Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19F3: CME watch - 4x4 - AEC 2 - Be-thin (Be-Med) - 240s cad

Term	Pointing (x, y)	Comment
02/12 17:38:00 - 02/12 18:19:00	Fixed (-15.0, -965.0)	# HOP 79 S-N scan. 1/20.
02/12 18:46:30 - 02/12 19:13:24	Fixed (-15.0, -865.0)	# 2/20
02/12 19:16:30 - 02/12 19:56:00	Fixed (-15.0, -765.0)	# 3/20
02/12 20:23:30 - 02/12 20:50:24	Fixed (-15.0, -665.0)	# 4/20
02/12 20:53:30 - 02/12 21:33:30	Fixed (-15.0, -565.0)	# 5/20
02/12 22:00:00 - 02/12 22:26:54	Fixed (-15.0, -465.0)	# 6/20
02/12 22:30:00 - 02/12 23:11:00	Fixed (-15.0, -365.0)	# 7/20
02/12 23:30:00 - 02/12 23:56:54	Fixed (-15.0, -265.0)	# 8/20
02/13 00:00:00 - 02/13 00:42:00	Fixed (-15.0, -165.0)	# 9/20
02/13 00:58:30 - 02/13 01:25:24	Fixed (-15.0, -65.0)	# 10/20
02/13 01:41:06 - 02/13 02:10:00	Fixed (-15.0, 25.0)	# 11/20
02/13 02:34:31 - 02/13 03:01:24	Fixed (-15.0, 125.0)	# 12/20
02/13 03:04:30 - 02/13 03:38:00	Fixed (-15.0, 225.0)	# 13/20
02/13 04:12:00 - 02/13 04:38:54	Fixed (-15.0, 325.0)	# 14/20
02/13 04:42:00 - 02/13 05:18:30	Fixed (-15.0, 425.0)	# 15/20
02/13 05:49:30 - 02/13 06:16:24	Fixed (-15.0, 525.0)	# 16/20
02/13 06:29:30 - 02/13 06:58:30	Fixed (-15.0, 625.0)	# 17/20
02/13 07:26:30 - 02/13 07:53:24	Fixed (-15.0, 725.0)	# 18/20
02/13 07:56:30 - 02/13 08:38:30	Fixed (-15.0, 825.0)	# 19/20
02/13 09:03:00 - 02/13 09:25:00	Fixed (-15.0, 925.0)	# 20/20

PROG= 03 Inf.-time(s)

Subr= 1 15-time(s) 240.0sec

Seqn= 19 1-time(s) 4.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19DC: Synoptic 9 Filter w/ Triplet Exposures - 2x2 Q98 - Gband(8ms)

Term	Pointing (x, y)	Comment
02/13 01:28:30 - 02/13 01:38:00	Fixed (-15.0, 25.0)	# 11/20

PROG= 07 1-time(s)

Subr= 1 1-time(s) 12.0sec

Seqn= 84 1-time(s) 4.0sec

Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 5 1-time(s) 2.0sec

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec

Seqn= 86 1-time(s) 4.0sec

Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 95 1-time(s) 2.0sec

thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 6 1-time(s) 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Subr= 2 2-time(s) 12.0sec

Seqn= 56 1-time(s) 2.0sec

Al-poly/Open	Al-poly/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 22 1-time(s) 2.0sec

C-poly/Open	C-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
C-poly/Open	C-poly/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Seqn= 43 1-time(s) 2.0sec

Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
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Seqn= 38 1-time(s) 4.0sec

med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
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Seqn= 46 1-time(s) 4.0sec

Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
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Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
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Flare mode

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XOB #19F0: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops-lowered-gband

Term	Pointing (x, y)	Comment
02/11 11:19:00 - 02/11 17:43:00	Track (-922.3, 238.0) ^{© 02/11 11:16:00}	# OP start + 10min, Returning AR on NE limb for EIS.
02/11 18:19:30 - 02/12 05:53:24	Track (-692.0, -193.6) ^{© 02/11 18:16:30}	# AR 11976 observations.
02/12 06:06:30 - 02/12 17:24:54	Track (-616.6, -184.8) ^{© 02/12 06:03:30}	# AR cont.
02/12 17:38:00 - 02/12 18:19:00	Fixed (-15.0, -965.0)	# HOP 79 S-N scan. 1/20.
02/12 18:46:30 - 02/12 19:13:24	Fixed (-15.0, -865.0)	# 2/20
02/12 19:16:30 - 02/12 19:56:00	Fixed (-15.0, -765.0)	# 3/20
02/12 20:23:30 - 02/12 20:50:24	Fixed (-15.0, -665.0)	# 4/20
02/12 20:53:30 - 02/12 21:33:30	Fixed (-15.0, -565.0)	# 5/20
02/12 22:00:00 - 02/12 22:26:54	Fixed (-15.0, -465.0)	# 6/20
02/12 22:30:00 - 02/12 23:11:00	Fixed (-15.0, -365.0)	# 7/20
02/12 23:30:00 - 02/12 23:56:54	Fixed (-15.0, -265.0)	# 8/20
02/13 00:00:00 - 02/13 00:42:00	Fixed (-15.0, -165.0)	# 9/20
02/13 00:58:30 - 02/13 01:25:24	Fixed (-15.0, -65.0)	# 10/20
02/13 01:41:06 - 02/13 02:10:00	Fixed (-15.0, 25.0)	# 11/20
02/13 02:34:31 - 02/13 03:01:24	Fixed (-15.0, 125.0)	# 12/20
02/13 03:04:30 - 02/13 03:38:00	Fixed (-15.0, 225.0)	# 13/20
02/13 04:12:00 - 02/13 04:38:54	Fixed (-15.0, 325.0)	# 14/20
02/13 04:42:00 - 02/13 05:18:30	Fixed (-15.0, 425.0)	# 15/20
02/13 05:49:30 - 02/13 06:16:24	Fixed (-15.0, 525.0)	# 16/20
02/13 06:29:30 - 02/13 06:58:30	Fixed (-15.0, 625.0)	# 17/20
02/13 07:26:30 - 02/13 07:53:24	Fixed (-15.0, 725.0)	# 18/20
02/13 07:56:30 - 02/13 08:38:30	Fixed (-15.0, 825.0)	# 19/20
02/13 09:03:00 - 02/13 09:25:00	Fixed (-15.0, 925.0)	# 20/20

PROG= 02 12-time(s)

Subr= 1	45-time(s)	10.0sec										
├─ Seqn= 9	1-time(s)	2.0sec										
├─ thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
├─ Seqn= 10	1-time(s)	2.0sec										
├─ med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
├─ Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
├─ Seqn= 11	1-time(s)	2.0sec										
├─ Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
├─ Subr= 2	1-time(s)	10.0sec										
├─ Seqn= 17	1-time(s)	2.0sec										
├─ Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
├─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
├─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

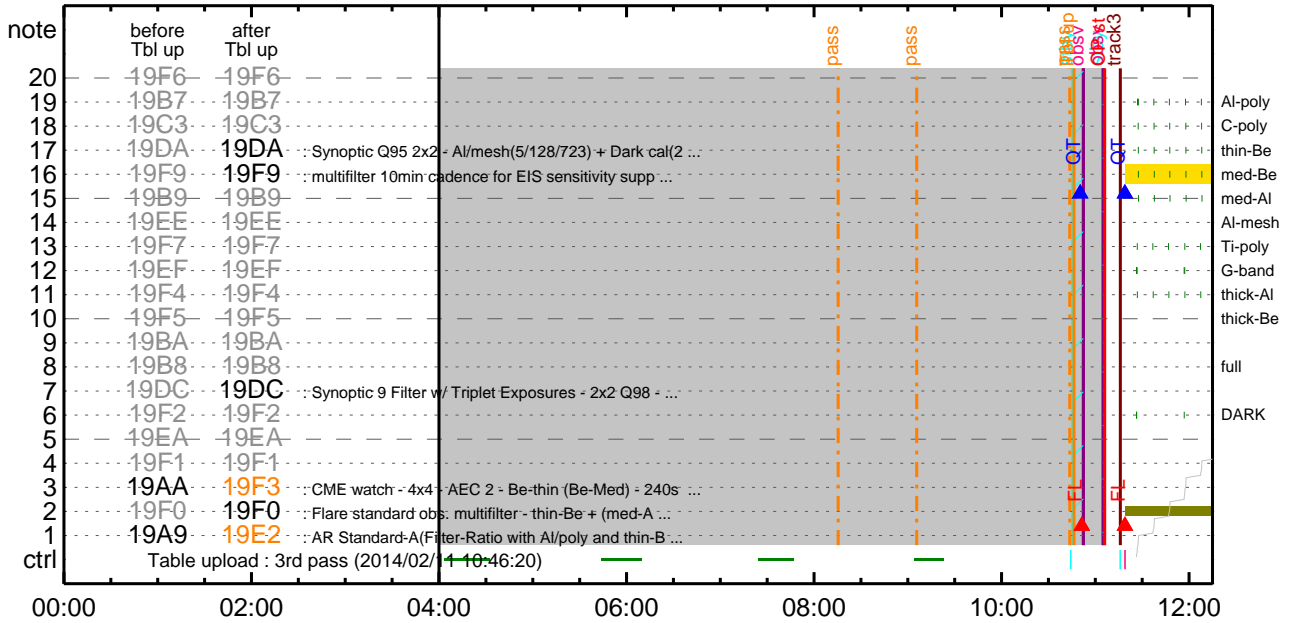
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Flare Detection

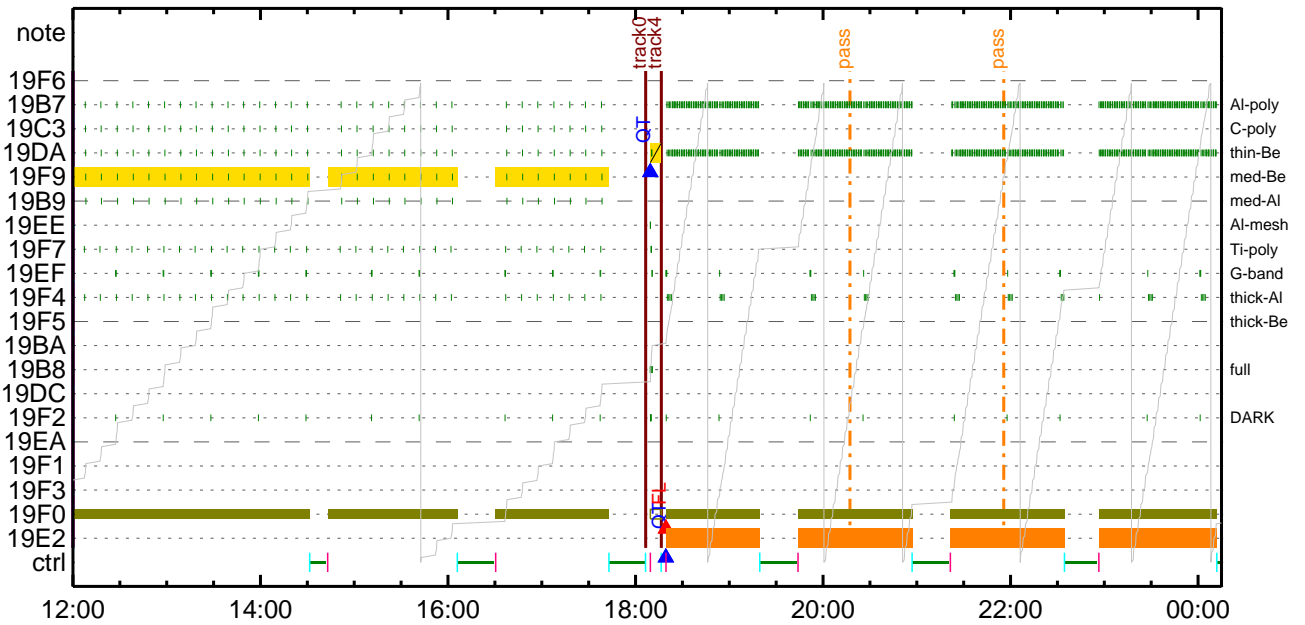
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FLD Patrol												
Term	Pointing (x, y)		Comment									
02/11 18:16:26 - 02/12 05:53:46	Fixed (0.0, 0.0)	synoptic, shifted 6.5 min									
02/12 06:03:26 - 02/12 17:25:16	Fixed (0.0, 0.0)	synoptic, shifted -6.5 min									
02/12 17:34:56 - 02/13 01:25:46	Fixed (0.0, 0.0)	synoptic, shifted -31.0 min									
02/13 01:38:02 - 02/13 06:16:46	Fixed (-15.0, 25.0)	# 11/20									
02/13 06:26:26 - 02/15 10:18:00	Fixed (0.0, 0.0)	synoptic, shifted 16.5 mins									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50		30sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

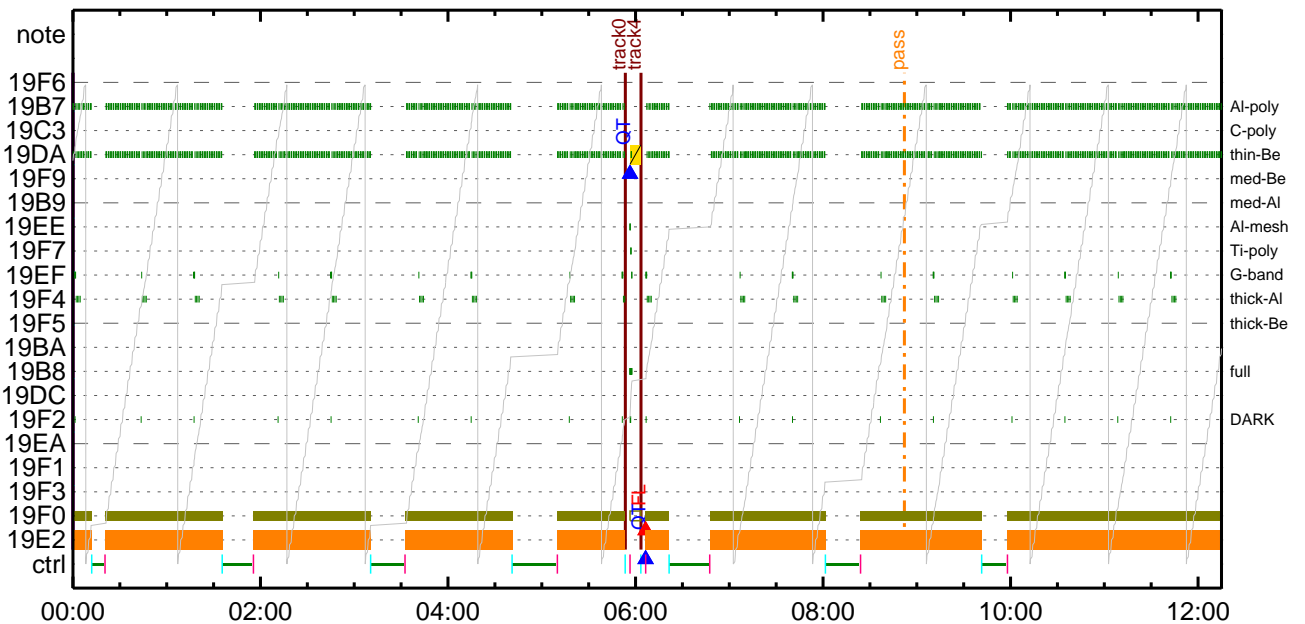
CMDI #0089 2014/02/11



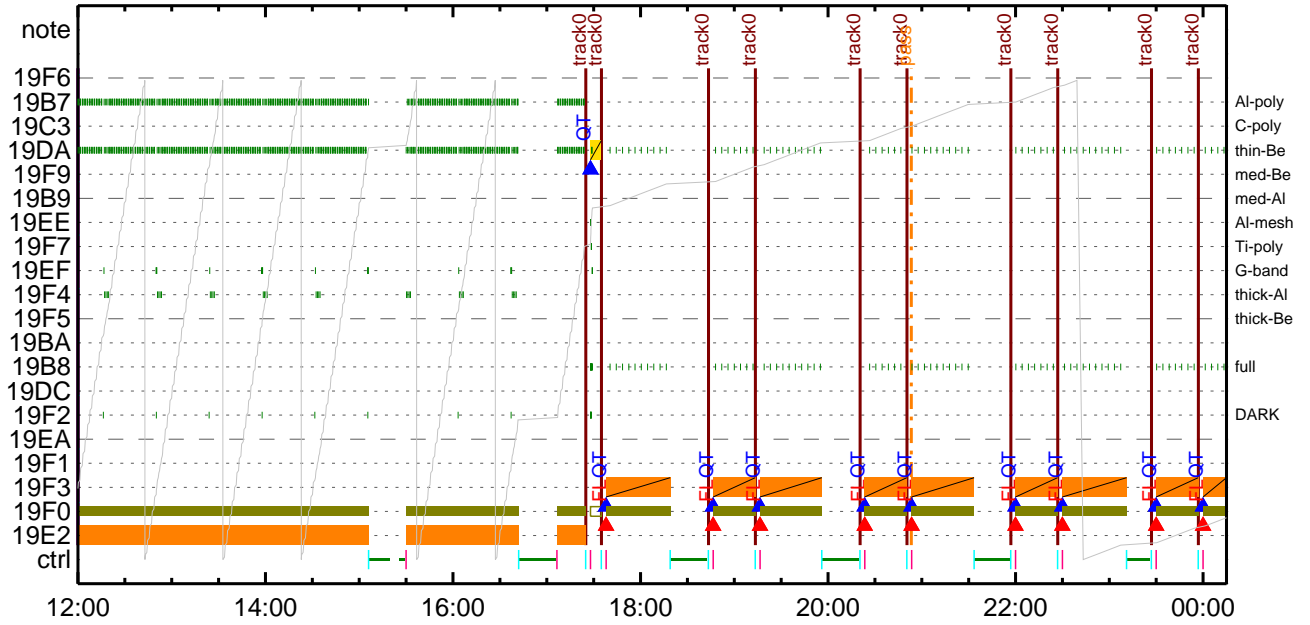
CMDI #0089 2014/02/11



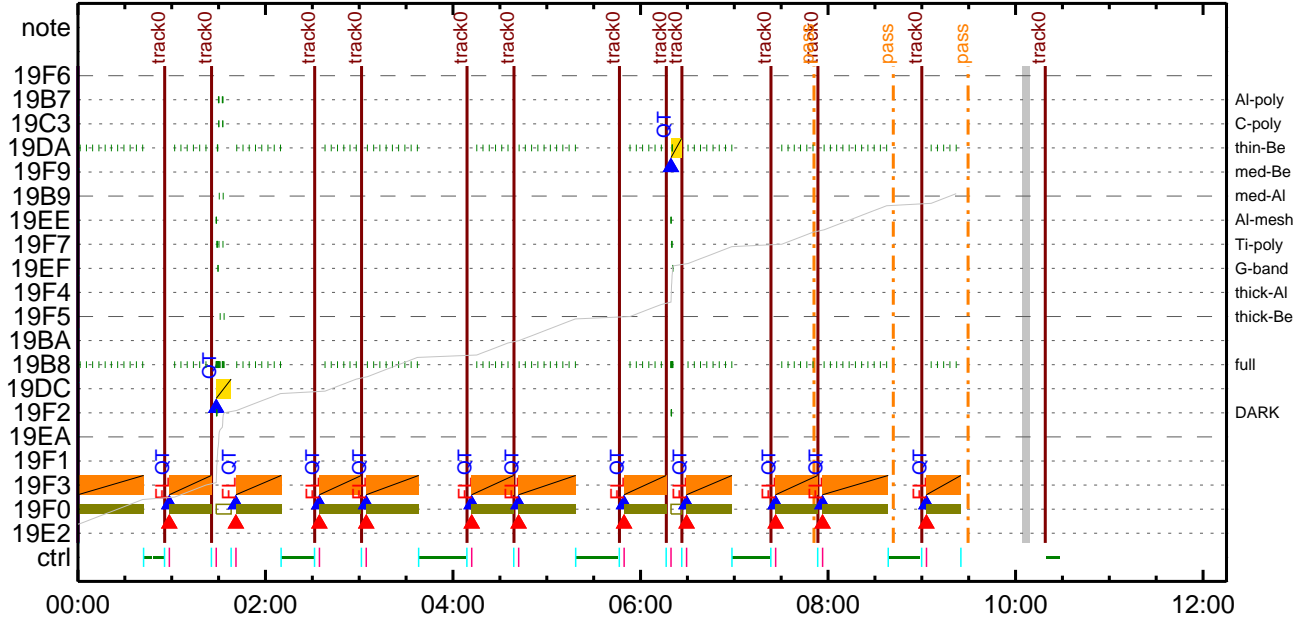
CMDI #0089 2014/02/12



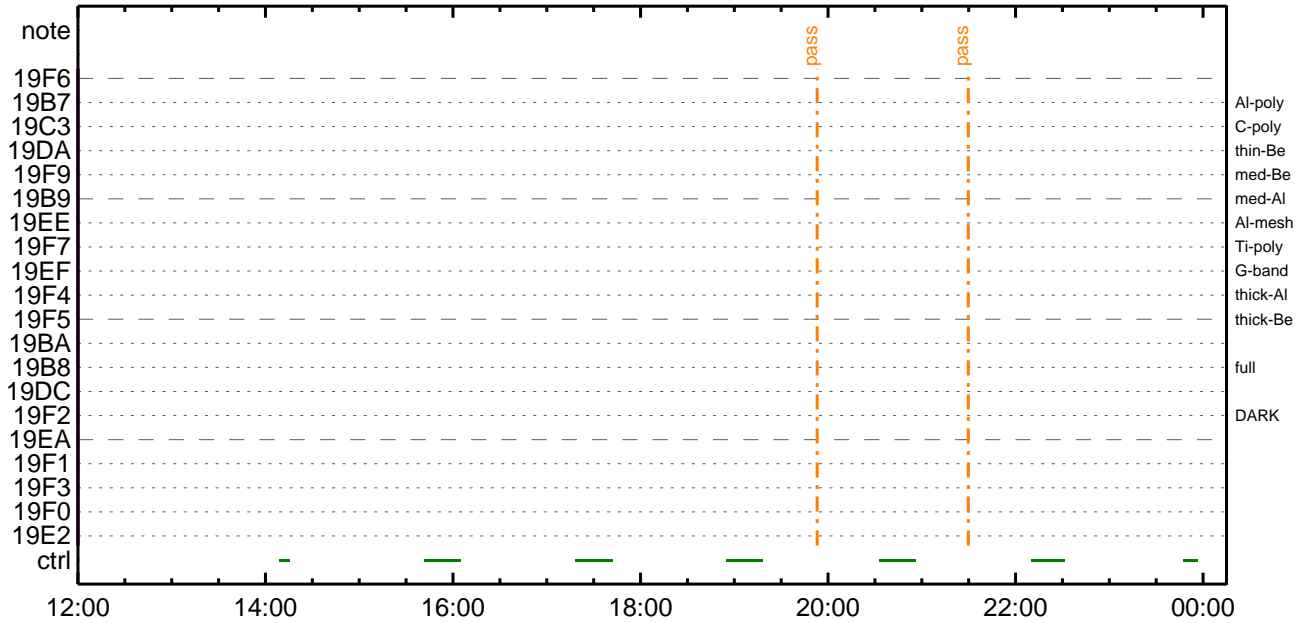
CMDI #0089 2014/02/12



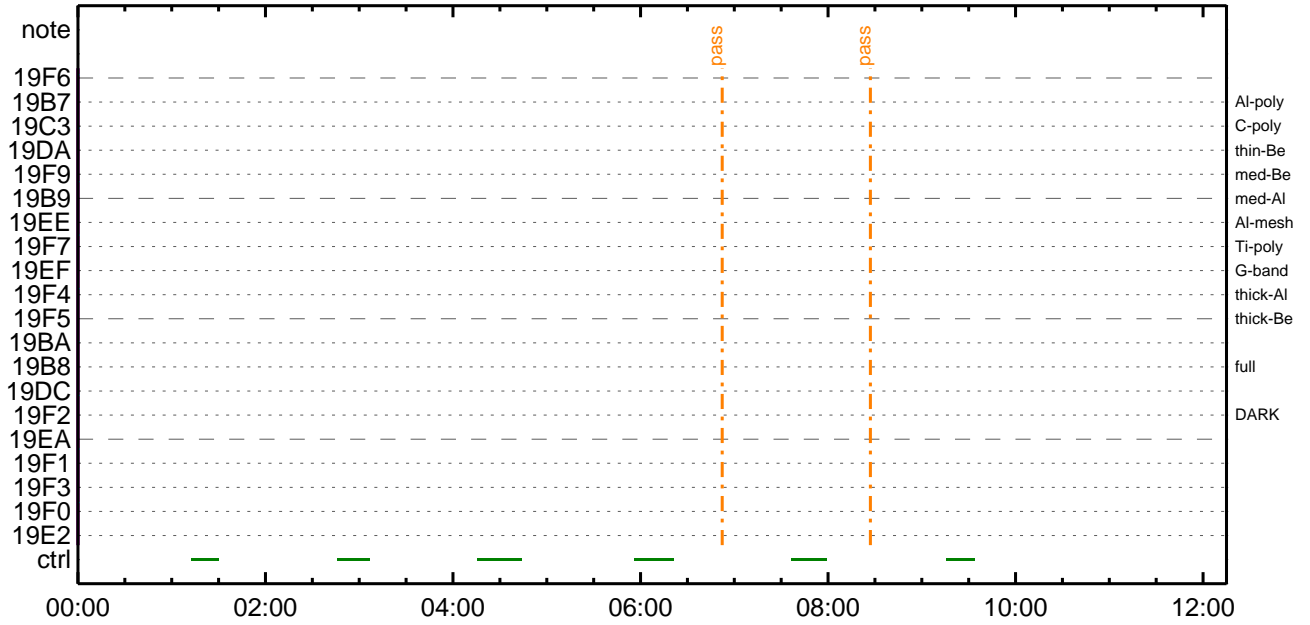
CMDI #0089 2014/02/13



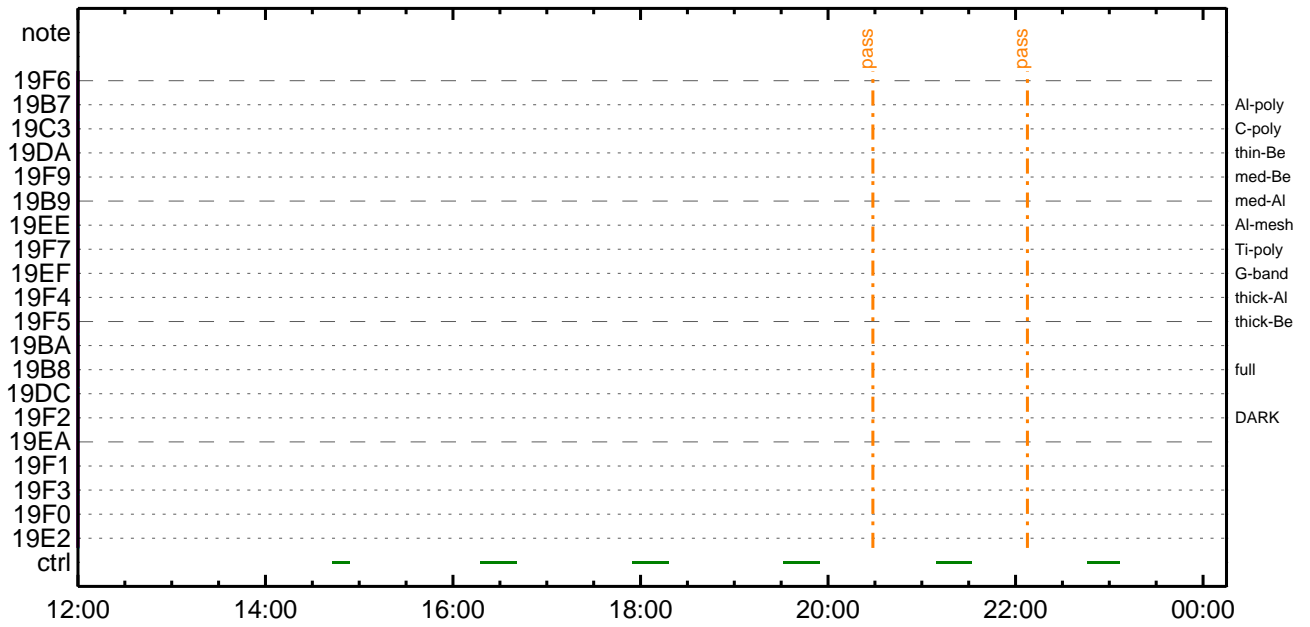
CMDI #0089 2014/02/13



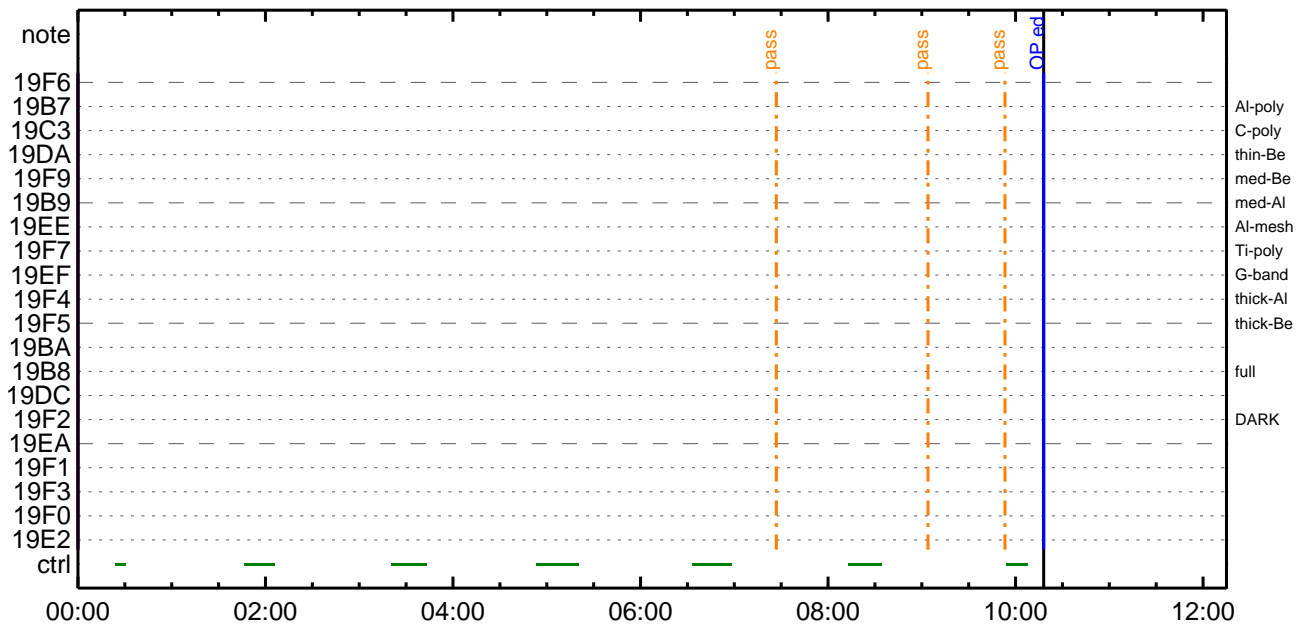
CMDI #0089 2014/02/14



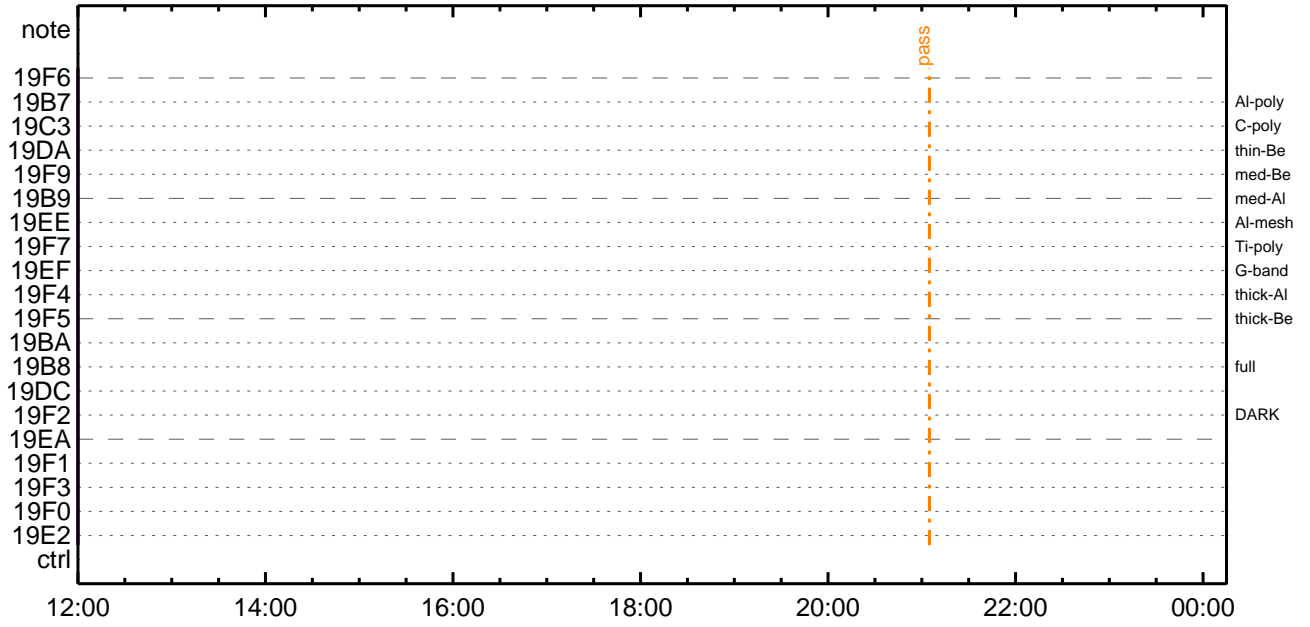
CMDI #0089 2014/02/14



CMDI #0089 2014/02/15



CMDI #0089 2014/02/15




```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-201:OP
0104 ( )
0105 S. OG og-201:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°è¥ÅYôYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. ¥ÅYôYx½ªî»ð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î¼Ê¹ç•è²îOK²³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. ¥ÅYôYx½ªî»ð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î¼Ê¹ç•è²îOK²³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. ¥ÅYôYx½ªî»ð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î¼Ê¹ç•è²îOK²³îÇ§
0165 C.
0166 C. ***** òÊ²¼²î¼Ê¹ç•è²îOK²³îÇ§ *****
0167 C. DHUYâ;4YE;Ê¼Y½, ¥î;4YE;Ê²ðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷çîNG²î¼Ê¹ç•è²¼²îTI-CMDÁ÷çî¼Ê¹Ô².²Ê²²²³²Ê;f
0180 C. ²²²ç;çSET²ÊDUMP²îÆ±²îYÑ²¹²ç¹Ô²|²³²Ê;f
0181 C.
0182 C. TIY³Y²YôYÊ²²ð²îÄîç(UT)
0183 +. TI 2014-02-11 11:01:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2014-02-11 11:01:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2014-02-11 11:01:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

0194 C.
0195 +. TI 2014-02-11 11:05:59.5
0196 DC 01-B2 DHU_OP_START
0197 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄè%îíñαîŷÄŷ§ŷÄŷ-¹àîŷ
0200 C. çç[HK1_TI_CMD_ENA/DIS] EQ ENA
0201 C. çç[HK1_TI_CMD_NUM] EQ 4
0202 C. çç[HK1_NEXT_EXEC_PIM] EQ DHU
0203 C. çç[HK1_NEXT_EXEC_DC] EQ 0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C. çç[HK1_DMP_TOP_ADRS_1] EQ 07
0213 C. çç[HK1_DMP_TOP_ADRS_0] EQ 2B
0214 C. çç[HK1_DMP_BLOCK_NUM] EQ 3
0215 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0216 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C. çç[HK1_PKT_FORM_NO] EQ 7
0220 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0221 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0222 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0223 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»αð³îç§
0226 C. çç[HK1_DMP_CHK_FLG] EQ NON
0227 C.
0228 C. RAM ID=TI_TBLαîŷÄŷ¹ç•è²îOKαð³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÉ;È¼ŷ¼. ŷî;¼ŷÈ;Èαðîâα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C. çç[HK1_PKT_FORM_NO] EQ 2
0234 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0235 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0236 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2014-02-11 11:05:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM = 1 CNTUP []
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2014-02-11 11:05:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC (21 02)
0258 +. TI 2014-02-11 11:05:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC (22)
0261 C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2014-02-11 11:05:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC (c3)
0271 C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP `ûÃîαî»ö¼ŷαÈÄðα¹αèDCBC•x²è *****
0276 C. (¼á°îŷÖŷÄŷÈŷŷÈŷÄŷçŷèè¼αα¼Ä»ŷα¹αè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷÐŷ¹•î Daily±çîñαè'Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»ŷ;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

(a) Spacecraft Operation Procedure (real-commands)

```
main-202 2014-02-11 11:32:54 91 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY~¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YBYÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èó¿òÁó•µ°È»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈè¼µ•íÉ;ÈóÈ¼°ÇÓó•ó¿¼í¹çóÍ;çÁ®, ùó¹óÈóBóÇÁ+¿®ó•óÈóóó³óÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCs Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. ***** AOCs Commands (Orbital Element Update) *****
0046 C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048 BC (16)
0049 +. DC 02-8E AOCU_ORB_UPD
0050 C.
0051 C. <A_ORB>[ORBIT] EPC = 6285903.9 +- 1.0 (s) [ ]
0052 C.
0053 C.
0054 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055 +. DC 07-FC EIS_MODE_MANU
0056 BC (21 02)
0057 . C. Verify EIS in MANUAL mode
0058 C. Estimated OBSTBL upload time is 53s
0059 C. *****
0060 C. EIS START OBSTBL LOAD
0061 C. *****
0062 . S. RAM ram-820:EIS_OBSTBL
0063 ( )
0064 +. DC 07-FC EIS_DUMP_OBSTBL
0065 BC (07 07 07 00 00 70 00)
0066 C.
0067 C. Execute, after the success of OBSTBL upload.
0068 C. Set EIS TI-commands
0069 +. TI 2014-02-11 11:05:50.0
0070 DC 07-FC EIS_MODE_CHG_ENA
0071 BC (20)
0072 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0073 C. *****
0074 C. EIS END OBSTBL LOAD
0075 C. *****
0076 C.
0077 . C. ***** MDP `úÃÎóÎ`ó¼YóÈÁÐó¹óÈDCBC•×²è *****
0078 C. (¼á°íYÓYÁYBYPYÉYáYçYè¼¼ó¼¼¼Á»Ûó¹óÈ)
0079 . S. DC-BC dcbc-402:DCBC
0080 (MDP_known_event)
0081 C.
0082 C.
0083 . C. ***** YDY¹!·Ï Daily±¿ÍÑóÈ´Øó¹óÈDCBC•×²è *****
0084 . S. DC-BC dcbc-153:DCBC
0085 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0086 C.
0087 C.
0088 . C. ;ãLOSÁY$YÁY~¼Á»Û;ã
0089 C.
0090 . C. ***** LOS *****
0091 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-203 2014-02-11 11:32:54 159 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Ü;ã
0005 C.
0006 C. YÀYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿òÁò•µ°È»Í×ÁÇóÍY¿Y×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈòÈ¼°ÇÔò•ò¿¼í¹çòÍ;çÀ®, ùò¹òèòòòçÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop SP table >
0018 +. DC 07-F0 MDP_SP_CTRL_MANU
0019 BC (61)
0020 C. -----
0021 C. MDP_SP_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload SP Observation Table>
0025 . S. RAM ram-282:MDP_OBS_S
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_S >
0029 +. DC 07-F0 MDP_DUMP_SPTBL
0030 BC (83 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_S verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Upload DPL table >
0036 +. DC 07-F0 MDP_FG_CTRL_MANU
0037 BC (51)
0038 C. -----
0039 C. MDP_FG_CTRL_MODE = MANU [ ]
0040 C. -----
0041 C.
0042 C. Y¿YÁY×Yí;¼YÉòîÁ°òÈSTS_CHKòðOFFòÈò¹òè
0043 C.
0044 . S. RAM ram-271:MDP_DPL
0045 ( )
0046 C.
0047 . C. < Dump RAMID=MDP_DPL >
0048 +. DC 07-F0 MDP_DUMP_FGTBL
0049 BC (82 07 00 38 b8 00 40)
0050 C. -----
0051 C. MDP_DPL verify = OK [ ]
0052 C. -----
0053 C.
0054 C. STS_CHKòðONòÈò¹òè
0055 C.
0056 . C. < Update MDP DSC PAR1 >
0057 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0058 BC (4c)
0059 C. MDP_CMD_CODE = F04C0700[ ]
0060 C. MDP_CMD_CNT (count-up 1) [ ]
0061 C. -----
0062 C.
0063 C.
0064 C. *****
0065 C. SOT TI command set
0066 C. *****
0067 C. Execute, after the success of TBL upload.
0068 +. TI 2014-02-11 11:05:18.0
0069 DC 07-F0 MDP_SOT_MODE_OBSV
0070 BC (40)
0071 C. -----
0072 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0073 C. -----
0074 C.
0075 C.
0076 C. ***** XRT START *****
0077 C.
0078 +. DC 07-F0 MDP_XRT_CTRL_MANU
0079 BC (c1)
0080 +. DC 07-F0 MDP_XRT_MODE_STBY
0081 BC (c3)
0082 . C. ----- Success Verify ? OK / NG_____
0083 C.
0084 C. XRT Obs. Table Upload
0085 . S. RAM ram-291:MDP_OBS_X
0086 ( )
0087 C.
0088 +. DC 07-F0 MDP_DUMP_XRTTBL
0089 BC (84 07 00 00 00 3a d4)
0090 . C. ----- Comparison Check ? OK / ERR _____
0091 C.
0092 C.
0093 +. DC 07-F0 MDP_XRT_ROI_SET
0094 BC (cd 01 b1 b1 04 04)
0095 +. DC 07-F0 MDP_XRT_ROI_SET
```

```

0096 BC (cd 02 b1 b1 08 08)
0097 + DC 07-F0 MDP_XRT_ROI_SET
0098 BC (cd 03 b1 b1 08 08)
0099 + DC 07-F0 MDP_XRT_ROI_SET
0100 BC (cd 04 b1 b1 06 06)
0101 + DC 07-F0 MDP_XRT_ROI_SET
0102 BC (cd 05 85 83 06 06)
0103 + DC 07-F0 MDP_XRT_ROI_SET
0104 BC (cd 06 85 83 06 06)
0105 + DC 07-F0 MDP_XRT_ROI_SET
0106 BC (cd 07 80 80 20 20)
0107 + DC 07-F0 MDP_XRT_ROI_SET
0108 BC (cd 08 80 80 20 08)
0109 + DC 07-F0 MDP_XRT_ROI_SET
0110 BC (cd 09 80 80 08 20)
0111 + DC 07-F0 MDP_XRT_ROI_SET
0112 BC (cd 0a 85 83 08 08)
0113 + DC 07-F0 MDP_XRT_ROI_SET
0114 BC (cd 0f 80 80 06 06)
0115 + DC 07-F0 MDP_XRT_ROI_SET
0116 BC (cd 10 80 80 08 08)
0117 + DC 07-F0 MDP_XRT_FLD_ENA
0118 BC (d8)
0119 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0120 BC (c8)
0121 + DC 07-F0 MDP_XRT_AEC_RESET
0122 BC (d0)
0123 + DC 07-F0 MDP_XRT_ARS_DIS
0124 BC (d5)
0125 + DC 07-F0 MDP_XRT_FLD_RESET
0126 BC (da)
0127 + DC 07-F0 MDP_XRT_QT_PROG_SET
0128 BC (c4 10)
0129 + DC 07-F0 MDP_XRT_FL_PROG_SET
0130 BC (c5 02)
0131 . C. ----- Success Verify ? OK / NG ____
0132 C.
0133 C.
0134 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0135 C.
0136 + DC 07-F0 MDP_XRT_MODE_OBSV
0137 BC (c2)
0138 + TI 2014-02-11 11:05:02.0
0139 DC 07-F0 MDP_XRT_MODE_OBSV
0140 BC (c2)
0141 . C. ----- Success Verify ? OK / NG ____
0142 C.
0143 C. ***** XRT END *****
0144 C.
0145 . C. ***** MDP `ûÃîñî»ò%ÿñÊÃð¹ñèDCBC•x²è *****
0146 C. (%ã°îÿÓÿÃÿÈÿPÿËÿÿãÿçÿèñE%¼ñ¼Ã»Ûñ¹ñè)
0147 . S. DC-BC dcbc-402:DCBC
0148 (MDP_known_event)
0149 C.
0150 C.
0151 . C. ***** ÿÐÿ¹•Ï Daily±;îÑñÈ´Øñ¹ñèDCBC•x²è *****
0152 . S. DC-BC dcbc-153:DCBC
0153 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0154 C.
0155 C.
0156 . C. ;ãLOSÿÃÿSÿÿÿÿ¼Ã»Û;ã
0157 C.
0158 . C. ***** LOS *****
0159 C.

```

Feb 11, 14 11:34

XRT_OGLIST_0089.chk

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*** OP Sequence for XRT ***

2014/02/11	11:15:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/11	11:15:56.0	XRT_FLD_ENA_440_OG [0x1b8]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/11	11:15:58.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/11	11:16:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2014/02/11	11:16:00.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/11	11:16:02.5	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/11	11:16:04.5	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/11	11:16:06.5	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/02/11	11:18:56.5	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10				
2014/02/11	11:18:58.5	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/02/11	11:19:00.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/11	14:31:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/11	14:31:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/11	14:31:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/11	14:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/11	14:42:00.0	XRT_Custom_430_OG [0x1ae]							
2014/02/11	14:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/11	16:06:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/11	16:06:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/11	16:06:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/11	16:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/11	16:29:30.0	XRT_Custom_430_OG [0x1ae]							
2014/02/11	16:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/11	17:43:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/11	17:43:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/11	17:43:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/11	17:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/11	18:06:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/11	18:06:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/02/11	18:06:30.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/02/11	18:06:46.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/02/11	18:09:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/02/11	18:09:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/11	18:09:28.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2014/02/11	18:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/11	18:16:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/11	18:16:26.0	XRT_FLD_ENA_440_OG [0x1b8]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/11	18:16:28.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/11	18:16:30.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2014/02/11	18:16:30.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/11	18:16:32.5	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/11	18:16:34.5	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/11	18:16:36.5	XRT_FOCUS_POSITION_401_OG [0x191]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/02/11	18:19:26.5	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2014/02/11	18:19:28.5	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/02/11	18:19:30.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/11	19:19:30.0	XRT_CTRL_MANU_400_OG [0x190]							

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2014/02/11	19:19:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/11	19:19:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/11	19:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/11	19:43:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/11	19:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/11	20:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/11	20:57:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/11	20:57:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/11	21:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/11	21:20:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/11	21:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/11	22:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/11	22:34:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/11	22:34:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/11	22:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/11	22:55:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/11	22:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/12	00:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/12	00:12:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/12	00:12:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/12	00:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/12	00:19:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/12	00:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/12	01:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/12	01:35:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/12	01:35:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/12	01:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/12	01:54:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/12	01:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/12	03:10:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/12	03:10:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/12	03:10:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/12	03:13:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/12	03:31:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/12	03:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/12	04:41:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/12	04:41:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/12	04:41:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/12	04:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/12	05:09:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/12	05:10:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/12	05:53:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/12	05:53:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/12	05:53:30.0	AOCS_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/02/12	05:53:46.0	XRT_FLD_DIS_434_OG [0x1b2]	AOCU_NM	5	02-76	00 00 00 00 00
2014/02/12	05:56:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2014/02/12	05:56:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/02/12	05:56:28.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/02/12	05:56:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2014/02/12	06:03:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2014/02/12	06:03:26.0	XRT_FLD_ENA_440_OG [0x1b8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/02/12	06:03:28.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/02/12	06:03:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	04 00 00 00 00	
2014/02/12	06:03:30.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/12	06:03:32.5	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/12	06:03:34.5	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/02/12	06:03:36.5	XRT_FOCUS_POSITION_401_OG [0x191]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2014/02/12	06:06:26.5	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01	
2014/02/12	06:06:28.5	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2014/02/12	06:06:30.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/12	06:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/12	06:21:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/12	06:21:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/12	06:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/12	06:46:30.0	XRT_Custom_430_OG [0x1ae]					
2014/02/12	06:47:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/12	08:01:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/12	08:01:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/12	08:01:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/12	08:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/12	08:23:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/12	08:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/12	09:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/12	09:41:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/12	09:41:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/12	09:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/12	09:57:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/12	09:58:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/12	15:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/12	15:06:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/12	15:06:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/12	15:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/12	15:29:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/12	15:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/12	16:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/12	16:42:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/12	16:42:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/12	16:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/12	17:05:30.0	XRT_Custom_430_OG [0x1ae]					
2014/02/12	17:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/12	17:24:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/12	17:24:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/02/12	17:25:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2014/02/12	17:25:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/02/12	17:27:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/02/12	17:27:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/12	17:27:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2014/02/12	17:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/12	17:34:54.0	XRT_CTRL_MANU_402_OG [0x192]					

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2014/02/12	17:34:56.0	XRT_FLD_ENA_440_OG [0x1b8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/02/12	17:34:58.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/02/12	17:35:00.0	AOCS_Ore-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00 55 ca 01 58	
2014/02/12	17:35:00.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/12	17:35:02.5	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/12	17:35:04.5	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/02/12	17:35:06.5	XRT_FOCUS_POSITION_421_OG [0x1a5]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/02/12	17:37:56.5	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2014/02/12	17:37:58.5	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2014/02/12	17:38:00.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/12	18:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/12	18:19:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/12	18:19:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/12	18:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/12	18:43:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/12	18:43:26.0	XRT_FLD_ENA_440_OG [0x1b8]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/02/12	18:43:28.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/02/12	18:43:30.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 4c e5 01 58	
2014/02/12	18:43:30.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/12	18:43:32.5	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/12	18:43:34.5	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/02/12	18:43:36.5	XRT_FOCUS_POSITION_421_OG [0x1a5]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/02/12	18:46:26.5	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2014/02/12	18:46:28.5	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2014/02/12	18:46:30.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/12	19:13:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/12	19:13:26.0	XRT_FLD_ENA_440_OG [0x1b8]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/02/12	19:13:28.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/02/12	19:13:30.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 43 ff 01 58	
2014/02/12	19:13:30.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/12	19:13:32.5	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/12	19:13:34.5	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/02/12	19:13:36.5	XRT_FOCUS_POSITION_421_OG [0x1a5]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/02/12	19:16:26.5	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2014/02/12	19:16:28.5	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2014/02/12	19:16:30.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/12	19:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/12	19:56:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/12	19:56:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/12	19:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/12	20:20:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/12	20:20:26.0	XRT_FLD_ENA_440_OG [0x1b8]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/02/12	20:20:28.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/02/12	20:20:30.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 3b 1a 01 58	
2014/02/12	20:20:30.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/12	20:20:32.5	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5	

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2014/02/12	20:20:34.5	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/12	20:20:36.5	XRT_FOCUS_POSITION_421_OG [0x1a5]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/02/12	20:23:26.5	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2014/02/12	20:23:28.5	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/02/12	20:23:30.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/12	20:50:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/12	20:50:26.0	XRT_FLD_ENA_440_OG [0x1b8]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/12	20:50:28.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/12	20:50:30.0	AOCS_OrE-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00 32 35 01 58				
2014/02/12	20:50:30.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/12	20:50:32.5	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/12	20:50:34.5	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/12	20:50:36.5	XRT_FOCUS_POSITION_421_OG [0x1a5]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/02/12	20:53:26.5	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2014/02/12	20:53:28.5	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/02/12	20:53:30.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/12	21:33:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/12	21:33:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/12	21:33:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/12	21:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/12	21:56:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/12	21:56:56.0	XRT_FLD_ENA_440_OG [0x1b8]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/12	21:56:58.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/12	21:57:00.0	AOCS_OrE-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 29 58 01 58				
2014/02/12	21:57:00.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/12	21:57:02.5	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/12	21:57:04.5	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/12	21:57:06.5	XRT_FOCUS_POSITION_421_OG [0x1a5]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/02/12	21:59:56.5	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2014/02/12	21:59:58.5	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/02/12	22:00:00.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/12	22:26:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/12	22:26:56.0	XRT_FLD_ENA_440_OG [0x1b8]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/12	22:26:58.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/12	22:27:00.0	AOCS_OrE-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00 20 72 01 58				
2014/02/12	22:27:00.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/12	22:27:02.5	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/12	22:27:04.5	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/12	22:27:06.5	XRT_FOCUS_POSITION_421_OG [0x1a5]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/02/12	22:29:56.5	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2014/02/12	22:29:58.5	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/02/12	22:30:00.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/12	23:11:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/12	23:11:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/12	23:11:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/12	23:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/12	23:26:54.0	XRT_CTRL_MANU_402_OG [0x192]							

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2014/02/12	23:26:56.0	XRT_FLD_ENA_440_OG [0x1b8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/02/12	23:26:58.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/02/12	23:27:00.0	AOCS_OrE-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	00 17 8d 01 58		
2014/02/12	23:27:00.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/12	23:27:02.5	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/02/12	23:27:04.5	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/02/12	23:27:06.5	XRT_FOCUS_POSITION_421_OG [0x1a5]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2014/02/12	23:29:56.5	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2014/02/12	23:29:58.5	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
2014/02/12	23:30:00.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/12	23:56:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/12	23:56:56.0	XRT_FLD_ENA_440_OG [0x1b8]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/02/12	23:56:58.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/02/12	23:57:00.0	AOCS_OrE-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00 0e a7 01 58		
2014/02/12	23:57:00.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/12	23:57:02.5	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/02/12	23:57:04.5	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/02/12	23:57:06.5	XRT_FOCUS_POSITION_421_OG [0x1a5]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2014/02/12	23:59:56.5	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2014/02/12	23:59:58.5	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
2014/02/13	00:00:00.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/13	00:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/13	00:42:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/13	00:42:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/02/13	00:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/13	00:55:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/13	00:55:26.0	XRT_FLD_ENA_440_OG [0x1b8]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/02/13	00:55:28.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/02/13	00:55:30.0	AOCS_OrE-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00 05 ca 01 58		
2014/02/13	00:55:30.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/13	00:55:32.5	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/02/13	00:55:34.5	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/02/13	00:55:36.5	XRT_FOCUS_POSITION_421_OG [0x1a5]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2014/02/13	00:58:26.5	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2014/02/13	00:58:28.5	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
2014/02/13	00:58:30.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/13	01:25:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/13	01:25:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2014/02/13	01:25:30.0	AOCS_OrE-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00 fd cb 01 58		
2014/02/13	01:25:46.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2014/02/13	01:28:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2014/02/13	01:28:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/02/13	01:28:28.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07		
2014/02/13	01:28:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/13	01:38:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/13	01:38:02.0	XRT_FLD_ENA_440_OG [0x1b8]	MDP_XRT_FLD_ENA	1	07-F0	d8		

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2014/02/13	01:38:04.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/13	01:38:06.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/13	01:38:08.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/13	01:38:10.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/13	01:38:12.0	XRT_FOCUS_POSITION_421_OG [0x1a5]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/02/13	01:41:02.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2014/02/13	01:41:04.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/02/13	01:41:06.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/13	02:10:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/13	02:10:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/13	02:10:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/13	02:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/13	02:31:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/13	02:31:26.0	XRT_FLD_ENA_440_OG [0x1b8]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/13	02:31:28.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/13	02:31:30.5	AOCS_OrE-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00 f4 e6 01 58				
2014/02/13	02:31:31.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/13	02:31:33.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/13	02:31:35.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/13	02:31:37.0	XRT_FOCUS_POSITION_421_OG [0x1a5]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/02/13	02:34:27.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2014/02/13	02:34:29.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/02/13	02:34:31.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/13	03:01:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/13	03:01:26.0	XRT_FLD_ENA_440_OG [0x1b8]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/13	03:01:28.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/13	03:01:30.0	AOCS_OrE-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	00 ec 00 01 58				
2014/02/13	03:01:30.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/13	03:01:32.5	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/13	03:01:34.5	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/13	03:01:36.5	XRT_FOCUS_POSITION_421_OG [0x1a5]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/02/13	03:04:26.5	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2014/02/13	03:04:28.5	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/02/13	03:04:30.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/13	03:38:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/13	03:38:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/13	03:38:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/13	03:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/13	04:08:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/13	04:08:56.0	XRT_FLD_ENA_440_OG [0x1b8]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/13	04:08:58.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/13	04:09:00.0	AOCS_OrE-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	00 e3 1b 01 58				
2014/02/13	04:09:00.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/13	04:09:02.5	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/13	04:09:04.5	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/13	04:09:06.5	XRT_FOCUS_POSITION_421_OG [0x1a5]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/02/13	04:11:56.5	XRT_QT_PROG_SET_422_OG [0x1a6]							

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2014/02/13	04:11:58.5	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03	
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	02	
2014/02/13	04:12:00.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/13	04:38:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/13	04:38:56.0	XRT_FLD_ENA_440_OG [0x1b8]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/02/13	04:38:58.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/02/13	04:39:00.0	AOCS_Orе-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00	da 36 01 58	
2014/02/13	04:39:00.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/13	04:39:02.5	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/02/13	04:39:04.5	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/02/13	04:39:06.5	XRT_FOCUS_POSITION_421_OG [0x1a5]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2014/02/13	04:41:56.5	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03	
2014/02/13	04:41:58.5	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02	
2014/02/13	04:42:00.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/13	05:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/13	05:18:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/13	05:18:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/02/13	05:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/13	05:46:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/13	05:46:26.0	XRT_FLD_ENA_440_OG [0x1b8]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/02/13	05:46:28.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/02/13	05:46:30.0	AOCS_Orе-point_Start_19_OG [0x0a9]	AOCU_NM	5	02-76	00	d1 59 01 58	
2014/02/13	05:46:30.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/13	05:46:32.5	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/02/13	05:46:34.5	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/02/13	05:46:36.5	XRT_FOCUS_POSITION_421_OG [0x1a5]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2014/02/13	05:49:26.5	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03	
2014/02/13	05:49:28.5	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02	
2014/02/13	05:49:30.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/13	06:16:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/13	06:16:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2014/02/13	06:16:30.0	AOCS_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00	
2014/02/13	06:16:46.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2014/02/13	06:19:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2014/02/13	06:19:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/02/13	06:19:28.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11	
2014/02/13	06:19:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/13	06:26:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/13	06:26:26.0	XRT_FLD_ENA_440_OG [0x1b8]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/02/13	06:26:28.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/02/13	06:26:30.0	AOCS_Orе-point_Start_20_OG [0x0aa]	AOCU_NM	5	02-76	00	c8 73 01 58	
2014/02/13	06:26:30.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/13	06:26:32.5	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/02/13	06:26:34.5	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/02/13	06:26:36.5	XRT_FOCUS_POSITION_421_OG [0x1a5]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2014/02/13	06:29:26.5	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03	
2014/02/13	06:29:28.5	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02	

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2014/02/13	06:29:30.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/13	06:58:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/13	06:58:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/13	06:58:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/13	07:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/13	07:23:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/13	07:23:26.0	XRT_FLD_ENA_440_OG [0x1b8]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/13	07:23:28.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/13	07:23:30.0	AOCS_ORe-point_Start_21_OG [0x0ab]							
		AOCU_NM	5	02-76	00 bf 8e 01 58				
2014/02/13	07:23:30.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/13	07:23:32.5	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/13	07:23:34.5	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/13	07:23:36.5	XRT_FOCUS_POSITION_421_OG [0x1a5]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/02/13	07:26:26.5	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2014/02/13	07:26:28.5	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/02/13	07:26:30.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/13	07:53:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/13	07:53:26.0	XRT_FLD_ENA_440_OG [0x1b8]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/13	07:53:28.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/13	07:53:30.0	AOCS_ORe-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	00 b6 a8 01 58				
2014/02/13	07:53:30.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/13	07:53:32.5	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/13	07:53:34.5	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/13	07:53:36.5	XRT_FOCUS_POSITION_421_OG [0x1a5]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/02/13	07:56:26.5	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2014/02/13	07:56:28.5	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/02/13	07:56:30.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/13	08:38:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/13	08:38:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/13	08:38:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/13	08:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/13	08:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/13	08:59:56.0	XRT_FLD_ENA_440_OG [0x1b8]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/13	08:59:58.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/13	09:00:00.0	AOCS_ORe-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00 ad cb 01 58				
2014/02/13	09:00:00.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/13	09:00:02.5	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/13	09:00:04.5	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/13	09:00:06.5	XRT_FOCUS_POSITION_421_OG [0x1a5]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/02/13	09:02:56.5	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2014/02/13	09:02:58.5	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/02/13	09:03:00.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/13	09:25:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/13	10:19:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				