

XRT Timeline to be uploaded on 2014/02/13

Period: 2014/02/13 10:09:00 - 2014/02/18 10:29:00

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Normal mode

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XOB #19FB: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 512x512 at 1064 1048, thin-Be, thick-Al, Al/Poly, and Ti/Poly context, with G-band													
Term	Pointing (x, y)		Comment										
02/13 10:29:30 - 02/13 17:58:54	Track (241.0, -112.2) ^{Ⓜ 02/13 10:19:00}		# OP start + 10min, Major FLare Watch AR 11974										
02/13 18:12:00 - 02/14 05:28:24	Track (309.5, -114.4) ^{Ⓜ 02/13 18:09:00}		# Flare watch continued.										
02/14 05:41:30 - 02/14 17:54:30	Track (406.5, -118.7) ^{Ⓜ 02/14 05:38:30}		# Flare obs.										
02/14 18:31:00 - 02/15 06:03:24	Track (508.9, -125.0) ^{Ⓜ 02/14 18:28:00}		# Flare obs.										
02/15 06:16:30 - 02/15 09:50:00	Track (595.8, -132.1) ^{Ⓜ 02/15 06:13:30}		# Flare obs.										
PROG= 08 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 71 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2 2-time(s) 2.0sec													
└─ Seqn= 65 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 39 2-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 76 12-time(s) 300.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19FA: Synoptic Q95 2x2 - Al/mesh(3/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(8/362/1443) + Thin												
Term	Pointing (x, y)		Comment									
02/13 18:02:00 - 02/13 18:08:54	Fixed (0.0, 0.0)		synoptic, shifted -1.0 min									
02/14 05:31:30 - 02/14 05:38:24	Fixed (0.0, 0.0)		synoptic, shifted -31.5 min									
02/14 18:21:00 - 02/14 18:27:54	Fixed (0.0, 0.0)		synoptic, shifted 18.0 min									
02/15 06:06:30 - 02/15 06:13:24	Fixed (0.0, 0.0)		synoptic, shifted 3.5 min									

PROG= 19 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 35 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	3ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 72 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 74 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 6 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #19F0: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops-lowered-gband												
Term	Pointing (x, y)		Comment									
02/13 10:29:30 - 02/13 17:58:54	Track (241.0, -112.2) ^{Ⓜ 02/13 10:19:00}		# OP start + 10min, Major FLare Watch AR 11974									
02/13 18:12:00 - 02/14 05:28:24	Track (309.5, -114.4) ^{Ⓜ 02/13 18:09:00}		# Flare watch continued.									
02/14 05:41:30 - 02/14 17:54:30	Track (406.5, -118.7) ^{Ⓜ 02/14 05:38:30}		# Flare obs.									
02/14 18:31:00 - 02/15 06:03:24	Track (508.9, -125.0) ^{Ⓜ 02/14 18:28:00}		# Flare obs.									
02/15 06:16:30 - 02/15 09:50:00	Track (595.8, -132.1) ^{Ⓜ 02/15 06:13:30}		# Flare obs.									

PROG= 02 12-time(s)												
└─ Subr= 1 45-time(s) 10.0sec												
└─ Seqn= 9 1-time(s) 2.0sec												

thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 10			1-time(s)			2.0sec						
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11			1-time(s)			2.0sec						
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2			1-time(s)			10.0sec						
Seqn= 17			1-time(s)			2.0sec						
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

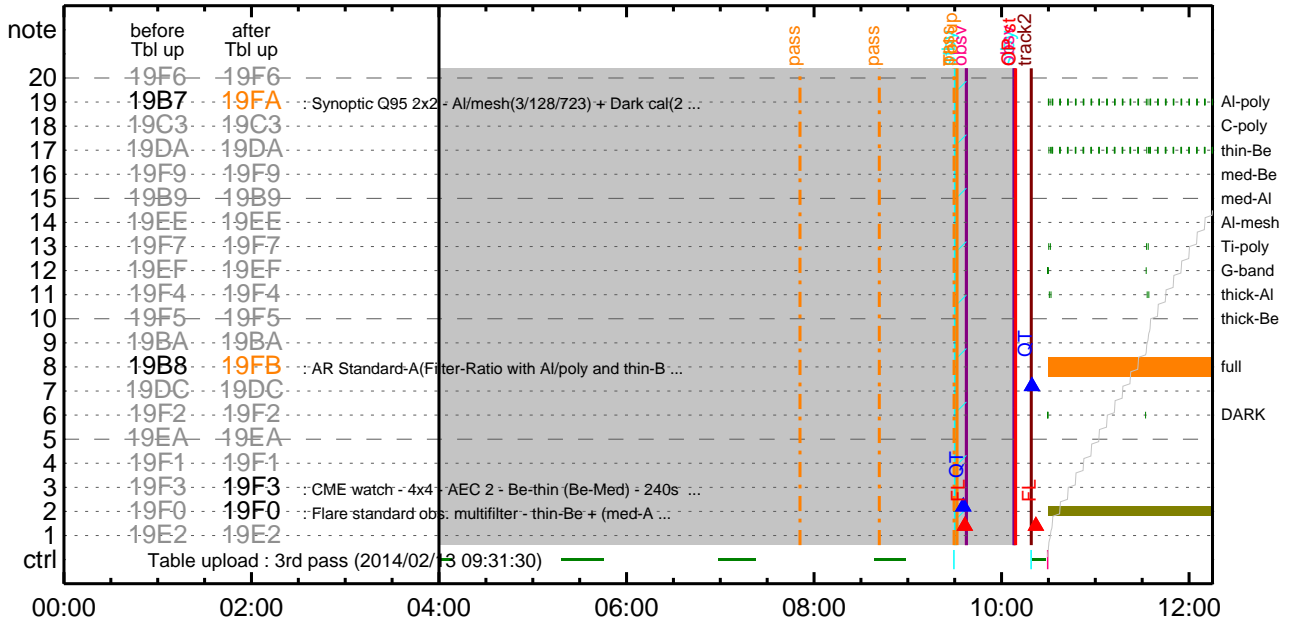
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Flare Detection

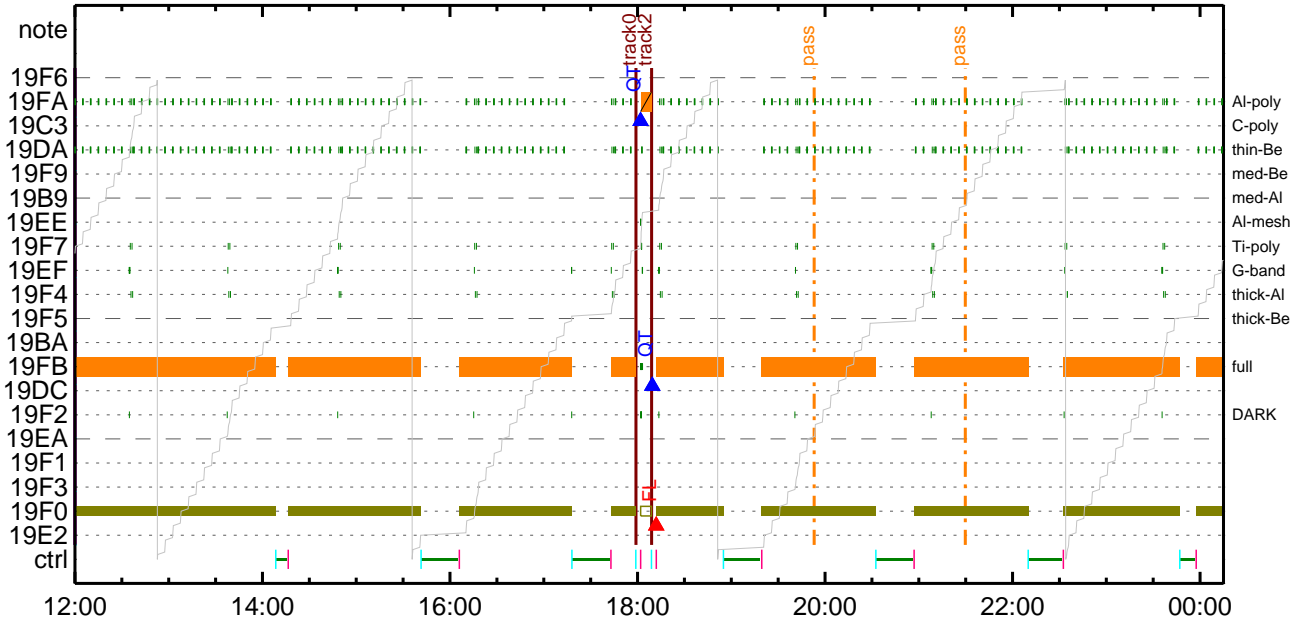
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FLD Patrol												
Term	Pointing (x, y)							Comment				
02/13 18:09:16 - 02/14 05:28:46	Track (309.5, -114.4)	<small>© 02/13 18:09:00</small>	#	Flare watch continued.								
02/14 05:38:46 - 02/14 18:18:16	Track (406.5, -118.7)	<small>© 02/14 05:38:30</small>	#	Flare obs.								
02/14 18:28:16 - 02/15 06:03:46	Track (508.9, -125.0)	<small>© 02/14 18:28:00</small>	#	Flare obs.								
02/15 06:13:46 - 02/18 10:29:00	Track (595.8, -132.1)	<small>© 02/15 06:13:30</small>	#	Flare obs.								
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

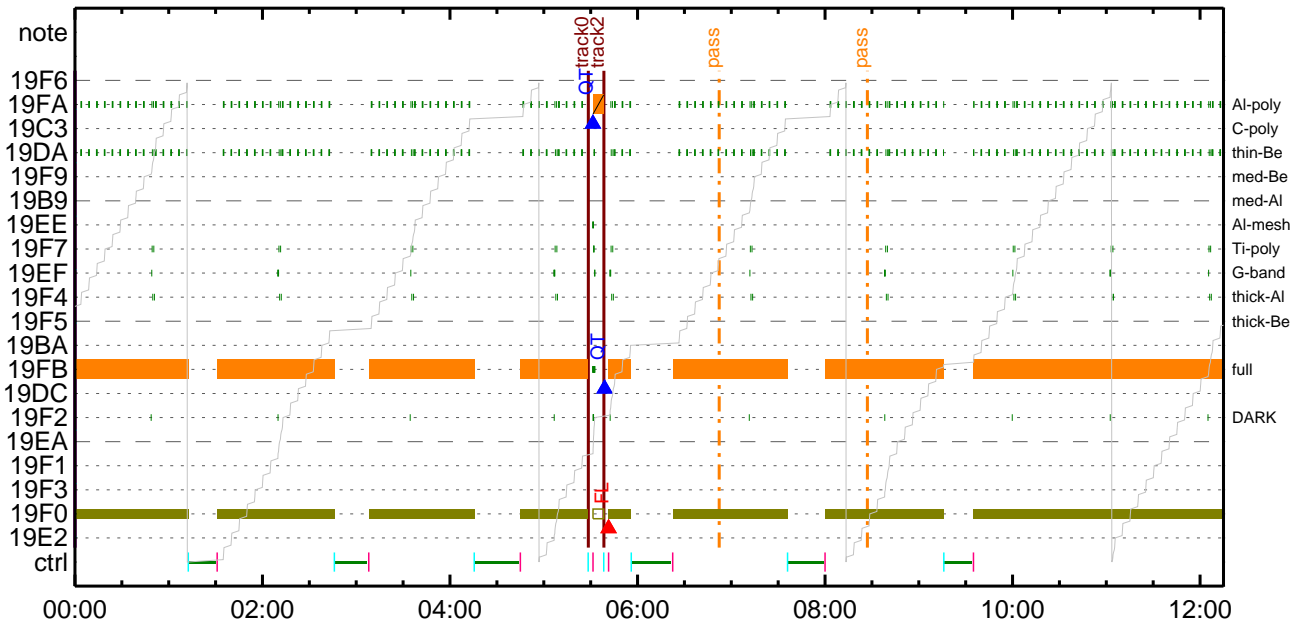
CMDI #0093 2014/02/13



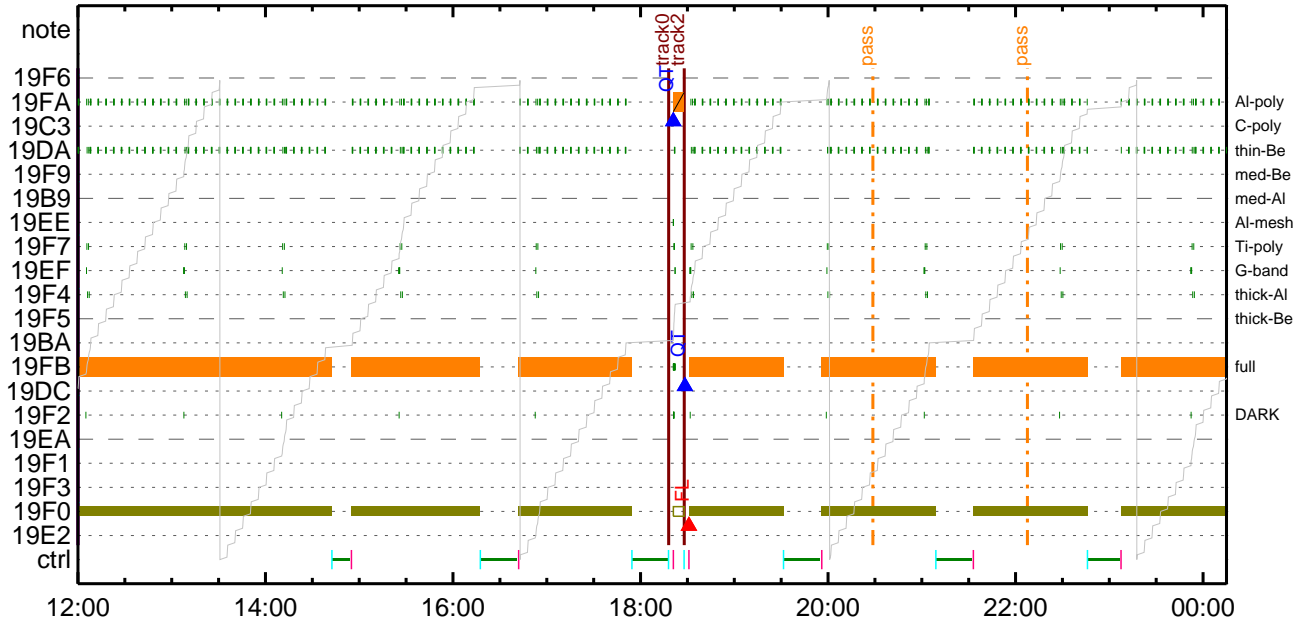
CMDI #0093 2014/02/13



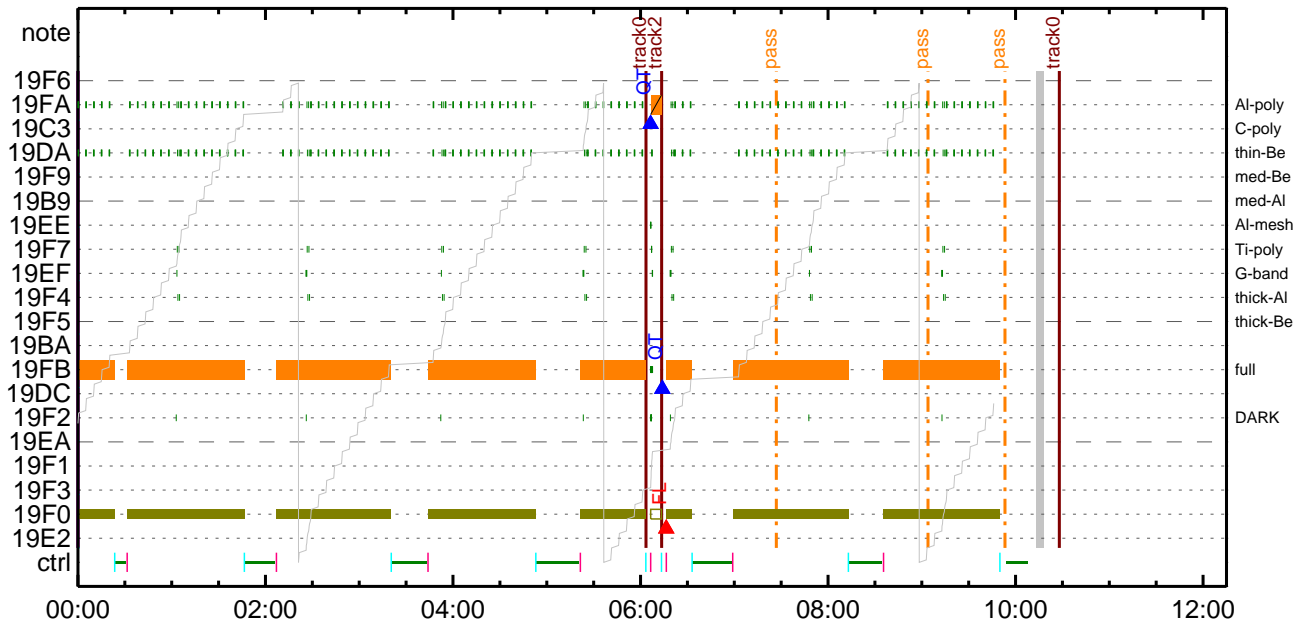
CMDI #0093 2014/02/14



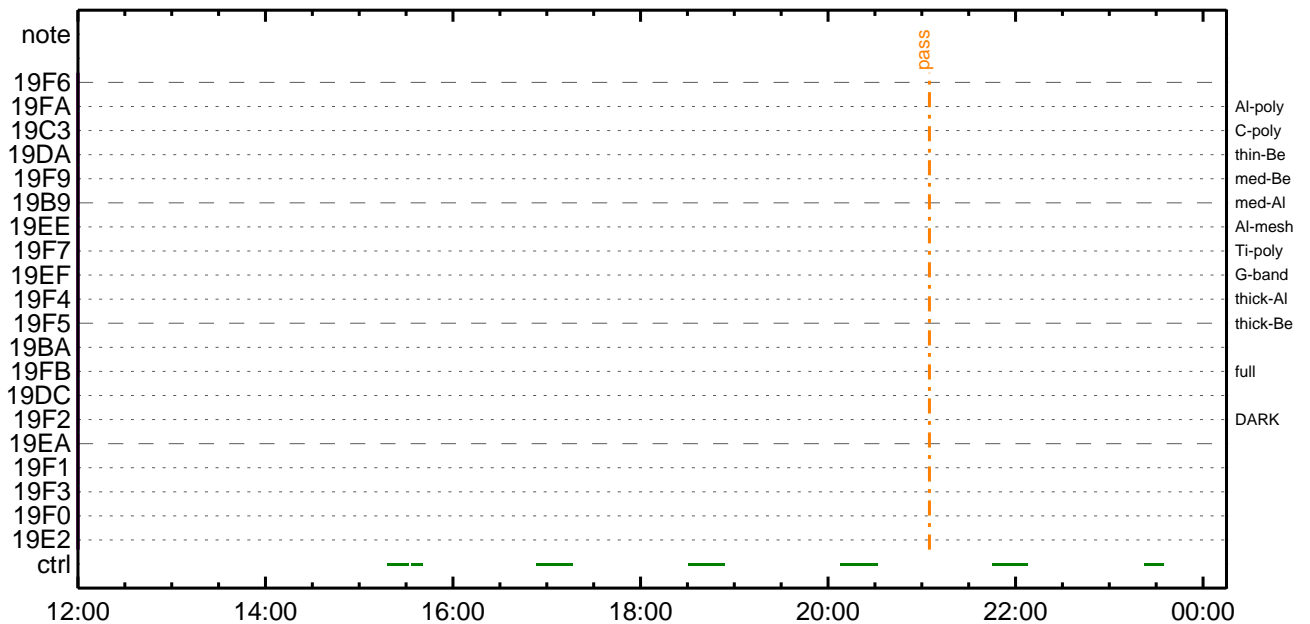
CMDI #0093 2014/02/14



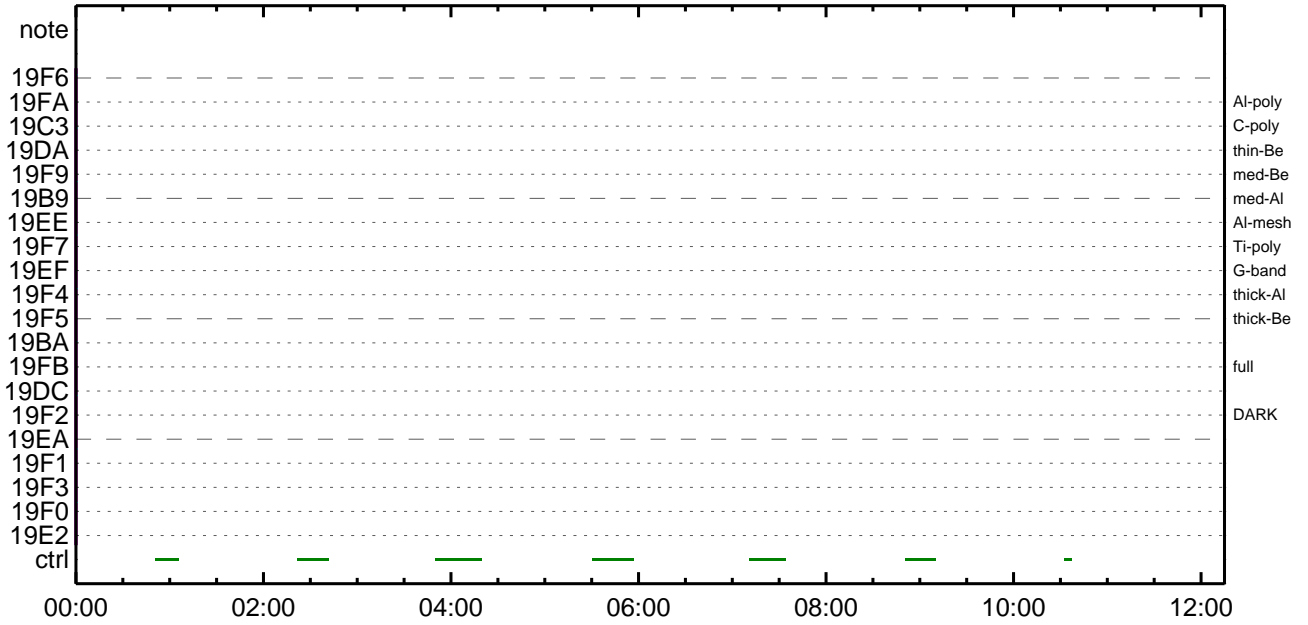
CMDI #0093 2014/02/15



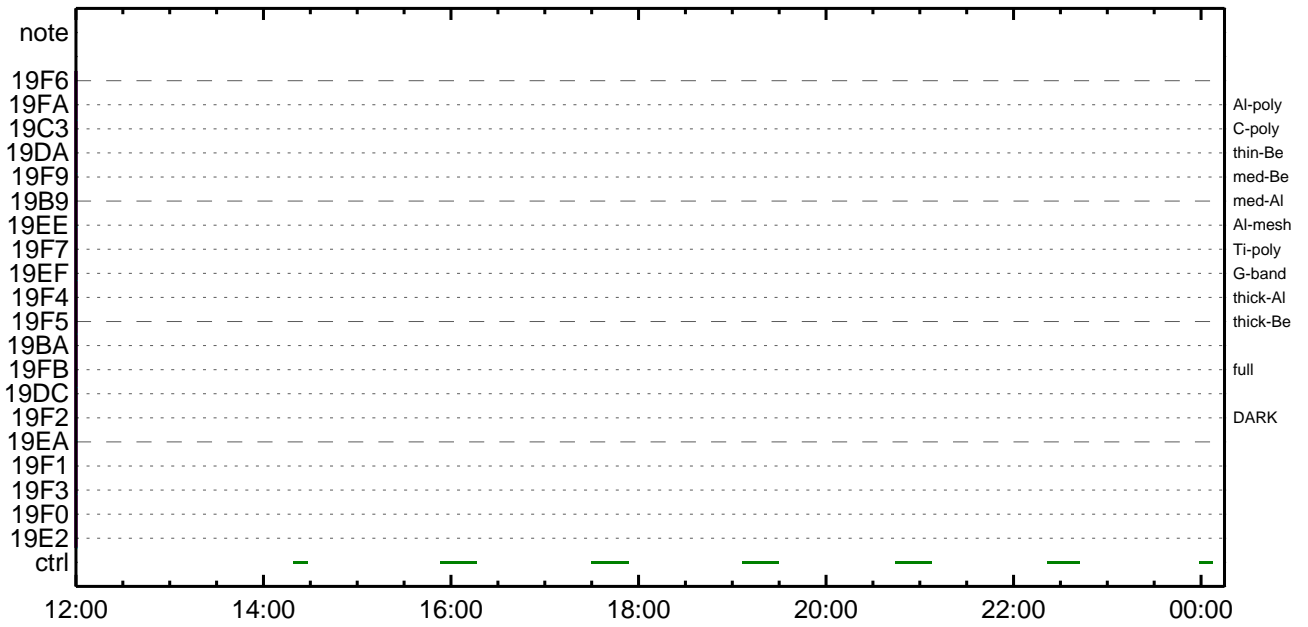
CMDI #0093 2014/02/15



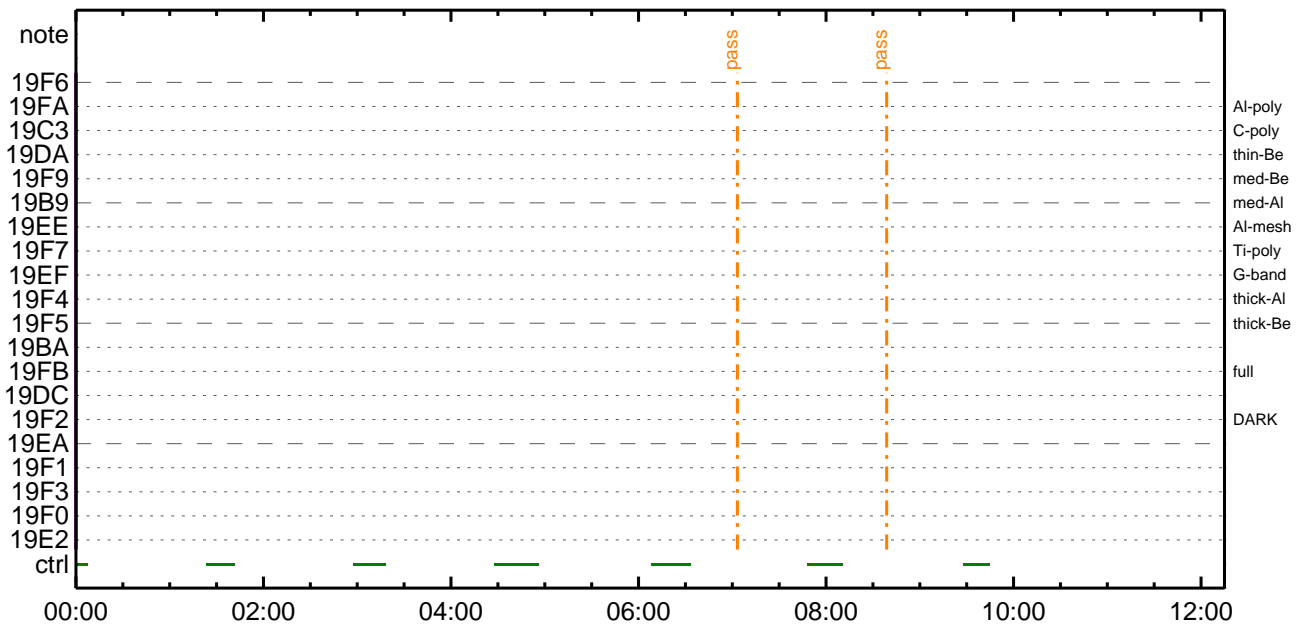
CMDI #0093 2014/02/16



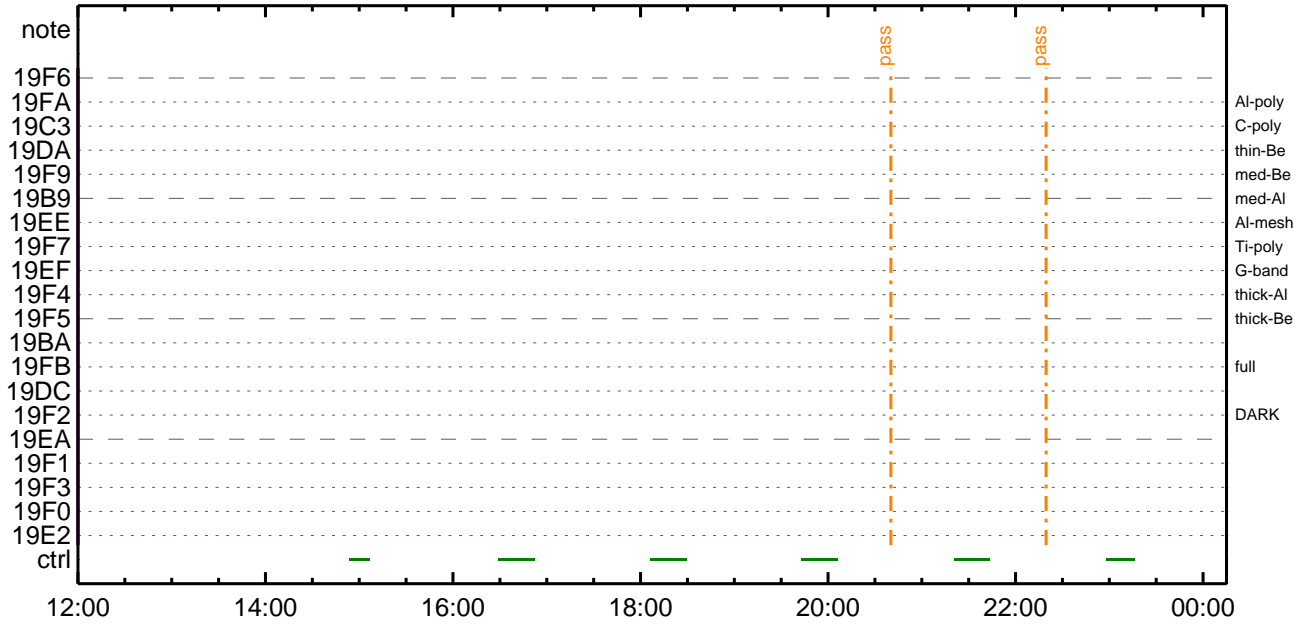
CMDI #0093 2014/02/16



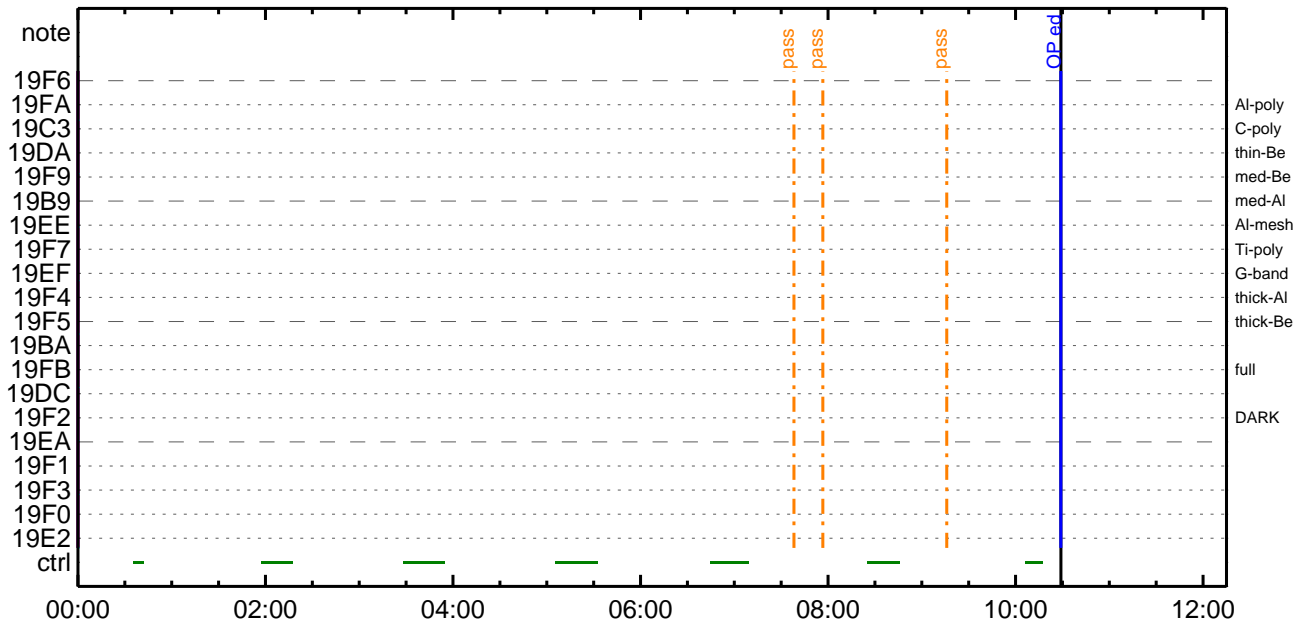
CMDI #0093 2014/02/17



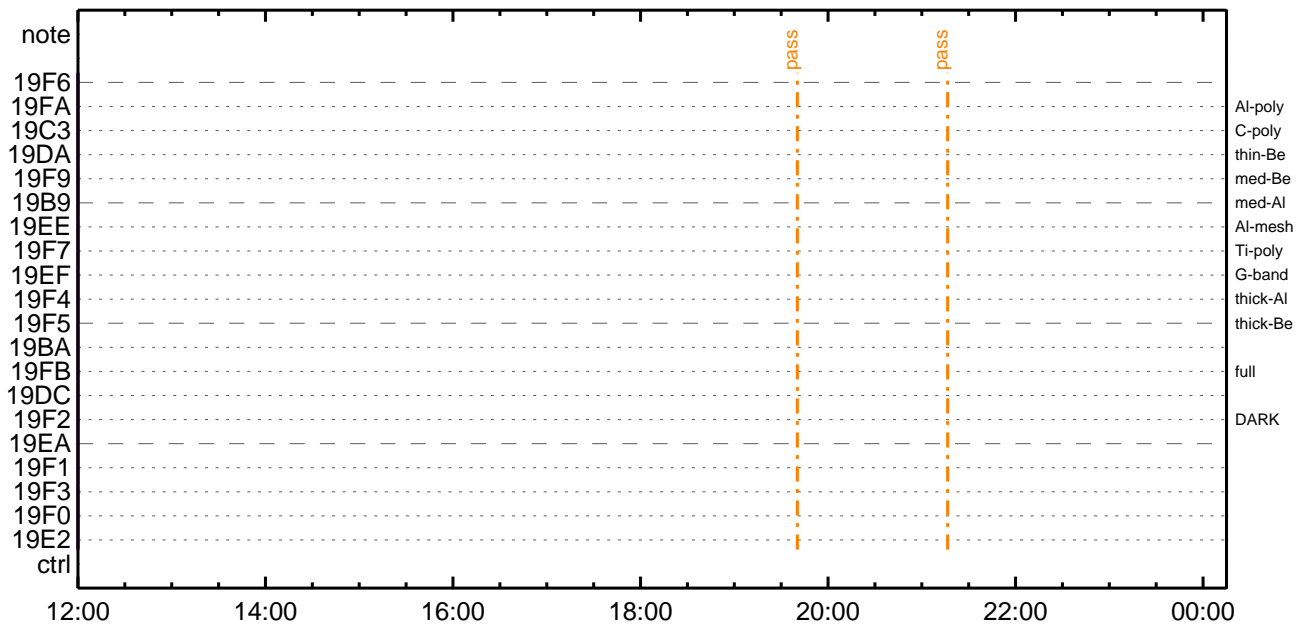
CMDI #0093 2014/02/17



CMDI #0093 2014/02/18



CMDI #0093 2014/02/18



(a) Spacecraft Operation Procedure (real-commands)

```
main-205 2014-02-13 12:52:43 278 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY-¼Ä»Ü;ä
0005 C.
0006 C. YÀYË;¼Y³YÞYÖYÉÄ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Ëç¿ãâ•µ°•ï×ÁÇçíYçYÄY×Yí;¼YË;ËËÈµ•íÉ;È•È¼°ÇÖã•ç¿i¹ççí;çÄ®, ùã¹ãããããçÄ+ç®ã•ããããããË;ç
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ç¿µ;ON
0016 C. *****
0017 C. ç" °ËÄ, í×ÈYããLOSããççí»p´Öãð¹íí, ç•; çÉÖí×ãÈXÁÖONãí¹ÓãÈíããããããË;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÖYÉYíYÄY-¼ÖÁÖã-°ÁÄêã•ç¿çé; ç°Ê²¼ãí°ËÄ, ¼ê¼çãð¼Ä¹Óã¹çé;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼i°ËÄ,
0033 C. *****
0034 C. ç" RESTART;ÈPT1;Ëã•ç¿çç¿i¹ççí; ç°Ê²¼ãí°ËÄ¹Óã»°; çDCBC-150ã¿çÈãã;ç
0035 C.
0036 . C. ;ãPT1°ËÄ, ³«»í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÖYÄYËÄÚÁØ;ÈÄ•Ä°²óÈð;Ë, ããí°ËÄ, °Ë³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ËÄ, ç-¼«Ë°Äã»ßã•ç¿, ä; ç°Ê²¼ãð¼Ä¹Óã¹çé;ç
0055 C. YçYÖYÄYËÄÚÁØãÄ•Ä°²óÈðã-¶áãç¿i¹ççí°í»ã¹ãããããçÄÖãÄ;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼i°ËÄ,
0059 C. *****
0060 C. ç" RESTART;ÈPT2;Ëã•ç¿çç¿i¹ççí; ç°Ê²¼ãí°ËÄ¹Óã»°; çDCBC-151ã¿çÈãã;ç
0061 C.
0062 . C. ;ãPT2°ËÄ, ³«»í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÖYÄYËÄÚÁØ;ÈÄ•Ä°²óÈð;Ë, ããí°ËÄ, °Ë³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ËÄ, Äã»ß;çXÁ+ç¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ËÄ, Äã»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ç¿µ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;aOP/OGY1;4YE;a
0103 S. OP op-205:OP
0104 (
0105 S. OG og-205:OG
0106 (
0107 C.
0108 C. ;aNMOG&OPf^°eYAYOX;a
0109 C. NMOG(0x200000-0x207FFF;s 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. ;c[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. ;c[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. ;c[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. ;c[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. ;c[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. ;c[HK1_PKT_FORM_NO] EQ 7
0120 C. ;c[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. ;c[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. ;c[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. ;c[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx1/2^a^i>od^3^iCS
0125 C. ;c[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGaf^E^1c.e^2^iOKod^3^iCS
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;s 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. ;c[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. ;c[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. ;c[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. ;c[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. ;c[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. ;c[HK1_PKT_FORM_NO] EQ 7
0139 C. ;c[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. ;c[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. ;c[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. ;c[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx1/2^a^i>od^3^iCS
0144 C. ;c[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGaf^E^1c.e^2^iOKod^3^iCS
0146 C.
0147 C. NMOG(0x210000-0x2100FF;s 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. ;c[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. ;c[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. ;c[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. ;c[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. ;c[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. ;c[HK1_PKT_FORM_NO] EQ 7
0158 C. ;c[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. ;c[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. ;c[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. ;c[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx1/2^a^i>od^3^iCS
0163 C. ;c[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPaf^E^1c.e^2^iOKod^3^iCS
0165 C.
0166 C. ***** oE^2/4oI^3A^'¶A^oEE^-oA^:z@ (%amu-YAYOXx1/2e^1/2caOAOAeoc1/2^a^oE%i^1coca) *****
0167 C. DHUYa;4YE;E^1/2Y^1/2Y;4YE;EodIa^1
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. ;c[HK1_PKT_FORM_NO] EQ 2
0171 C. ;c[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. ;c[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. ;c[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;s OPOG UPLOADa-A^:z@NGuI^1i^1c;^oE^2/4oI^TI-CMDA^:z@oI^A^1Oa.oEooc^3oE;f
0180 C. oEo;^cSEToEDUMPaI^E^±oI^N^1c^1Oa|o^3oE;f
0181 C.
0182 C. TIY^3Y^pY^oY^EodA^I^z(UT)
0183 +. TI 2014-02-13 10:04:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. ;c[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2014-02-13 10:04:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. ;c[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2014-02-13 10:04:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. ;c[HK1_TI_CMD_NUM] EQ 1COUNTUP


```
0194 C.
0195 +. TI 2014-02-13 10:08:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °È²¼øïÄê%ííñøîŷÄŷ§ŷÄŷ~¹àitÜ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷØŷx
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]       EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]       EQ      EXEC
0224 C.
0225 C. ŷÄŷØŷx½ªî»ò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]       EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLøîŷÈ¹ç•è²ìOKøð³îç§
0229 C.
0230 C. DHUŷà;¼ŷÈ;Èŷ½. ŷî;¼ŷÈ;Èøðîäø¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2014-02-13 10:08:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC (21 02)
0247 +. TI 2014-02-13 10:08:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC (22)
0250 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C.
0255 C. ***** XRT START *****
0256 C. Execute, after the success of OP upload.
0257 +. TI 2014-02-13 10:08:00.0
0258 DC 07-F0 MDP_XRT_MODE_STBY
0259 BC (c3)
0260 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0261 C.
0262 C. ***** XRT END *****
0263 C.
0264 C. ***** MDP ´ûÄîøî»ò¼ŷøÈÄðø¹øèDCBC•x²è *****
0265 C. (¼ª°îŷØŷÄŷÈŷËŷÈŷáŷçŷèøÈ¼øø¼Ä»Ûø¹øè)
0266 S. DC-BC dcbc-402:DCBC
0267 (MDP_known_event)
0268 C.
0269 C.
0270 C. ***** ŷÐŷ¹.İ Daily±;îñøÈ´øø¹øèDCBC•x²è *****
0271 S. DC-BC dcbc-153:DCBC
0272 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0273 C.
0274 C.
0275 C. ;äLOSŷÄŷŷÄŷŷÄŷ~¼Ä»Û;ä
0276 C.
0277 C. ***** LOS *****
0278 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```

main-206 2014-02-13 12:52:43 82 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;äAOSYÁY$YÁY-¼Á»Ü;ä
0005 C.
0006 C. YÁY$;¼Y³YFÝÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿µÁß•µ°Æ»Í×ÁÇøíYçYÁY×Yí;¼YÉ;ÈÈ%µ•íÉ;ÈßÈ¼°ÇÖß•µ¿¼i¹çµÍ;çÁ®,ù¹æñßµÇÁ+¿®µ•ñÈµµ³µÈ;f
0011 +. DC 02-8E AOCS_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCS_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCS_DUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCS_ORB_UPD
0044 . C.
0045 C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 . C. Verify EIS in MANUAL mode
0049 . C. Estimated OBSTBL upload time is 10s
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-820:EIS_OBSTBL
0054 ( )
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2014-02-13 10:08:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 . C. ***** MDP `úÁîòî»ö¼YøÈÁßø¹µèDCBC•x²è *****
0069 C. (¼á°íYÓYÁYÈYÞYÉYáYçYÈøE¼µ¼Á»Üø¹µè)
0070 . S. DC-BC dcbc-402:DCBC
0071 (MDP_known_event)
0072 C.
0073 C.
0074 . C. ***** YDY¹.Ī Daily±¿İÑøÈ´Øø¹µèDCBC•x²è *****
0075 . S. DC-BC dcbc-153:DCBC
0076 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077 C.
0078 C.
0079 . C. ;äLOSÁY$YÁY-¼Á»Ü;ä
0080 C.
0081 . C. ***** LOS *****
0082 C.

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 80 80 20 20)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 85 83 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 85 83 06 06)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 + DC 07-F0 MDP_XRT_FLD_ENA
0141 BC (d8)
0142 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0143 BC (c8)
0144 + DC 07-F0 MDP_XRT_AEC_RESET
0145 BC (d0)
0146 + DC 07-F0 MDP_XRT_ARS_DIS
0147 BC (d5)
0148 + DC 07-F0 MDP_XRT_FLD_RESET
0149 BC (da)
0150 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0151 BC (c4 03)
0152 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0153 BC (c5 02)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C.
0157 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0158 C.
0159 +. DC 07-F0 MDP_XRT_MODE_OBSV
0160 BC (c2)
0161 +. TI 2014-02-13 10:08:02.0
0162 DC 07-F0 MDP_XRT_MODE_OBSV
0163 BC (c2)
0164 . C. ----- Success Verify ? OK / NG ____
0165 C.
0166 C. ***** XRT END *****
0167 C.
0168 . C. ***** MDP `úÃîï»ö%ÝðËÄð¹ðèDCBC•x²è *****
0169 C. (%á°îÿÓYÁYÈYÏYÉYáYçYèèE%¼ð¼Á»Û¹ðè)
0170 . S. DC-BC dcbc-402:DCBC
0171 (MDP_known_event)
0172 C.
0173 C.
0174 . C. ***** ¥D¥¹•İ Daily±çİÑðË'Ø¹ðèDCBC•x²è *****
0175 . S. DC-BC dcbc-153:DCBC
0176 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0177 C.
0178 C.
0179 . C. ;ãLOS¥Á¥$¥Ã¥¹¼Á»Û;ã
0180 C.
0181 . C. ***** LOS *****
0182 C.

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*** OP Sequence for XRT ***

2014/02/13	10:18:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/13	10:18:56.0	XRT_FOCUS_POSITION_449_OG [0x1c1]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/02/13	10:19:00.0	AOCS_Orе-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2014/02/13	10:19:16.0	XRT_FLD_ENA_440_OG [0x1b8]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/02/13	10:19:18.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/02/13	10:19:20.5	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/02/13	10:19:22.5	XRT_ARS_DIS_427_OG [0x1ab]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/02/13	10:19:24.5	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/13	10:19:26.5	XRT_QT_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2014/02/13	10:21:58.5	XRT_FL_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2014/02/13	10:28:30.0	XRT_Custom_430_OG [0x1ae]			
2014/02/13	10:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/13	14:08:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/13	14:08:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/13	14:08:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/13	14:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/13	14:15:30.0	XRT_Custom_430_OG [0x1ae]			
2014/02/13	14:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/13	15:41:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/13	15:41:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/13	15:41:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/13	15:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/13	16:05:00.0	XRT_Custom_430_OG [0x1ae]			
2014/02/13	16:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/13	17:18:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/13	17:18:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/13	17:18:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/13	17:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/13	17:42:00.0	XRT_Custom_430_OG [0x1ae]			
2014/02/13	17:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/13	17:58:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/13	17:58:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/02/13	17:59:00.0	AOCS_Orе-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2014/02/13	17:59:16.0	XRT_FLD_DIS_434_OG [0x1b2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2014/02/13	18:01:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/02/13	18:01:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/02/13	18:01:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2014/02/13	18:02:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/13	18:08:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/13	18:08:56.0	XRT_FOCUS_POSITION_449_OG [0x1c1]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/02/13	18:09:00.0	AOCS_Orе-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	02 00 00 00 00
2014/02/13	18:09:16.0	XRT_FLD_ENA_440_OG [0x1b8]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/02/13	18:09:18.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/02/13	18:09:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/02/13	18:09:22.0	XRT_ARS_DIS_427_OG [0x1ab]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/02/13	18:09:24.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/13	18:09:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08
2014/02/13	18:11:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]			

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2014/02/13	18:12:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/13	18:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/13	18:55:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/13	18:55:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/13	18:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/13	19:18:30.0	XRT_Custom_430_OG [0x1ae]					
2014/02/13	19:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/13	20:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/13	20:32:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/13	20:32:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/13	20:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/13	20:56:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/13	20:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/13	22:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/13	22:10:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/13	22:10:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/13	22:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/13	22:31:30.0	XRT_Custom_430_OG [0x1ae]					
2014/02/13	22:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/13	23:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/13	23:47:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/13	23:47:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/13	23:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/13	23:56:30.0	XRT_Custom_430_OG [0x1ae]					
2014/02/13	23:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/14	01:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/14	01:12:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/14	01:12:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/14	01:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/14	01:30:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/14	01:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/14	02:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/14	02:46:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/14	02:46:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/14	02:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/14	03:07:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/14	03:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/14	04:15:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/14	04:15:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/14	04:15:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/14	04:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/14	04:44:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/14	04:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/14	05:28:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/14	05:28:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/02/14	05:28:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2014/02/14	05:28:46.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/02/14	05:31:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/02/14	05:31:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/14	05:31:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]					

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2014/02/14	05:31:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/14	05:38:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/14	05:38:26.0	XRT_FOCUS_POSITION_449_OG [0x1c1]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2014/02/14	05:38:30.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00	
2014/02/14	05:38:46.0	XRT_FLD_ENA_440_OG [0x1b8]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/02/14	05:38:48.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/02/14	05:38:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/02/14	05:38:52.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/14	05:38:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/14	05:38:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08	
2014/02/14	05:41:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2014/02/14	05:41:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/14	05:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/14	05:56:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/14	05:56:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/14	05:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/14	06:21:30.0	XRT_Custom_430_OG [0x1ae]					
2014/02/14	06:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/14	07:36:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/14	07:36:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/14	07:36:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/14	07:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/14	07:59:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/14	08:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/14	09:16:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/14	09:16:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/14	09:16:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/14	09:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/14	09:34:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/14	09:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/14	14:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/14	14:42:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/14	14:42:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/14	14:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/14	14:54:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/14	14:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/14	16:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/14	16:17:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/14	16:17:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/14	16:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/14	16:41:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/14	16:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/14	17:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/14	17:54:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/14	17:54:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/14	17:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/14	18:17:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/14	18:17:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/02/14	18:18:00.0	AOCS_OrE-point_Start_2_OG [0x098]					

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2014/02/14	18:18:16.0	XRT_FLD_DIS_434_OG [0x1b2]	AOCU_NM	5	02-76	00	00	00	00	00
		MDP_XRT_FLD_DIS		1	07-F0	d9				
2014/02/14	18:20:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]		1	07-F0	c9				
		MDP_XRT_FLRCTRL_DIS		1	07-F0	c9				
2014/02/14	18:20:56.0	XRT_ARS_DIS_423_OG [0x1a7]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2014/02/14	18:20:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]		2	07-F0	c4	13			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	13			
2014/02/14	18:21:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2014/02/14	18:27:54.0	XRT_CTRL_MANU_402_OG [0x192]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2014/02/14	18:27:56.0	XRT_FOCUS_POSITION_449_OG [0x1c1]		4	07-F8	22	fe	97	00	
		XRT_FOCUS_POSITION		4	07-F8	22	fe	97	00	
2014/02/14	18:28:00.0	AOCs_Ore-point_Start_1_OG [0x097]		5	02-76	02	00	00	00	00
		AOCU_NM		5	02-76	02	00	00	00	00
2014/02/14	18:28:16.0	XRT_FLD_ENA_440_OG [0x1b8]		1	07-F0	d8				
		MDP_XRT_FLD_ENA		1	07-F0	d8				
2014/02/14	18:28:18.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]		1	07-F0	c8				
		MDP_XRT_FLRCTRL_ENA		1	07-F0	c8				
2014/02/14	18:28:20.0	XRT_AEC_RESET_413_OG [0x19d]		1	07-F0	d0				
		MDP_XRT_AEC_RESET		1	07-F0	d0				
2014/02/14	18:28:22.0	XRT_ARS_DIS_427_OG [0x1ab]		1	07-F0	d5				
		MDP_XRT_ARS_DIS		1	07-F0	d5				
2014/02/14	18:28:24.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2014/02/14	18:28:26.0	XRT_QT_PROG_SET_444_OG [0x1bc]		2	07-F0	c4	08			
		MDP_XRT_QT_PROG_SET		2	07-F0	c4	08			
2014/02/14	18:30:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]		2	07-F0	c5	02			
		MDP_XRT_FL_PROG_SET		2	07-F0	c5	02			
2014/02/14	18:31:00.0	XRT_CTRL_AUTO_408_OG [0x198]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2014/02/14	19:31:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2014/02/14	19:31:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2014/02/14	19:31:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2014/02/14	19:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2014/02/14	19:55:00.0	XRT_Custom_430_OG [0x1ae]								
2014/02/14	19:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2014/02/14	21:09:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2014/02/14	21:09:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2014/02/14	21:09:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2014/02/14	21:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2014/02/14	21:32:00.0	XRT_Custom_430_OG [0x1ae]								
2014/02/14	21:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2014/02/14	22:46:00.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2014/02/14	22:46:02.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2014/02/14	22:46:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2014/02/14	22:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2014/02/14	23:06:30.0	XRT_Custom_430_OG [0x1ae]								
2014/02/14	23:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2014/02/15	00:23:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2014/02/15	00:23:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2014/02/15	00:23:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2014/02/15	00:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2014/02/15	00:30:30.0	XRT_Custom_430_OG [0x1ae]								
2014/02/15	00:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2014/02/15	01:46:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2014/02/15	01:46:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2014/02/15	01:46:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		1	07-F0	e8				
		MDP_XRT_PREFLR_STRT		1	07-F0	e8				
2014/02/15	01:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		1	07-F0	e9				
		MDP_XRT_PREFLR_STOP		1	07-F0	e9				
2014/02/15	02:06:00.0	XRT_Custom_430_OG [0x1ae]								
2014/02/15	02:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		1	07-F0	c0				
		MDP_XRT_CTRL_AUTO		1	07-F0	c0				
2014/02/15	03:20:30.0	XRT_CTRL_MANU_400_OG [0x190]		1	07-F0	c1				
		MDP_XRT_CTRL_MANU		1	07-F0	c1				
2014/02/15	03:20:32.0	XRT_FLD_RESET_415_OG [0x19f]		1	07-F0	da				
		MDP_XRT_FLD_RESET		1	07-F0	da				
2014/02/15	03:20:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]								

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2014/02/15	03:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/15	03:43:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/15	03:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/15	04:53:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/15	04:53:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/15	04:53:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/15	04:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/15	05:20:30.0	XRT_Custom_430_OG [0x1ae]					
2014/02/15	05:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/15	06:03:24.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/15	06:03:26.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/02/15	06:03:30.0	AOCS_OrE-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2014/02/15	06:03:46.0	XRT_FLD_DIS_434_OG [0x1b2]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/02/15	06:06:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/02/15	06:06:26.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/15	06:06:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2014/02/15	06:06:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/15	06:13:24.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/15	06:13:26.0	XRT_FOCUS_POSITION_449_OG [0x1c1]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2014/02/15	06:13:30.0	AOCS_OrE-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	02 00 00 00 00	
2014/02/15	06:13:46.0	XRT_FLD_ENA_440_OG [0x1b8]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/02/15	06:13:48.0	XRT_FLRCTRL_ENA_448_OG [0x1c0]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/02/15	06:13:50.0	XRT_AEC_RESET_413_OG [0x19d]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/02/15	06:13:52.0	XRT_ARS_DIS_427_OG [0x1ab]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/15	06:13:54.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/15	06:13:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 08	
2014/02/15	06:16:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2014/02/15	06:16:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/15	06:33:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/15	06:33:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/15	06:33:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/15	06:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/15	06:58:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/15	06:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/15	08:13:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/15	08:13:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/15	08:13:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/15	08:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/15	08:34:30.0	XRT_Custom_430_OG [0x1ae]					
2014/02/15	08:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/15	09:50:00.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/15	10:28:00.0	AOCS_OrE-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	