

# XRT Timeline to be uploaded on 2014/02/18

Period: 2014/02/18 10:29:00 - 2014/02/22 09:06:00

\* \* \* \* \*

**Normal mode**

\* \* \* \* \*

NOT USED

\* \* \* \* \*

**Flare mode**

\* \* \* \* \*

NOT USED

\* \* \* \* \*

**Active Region Search**

\* \* \* \* \*

NOT USED

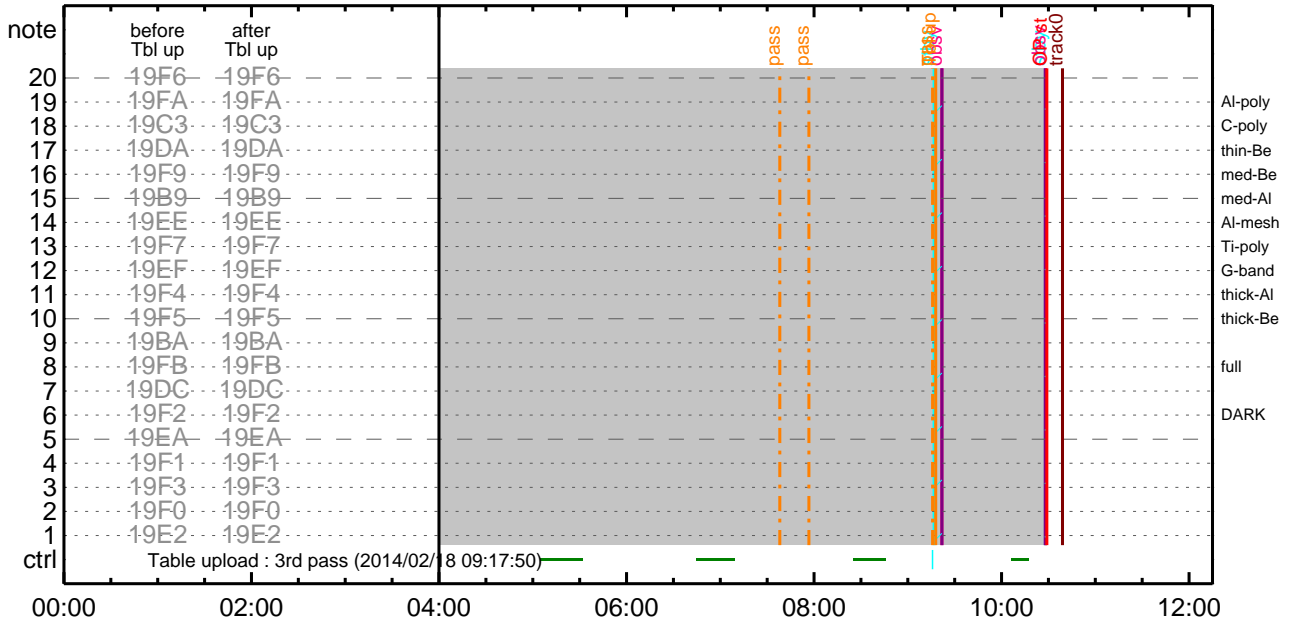
\* \* \* \* \*

**Flare Detection**

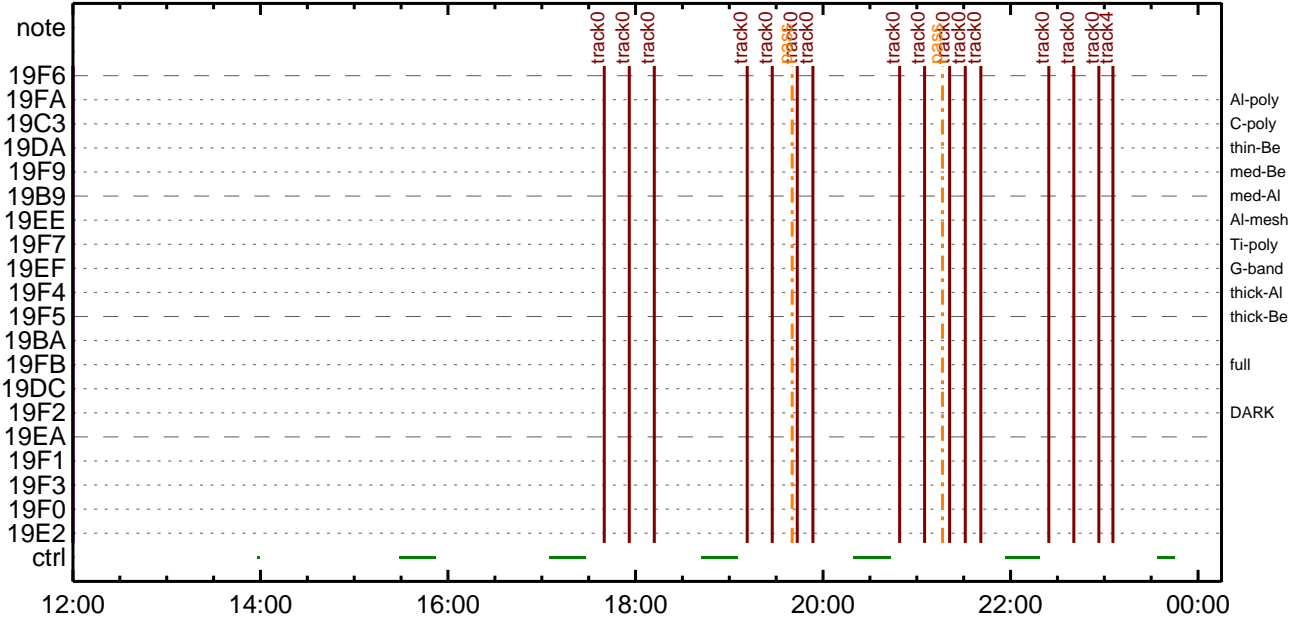
\* \* \* \* \*

NOT USED

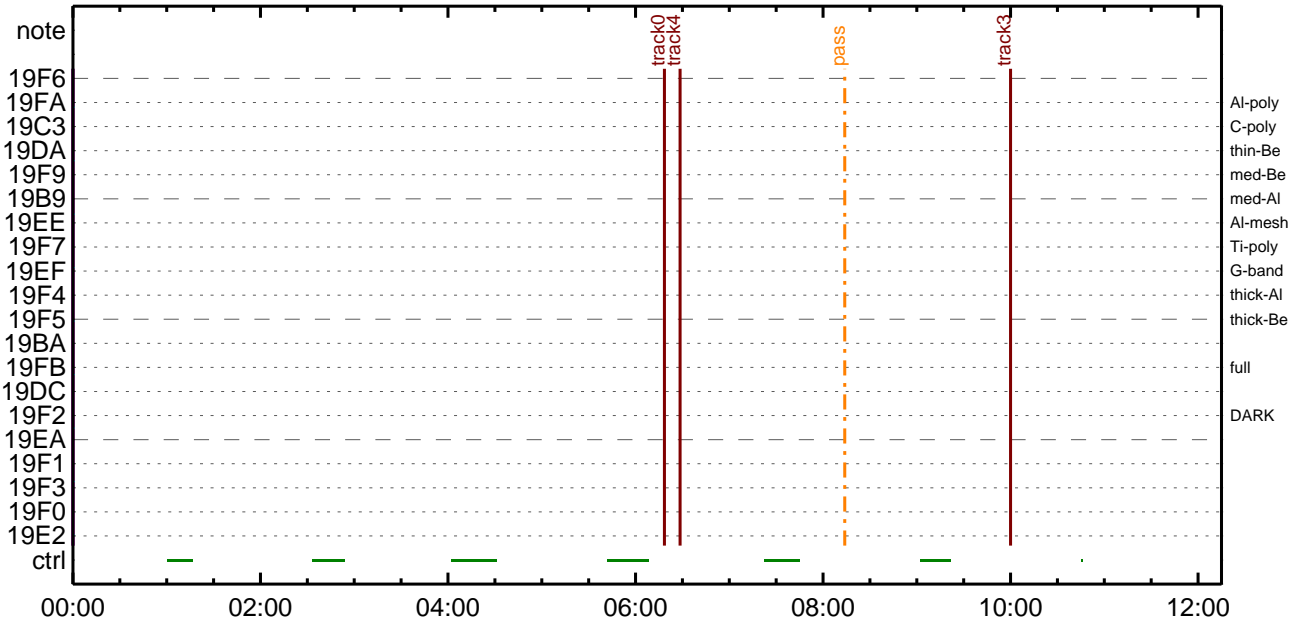
### CMDI #0102 2014/02/18



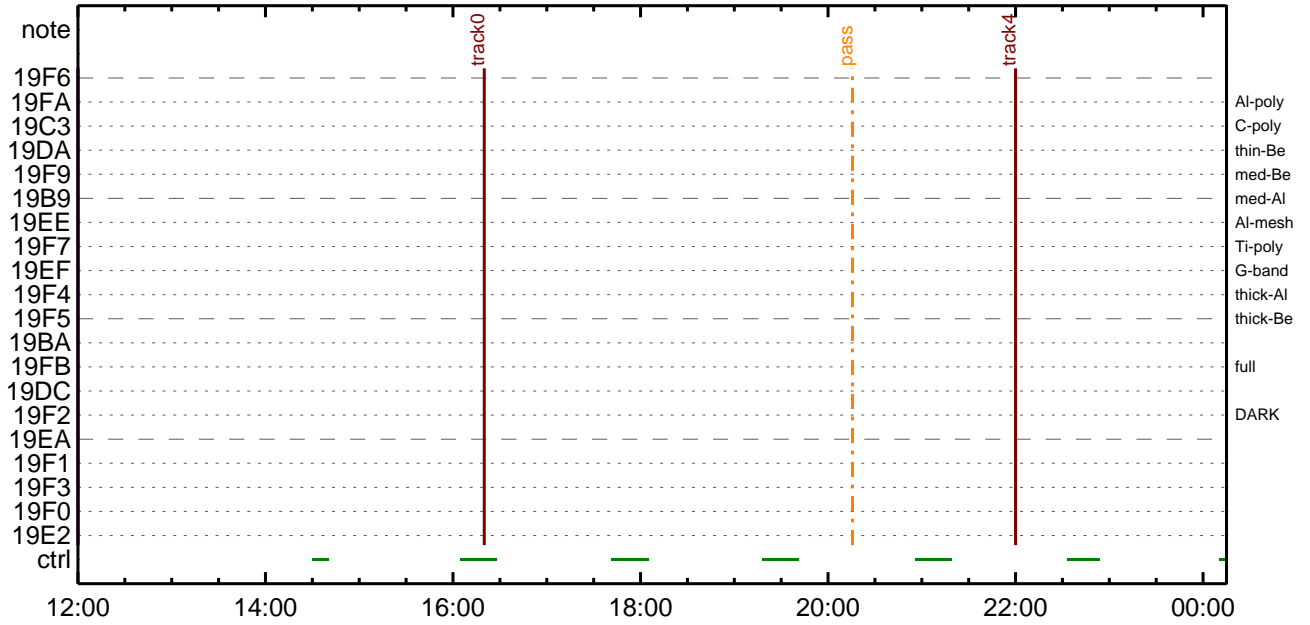
### CMDI #0102 2014/02/18



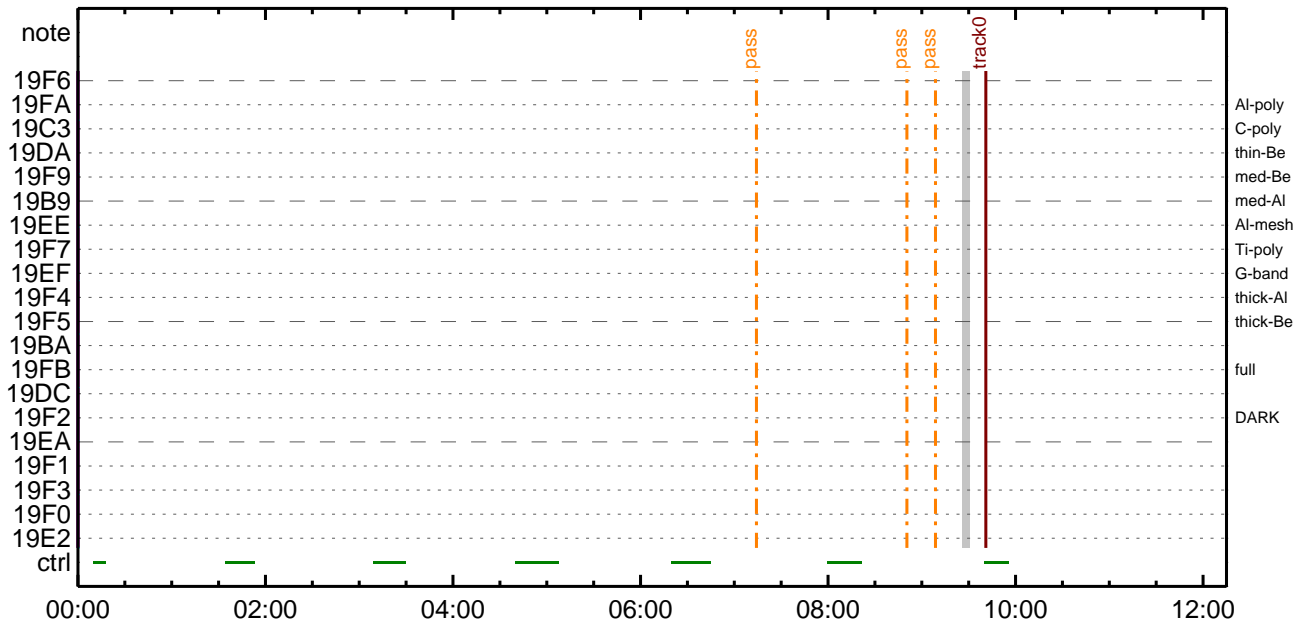
### CMDI #0102 2014/02/19



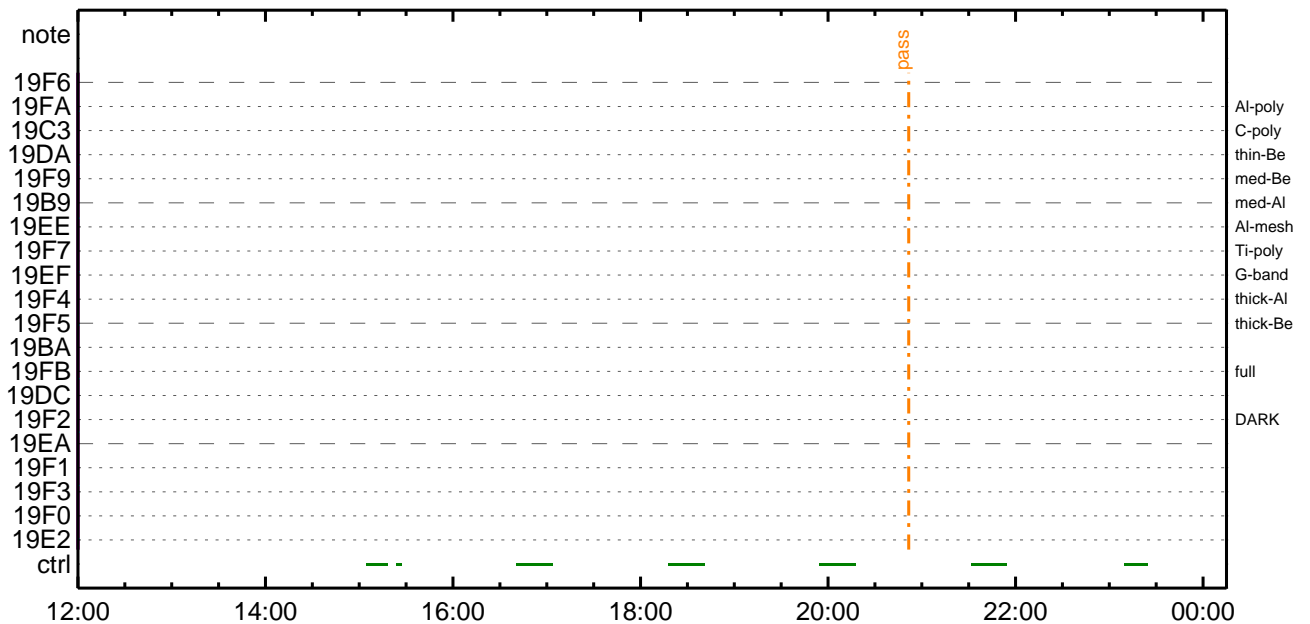
CMDI #0102 2014/02/19



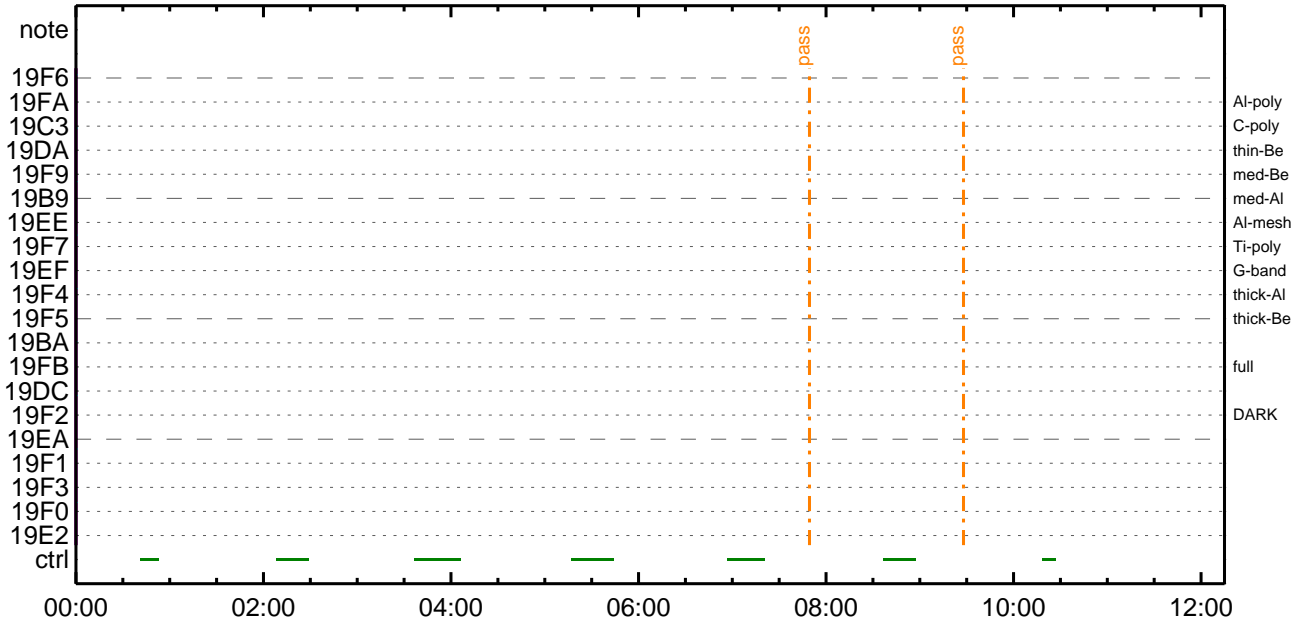
CMDI #0102 2014/02/20



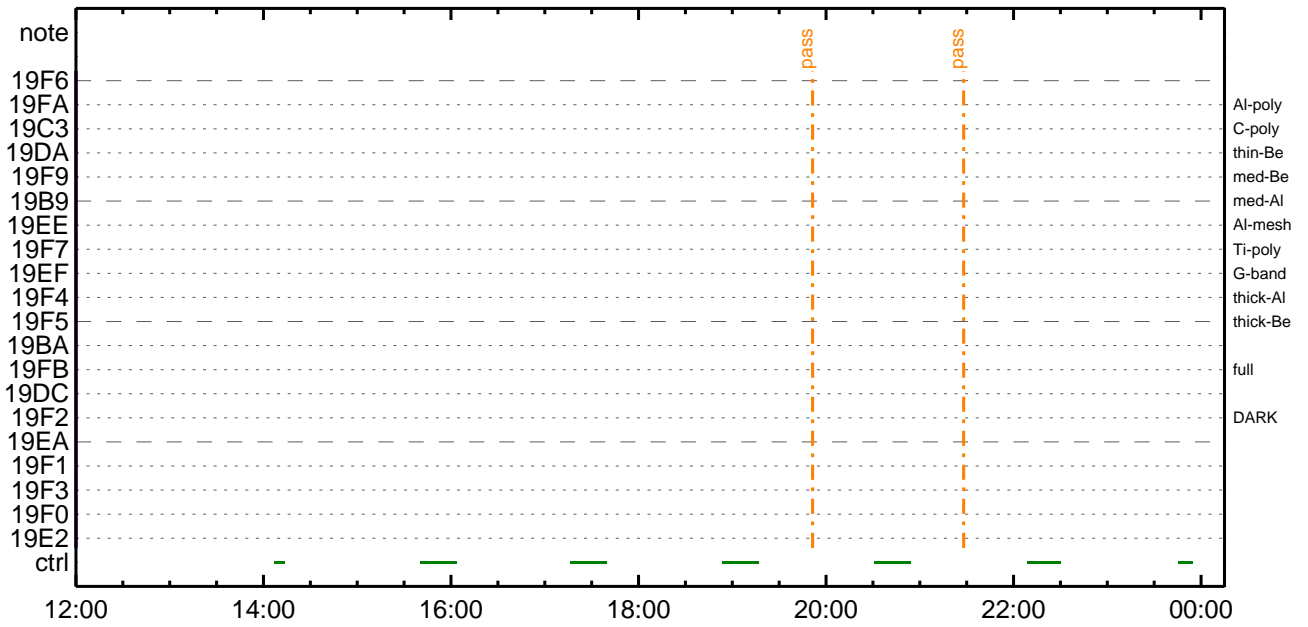
CMDI #0102 2014/02/20



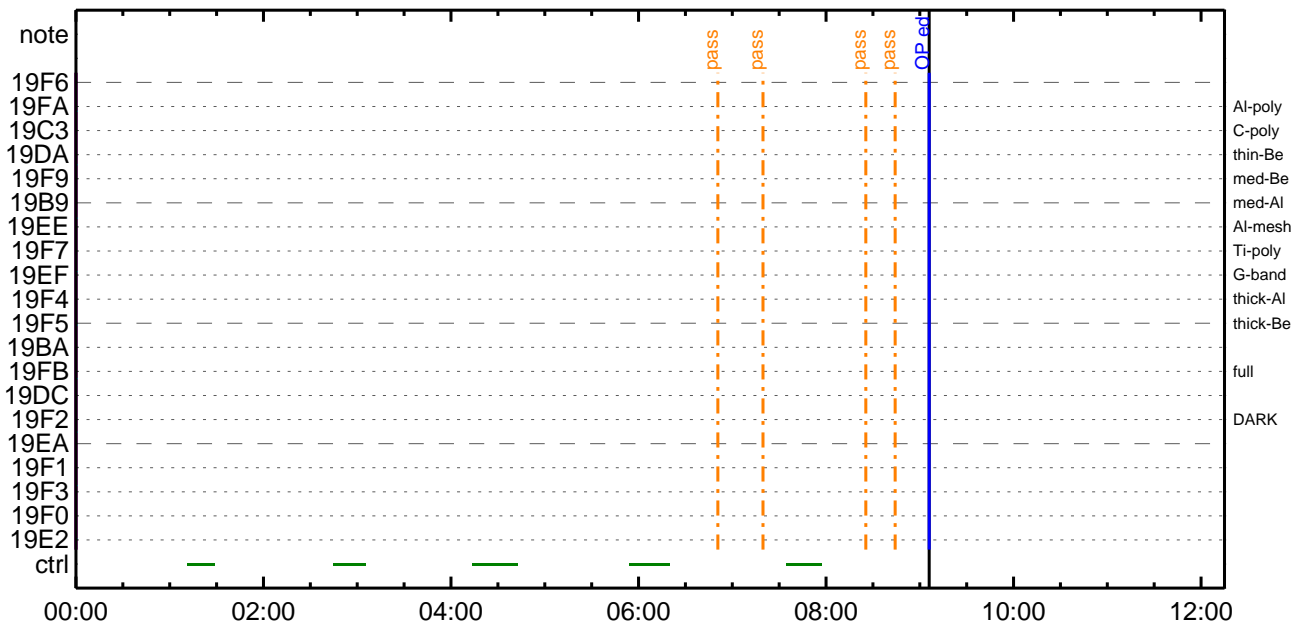
CMDI #0102 2014/02/21



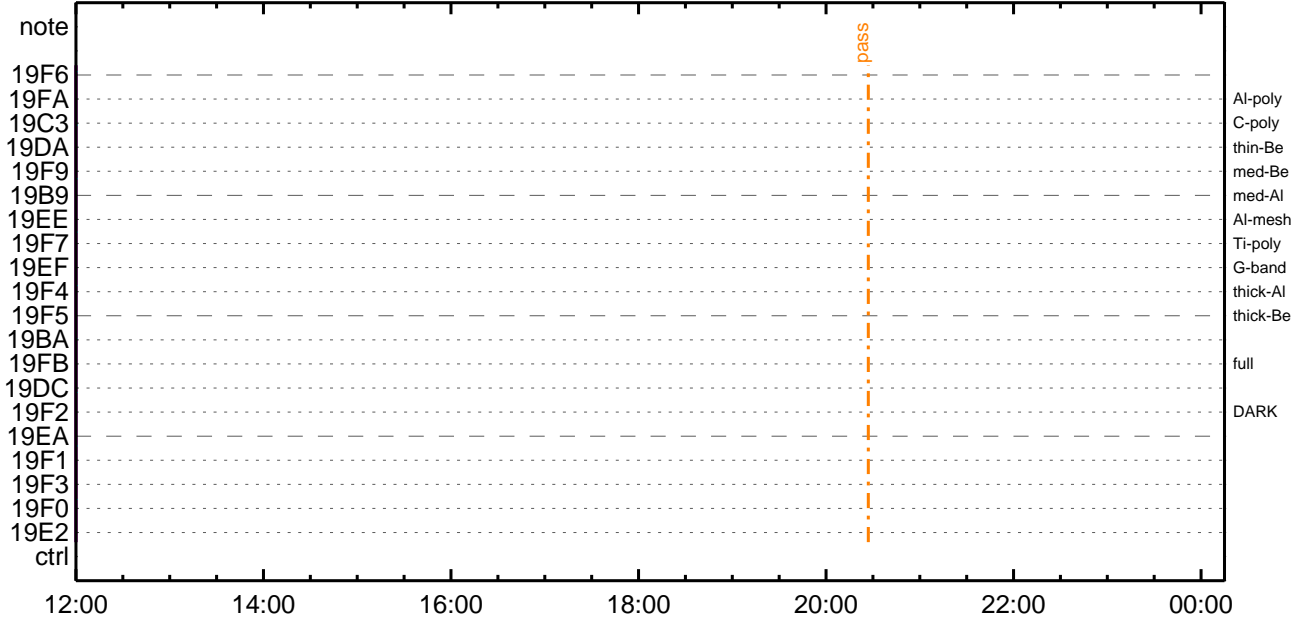
CMDI #0102 2014/02/21



CMDI #0102 2014/02/22



CMDI #0102 2014/02/22





```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-215:OP
0104 ( )
0105 S. OG og-215:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶A°EÉ¬òA÷¿@ (½âµ-YAYOYx½ê½çòðAÓÆòÇ½ª°²òE½i¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½, Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½i¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½A¹Ô²°²E²²²³òE;f
0180 C. ²²²¿;çSET²E²DUMP²îE±²îYÑY¹²ç¹Ô²²²³²E;f
0181 C.
0182 C. TIY³Y²Y³Y²E²òðA²î¿(UT)
0183 +. TI 2014-02-18 10:24:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2014-02-18 10:24:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2014-02-18 10:24:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```









```

0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-268:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Stop SP table >
0120 +. DC 07-F0 MDP_SP_CTRL_MANU
0121 BC (61)
0122 C. -----
0123 C. MDP_SP_CTRL_MODE = MANU [ ]
0124 C. -----
0125 C.
0126 . C. <Upload SP Observation Table>
0127 . S. RAM ram-284:MDP_OBS_S
0128 ( )
0129 C.
0130 . C. < Dump RAMID=MDP_OBS_S >
0131 +. DC 07-F0 MDP_DUMP_SPTBL
0132 BC (83 07 00 00 00 38 b8)
0133 C. -----
0134 C. MDP_OBS_S verify = OK/NG [ ]
0135 C. -----
0136 C.
0137 . C. < Upload DPL table >
0138 C.
0139 C. ¥ç¥Ã¥×¥í;¼¥É°âÊSTS_CHKðOFFðËð¹ðë
0140 C.
0141 . S. RAM ram-271:MDP_DPL
0142 ( )
0143 C.
0144 . C. < Dump RAMID=MDP_DPL >
0145 +. DC 07-F0 MDP_DUMP_FGTBL
0146 BC (82 07 00 38 b8 00 40)
0147 C. -----
0148 C. MDP_DPL verify = OK [ ]
0149 C. -----
0150 C.
0151 C. STS_CHKðONðËð¹ðë
0152 C.
0153 . C. < Update MDP DSC PAR1 >
0154 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0155 BC (4c)
0156 C. MDP_CMD_CODE = F04C0700[ ]
0157 C. MDP_CMD_CNT (count-up 1) [ ]
0158 C. -----
0159 C.
0160 . C.
0161 C. *****
0162 C. SOT TI command set
0163 C. *****
0164 C. Execute, after the success of TBL upload.
0165 +. TI 2014-02-18 10:28:18.0
0166 DC 07-F0 MDP_SOT_MODE_OBSV
0167 BC (40)
0168 . C. -----
0169 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0170 C. -----
0171 C.
0172 C.
0173 C. ***** XRT START *****
0174 C.
0175 +. DC 07-F0 MDP_XRT_CTRL_MANU
0176 BC (c1)
0177 + DC 07-F0 MDP_XRT_MODE_STBY
0178 BC (c3)
0179 . C. ----- Success Verify ? OK / NG____
0180 C.
0181 C. XRT Obs. Table Upload
0182 . S. RAM ram-291:MDP_OBS_X
0183 ( )
0184 C.
0185 +. DC 07-F0 MDP_DUMP_XRTTBL
0186 BC (84 07 00 00 00 3a d4)
0187 . C. ----- Comparison Check ? OK / ERR ____
0188 C.
0189 C.
0190 +. DC 07-F0 MDP_XRT_ROI_SET
0191 BC (cd 01 b1 b1 04 04)
0192 + DC 07-F0 MDP_XRT_ROI_SET
0193 BC (cd 02 b1 b1 08 08)

```

```

0194 + DC 07-F0 MDP_XRT_ROI_SET
0195 BC (cd 03 b1 b1 08 08)
0196 + DC 07-F0 MDP_XRT_ROI_SET
0197 BC (cd 04 b1 b1 06 06)
0198 + DC 07-F0 MDP_XRT_FLD_ENA
0199 BC (d8)
0200 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0201 BC (c8)
0202 + DC 07-F0 MDP_XRT_AEC_RESET
0203 BC (d0)
0204 + DC 07-F0 MDP_XRT_ARS_DIS
0205 BC (d5)
0206 + DC 07-F0 MDP_XRT_FLD_RESET
0207 BC (da)
0208 . C. ----- Success Verify ? OK / NG ____
0209 . C.
0210 . C.
0211 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0212 . C.
0213 +. DC 07-F0 MDP_XRT_MODE_OBSV
0214 BC (c2)
0215 +. TI 2014-02-18 10:28:02.0
0216 DC 07-F0 MDP_XRT_MODE_OBSV
0217 BC (c2)
0218 . C. ----- Success Verify ? OK / NG ____
0219 . C.
0220 . C. ***** XRT END *****
0221 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0222 +. DC 07-FC EIS_MODE_MANU
0223 BC (21 02)
0224 . C. Verify EIS in MANUAL mode
0225 . C. Estimated OBSTBL upload time is lmls
0226 . C. *****
0227 . C. EIS START OBSTBL LOAD
0228 . C. *****
0229 . S. RAM ram-820:EIS_OBSTBL
0230 . C.
0231 +. DC 07-FC EIS_DUMP_OBSTBL
0232 BC (07 07 07 00 00 70 00)
0233 . C.
0234 . C. Execute, after the success of OBSTBL upload.
0235 . C. Set EIS TI-commands
0236 +. TI 2014-02-18 10:28:50.0
0237 DC 07-FC EIS_MODE_CHG_ENA
0238 BC (20)
0239 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0240 . C. *****
0241 . C. EIS END OBSTBL LOAD
0242 . C. *****
0243 . C.
0244 . C. ***** MDP 'uãîñî»ö¼ýñÊãðñ¹ñèDCBC•x²è *****
0245 . C. (%ã°îÿÓÿÄÿËÿÏÿËÿäÿçÿèñÊ¼ñ¼Ä»Ûñ¹ñé)
0246 . S. DC-BC dcbc-402:DCBC
0247 (MDP_known_event)
0248 . C.
0249 . C.
0250 . C. ***** ÿÐÿ¹•ï Daily±;îññÉ´øñ¹ñèDCBC•x²è *****
0251 . S. DC-BC dcbc-153:DCBC
0252 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0253 . C.
0254 . C.
0255 . C. ;ãLOSÿÄÿÏÿËÿäÿçÿèñÊ¼ñ¼Ä»Ûñ¹ñé
0256 . C.
0257 . C. ***** LOS *****
0258 . C.

```

Feb 18, 14 12:35

XRT\_OGLIST\_0102.chk

Page 1/1

\*\*\* OP Sequence for XRT \*\*\*

2014/02/18	10:39:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	55	ca	01	58
2014/02/18	17:40:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	ac	cd
2014/02/18	17:56:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	d6	67
2014/02/18	18:12:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	00	00	00	00
2014/02/18	19:11:30.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	00	00	29	99
2014/02/18	19:27:30.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	00	00	53	33
2014/02/18	19:43:30.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00	d6	36	b7	8e
2014/02/18	19:53:30.0	AOCS_ORe-point_Start_8_OG [0x09e]							
		AOCU_NM	5	02-76	00	b4	b5	db	75
2014/02/18	20:49:00.0	AOCS_ORe-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	ac	5b	00	00
2014/02/18	21:05:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]							
		AOCU_NM	5	02-76	00	b4	b5	24	8b
2014/02/18	21:21:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]							
		AOCU_NM	5	02-76	00	d6	36	48	72
2014/02/18	21:31:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]							
		AOCU_NM	5	02-76	00	29	ca	b7	8e
2014/02/18	21:41:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]							
		AOCU_NM	5	02-76	00	4b	4b	db	75
2014/02/18	22:24:30.0	AOCS_ORe-point_Start_14_OG [0x0a4]							
		AOCU_NM	5	02-76	00	53	a5	00	00
2014/02/18	22:40:30.0	AOCS_ORe-point_Start_15_OG [0x0a5]							
		AOCU_NM	5	02-76	00	4b	4b	24	8b
2014/02/18	22:56:30.0	AOCS_ORe-point_Start_16_OG [0x0a6]							
		AOCU_NM	5	02-76	00	29	db	48	72
2014/02/18	23:05:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	04	00	00	00	00
2014/02/19	06:18:30.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	00	00	00	00
2014/02/19	06:28:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	04	00	00	00	00
2014/02/19	10:00:00.0	AOCS_ORe-point_Start_18_OG [0x0a8]							
		AOCU_NM	5	02-76	03	00	00	00	00
2014/02/19	16:20:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00	55	ca	01	58
2014/02/19	22:00:00.0	AOCS_ORe-point_Start_17_OG [0x0a7]							
		AOCU_NM	5	02-76	04	00	00	00	00
2014/02/20	06:00:00.0	XRT_TCIB_XRT_S_HTR_A_DIS_437_OG [0x1b5]							
		TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2014/02/20	09:41:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	00	00	00	00