

XRT Timeline to be uploaded on 2014/02/22

Period: 2014/02/22 09:06:00 - 2014/02/27 10:29:00

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Normal mode

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XOB #19E2: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)

Term	Pointing (x, y)	Comment
02/22 09:33:30 - 02/22 11:28:00	Track (-215.7, -39.5) @ 02/22 09:16:00	# OP start + 10min AR 11982 Tracking
02/22 16:03:00 - 02/22 17:53:00	Track (-154.3, -37.9) @ 02/22 16:00:00	Track AR 11982
02/22 18:29:00 - 02/22 22:44:30	Track (-132.0, -37.4) @ 02/22 18:26:00	Track AR 11982
02/23 18:13:00 - 02/23 22:59:54	Track (87.9, -36.4) @ 02/23 18:10:00	Track AR 11982
02/24 06:18:00 - 02/24 08:48:30	Track (198.7, -38.2) @ 02/24 06:15:00	Track AR 11982

PROG= 01 Inf.-time(s)

- Subr= 1 1-time(s) 2.0sec
 - Seqn= 8 2-time(s) 2.0sec
 - Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
- Subr= 2 2-time(s) 2.0sec
 - Seqn= 24 1-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
 - Open/G-band Open/G-band open Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
 - Seqn= 42 4-time(s) 2.0sec
 - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
 - Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - Seqn= 62 20-time(s) 90.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
 - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
 - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #19FA: Synoptic Q95 2x2 - Al/mesh(3/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(8/362/1443) + Thin-

Term	Pointing (x, y)	Comment
02/22 18:19:00 - 02/22 18:25:54	Fixed (0.0, 0.0)	synoptic, shifted 16.0 min
02/23 06:05:00 - 02/23 06:31:30	Fixed (0.0, 0.0)	synoptic, shifted 2.0 min
02/23 18:03:00 - 02/23 18:09:54	Fixed (0.0, 0.0)	synoptic
02/24 06:08:00 - 02/24 06:14:54	Fixed (0.0, 0.0)	synoptic, shifted 5.0 min
02/24 17:56:30 - 02/24 19:05:00	Fixed (0.0, 0.0)	synoptic, shifted -6.5 min
02/25 05:41:30 - 02/25 06:06:00	Fixed (0.0, 0.0)	synoptic, shifted -21.5 min

PROG= 19 1-time(s)

- Subr= 1 1-time(s) 12.0sec
 - Seqn= 35 1-time(s) 4.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 3ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 5 1-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
 - Seqn= 72 1-time(s) 4.0sec
 - Open/Ti-poly Open/Ti-poly close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Ti-poly Open/Ti-poly close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 74 1-time(s) 2.0sec
 - thin-Be/Open thin-Be/Open close Safe Norm 32ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - thin-Be/Open thin-Be/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - thin-Be/Open thin-Be/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 6 1-time(s) 2.0sec
 - Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
 - Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

XOB #19FC: AR standard-B (thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, With G-band (33ms/45ms leak), 10s cad, IHOP

Term	Pointing (x, y)	Comment
02/23 11:33:00 - 02/23 15:16:00	Track (26.1, -36.0) @ 02/23 11:30:00	IHOP247 (AR)
02/23 23:03:00 - 02/24 02:20:00	Track (132.4, -36.9) @ 02/23 23:00:00	IHOP247 (AR)

PROG= 11 Inf.-time(s)

- Subr= 1 1-time(s) 2.0sec
 - Seqn= 8 2-time(s) 2.0sec
 - Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
- Subr= 2 2-time(s) 2.0sec
 - Seqn= 24 1-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec

Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 42 4-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 55 55-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	3.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	3.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	3.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #19F0: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops-lowered-gband

Term	Pointing (x, y)	Comment
02/22 09:33:30 - 02/22 11:28:00	Track (-215.7, -39.5) @ 02/22 09:16:00	# OP start + 10min AR 11982 Tracking
02/22 16:03:00 - 02/22 17:53:00	Track (-154.3, -37.9) @ 02/22 16:00:00	Track AR 11982
02/22 18:29:00 - 02/22 22:44:30	Track (-132.0, -37.4) @ 02/22 18:26:00	Track AR 11982
02/23 11:33:00 - 02/23 15:16:00	Track (26.1, -36.0) @ 02/23 11:30:00	IHOP247 (AR)
02/23 18:13:00 - 02/23 22:59:54	Track (87.9, -36.4) @ 02/23 18:10:00	Track AR 11982
02/23 23:03:00 - 02/24 02:20:00	Track (132.4, -36.9) @ 02/23 23:00:00	IHOP247 (AR)
02/24 06:18:00 - 02/24 08:48:30	Track (198.7, -38.2) @ 02/24 06:15:00	Track AR 11982

PROG= 02 12-time(s)

Subr= 1 45-time(s) 10.0sec												
Seqn= 9 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 10.0sec												
Seqn= 17 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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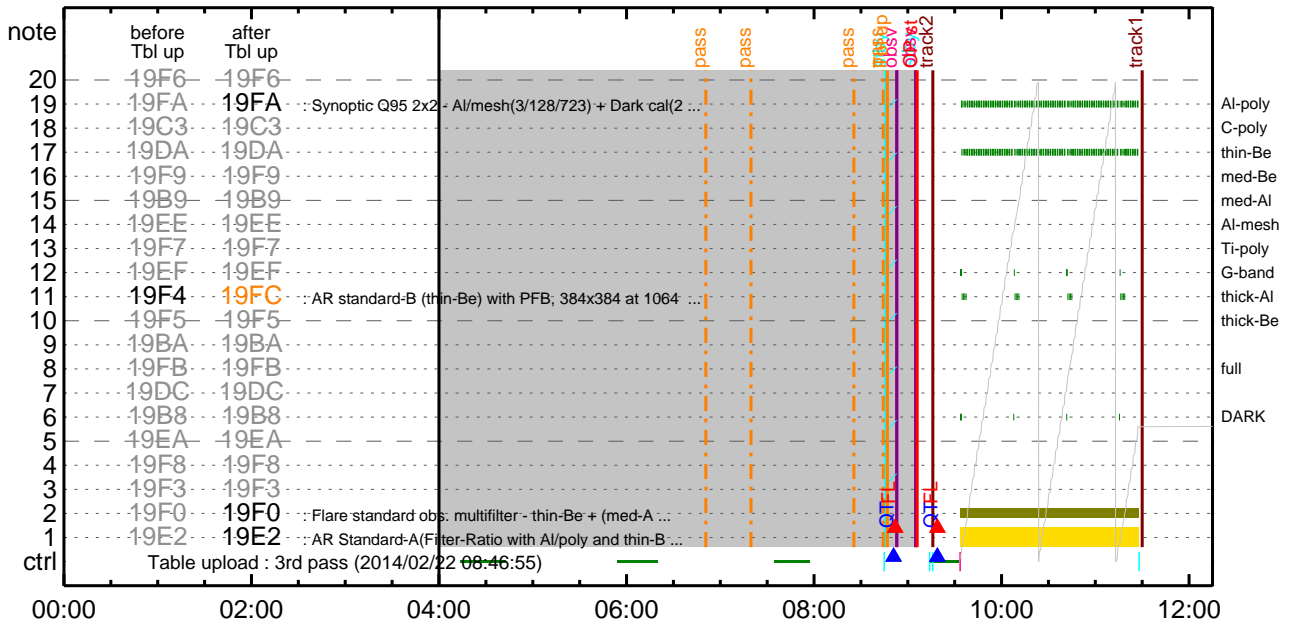
Flare Detection

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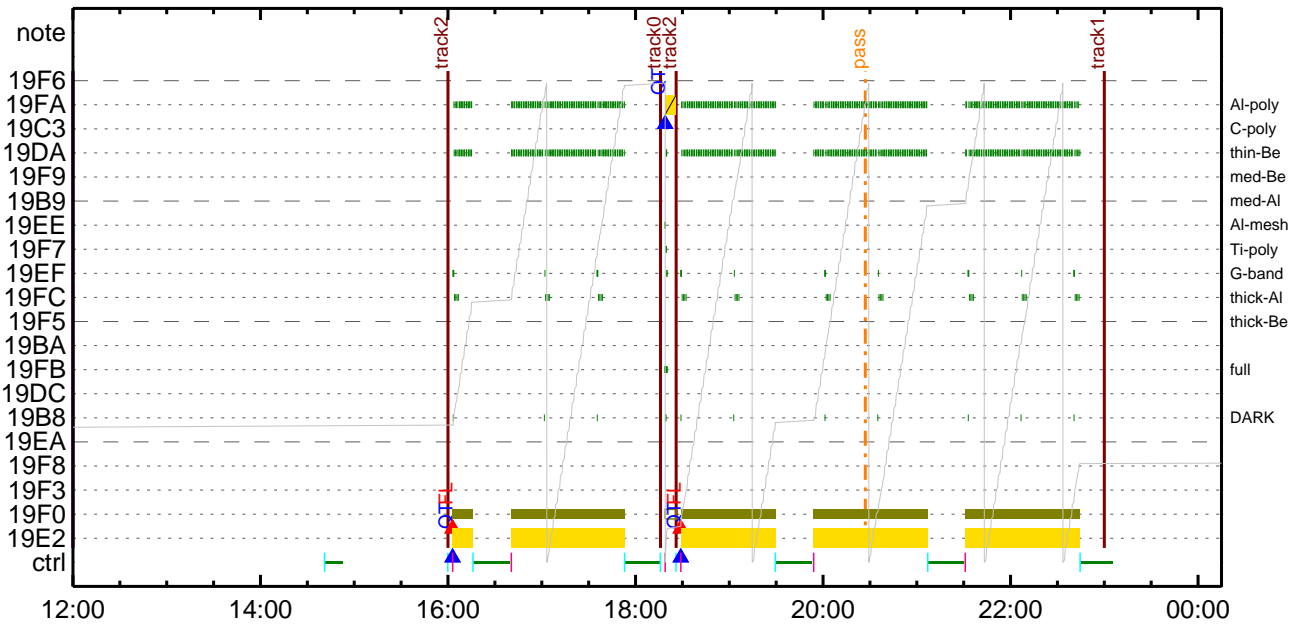
FLD Patrol

Term	Pointing (x, y)	Comment									
02/22 18:26:16 - 02/23 06:02:16	Track (-132.0, -37.4) @ 02/22 18:26:00	Track AR 11982									
02/23 11:30:16 - 02/23 18:00:16	Track (26.1, -36.0) @ 02/23 11:30:00	IHOP247 (AR)									
02/23 18:10:16 - 02/24 06:05:16	Track (87.9, -36.4) @ 02/23 18:10:00	Track AR 11982									
02/24 06:15:16 - 02/24 17:53:46	Track (198.7, -38.2) @ 02/24 06:15:00	Track AR 11982									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

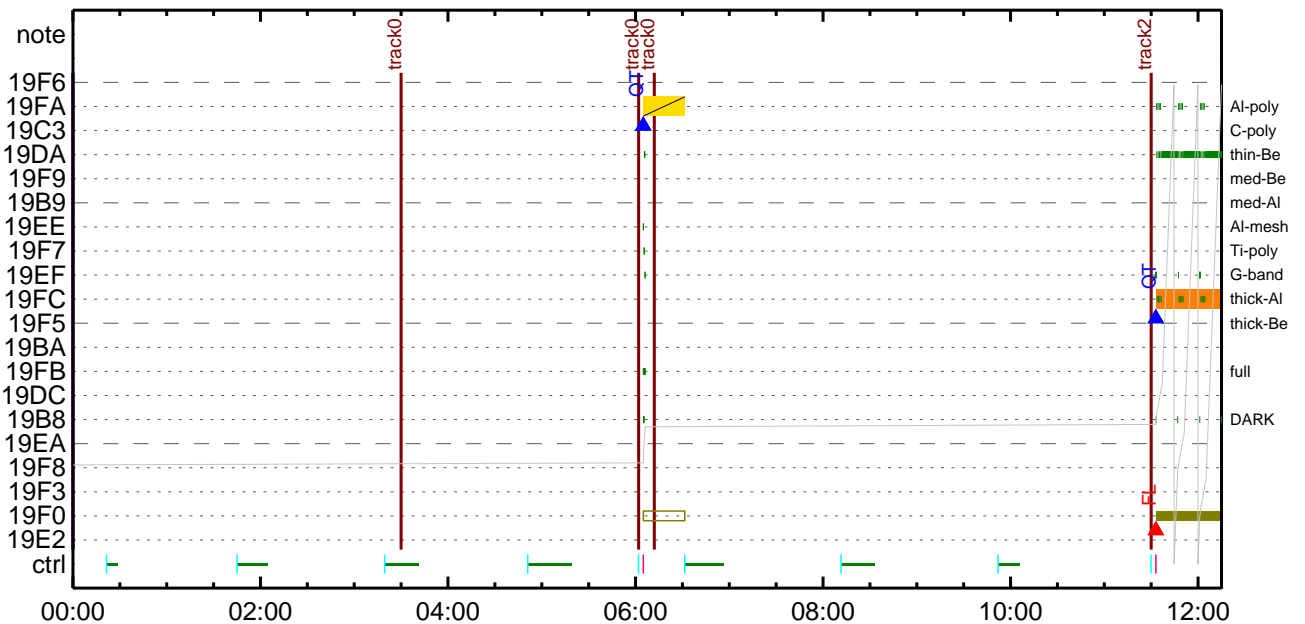
CMDI #0112 2014/02/22



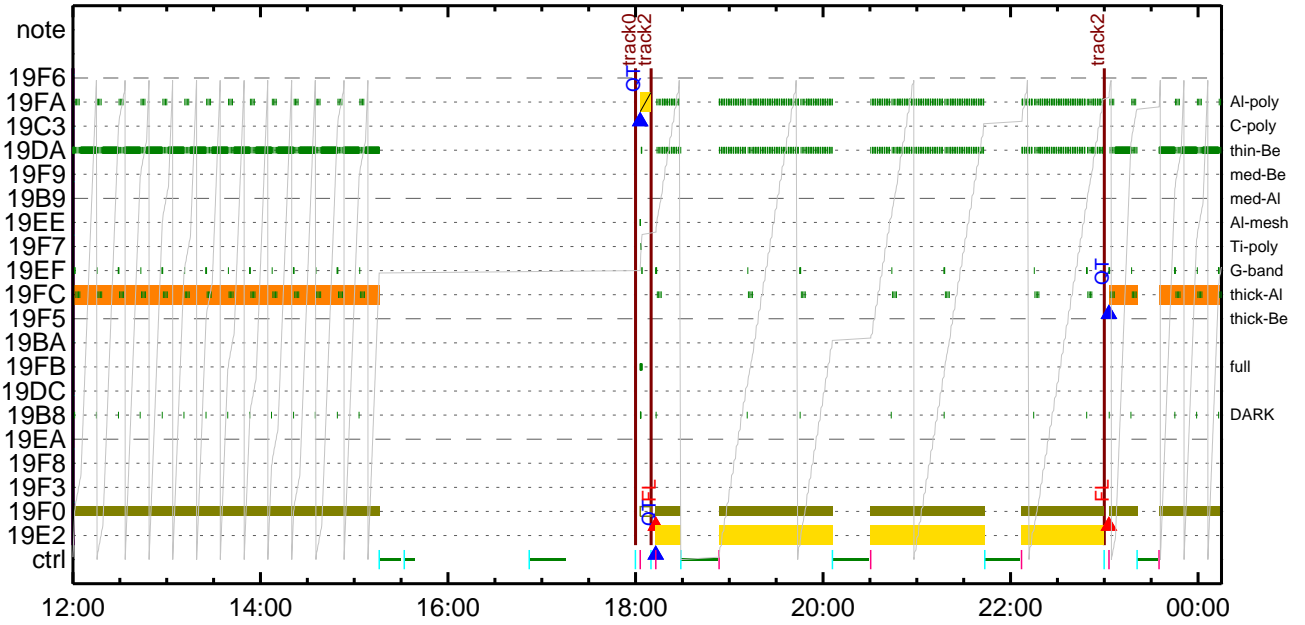
CMDI #0112 2014/02/22



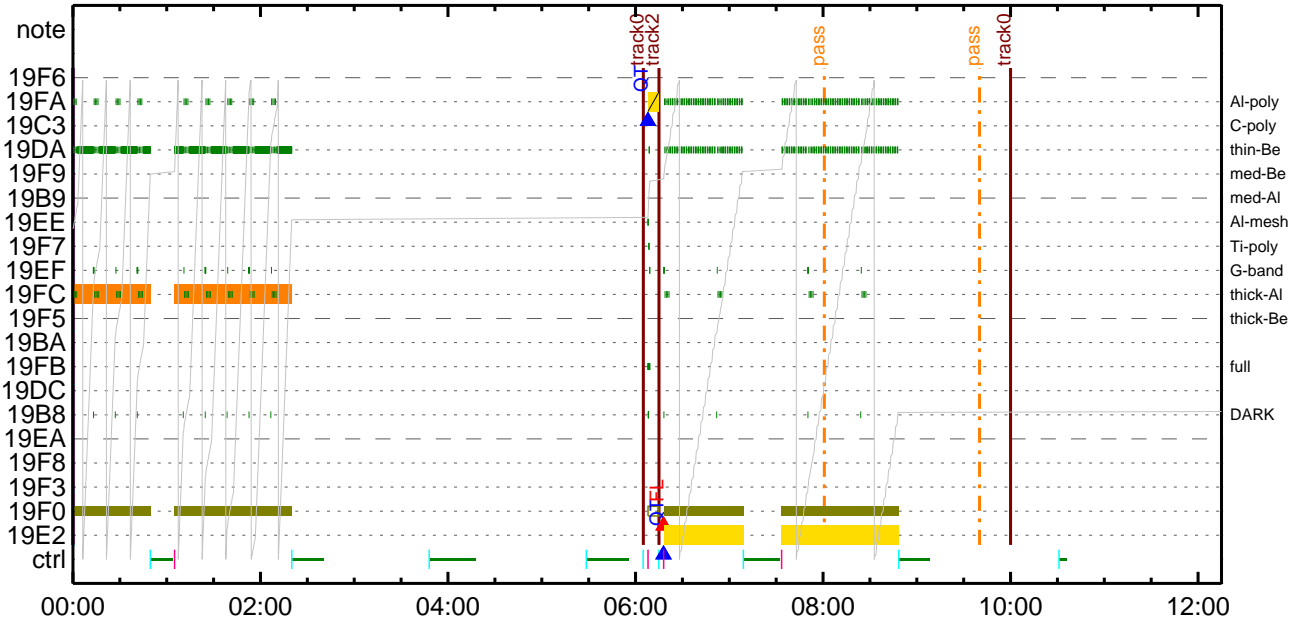
CMDI #0112 2014/02/23



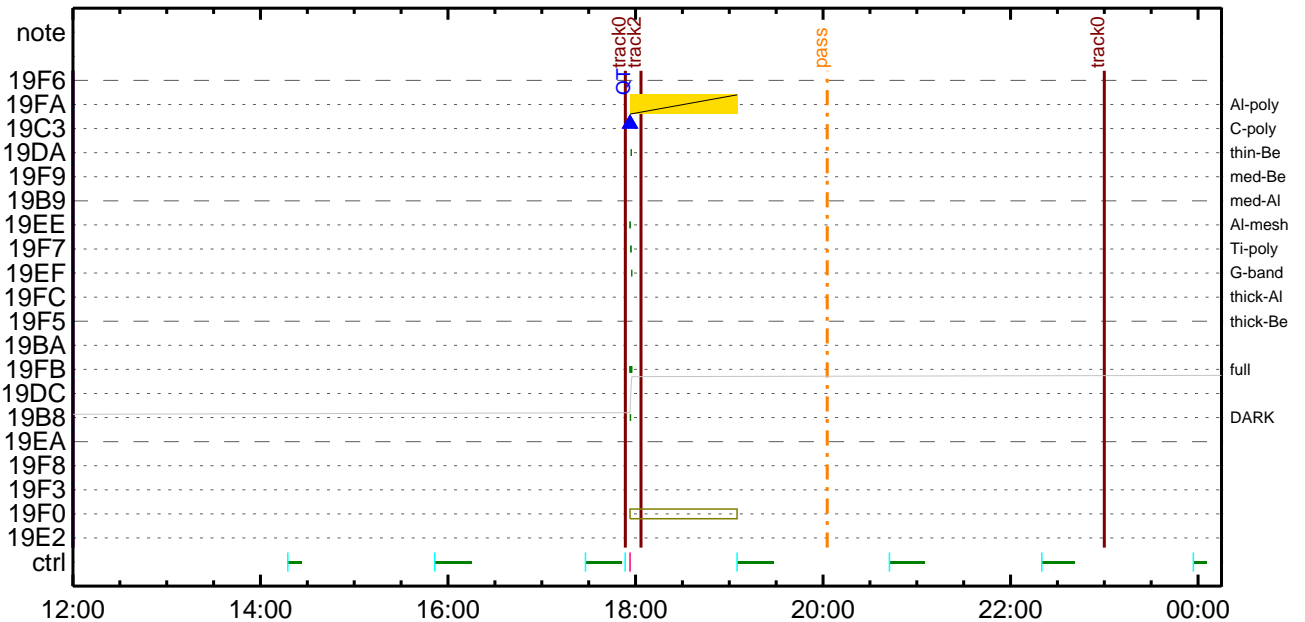
CMDI #0112 2014/02/23



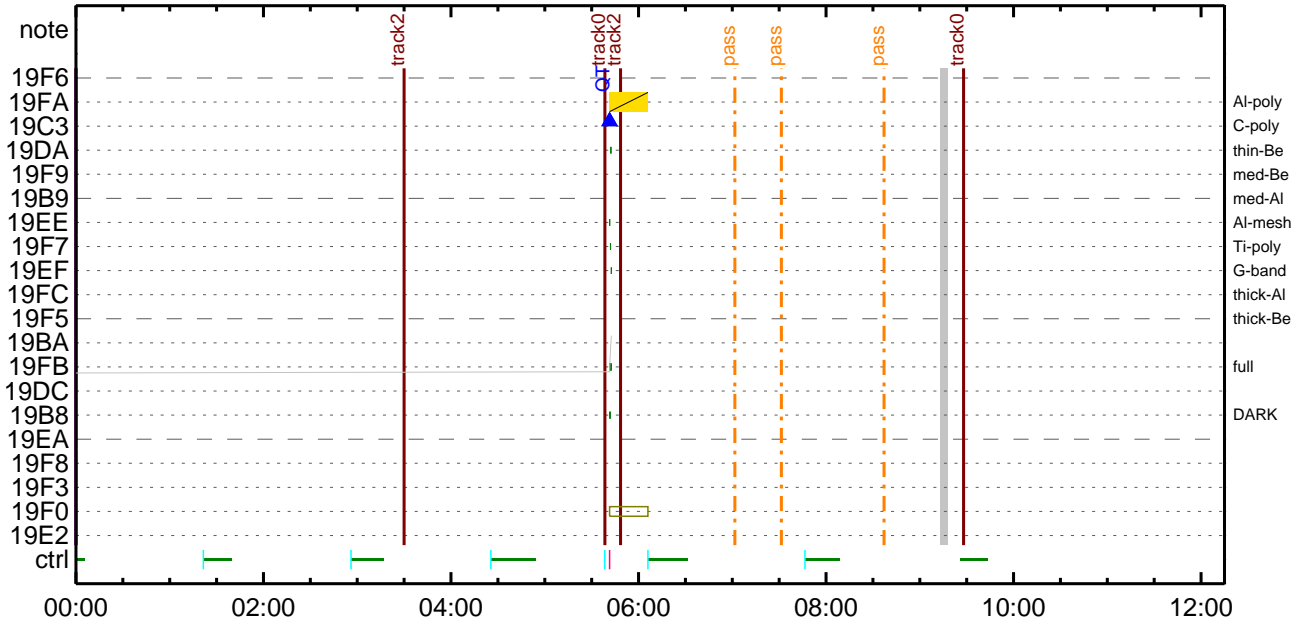
CMDI #0112 2014/02/24



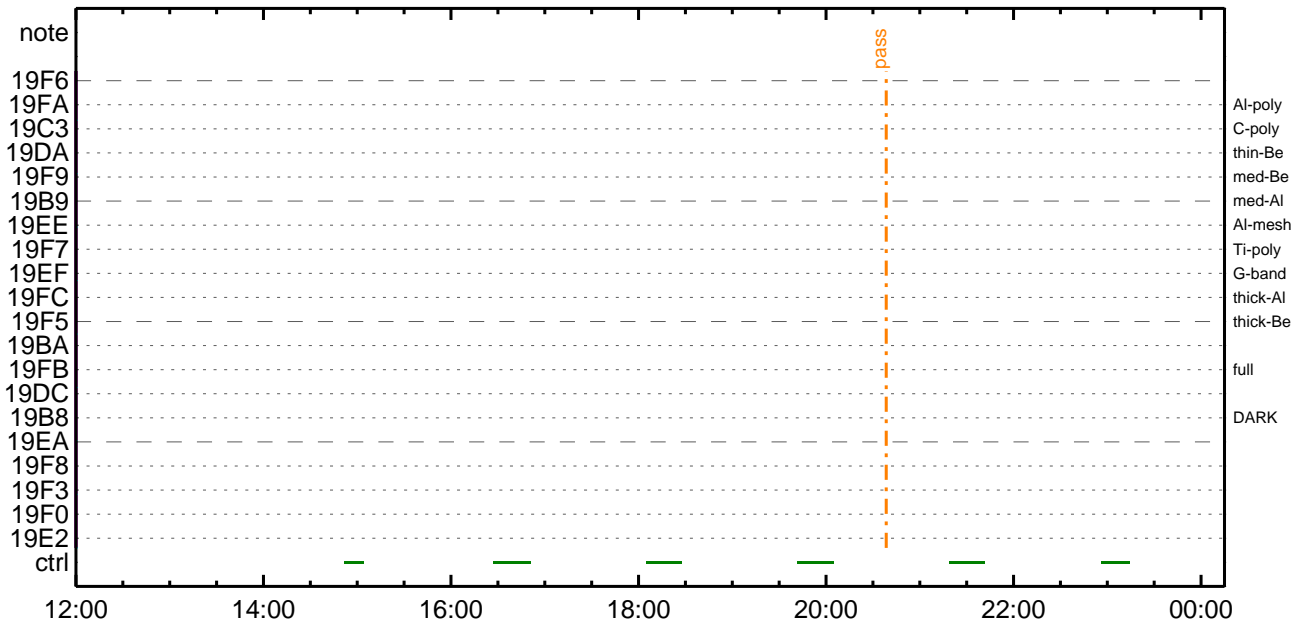
CMDI #0112 2014/02/24



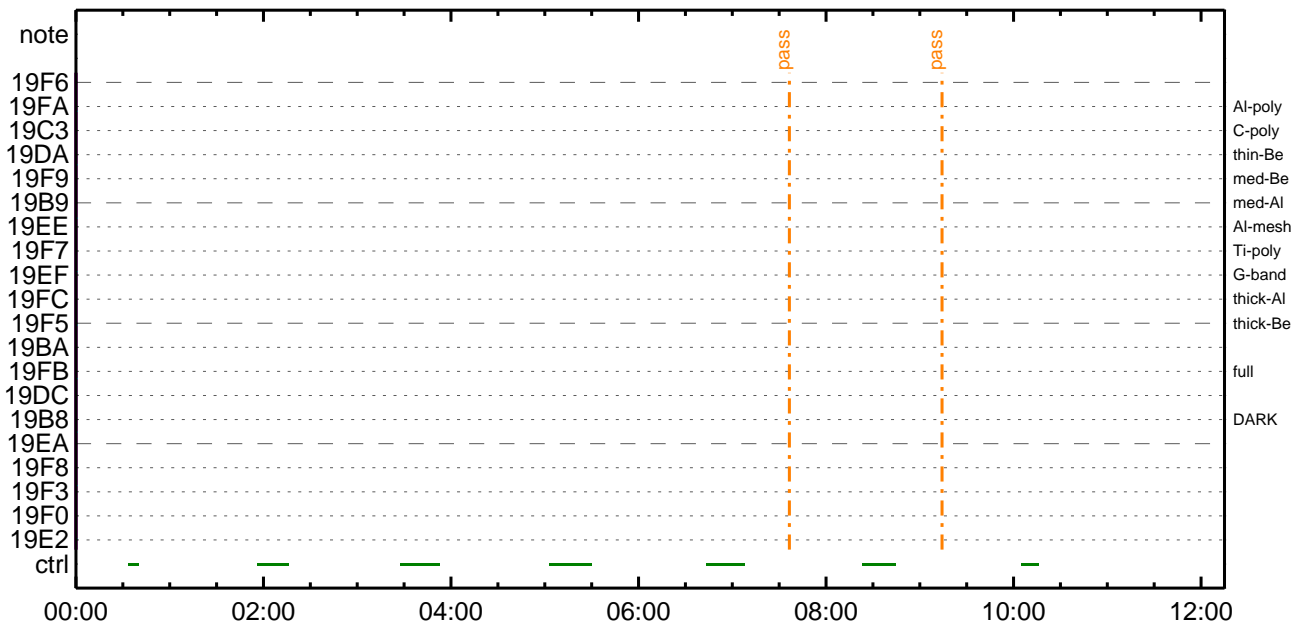
CMDI #0112 2014/02/25



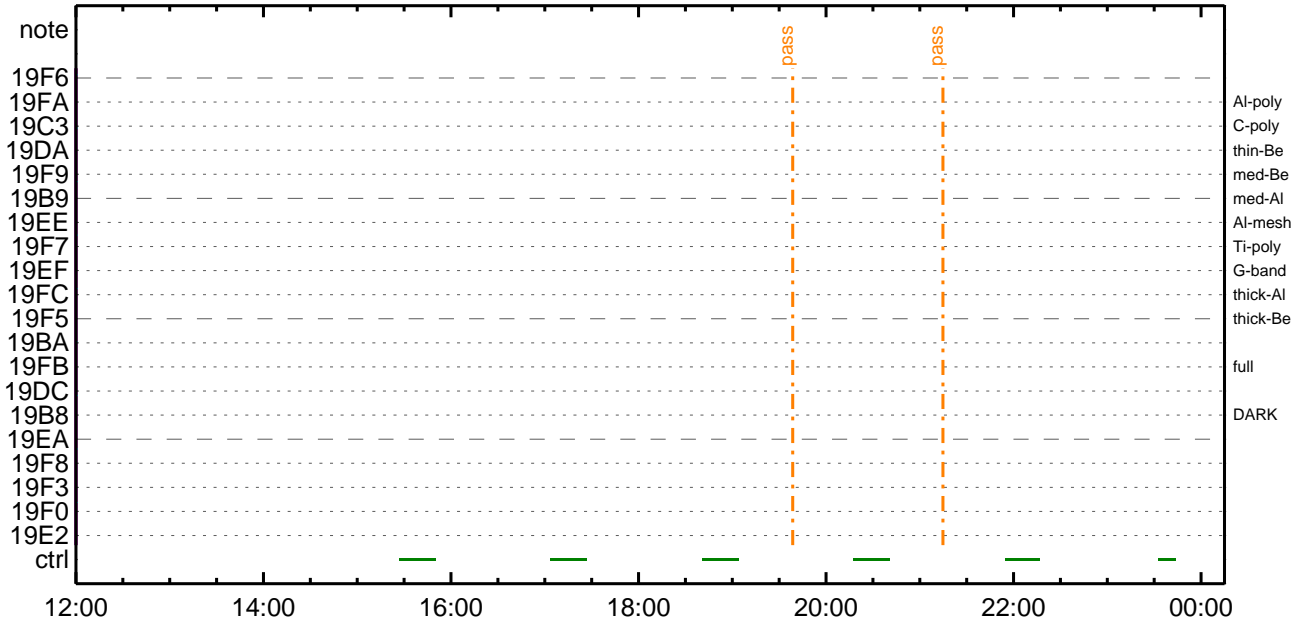
CMDI #0112 2014/02/25



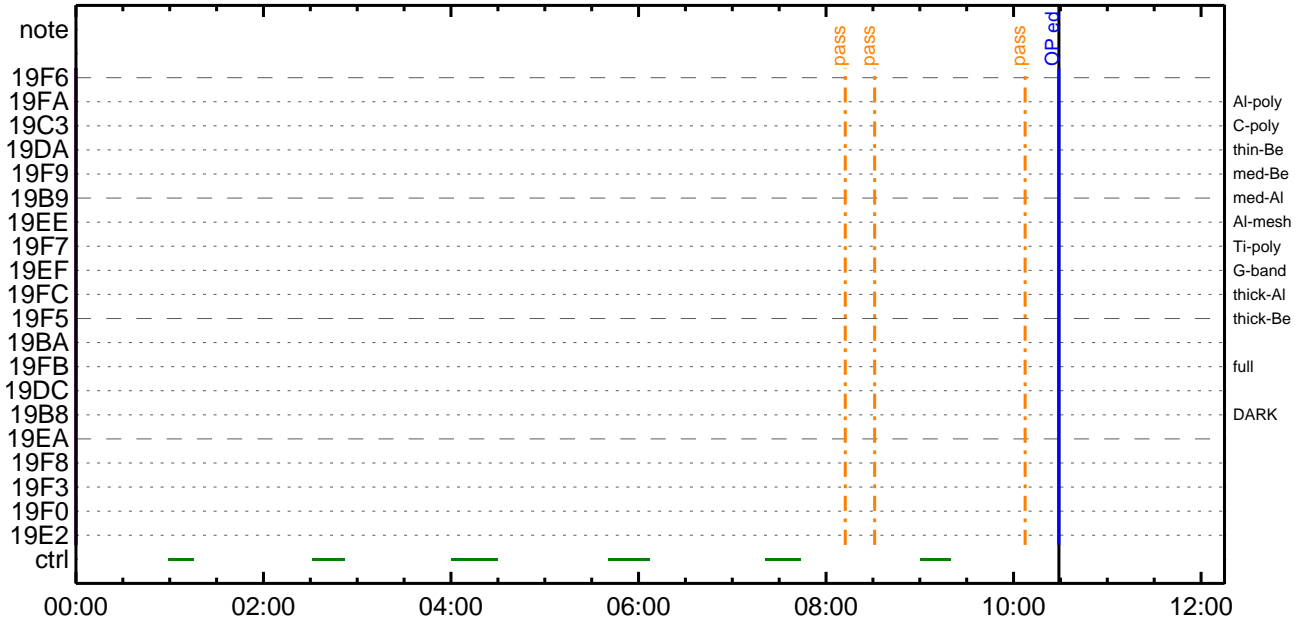
CMDI #0112 2014/02/26



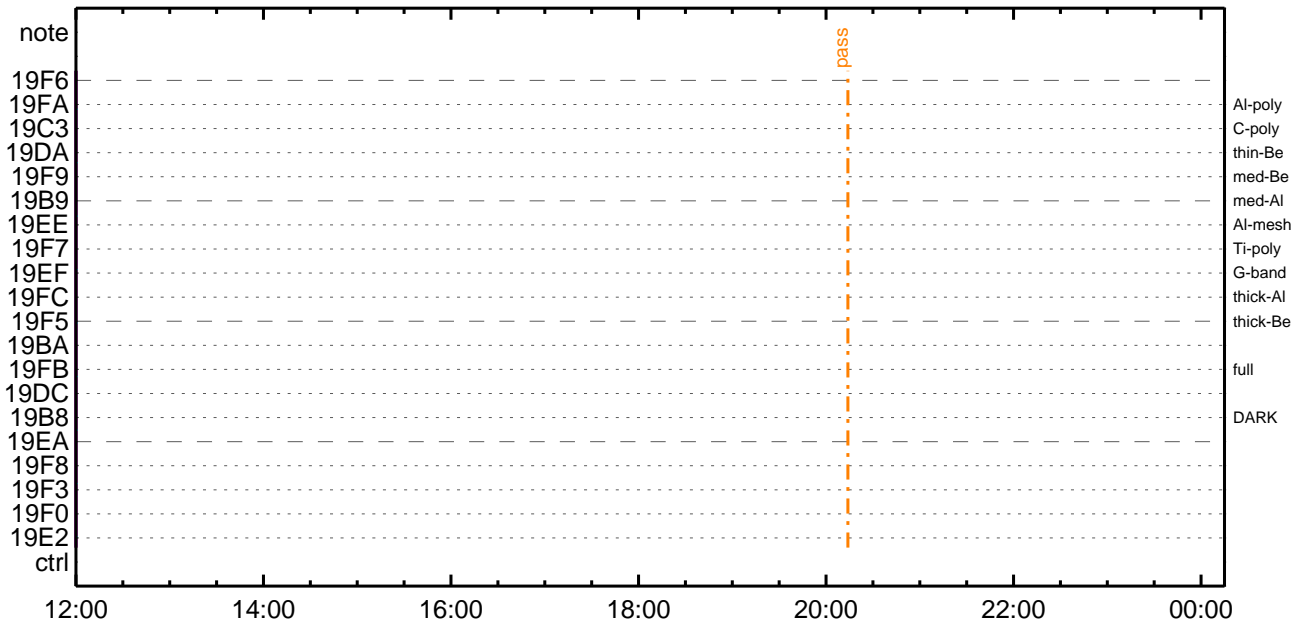
CMDI #0112 2014/02/26



CMDI #0112 2014/02/27



CMDI #0112 2014/02/27




```
0096 C.                0x0000; SET 0x0000 DUMP 0x0000 0x0000 0x0000 0x0000;
0097 C.
0098 . C. TI 2014-02-22 09:01:00.0
0099 +. TI 2014-02-22 09:01:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                0x0000 [HK1_TI_CMD_NUM]          EQ      1COUNTUP
0102 C.
0103 +. TI 2014-02-22 09:01:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                0x0000 [HK1_TI_CMD_NUM]          EQ      1COUNTUP
0106 C.
0107 +. TI 2014-02-22 09:01:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                0x0000 [HK1_TI_CMD_NUM]          EQ      1COUNTUP
0110 C.
0111 +. TI 2014-02-22 09:05:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                0x0000 [HK1_TI_CMD_NUM]          EQ      1COUNTUP
0114 C.
0115 C.                0x0000 [HK1_TI_CMD_ENA/DIS]       EQ      ENA
0116 C.                0x0000 [HK1_TI_CMD_NUM]          EQ      4
0117 C.                0x0000 [HK1_NEXT_EXEC_PIM]       EQ      DHU
0118 C.                0x0000 [HK1_NEXT_EXEC_DC]        EQ      0xB3
0119 C.
0120 C.
0121 . C. *****
0122 C. TI 2014-02-22 09:05:59.5
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                0x0000 [HK1_DMP_TOP_ADRS_1]       EQ      07
0129 C.                0x0000 [HK1_DMP_TOP_ADRS_0]       EQ      2B
0130 C.                0x0000 [HK1_DMP_BLOCK_NUM]        EQ      3
0131 C.                0x0000 [HK1_DMP_REPEAT_NUM]       EQ      0
0132 C.                0x0000 [HK1_DMA_DMP_PIM]         EQ      DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                0x0000 [HK1_PKT_FORM_NO]          EQ      7
0136 C.                0x0000 [HK1_PKT_GEN_TIME]         EQ      0.25 s
0137 C.                0x0000 [HK1_S_TLM_BIT_RATE]       EQ      32k
0138 C.                0x0000 [HK1_X_TLM_BIT_RATE]      EQ      4M
0139 C.                0x0000 [HK1_DMP_CHK_FLG]         EQ      EXEC
0140 C.
0141 . C. 0x0000 [HK1_DMP_CHK_FLG]          EQ      NON
0142 C.
0143 C.
0144 . C. RAM ID=TI_TBL(0x03AB00-0x03AEFF; 1024byte)
0145 C.
0146 . C. DHU 2014-02-22 09:05:59.5
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                0x0000 [HK1_PKT_FORM_NO]          EQ      2
0150 C.                0x0000 [HK1_PKT_GEN_TIME]         EQ      0.5S
0151 C.                0x0000 [HK1_S_TLM_BIT_RATE]       EQ      32K
0152 C.                0x0000 [HK1_X_TLM_BIT_RATE]      EQ      4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2014-02-22 09:05:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 . C. -----
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 . C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2014-02-22 09:05:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC (21 02)
0174 +. TI 2014-02-22 09:05:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC (22)
0177 . C. [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2014-02-22 09:05:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC (c3)
0187 . C. [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 . C. ***** MDP 2014-02-22 09:05:00.0 *****
0192 C. (0x0000 [HK1_DMP_CHK_FLG] EQ 0xB3)
0193 . S. DC-BC dcbc-402:DCBC
```



```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```


(a) Spacecraft Operation Procedure (real-commands)

```
main-226 2014-02-22 13:14:47 130 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YF¥ÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿òÁò•µ°È»Í×ÁÇòÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈòÈ¼°ÇÒò•ò¿¼í¹çòÍ;çÀ®, ùò¹òèòòòçÁ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-261:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2014-02-22 09:05:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 C. ***** XRT START *****
0048 C.
0049 +. DC 07-F0 MDP_XRT_CTRL_MANU
0050 BC (c1)
0051 + DC 07-F0 MDP_XRT_MODE_STBY
0052 BC (c3)
0053 . C. ----- Success Verify ? OK / NG_____
0054 C.
0055 C. XRT Obs. Table Upload
0056 . S. RAM ram-291:MDP_OBS_X
0057 ( )
0058 C.
0059 +. DC 07-F0 MDP_DUMP_XRTTBL
0060 BC (84 07 00 00 00 3a d4)
0061 . C. ----- Comparison Check ? OK / ERR _____
0062 C.
0063 C.
0064 +. DC 07-F0 MDP_XRT_ROI_SET
0065 BC (cd 01 b1 b1 04 04)
0066 + DC 07-F0 MDP_XRT_ROI_SET
0067 BC (cd 02 b1 b1 08 08)
0068 + DC 07-F0 MDP_XRT_ROI_SET
0069 BC (cd 03 b1 b1 08 08)
0070 + DC 07-F0 MDP_XRT_ROI_SET
0071 BC (cd 04 b1 b1 06 06)
0072 + DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 05 85 83 06 06)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 06 85 83 06 06)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 07 85 83 08 08)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 08 80 80 20 20)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 09 80 80 20 08)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 0a 80 80 08 20)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 0f 80 80 06 06)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 10 80 80 08 08)
0088 + DC 07-F0 MDP_XRT_FLD_ENA
0089 BC (d8)
0090 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0091 BC (c8)
0092 + DC 07-F0 MDP_XRT_AEC_RESET
0093 BC (d0)
0094 + DC 07-F0 MDP_XRT_ARS_DIS
0095 BC (d5)
```

```
0096 + DC 07-F0 MDP_XRT_FLD_RESET
0097 BC (da)
0098 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0099 BC (c4 01)
0100 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0101 BC (c5 02)
0102 . C. ----- Success Verify ? OK / NG ____
0103 C.
0104 C.
0105 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0106 C.
0107 +. DC 07-F0 MDP_XRT_MODE_OBSV
0108 BC (c2)
0109 +. TI 2014-02-22 09:05:02.0
0110 DC 07-F0 MDP_XRT_MODE_OBSV
0111 BC (c2)
0112 . C. ----- Success Verify ? OK / NG ____
0113 C.
0114 C. ***** XRT END *****
0115 C.
0116 . C. ***** MDP 'úÃîâî»ö¼ÝðÊÂð¹ñèDCBC•x²è *****
0117 C. (%ã°îÿÓÿÄÿËÿÌÿÍÿÏÿÐÿÑÿÒÿÓÿÔÿÕÿÖÿ×ÿØÿÙÿÚÿÛÿÜÿÝÿÞÿßÿàÿáÿâÿãÿäÿåÿæÿçÿèÿéÿêÿëÿìÿíÿîÿïÿðÿñÿ)
0118 . S. DC-BC dcbc-402:DCBC
0119 (MDP_known_event)
0120 C.
0121 C.
0122 . C. ***** ÿÐÿ¹•Ï Daily±¿ÎñÈ´Ø¹ñèDCBC•x²è *****
0123 . S. DC-BC dcbc-153:DCBC
0124 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0125 C.
0126 C.
0127 . C. ;ãLOSÿÁÿËÿÛÿÄÿ-¼Â»Û;ä
0128 C.
0129 . C. ***** LOS *****
0130 C.
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*** OP Sequence for XRT ***

2014/02/22	09:14:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/22	09:14:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/22	09:14:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/22	09:15:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/22	09:15:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/02/22	09:16:00.5	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2014/02/22	09:16:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/22	09:16:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/22	09:16:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/22	09:16:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/22	09:16:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/22	09:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/22	09:18:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2014/02/22	09:18:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/02/22	09:32:30.0	XRT_Custom_430_OG [0x1ae]							
2014/02/22	09:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/22	11:28:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/22	11:30:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2014/02/22	14:41:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/22	14:41:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/22	14:41:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/22	14:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/22	15:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/22	15:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/02/22	16:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2014/02/22	16:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/22	16:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/22	16:00:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/22	16:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/22	16:00:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/22	16:02:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 01				
2014/02/22	16:02:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/02/22	16:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/22	16:16:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/22	16:16:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/22	16:16:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/22	16:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/22	16:39:30.0	XRT_Custom_430_OG [0x1ae]							
2014/02/22	16:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/22	17:53:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/22	17:53:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/22	17:53:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/22	17:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/22	18:15:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/22	18:15:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/02/22	18:16:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/02/22	18:16:16.0	XRT_FLD_DIS_434_OG [0x1b2]							

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2014/02/22	18:18:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/02/22	18:18:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/22	18:18:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	13
2014/02/22	18:19:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/22	18:25:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/22	18:25:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2014/02/22	18:26:00.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02	00 00 00 00
2014/02/22	18:26:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/02/22	18:26:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/02/22	18:26:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/02/22	18:26:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/22	18:26:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/22	18:28:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	01
2014/02/22	18:28:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02
2014/02/22	18:29:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/22	19:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/22	19:29:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/22	19:29:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/22	19:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/22	19:53:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/22	19:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/22	21:07:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/22	21:07:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/22	21:07:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/22	21:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/22	21:30:00.0	XRT_Custom_430_OG [0x1ae]					
2014/02/22	21:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/22	22:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/22	22:44:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/22	22:44:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/22	22:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/22	23:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01	00 00 00 00
2014/02/23	00:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/23	00:21:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/23	00:21:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/23	00:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/23	01:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/23	01:45:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/23	01:45:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/23	01:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/23	03:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/23	03:19:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/23	03:19:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/23	03:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/23	03:30:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	00	11 ca b0 00
2014/02/23	04:51:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/23	04:51:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	

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2014/02/23	04:51:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/23	04:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/23	06:01:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/23	06:01:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/02/23	06:02:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/02/23	06:02:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/02/23	06:04:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/02/23	06:04:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/23	06:04:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2014/02/23	06:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/23	06:12:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 11 ca b0 00				
2014/02/23	06:31:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/23	06:31:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/23	06:31:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/23	06:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/23	08:11:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/23	08:11:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/23	08:11:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/23	08:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/23	09:52:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/23	09:52:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/23	09:52:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/23	09:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/23	11:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/23	11:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/02/23	11:30:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2014/02/23	11:30:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/23	11:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/23	11:30:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/23	11:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/23	11:30:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/23	11:32:56.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b				
2014/02/23	11:32:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/02/23	11:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/23	15:16:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/23	15:16:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/23	15:16:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/23	15:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/23	15:32:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/23	15:32:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/23	15:32:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/23	15:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/23	16:52:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/23	16:52:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/23	16:52:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/23	16:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/23	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							

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2014/02/23	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/23	18:00:00.0	AOCs_OrE-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/02/23	18:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]	AOCU_NM	5	02-76	00 00 00 00
2014/02/23	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2014/02/23	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/02/23	18:02:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/02/23	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13
2014/02/23	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/23	18:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/23	18:10:00.0	AOCs_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/02/23	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00
2014/02/23	18:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2014/02/23	18:10:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/02/23	18:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2014/02/23	18:10:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/02/23	18:12:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/23	18:12:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01
2014/02/23	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2014/02/23	18:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/23	18:29:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/23	18:29:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/23	18:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/23	18:52:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/23	18:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2014/02/23	20:06:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2014/02/23	20:06:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/23	20:06:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/23	20:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/23	20:29:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/23	20:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/23	21:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2014/02/23	21:43:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/23	21:43:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/23	21:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/23	22:06:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/02/23	22:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/02/23	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_Custom_430_OG [0x1ae]			
2014/02/23	22:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2014/02/23	23:00:00.0	AOCs_OrE-point_Start_1_OG [0x097]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/02/23	23:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/02/23	23:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/02/23	23:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	AOCU_NM	5	02-76	02 00 00 00
2014/02/23	23:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLD_ENA	1	07-F0	d8
2014/02/23	23:00:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/02/23	23:02:56.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_AEC_RESET	1	07-F0	d0
2014/02/23	23:02:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/02/23	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/02/23	23:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0b
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
			MDP_XRT_CTRL_AUTO	1	07-F0	c0

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2014/02/23	23:21:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/23	23:21:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/23	23:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/23	23:34:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/23	23:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/24	00:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/24	00:49:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/24	00:49:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/24	00:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/24	01:04:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/24	01:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/24	02:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/24	02:20:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/24	02:20:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/24	02:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/24	03:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/24	03:48:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/24	03:48:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/24	03:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/24	05:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/24	05:28:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/24	05:28:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/24	05:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/24	06:04:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/24	06:04:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
			MDP_XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2014/02/24	06:05:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00		
			AOCU_NM	5	02-76	00 00 00 00 00		
2014/02/24	06:05:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9		
			MDP_XRT_FLD_DIS	1	07-F0	d9		
2014/02/24	06:07:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2014/02/24	06:07:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/02/24	06:07:58.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
2014/02/24	06:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/24	06:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/24	06:14:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
			MDP_XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/02/24	06:15:00.0	AOCS_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00		
			AOCU_NM	5	02-76	02 00 00 00 00		
2014/02/24	06:15:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
			MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/02/24	06:15:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/02/24	06:15:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
			MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/02/24	06:15:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
			MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/02/24	06:15:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/24	06:17:56.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 01		
2014/02/24	06:17:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
2014/02/24	06:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/02/24	07:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
			MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/02/24	07:09:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
			MDP_XRT_FLD_RESET	1	07-F0	da		
2014/02/24	07:09:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/24	07:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
			MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/24	07:32:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/02/24	07:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		

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2014/02/24	08:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/24	08:48:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/02/24	08:48:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/02/24	08:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/02/24	10:00:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 ad cb 01 58			
			AOCU_NM	5	02-76	00 ad cb 01 58			
2014/02/24	10:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/24	10:31:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/02/24	10:31:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/02/24	10:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/02/24	14:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/24	14:17:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/02/24	14:17:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/02/24	14:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/02/24	15:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/24	15:51:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/02/24	15:51:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/02/24	15:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/02/24	17:28:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/24	17:28:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/02/24	17:28:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/02/24	17:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/02/24	17:53:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/24	17:53:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2014/02/24	17:53:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00			
			AOCU_NM	5	02-76	00 00 00 00 00			
2014/02/24	17:53:46.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9			
			MDP_XRT_FLD_DIS	1	07-F0	d9			
2014/02/24	17:56:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2014/02/24	17:56:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/02/24	17:56:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13			
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 13			
2014/02/24	17:56:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/02/24	18:03:30.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	02 00 00 00 00			
			AOCU_NM	5	02-76	02 00 00 00 00			
2014/02/24	19:05:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/24	19:05:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/02/24	19:05:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/02/24	19:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/02/24	20:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/24	20:42:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/02/24	20:42:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/02/24	20:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/02/24	22:20:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/24	22:20:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/02/24	22:20:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/02/24	22:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
			MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/02/24	23:00:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 55 ca 01 58			
			AOCU_NM	5	02-76	00 55 ca 01 58			
2014/02/24	23:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/24	23:57:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/02/24	23:57:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
			MDP_XRT_PREFLR_STRT	1	07-F0	e8			

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XRT_OGLIST_0112.chk

2014/02/25	00:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/25	01:21:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/25	01:21:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/25	01:21:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/25	01:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/25	02:56:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/25	02:56:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/25	02:56:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/25	02:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/25	03:30:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2014/02/25	04:25:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/25	04:25:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/25	04:25:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/25	04:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/25	05:38:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/25	05:38:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/02/25	05:38:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/02/25	05:38:46.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/02/25	05:41:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/02/25	05:41:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/25	05:41:28.0	XRT_QT_PROG_SET_436_OG [0x1b4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2014/02/25	05:41:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/25	05:48:30.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2014/02/25	06:06:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/25	06:06:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/25	06:06:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/25	06:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/25	07:46:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/25	07:46:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/25	07:46:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/25	07:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/25	09:28:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				