

# XRT Timeline to be uploaded on 2014/02/25

Period: 2014/02/25 09:18:00 - 2014/03/01 10:06:00

\* \* \* \* \*

Normal mode

\* \* \* \* \*

**XOB #19FD: AR standard-B (thin-Be) with PFB, 384 FOV at 1064x1048, thin-Be, thick-Al, and Al/Poly context, With G-band (33ms/45ms leak), 10s cad, lowe**

Term	Pointing (x, y)	Comment
02/25 11:33:00 - 02/25 14:30:00	Track ( 447.5, -68.0) @ 02/25 11:30:00	IHOP 247 (AR)
<b>PROG= 07 Inf.-time(s)</b>		
└ Subr= 1 1-time(s) 2.0sec		
└└ Seqn= 8 2-time(s) 2.0sec		
└└└ Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec		
└ Subr= 2 2-time(s) 2.0sec		
└└ Seqn= 24 1-time(s) 2.0sec		
└└└ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec		
└└└ Open/G-band Open/G-band open Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec		
└└ Seqn= 42 2-time(s) 2.0sec		
└└└ Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec		
└└└ thin-Be/Open med-Be/Open close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec		
└└└ Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec		
└└ Seqn= 87 75-time(s) 2.0sec		
└└└ thin-Be/Open med-Be/Open close Safe Norm 1.41s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 10.0sec		
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin	ROI: size (center) Comp. AEC Buffer Interval

**XOB #19FE: Synoptic Q95 2x2 - Al/mesh(3/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(8/362/1443) + Thin\_**

Term	Pointing (x, y)	Comment
02/25 17:44:00 - 02/25 17:50:54	Fixed ( 0.0, 0.0)	synoptic, shifted -19.0 min
02/26 07:13:00 - 02/26 07:19:54	Fixed ( 0.0, 0.0)	synoptic, shifted +70 min
02/26 16:53:00 - 02/26 16:59:54	Fixed ( 0.0, 0.0)	synoptic shifted -70min
02/27 06:24:00 - 02/27 06:26:30	Fixed ( 0.0, 0.0)	synoptic, shifted 17.0 min
<b>PROG= 17 1-time(s)</b>		
└ Subr= 1 1-time(s) 12.0sec		
└└ Seqn= 35 1-time(s) 4.0sec		
└└└ Open/Al-mesh Open/Al-mesh close Safe Norm 3ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		
└└└ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		
└└└ Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		
└└ Seqn= 5 1-time(s) 2.0sec		
└└└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec		
└└└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec		
└└└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec		
└└└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec		
└└└ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec		
└└ Seqn= 72 1-time(s) 4.0sec		
└└└ Open/Ti-poly Open/Ti-poly close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		
└└└ Open/Ti-poly Open/Ti-poly close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		
└└└ Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		
└└ Seqn= 67 1-time(s) 2.0sec		
└└└ thin-Be/Open thin-Be/Open close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		
└└└ thin-Be/Open thin-Be/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		
└└└ thin-Be/Open thin-Be/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		
└└ Seqn= 6 1-time(s) 2.0sec		
└└└ Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec		
└└└ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec		
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin	ROI: size (center) Comp. AEC Buffer Interval

**XOB #19A9: G-Band Alignment with North Pole Q90 2x2(G-band only) - 8msec - 5min cadence - Partial Sun-wNGT-2**

Term	Pointing (x, y)	Comment
02/25 18:06:00 - 02/25 19:59:54	Fixed ( 0.0, 945.0)	Co-alignment N pole
<b>PROG= 10 1-time(s)</b>		
└ Subr= 1 1-time(s) 360.0sec		
└└ Seqn= 93 24-time(s) 300.0sec		
└└└ Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x1536 (1024, 768) Q=90 0 0 2.0sec		
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin	ROI: size (center) Comp. AEC Buffer Interval

**XOB #19AA: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8msec - 8 min cadence-wNGT-2**

Term	Pointing (x, y)	Comment
02/25 20:15:00 - 02/25 22:56:00	Fixed ( -945.0, 0.0)	Co-alignment E limb
<b>PROG= 05 1-time(s)</b>		
└ Subr= 1 1-time(s) 360.0sec		
└└ Seqn= 94 15-time(s) 480.0sec		
└└└ Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 1536x2048 (1280, 1024) Q=90 0 0 2.0sec		
Default Filter	Thicker Filter VLS mode image Exp. CCD Bin	ROI: size (center) Comp. AEC Buffer Interval

**XOB #19E6: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms**

Term	Pointing (x, y)	Comment
------	-----------------	---------

02/26 07:23:00 - 02/26 11:29:54 Track ( -844.2, -171.3) © 02/26 07:20:00 Track AR 11990  
 02/26 14:05:00 - 02/26 16:49:54 Track ( -826.7, -167.1) © 02/26 11:30:00 IHOP 244 (AR11990)  
 02/26 17:28:00 - 02/26 21:55:00 Track ( -801.6, -161.7) © 02/26 17:00:00 IHOP 244 (AR11990)

**PROG= 13 Inf.-time(s)**

<b>Subr= 1 1-time(s) 2.0sec</b>													
<b>Seqn= 8 2-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
<b>Subr= 2 2-time(s) 2.0sec</b>													
<b>Seqn= 24 1-time(s) 2.0sec</b>													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Seqn= 42 4-time(s) 2.0sec</b>													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 62 8-time(s) 270.0sec</b>													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #19EF: AR Standard-B(Morphology), Be/thin 24s-cad, multifilter PFB (Al/poly, Be/thin, Be/med), multifilter context, 384x384, 1064x1048, G-band (33ms)**

Term Pointing (x, y) Comment  
 02/26 11:33:00 - 02/26 12:59:54 Track ( -826.7, -167.1) © 02/26 11:30:00 IHOP 244 (AR11990)

**PROG= 12 Inf.-time(s)**

<b>Subr= 1 1-time(s) 2.0sec</b>													
<b>Seqn= 8 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
<b>Seqn= 24 1-time(s) 2.0sec</b>													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Subr= 2 2-time(s) 2.0sec</b>													
<b>Seqn= 16 2-time(s) 2.0sec</b>													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Al/Open	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 52 100-time(s) 2.0sec</b>													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #19EE: AR Standard-B(Morphology), Be/thin 48s-cad, multifilter PFB (Al/poly, Be/thin, Be/med), multifilter context, 384x384, 1064x1048, G-band (33ms)**

Term Pointing (x, y) Comment  
 02/26 13:03:00 - 02/26 14:01:54 Track ( -826.7, -167.1) © 02/26 11:30:00 IHOP 244 (AR11990)

**PROG= 14 Inf.-time(s)**

<b>Subr= 1 1-time(s) 2.0sec</b>													
<b>Seqn= 8 1-time(s) 2.0sec</b>													
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
<b>Seqn= 24 1-time(s) 2.0sec</b>													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
<b>Subr= 2 2-time(s) 2.0sec</b>													
<b>Seqn= 16 2-time(s) 2.0sec</b>													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Al/Open	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
<b>Seqn= 29 50-time(s) 2.0sec</b>													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \*

### Flare mode

\* \* \* \* \*

#### XOB #19DE: Flare obs. dynamics - thin-Be high cadence long/short pairs + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2)-Gband (45ms)-15 loc

Term	Pointing (x, y)	Comment
02/25 11:33:00 - 02/25 14:30:00	Track ( 447.5, -68.0) @ 02/25 11:30:00	IHOP 247 (AR)
02/26 07:23:00 - 02/26 11:29:54	Track ( -844.2, -171.3) @ 02/26 07:20:00	Track AR 11990
02/26 11:33:00 - 02/26 12:59:54	Track ( -826.7, -167.1) @ 02/26 11:30:00	IHOP 244 (AR11990)
02/26 13:03:00 - 02/26 14:01:54	Track ( -826.7, -167.1) @ 02/26 11:30:00	IHOP 244 (AR11990)
02/26 14:05:00 - 02/26 16:49:54	Track ( -826.7, -167.1) @ 02/26 11:30:00	IHOP 244 (AR11990)
02/26 17:28:00 - 02/26 21:55:00	Track ( -801.6, -161.7) @ 02/26 17:00:00	IHOP 244 (AR11990)

#### PROG= 20 15-time(s)

Subr= 1 45-time(s) 2.0sec													
Seqn= 44 1-time(s) 8.0sec													
thin-Be/Open	med-Be/Open close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	2	0	2.0sec		
thin-Be/Open	med-Be/Open close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Subr= 2 1-time(s) 2.0sec													
Seqn= 10 1-time(s) 2.0sec													
med-Al/Open	med-Al/thick-Al close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Open/thick-Be	Open/thick-Be close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec		
Seqn= 11 1-time(s) 2.0sec													
Al-poly/Open	Al-poly/thick-Al close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Seqn= 15 1-time(s) 2.0sec													
Open/G-band	Open/G-band open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
Open/thick-Al	Open/thick-Al close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec		

\* \* \* \* \*

### Active Region Search

\* \* \* \* \*

NOT USED

\* \* \* \* \*

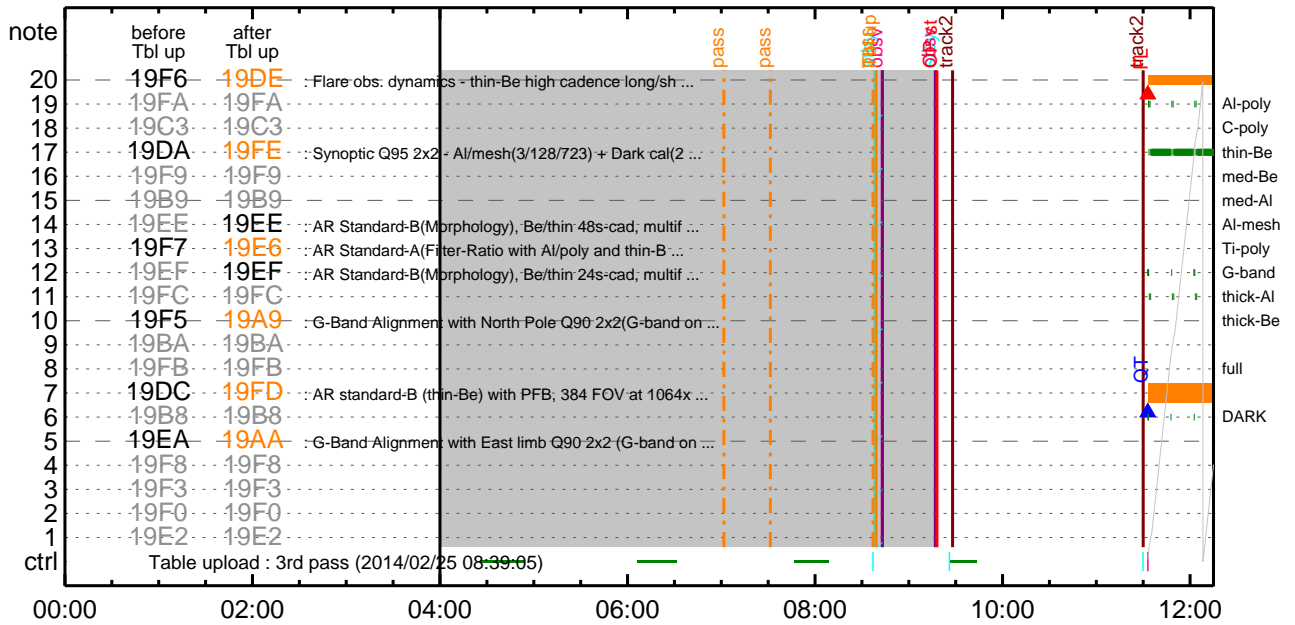
### Flare Detection

\* \* \* \* \*

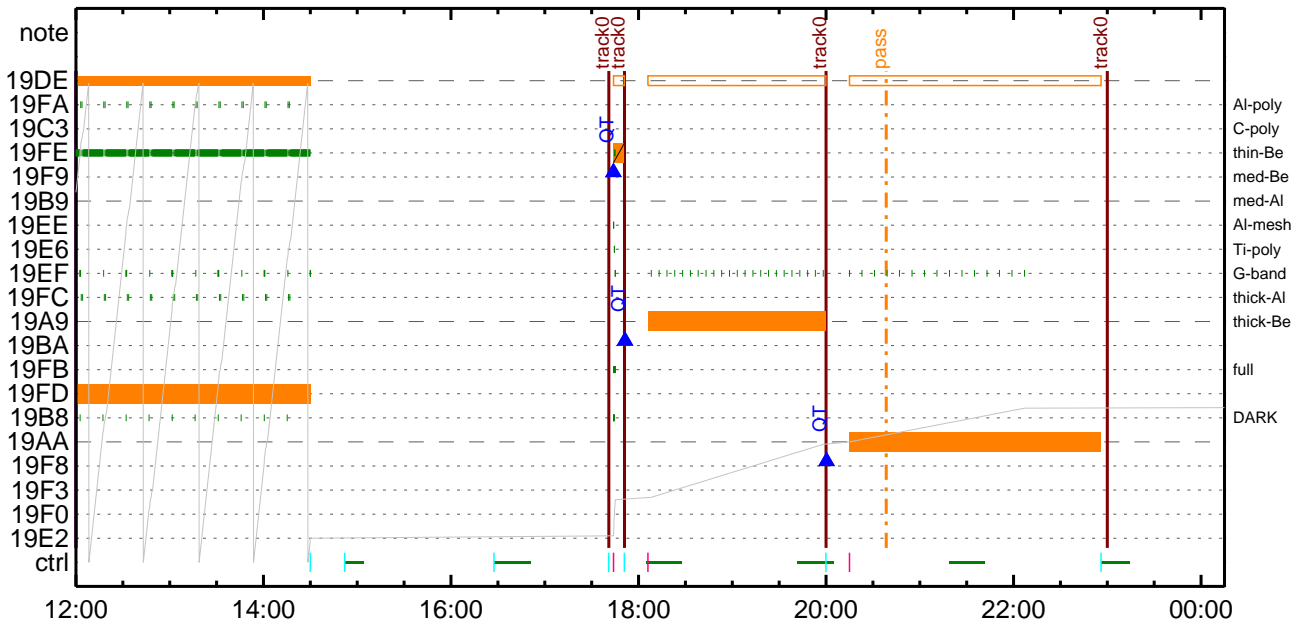
#### FLD Patrol

Term	Pointing (x, y)	Comment
02/26 07:20:16 - 02/26 16:50:16	Track ( -844.2, -171.3) @ 02/26 07:20:00	Track AR 11990
02/26 17:00:16 - 02/27 06:21:16	Track ( -801.6, -161.7) @ 02/26 17:00:00	IHOP 244 (AR11990)
Open/Ti-poly	Open/thick-Al close	Safe Norm 8ms Obs 8x8 Q=50 30sec

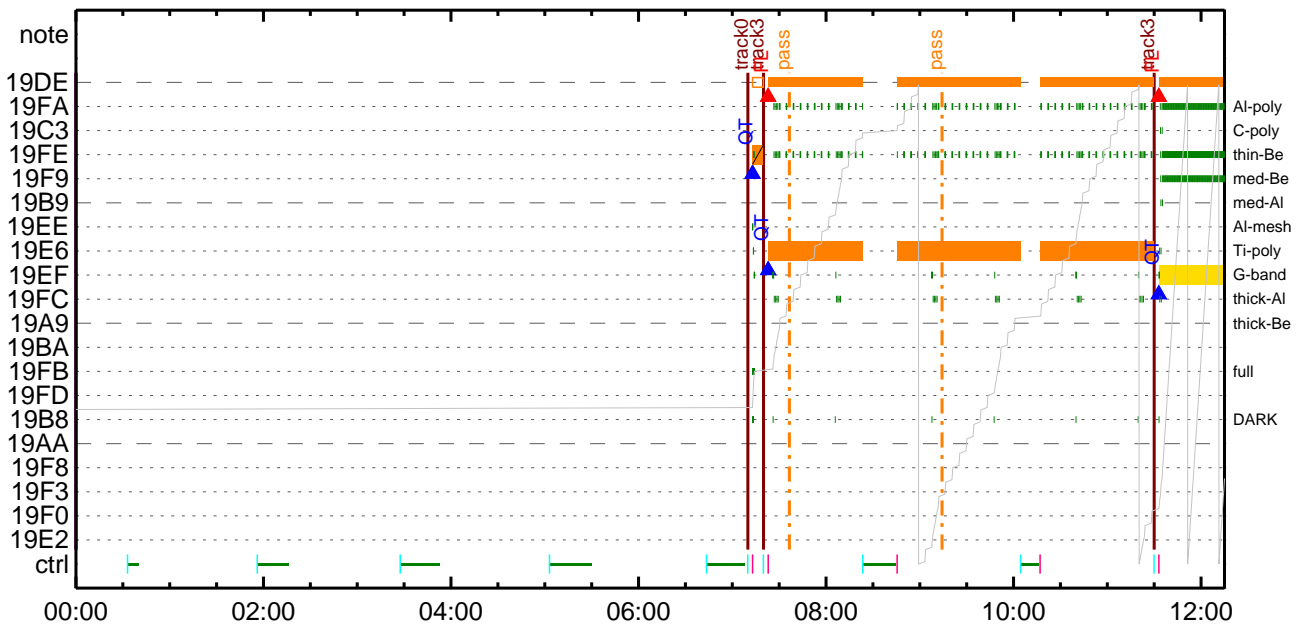
### CMDI #0116 2014/02/25



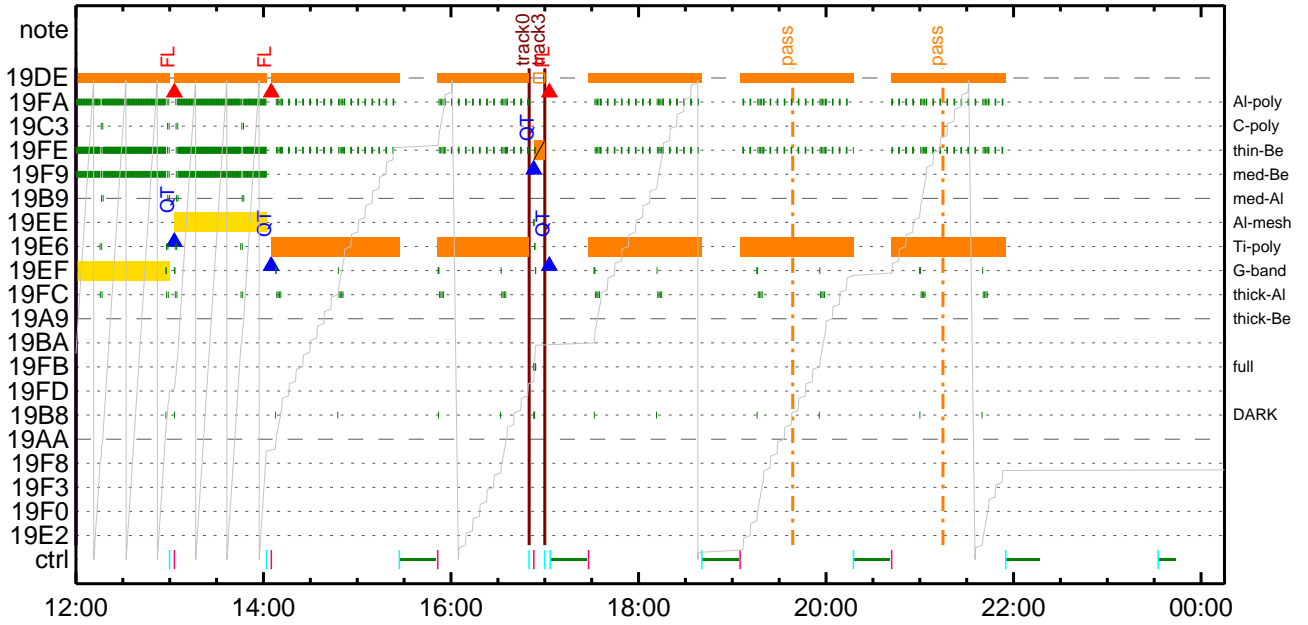
### CMDI #0116 2014/02/25



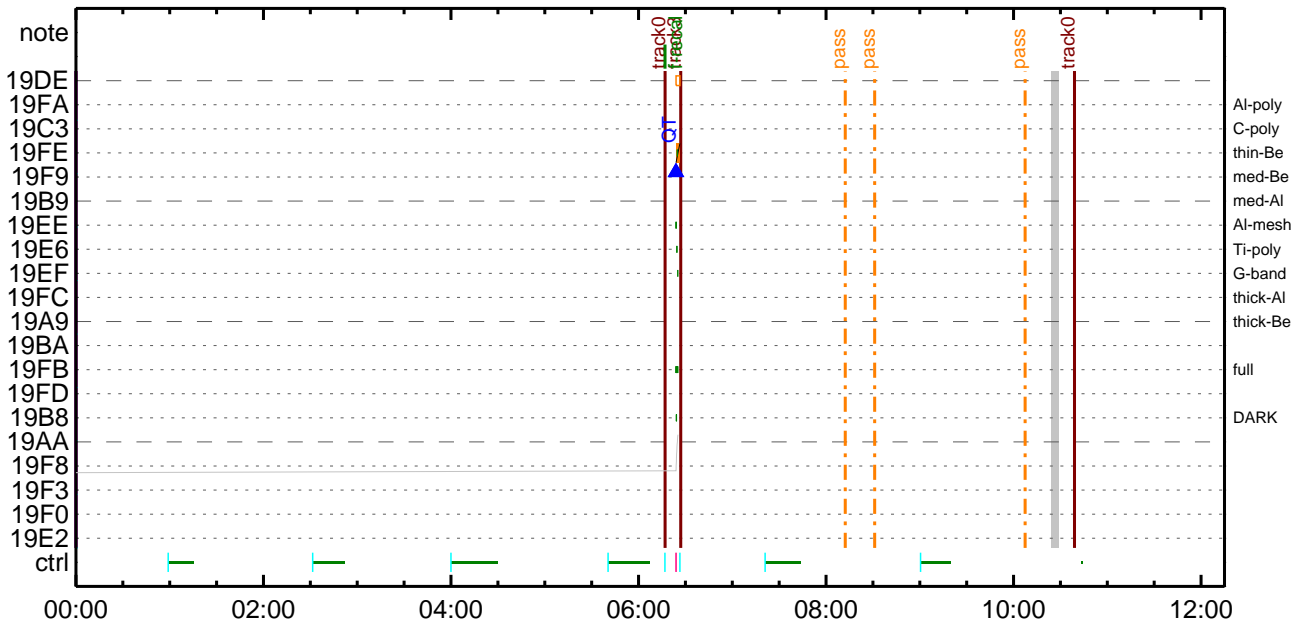
### CMDI #0116 2014/02/26



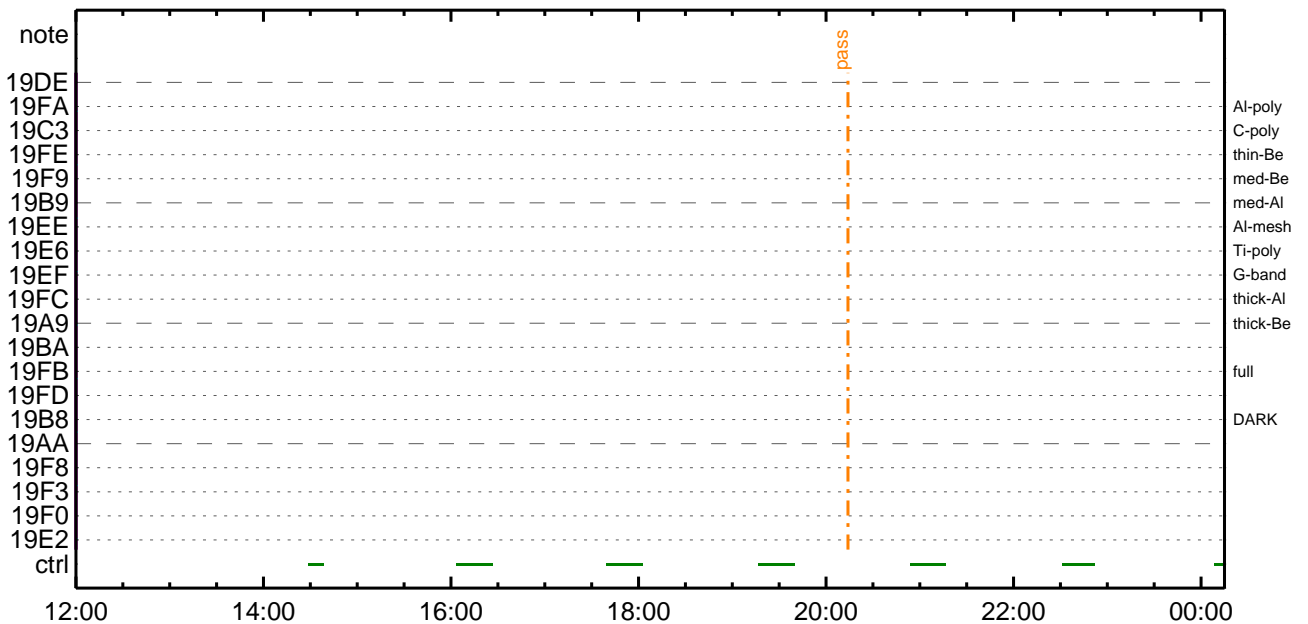
CMDI #0116 2014/02/26



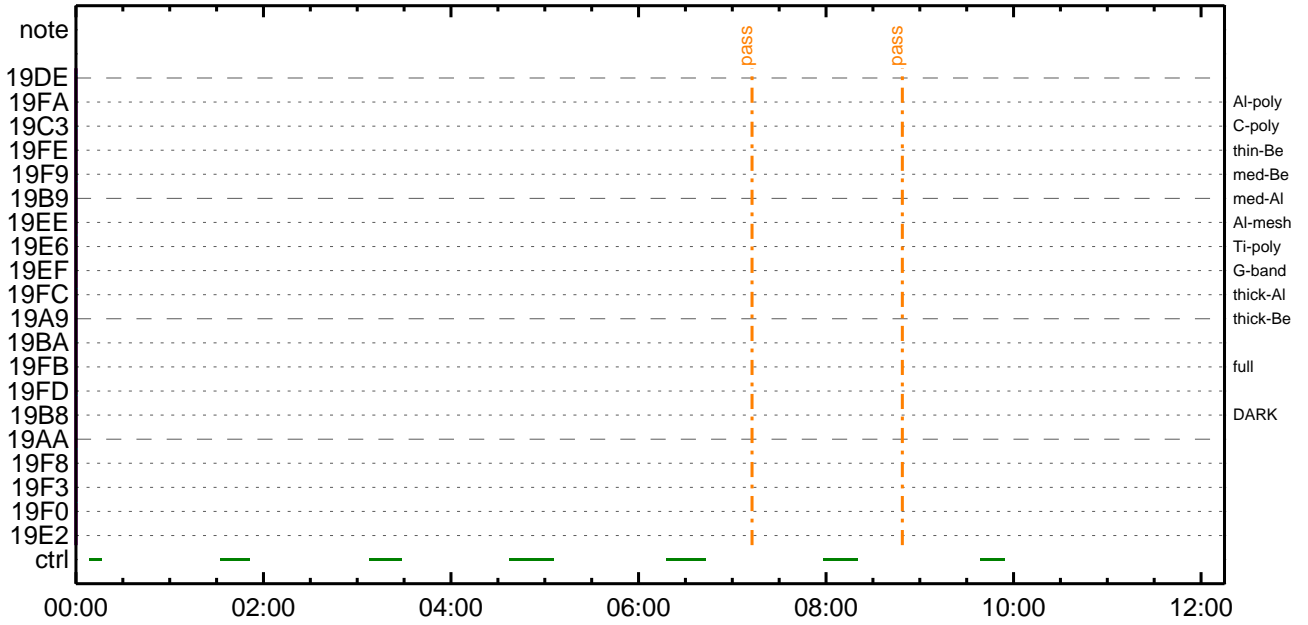
CMDI #0116 2014/02/27



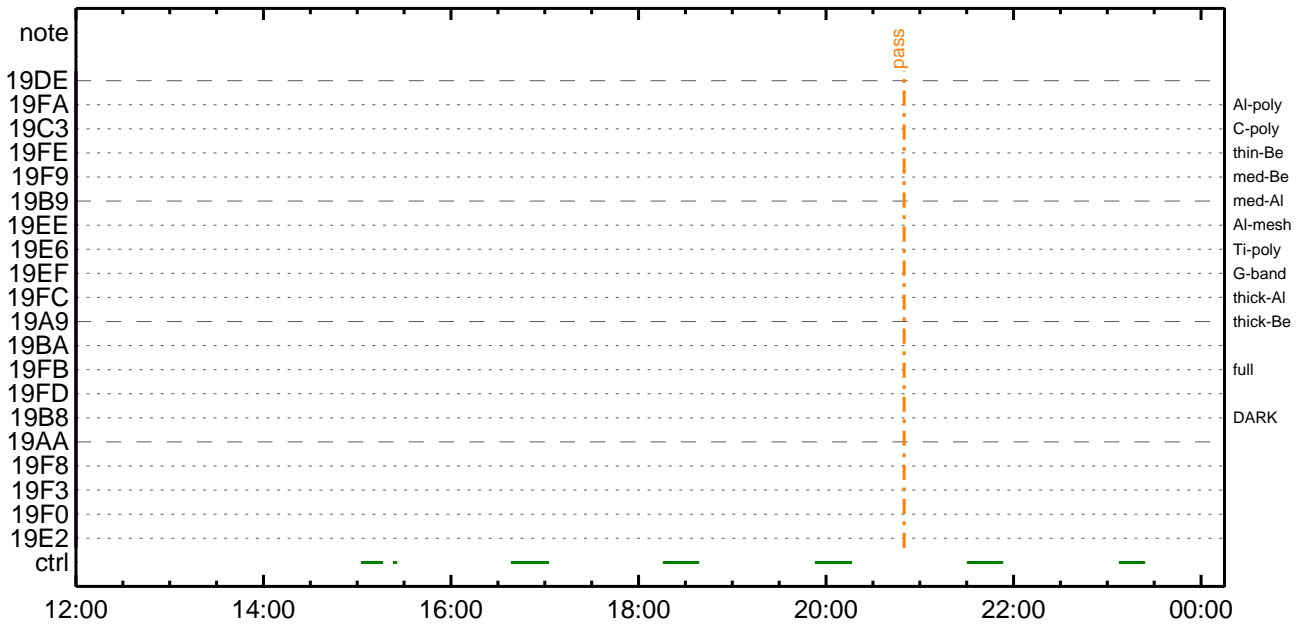
CMDI #0116 2014/02/27



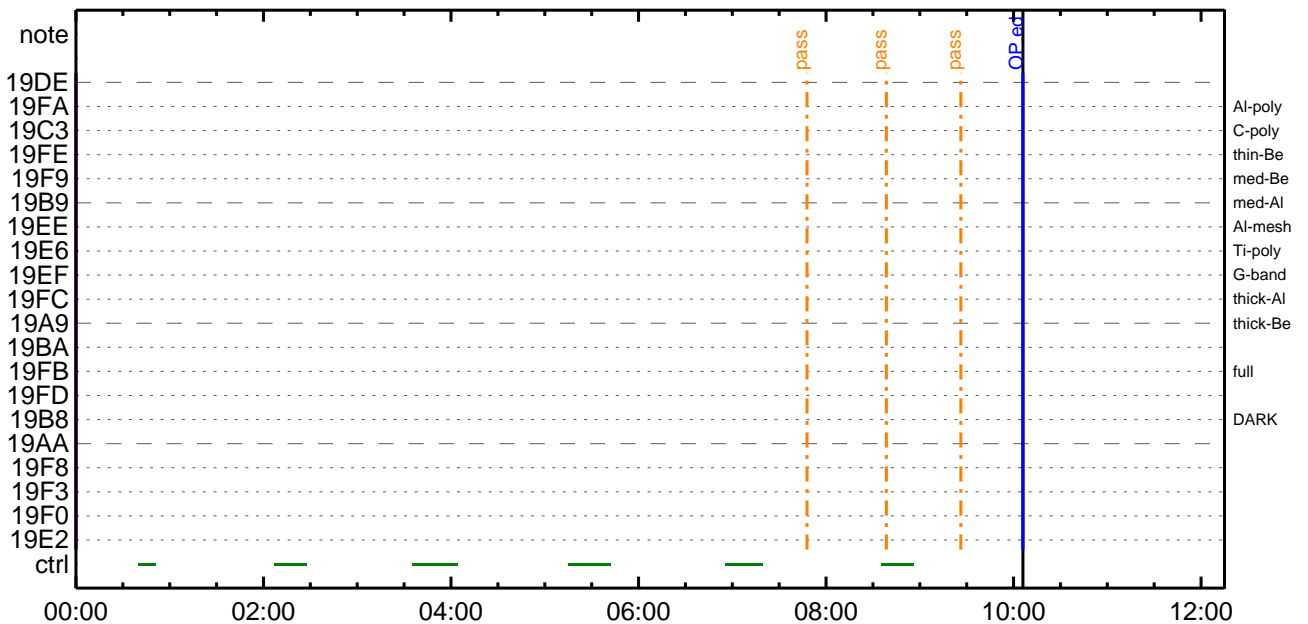
### CMDI #0116 2014/02/28



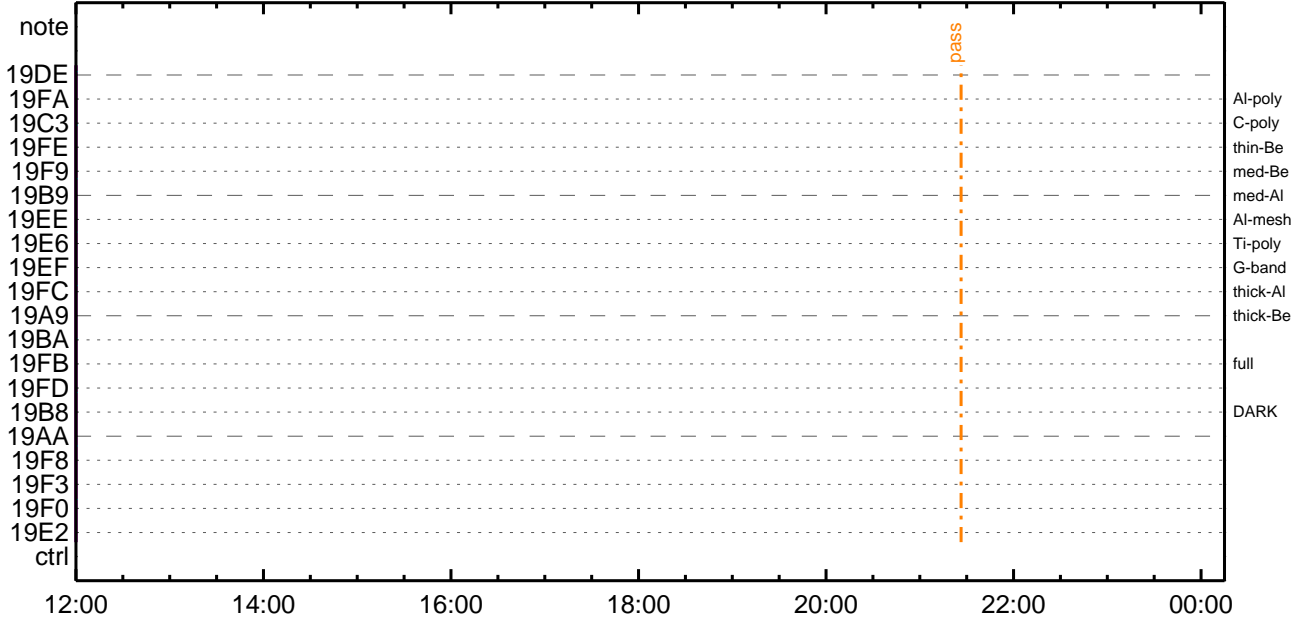
### CMDI #0116 2014/02/28



### CMDI #0116 2014/03/01



CMDI #0116 2014/03/01







```
0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|AYOX
0100 C. *****
0101 C.
0102 . C. ;aOP/OGY1;4YE;a
0103 . S. OP op-229:OP
0104 ( )
0105 . S. OG og-229:OG
0106 ( )
0107 C.
0108 . C. ;aNMOG&OPf^e^AYOX;a
0109 C. NMOG(0x200000-0x207FFF;s 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C.         c[Hk1_DMP_TOP_ADRS_1] EQ 40
0113 C.         c[Hk1_DMP_TOP_ADRS_0] EQ 0
0114 C.         c[Hk1_DMP_BLOCK_NUM] EQ 127
0115 C.         c[Hk1_DMP_REPEAT_NUM] EQ 0
0116 C.         c[Hk1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C.         c[Hk1_PKT_FORM_NO] EQ 7
0120 C.         c[Hk1_PKT_GEN_TIME] EQ 0.25 s
0121 C.         c[Hk1_S_TLM_BIT_RATE] EQ 32k
0122 C.         c[Hk1_X_TLM_BIT_RATE] EQ 4M
0123 C.         c[Hk1_DMP_CHK_FLG] EQ EXEC
0124 . C. AYOXx1/2^i>od^iCs
0125 C.         c[Hk1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOGaf1E^1c.e^iOKod^iCs
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;s 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C.         c[Hk1_DMP_TOP_ADRS_1] EQ 41
0132 C.         c[Hk1_DMP_TOP_ADRS_0] EQ 0
0133 C.         c[Hk1_DMP_BLOCK_NUM] EQ 127
0134 C.         c[Hk1_DMP_REPEAT_NUM] EQ 0
0135 C.         c[Hk1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C.         c[Hk1_PKT_FORM_NO] EQ 7
0139 C.         c[Hk1_PKT_GEN_TIME] EQ 0.25 s
0140 C.         c[Hk1_S_TLM_BIT_RATE] EQ 32k
0141 C.         c[Hk1_X_TLM_BIT_RATE] EQ 4M
0142 C.         c[Hk1_DMP_CHK_FLG] EQ EXEC
0143 . C. AYOXx1/2^i>od^iCs
0144 C.         c[Hk1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOGaf1E^1c.e^iOKod^iCs
0146 C.
0147 C. NMOG(0x210000-0x2100FF;s 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C.         c[Hk1_DMP_TOP_ADRS_1] EQ 42
0151 C.         c[Hk1_DMP_TOP_ADRS_0] EQ 0
0152 C.         c[Hk1_DMP_BLOCK_NUM] EQ 65
0153 C.         c[Hk1_DMP_REPEAT_NUM] EQ 0
0154 C.         c[Hk1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C.         c[Hk1_PKT_FORM_NO] EQ 7
0158 C.         c[Hk1_PKT_GEN_TIME] EQ 0.25 s
0159 C.         c[Hk1_S_TLM_BIT_RATE] EQ 32k
0160 C.         c[Hk1_X_TLM_BIT_RATE] EQ 4M
0161 C.         c[Hk1_DMP_CHK_FLG] EQ EXEC
0162 . C. AYOXx1/2^i>od^iCs
0163 C.         c[Hk1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG, RAM ID=OPaf1E^1c.e^iOKod^iCs
0165 C.
0166 . C. ***** oE^2/4oI^A^¶A^oEE^-oA^:z@ (%amu-AYOXx1/2e^1/2c^oA^O^A^oC^1/2^a^"oE%i^1c^oC^oA) *****
0167 C. DHUYa;4YE;E^1/2^Y;Y;1;4YE;E^oI^a^1
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C.         c[Hk1_PKT_FORM_NO] EQ 2
0171 C.         c[Hk1_PKT_GEN_TIME] EQ 0.5S
0172 C.         c[Hk1_S_TLM_BIT_RATE] EQ 32K
0173 C.         c[Hk1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 . C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE |s OPOG UPLOADa-A^:z@NGuI^1c; c^oE^2/4oI^TI-CMDA^:z@oI^A^1Oa.oE^o^o^o^oE;f
0180 C.         aP^z; cSETaEDUMPaI^E^±oI^N^1c^1Oa|o^oE;f
0181 C.
0182 . C. TIY^3Y^p^Y^o^E^o^o^A^I^z(UT)
0183 +. TI 2014-02-25 09:13:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C.         c[Hk1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2014-02-25 09:13:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C.         c[Hk1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2014-02-25 09:13:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C.         c[Hk1_TI_CMD_NUM] EQ 1COUNTUP
```



(a) Spacecraft Operation Procedure (real-commands)

```
main-230 2014-02-25 13:46:37 91 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSÿÁÿ$ÿÁÿ-¼Á»Ü;ã
0005 C.
0006 C. ÿÀÿB;¼ÿ³ÿBÿÓÿÉÀ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Èª¿ªÁª•µ°Æ»Í×ÁÇªíÿçÿÁÿ×ÿÿí;¼ÿÉ;ÈÈèµ•íÉ;ÈªÉ¼°ÇÓª•ª¿¼ì¹çªí;çÀ®, ùª¹ªèªªªªçÁ+¿ªª•ªèªªªªªªªªÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. ***** AOCS Commands (Orbital Element Update) *****
0046 C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048 BC (16)
0049 +. DC 02-8E AOCU_ORB_UPD
0050 C.
0051 C. <A_ORB>[ORBIT] EPC = 7491912.5 +- 1.0 (s) [ ]
0052 C.
0053 . C.
0054 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055 +. DC 07-FC EIS_MODE_MANU
0056 BC (21 02)
0057 . C. Verify EIS in MANUAL mode
0058 . C. Estimated OBSTBL upload time is 38s
0059 C. *****
0060 C. EIS START OBSTBL LOAD
0061 C. *****
0062 . S. RAM ram-820:EIS_OBSTBL
0063 ( )
0064 +. DC 07-FC EIS_DUMP_OBSTBL
0065 BC (07 07 07 00 00 70 00)
0066 C.
0067 C. Execute, after the success of OBSTBL upload.
0068 C. Set EIS TI-commands
0069 +. TI 2014-02-25 09:17:50.0
0070 DC 07-FC EIS_MODE_CHG_ENA
0071 BC (20)
0072 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0073 C. *****
0074 C. EIS END OBSTBL LOAD
0075 C. *****
0076 C.
0077 . C. ***** MDP `îÁîªî`ó¼ÿªÉÁªª¹ªèDCBC•x²è *****
0078 C. (¼ª°íÿÓÿÁÿBÿBÿÿÿÿáÿçÿÿèªª¾ªª¼ªª»Üª¹ªè)
0079 . S. DC-BC dcbc-402:DCBC
0080 (MDP_known_event)
0081 C.
0082 C.
0083 . C. ***** ÿBÿ¹•İ Daily±¿îÑªÉ`ª¹ªèDCBC•x²è *****
0084 . S. DC-BC dcbc-153:DCBC
0085 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0086 C.
0087 C.
0088 . C. ;ãLOSÿÁÿ$ÿÁÿ-¼Á»Ü;ã
0089 C.
0090 . C. ***** LOS *****
0091 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```

main-231 2014-02-25 13:46:37 238 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY~¼Ä»Û;ã
0005 C.
0006 C. YÀYß;¼Y³YÞYÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èµ¿µÄµ•µ°È»Í×ÁÇµÍYÇYÄY×YÍ;¼YÉ;ÈÈÈ¼µ•ÍÍÈ;ÈµÈ¼°ÇÖµ•µ¿¼l¹ÇµÍ;¿À®, ùµ¹µÈµµµÇÁ+¿®µ•µÈµµµ³µÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+¿µ;ON
0016 C. *****
0017 C. ¿ °ÄÄ, Í×ÈYµÄµµµÇµÍ»p´Öµµ¹ÍÍ, µ•; ¿ÈÖÍ×µÈXÁÖONµÍ¹ÖµÈµÍµÈµµµ³µÈ;f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. ¿¿[HK1_XPA_ON/OFF] EQ ON
0025 C. ¿¿[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. ¿¿[HK1_XMOD_ON/OFF] EQ ON
0027 C. ¿¿[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDYÓYÉYÍYÄY~¾ÖÄÖµ~°ÄÄÈµ•µ¿µé; ¿°È²¼µÍ°ÄÄ, ¼È¾Çµµµ¼Ä¹Öµ¹µé;f
0030 C.
0031 . C. *****
0032 C. DR PT1 ÄÍ¼i°ÄÄ,
0033 C. *****
0034 C. ¿" RESTART;ÈPT1;Èµ•µ¿µµ¼l¹ÇµÍ; ¿°È²¼µÍ°ÄÄ¹Öµµ°; ¿DCBC-150µ¿¿Èµà;f
0035 C.
0036 . C. ;ãPT1°ÄÄ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. ¿¿[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. ¿¿[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. ¿¿[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYÇYÓYÉYÈÄUÁÖ;ÈÄ•Ä°²óÈø;È, áµÍ°ÄÄ, °Ä³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. ¿¿[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. ¿¿[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. ¿¿[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÄÄ, µ~¼«Ä°Ä»ßµ•µ¿µ, á; ¿°È²¼µµ¼Ä¹Öµ¹µé;f
0055 C. YÇYÓYÉYÈÄUÁÖµÄÄ•Ä°²óÈøµ~¼áµµ¼l¹ÇµÍ´°Í»µ¹µÈµµµÇÁÖµÄ;f
0056 C.
0057 . C. *****
0058 C. DR PT2 ÄÍ¼i°ÄÄ,
0059 C. *****
0060 C. ¿" RESTART;ÈPT2;Èµ•µ¿µµ¼l¹ÇµÍ; ¿°È²¼µÍ°ÄÄ¹Öµµ°; ¿DCBC-151µ¿¿Èµà;f
0061 C.
0062 . C. ;ãPT2°ÄÄ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. ¿¿[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. ¿¿[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. ¿¿[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYÇYÓYÉYÈÄUÁÖ;ÈÄ•Ä°²óÈø;È, áµÍ°ÄÄ, °Ä³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. ¿¿[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. ¿¿[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. ¿¿[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÄÄ, Äá»ß;¿XÁ+¿µ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÄÄ, Äá»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. ¿¿[HK1_REP_STA/STP] EQ STOP
0087 C. ¿¿[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. ¿¿[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+¿µ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. ¿¿[HK1_XMOD_ON/OFF] EQ OFF
0095 C. ¿¿[HK1_XPA_ON/OFF] EQ OFF

```

```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 85 83 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0b 80 60 20 18)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 0c a0 80 18 20)
0140 + DC 07-F0 MDP_XRT_ROI_SET
0141 BC (cd 0f 80 80 06 06)
0142 + DC 07-F0 MDP_XRT_ROI_SET
0143 BC (cd 10 80 80 08 08)
0144 + DC 07-F0 MDP_XRT_FLD_ENA
0145 BC (d8)
0146 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0147 BC (c8)
0148 + DC 07-F0 MDP_XRT_AEC_RESET
0149 BC (d0)
0150 + DC 07-F0 MDP_XRT_ARS_DIS
0151 BC (d5)
0152 + DC 07-F0 MDP_XRT_FLD_RESET
0153 BC (da)
0154 . C. ----- Success Verify ? OK / NG ____
0155 C.
0156 C.
0157 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0158 C.
0159 +. DC 07-F0 MDP_XRT_MODE_OBSV
0160 BC (c2)
0161 +. TI 2014-02-25 09:17:02.0
0162 DC 07-F0 MDP_XRT_MODE_OBSV
0163 BC (c2)
0164 . C. ----- Success Verify ? OK / NG ____
0165 C.
0166 C. ***** XRT END *****
0167 . C. *****
0168 C. SOT table upload
0169 C. *****
0170 . C. < Stop FG table >
0171 +. DC 07-F0 MDP_FG_CTRL_MANU
0172 BC (51)
0173 . C. -----
0174 C. MDP_FG_CTRL_MODE = MANU [ ]
0175 C. -----
0176 C.
0177 . C. <Upload FG Observation Table>
0178 . S. RAM ram-262:MDP_OBS_F
0179 ( )
0180 C.
0181 . C. < Dump RAMID=MDP_OBS_F >
0182 +. DC 07-F0 MDP_DUMP_FGTBL
0183 BC (82 07 00 00 00 38 b8)
0184 C. -----
0185 C. MDP_OBS_F verify = OK/NG [ ]
0186 C. -----
0187 C.
0188 . C. < Upload DPL table >
0189 C.
0190 C. ¥çŸŸ×Ÿí;¼ŸÉ°îĀ°ĀĒSTS_CHKĀĀĀOFFĀĒĀĀĀĀ
0191 C.
0192 . S. RAM ram-271:MDP_DPL
0193 ( )

```

```

0194 C.
0195 . C. < Dump RAMID=MDP_DPL >
0196 +. DC 07-F0 MDP_DUMP_FGTBL
0197 BC (82 07 00 38 b8 00 40)
0198 C. -----
0199 C. MDP_DPL verify = OK [ ]
0200 C. -----
0201 C.
0202 C. STS_CHK=ON=
0203 C.
0204 . C. < Update MDP DSC PAR1 >
0205 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0206 BC (4c)
0207 C. MDP_CMD_CODE = F04C0700[ ]
0208 C. MDP_CMD_CNT (count-up 1) [ ]
0209 C. -----
0210 C.
0211 . C.
0212 C. *****
0213 C. SOT TI command set
0214 C. *****
0215 C. Execute, after the success of TBL upload.
0216 +. TI 2014-02-25 09:17:18.0
0217 DC 07-F0 MDP_SOT_MODE_OBSV
0218 BC (40)
0219 . C. -----
0220 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0221 C. -----
0222 C.
0223 C.
0224 . C. ***** MDP 'uAifI>ö%Y=EA=DCBC*x²è *****
0225 C. (%A°iYÓYÄYÈYBYËYáYçYè=É%α%Ä»Ûα¹è)
0226 . S. DC-BC dcbc-402:DCBC
0227 (MDP_known_event)
0228 C.
0229 C.
0230 . C. ***** YDY!·I Daily±¿ÍÑ=É`Øα¹èDCBC*x²è *****
0231 . S. DC-BC dcbc-153:DCBC
0232 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0233 C.
0234 C.
0235 . C. ;ãLOS¥Á¥$¥Ä¥¼Ä»Û;ä
0236 C.
0237 . C. ***** LOS *****
0238 C.

```

Feb 25, 14 13:46

XRT\_OGLIST\_0116.chk

Page 1/5

\*\*\* OP Sequence for XRT \*\*\*

2014/02/25	09:26:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/25	09:26:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/25	09:26:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/25	09:28:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2014/02/25	09:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/25	11:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/25	11:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/02/25	11:30:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2014/02/25	11:30:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/25	11:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/25	11:30:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/25	11:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/25	11:30:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/25	11:32:56.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07				
2014/02/25	11:32:58.0	XRT_FL_PROG_SET_407_OG [0x197]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 14				
2014/02/25	11:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/25	14:30:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/25	14:52:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/25	14:52:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/25	14:52:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/25	14:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/25	16:27:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/25	16:27:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/25	16:27:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/25	16:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/25	17:40:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/25	17:40:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/02/25	17:41:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/02/25	17:41:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/02/25	17:43:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/02/25	17:43:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/25	17:43:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2014/02/25	17:44:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/25	17:50:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/25	17:50:56.0	XRT_FOCUS_POSITION_449_OG [0x1c1]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/02/25	17:51:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 ac 00 00 00				
2014/02/25	17:51:16.0	XRT_FLD_DIS_420_OG [0x1a4]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/02/25	17:51:18.0	XRT_FLRCTRL_DIS_446_OG [0x1be]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/02/25	17:51:20.0	XRT_ARS_DIS_427_OG [0x1ab]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/25	17:51:22.0	XRT_QT_PROG_SET_409_OG [0x199]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0a				
2014/02/25	18:06:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/25	19:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/25	19:59:56.0	XRT_FOCUS_POSITION_449_OG [0x1c1]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/02/25	20:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 00 00 54 00				
2014/02/25	20:00:16.0	XRT_FLD_DIS_420_OG [0x1a4]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/02/25	20:00:18.0	XRT_FLRCTRL_DIS_446_OG [0x1be]							

Feb 25, 14 13:46

## XRT\_OGLIST\_0116.chk

Page 2/5

2014/02/25	20:00:20.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/25	20:00:22.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05
2014/02/25	20:15:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/25	22:56:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/25	22:56:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/25	22:56:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/25	22:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/25	23:00:00.0	AOCS_Orе-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 55	ca 01 58
2014/02/26	00:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/26	00:33:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/26	00:33:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/26	00:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/26	01:56:00.5	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/26	01:56:02.5	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/26	01:56:04.5	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/26	01:59:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/26	03:27:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/26	03:27:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/26	03:27:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/26	03:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/26	05:03:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/26	05:03:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/26	05:03:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/26	05:06:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/26	06:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/26	06:43:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/26	06:43:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/02/26	06:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/02/26	07:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/26	07:09:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff	aa 00
2014/02/26	07:10:00.0	AOCS_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00	00 00 00
2014/02/26	07:10:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/02/26	07:12:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/02/26	07:12:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/26	07:12:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2014/02/26	07:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/02/26	07:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/02/26	07:19:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97 00
2014/02/26	07:20:00.0	AOCS_Orе-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	03 00	00 00 00
2014/02/26	07:20:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/02/26	07:20:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/02/26	07:20:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/02/26	07:20:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/02/26	07:20:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/02/26	07:22:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d
2014/02/26	07:22:58.0	XRT_FL_PROG_SET_407_OG [0x197]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	14



Feb 25, 14 13:46

## XRT\_OGLIST\_0116.chk

Page 3/5

2014/02/26	07:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/26	08:23:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/26	08:23:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/26	08:23:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/26	08:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/26	08:44:30.0	XRT_Custom_430_OG [0x1ae]							
2014/02/26	08:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/26	10:04:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/26	10:04:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/26	10:04:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/26	10:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/26	10:16:00.0	XRT_Custom_430_OG [0x1ae]							
2014/02/26	10:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/26	11:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/26	11:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/02/26	11:30:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2014/02/26	11:30:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/26	11:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/26	11:30:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/26	11:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/26	11:30:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/26	11:32:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2014/02/26	11:32:58.0	XRT_FL_PROG_SET_407_OG [0x197]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 14				
2014/02/26	11:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/26	12:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/26	12:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/02/26	13:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/26	13:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/26	13:00:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/26	13:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/26	13:00:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/26	13:02:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2014/02/26	13:02:58.0	XRT_FL_PROG_SET_407_OG [0x197]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 14				
2014/02/26	13:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/26	14:01:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/26	14:01:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/02/26	14:02:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/02/26	14:02:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/02/26	14:02:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/02/26	14:02:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/26	14:02:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/26	14:04:56.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d				
2014/02/26	14:04:58.0	XRT_FL_PROG_SET_407_OG [0x197]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 14				
2014/02/26	14:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/26	15:27:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/26	15:27:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/26	15:27:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/26	15:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							

Feb 25, 14 13:46

## XRT\_OGLIST\_0116.chk

Page 4/5

2014/02/26	15:50:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/02/26	15:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
2014/02/26	16:49:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/02/26	16:49:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/26	16:50:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2014/02/26	16:50:16.0	XRT_FLD_DIS_434_OG [0x1b2]	AOCU_NM	5	02-76	00	00	00	00
2014/02/26	16:52:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2014/02/26	16:52:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2014/02/26	16:52:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/02/26	16:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11		
2014/02/26	16:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/02/26	16:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/26	17:00:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2014/02/26	17:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03	00	00	00
2014/02/26	17:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/02/26	17:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/02/26	17:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/02/26	17:00:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/02/26	17:02:56.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/02/26	17:02:58.0	XRT_FL_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d		
2014/02/26	17:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	14		
2014/02/26	17:03:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/26	17:03:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/02/26	17:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/02/26	17:27:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/02/26	17:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
2014/02/26	18:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/02/26	18:40:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/26	18:40:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/02/26	18:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/02/26	19:04:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/02/26	19:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
2014/02/26	20:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/02/26	20:17:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/26	20:17:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/02/26	20:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/02/26	20:41:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/02/26	20:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
2014/02/26	21:55:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/02/26	21:55:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/26	21:55:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/02/26	21:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/02/26	23:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/02/26	23:32:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/02/26	23:32:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/02/26	23:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/02/27	00:59:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/02/27	00:59:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_FLD_RESET	1	07-F0	da			

2014/02/27	00:59:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/27	01:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/27	02:31:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/27	02:31:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/27	02:31:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/27	02:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/27	04:00:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/27	04:00:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/27	04:00:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/27	04:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/27	05:40:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/27	05:40:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/27	05:40:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/27	05:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/27	06:16:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/27	06:16:56.0	XRT_FOCUS_RECALIBRATE_433_OG [0x1b1]							
		XRT_FOCUS_RECAL	2	07-F8	78 00				
2014/02/27	06:17:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/02/27	06:20:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/02/27	06:21:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/02/27	06:23:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/02/27	06:23:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/02/27	06:23:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2014/02/27	06:24:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/02/27	06:26:30.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/27	06:27:00.0	AOCS_Ore-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2014/02/27	07:21:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/27	07:21:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/27	07:21:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/27	07:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/27	09:00:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/02/27	09:00:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/02/27	09:00:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/02/27	09:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/02/27	10:39:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				