

XRT Timeline to be uploaded on 2014/03/01

Period: 2014/03/01 10:06:00 - 2014/03/06 09:53:00

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Normal mode

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XOB #1A00: AR Standard-B(Morphology), Be/thin 5min-cad, multifilter PFB (Al/poly, Be/thin, Be/med), multifilter context, 384x384, 1064x1048, G-band (3R)

Term	Pointing (x, y)	Comment
03/01 10:27:00 - 03/01 17:55:54	Track (-513.9, -322.3) @ 03/01 10:16:00	OP start + 10min AR 11991
03/01 18:09:00 - 03/01 22:59:54	Track (-460.3, -317.8) @ 03/01 18:06:00	AR11991 cont.
03/02 04:03:00 - 03/02 06:28:24	Track (-389.0, -312.8) @ 03/02 04:00:00	AR11991 cont.
03/02 06:41:30 - 03/02 11:29:54	Track (-369.4, -311.6) @ 03/02 06:38:30	AR11991 cont.
03/02 18:27:30 - 03/02 22:43:00	Track (-279.4, -307.1) @ 03/02 18:24:30	AR11991 cont.
03/03 04:03:00 - 03/03 05:59:54	Track (-203.5, -304.3) @ 03/03 04:00:00	AR11991 cont.
03/03 06:13:00 - 03/03 09:50:30	Track (-186.1, -303.8) @ 03/03 06:10:00	AR11991 cont.

PROG= 09 Inf.-time(s)

Subr=	Seqn=	1-time(s)	2-time(s)	2.0sec															
1	8	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384	(1064, 1048)	DPCM	0	0	2.0sec				
	24	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=98	0	0	2.0sec				
		Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384	(1064, 1048)	Q=98	0	0	2.0sec				
2	16	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec				
		Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec				
		Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec				
		C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec				
		thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec				
		med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec				
		med-Al/Open	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	2.0sec				
	20	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	0	13.0sec				
		Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	1	13.0sec				
		thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	2	13.0sec				
		med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	3	13.0sec				
		thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	1	13.0sec				
		Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	2	13.0sec				
		thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384	(1064, 1048)	Q=95	3	3	13.0sec				
		med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384	(1064, 1048)	Q=95	3	1	13.0sec				
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval					

XOB #19FE: Synoptic Q95 2x2 - Al/mesh(3/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(8/362/1443) + Thin...

Term	Pointing (x, y)	Comment
03/01 17:59:00 - 03/01 18:05:54	Fixed (0.0, 0.0)	synoptic, shifted -4.0 min
03/02 06:31:30 - 03/02 06:38:24	Fixed (0.0, 0.0)	synoptic, shifted 28.5 min
03/02 18:17:30 - 03/02 18:24:24	Fixed (0.0, 0.0)	synoptic, shifted 14.5 min
03/03 06:03:00 - 03/03 06:09:54	Fixed (0.0, 0.0)	synoptic
03/03 18:03:00 - 03/03 18:09:54	Fixed (0.0, 0.0)	synoptic
03/04 06:10:30 - 03/04 06:13:24	Fixed (0.0, 0.0)	synoptic, shifted 3.5 min

PROG= 17 1-time(s)

Subr=	Seqn=	1-time(s)	2-time(s)	12.0sec															
1	35	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	3ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec				
		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec				
		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec				
	5	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec				
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec				
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048	(1024, 1024)	Q=98	0	0	2.0sec				
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512	(1024, 1024)	DPCM	0	0	2.0sec				
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048	(1024, 1024)	DPCM	0	0	2.0sec				
	72	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	8ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec				
		Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec				
		Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec				
	67	thin-Be/Open	thin-Be/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec				
		thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec				
		thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec				
	6	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048	(1024, 1024)	Q=90	0	0	2.0sec				
		Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec				
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval					

XOB #19FF: AR Standard-B(Morphology), Be/thin 24s-cad, multifilter PFB (Al/poly, Be/thin, Be/med), multifilter context (5min), 384x384, 1064x1048, G-band

Term	Pointing (x, y)	Comment
03/01 23:03:00 - 03/02 03:59:54	Track (-212.3, -130.7) @ 03/01 23:00:00	IHOP246 hot plasma

03/02 23:04:30 - 03/03 03:59:54 Track (2.3, -127.5) @ 03/02 23:00:00 IHOP246 hot plasma
 03/03 23:03:00 - 03/04 03:46:30 Track (216.7, -130.5) @ 03/03 23:00:00 IHOP246 hot plasma

PROG= 03 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 8 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Seqn= 24 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec													
└─ Seqn= 16 2-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Al/Open	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 52 10-time(s) 45.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19FD: AR standard-B (thin-Be) with PFB, 384 FOV at 1064x1048, thin-Be, thick-Al, and Al/Poly context, With G-band (33ms/45ms leak), 10s cad, low

Term	Pointing (x, y)	Comment
03/02 11:33:00 - 03/02 13:29:54	Track (521.6, 64.2) @ 03/02 11:30:00	IHOP247 moss

PROG= 07 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 8 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec													
└─ Seqn= 24 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 42 2-time(s) 2.0sec													
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 87 75-time(s) 2.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	10.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19C3: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 45ms-2

Term	Pointing (x, y)	Comment
03/03 10:05:30 - 03/03 17:59:54	Fixed (-15.0, -940.0)	HOP 206 S pole

PROG= 18 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 13 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 30 1-time(s) 30.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Subr= 3 30-time(s) 2.0sec													
└─ Seqn= 57 1-time(s) 60.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #19F0: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops-lowered-gband

Term	Pointing (x, y)	Comment
03/01 10:27:00 - 03/01 17:55:54	Track (-513.9, -322.3) @ 03/01 10:16:00	OP start + 10min AR 11991
03/01 18:09:00 - 03/01 22:59:54	Track (-460.3, -317.8) @ 03/01 18:06:00	AR11991 cont.
03/01 23:03:00 - 03/02 03:59:54	Track (-212.3, -130.7) @ 03/01 23:00:00	IHOP246 hot plasma
03/02 04:03:00 - 03/02 06:28:24	Track (-389.0, -312.8) @ 03/02 04:00:00	AR11991 cont.
03/02 06:41:30 - 03/02 11:29:54	Track (-369.4, -311.6) @ 03/02 06:38:30	AR11991 cont.
03/02 11:33:00 - 03/02 13:29:54	Track (521.6, 64.2) @ 03/02 11:30:00	IHOP247 moss
03/02 18:27:30 - 03/02 22:43:00	Track (-279.4, -307.1) @ 03/02 18:24:30	AR11991 cont.
03/02 23:04:30 - 03/03 03:59:54	Track (2.3, -127.5) @ 03/02 23:00:00	IHOP246 hot plasma
03/03 04:03:00 - 03/03 05:59:54	Track (-203.5, -304.3) @ 03/03 04:00:00	AR11991 cont.
03/03 06:13:00 - 03/03 09:50:30	Track (-186.1, -303.8) @ 03/03 06:10:00	AR11991 cont.
03/03 10:05:30 - 03/03 17:59:54	Fixed (-15.0, -940.0)	HOP 206 S pole
03/03 23:03:00 - 03/04 03:46:30	Track (216.7, -130.5) @ 03/03 23:00:00	IHOP246 hot plasma

PROG= 02 12-time(s)

Subr= 1		45-time(s)	10.0sec										
Seqn= 9		1-time(s)	2.0sec										
	thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 10		1-time(s)	2.0sec										
	med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11		1-time(s)	2.0sec										
	Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2		1-time(s)	10.0sec										
Seqn= 17		1-time(s)	2.0sec										
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

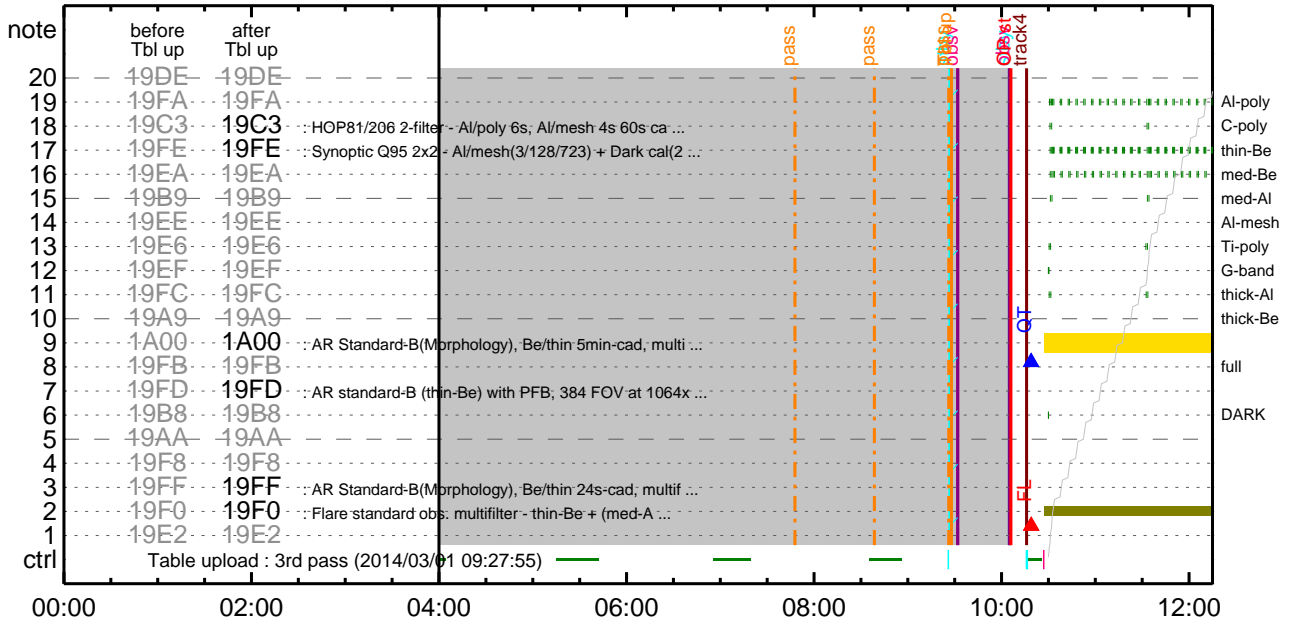
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Flare Detection

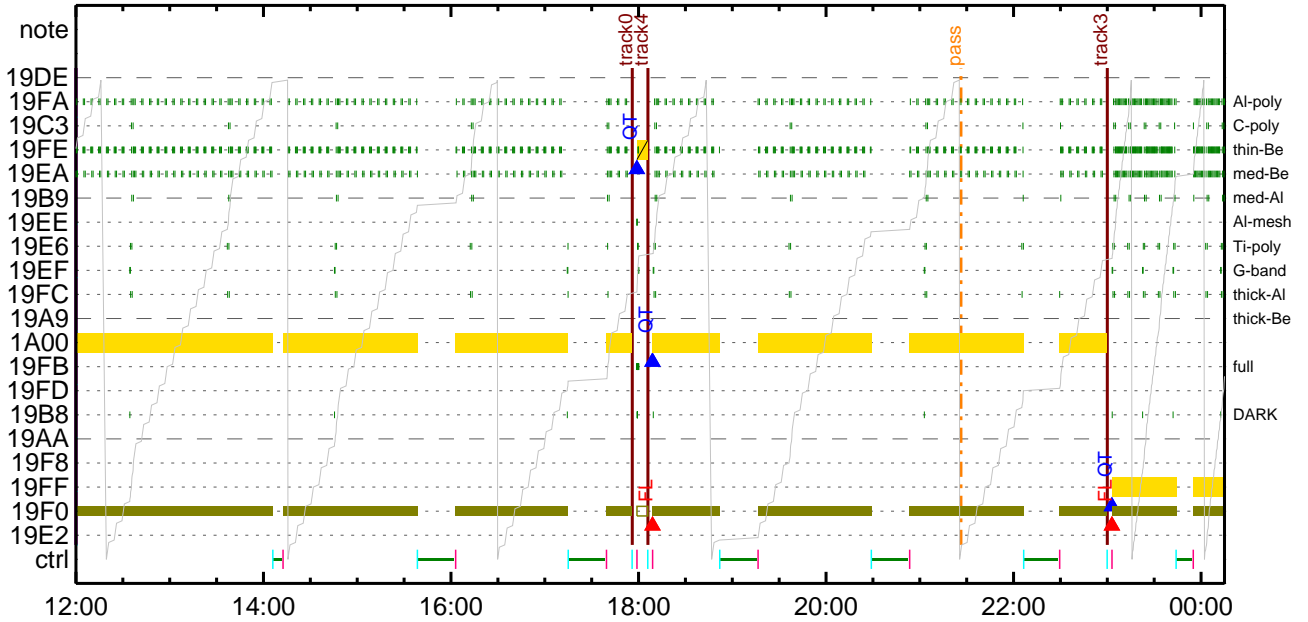
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FLD Patrol												
Term	Pointing (x, y)						Comment					
03/01 18:06:16 - 03/02 06:28:46	Track (-460.3, -317.8)	[Ⓢ] 03/01 18:06:00	AR11991 cont.									
03/02 06:38:46 - 03/02 18:14:46	Track (-369.4, -311.6)	[Ⓢ] 03/02 06:38:30	AR11991 cont.									
03/02 18:24:46 - 03/03 06:00:16	Track (-279.4, -307.1)	[Ⓢ] 03/02 18:24:30	AR11991 cont.									
03/03 06:10:16 - 03/03 18:00:16	Track (-186.1, -303.8)	[Ⓢ] 03/03 06:10:00	AR11991 cont.									
03/03 23:00:16 - 03/04 06:07:46	Track (216.7, -130.5)	[Ⓢ] 03/03 23:00:00	IHOP246 hot plasma									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	30sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

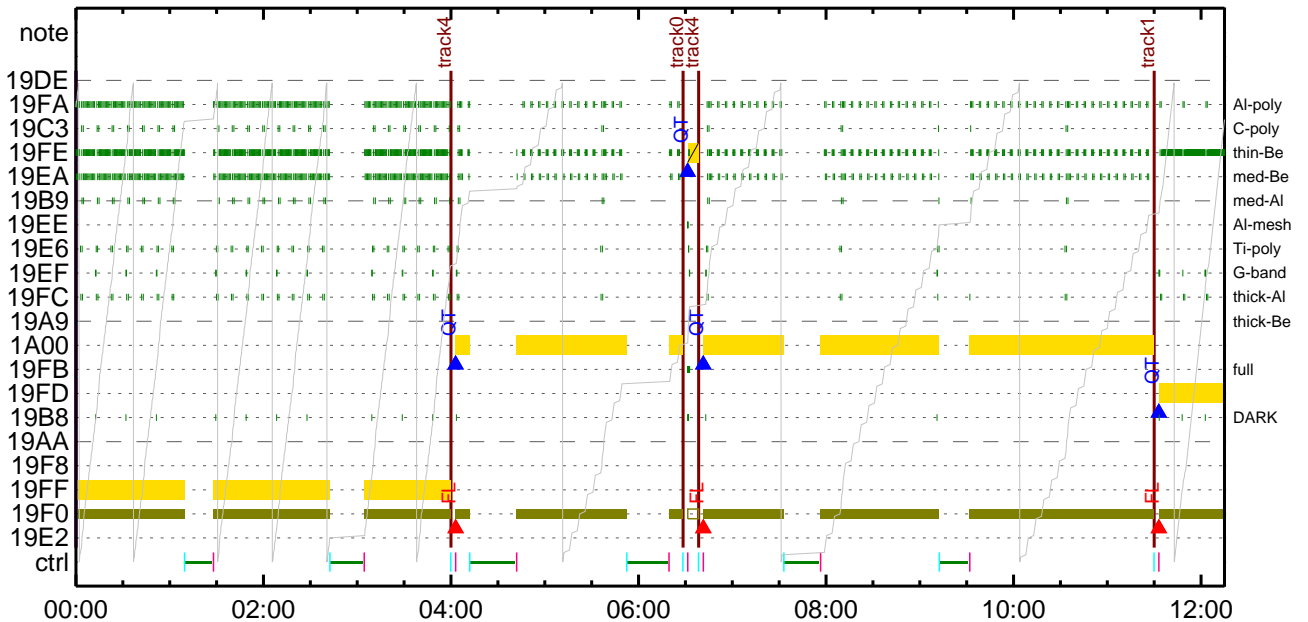
CMDI #0124 2014/03/01



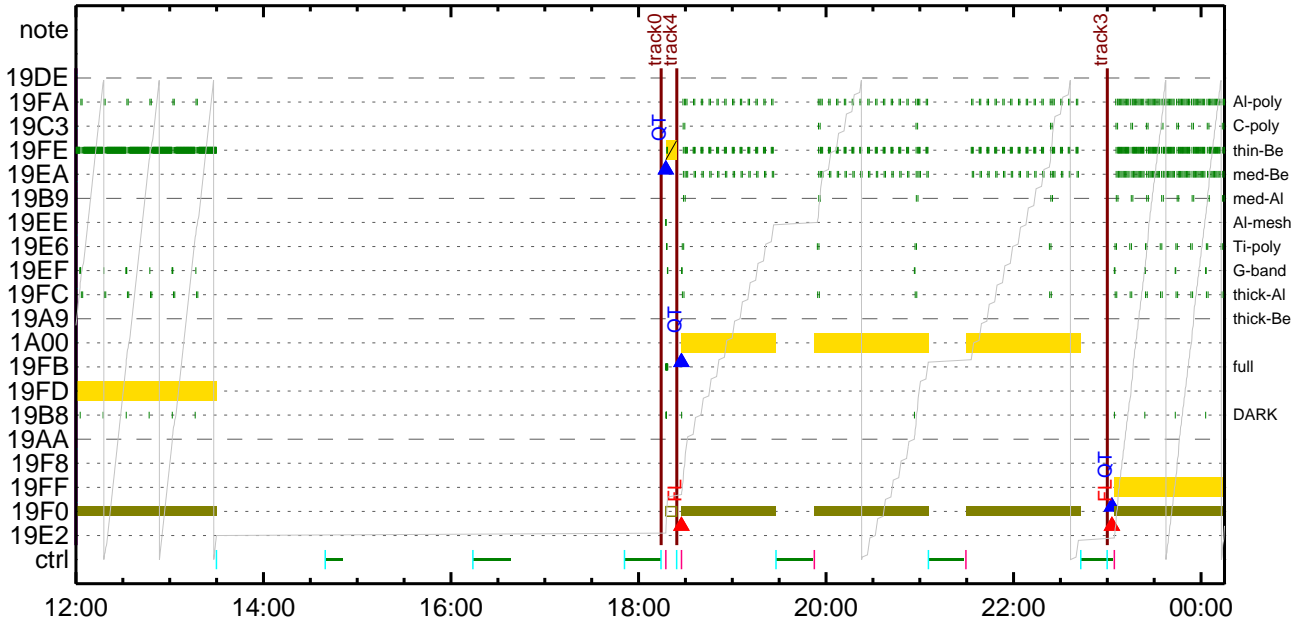
CMDI #0124 2014/03/01



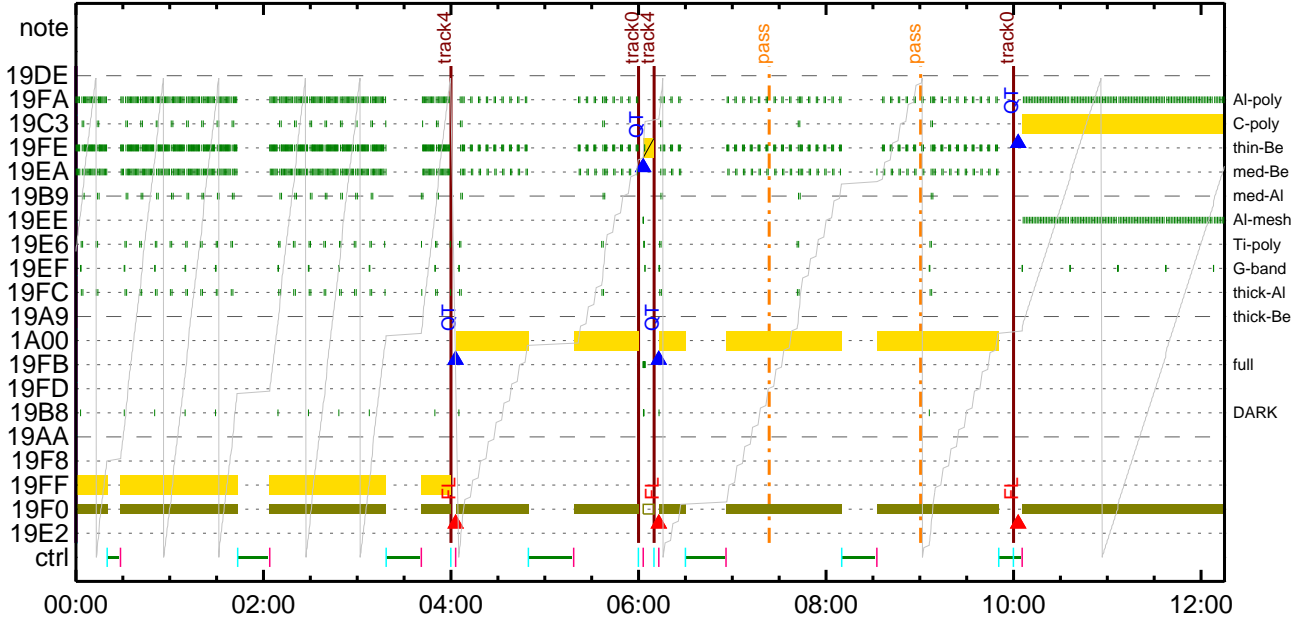
CMDI #0124 2014/03/02



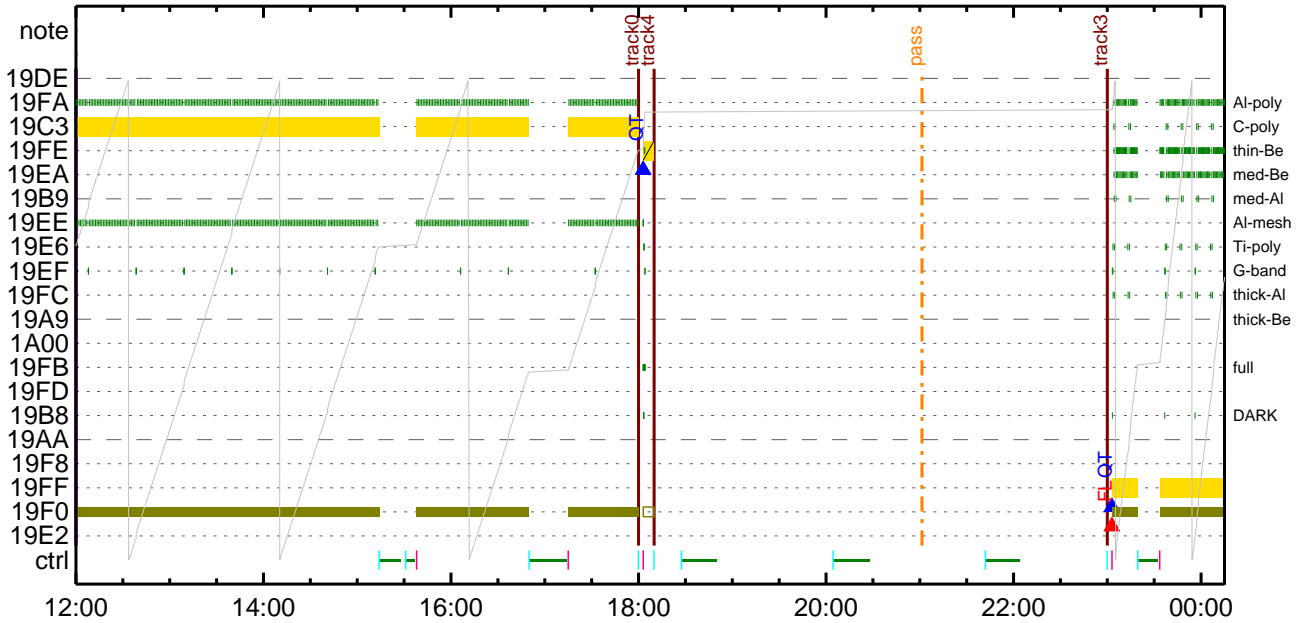
CMDI #0124 2014/03/02



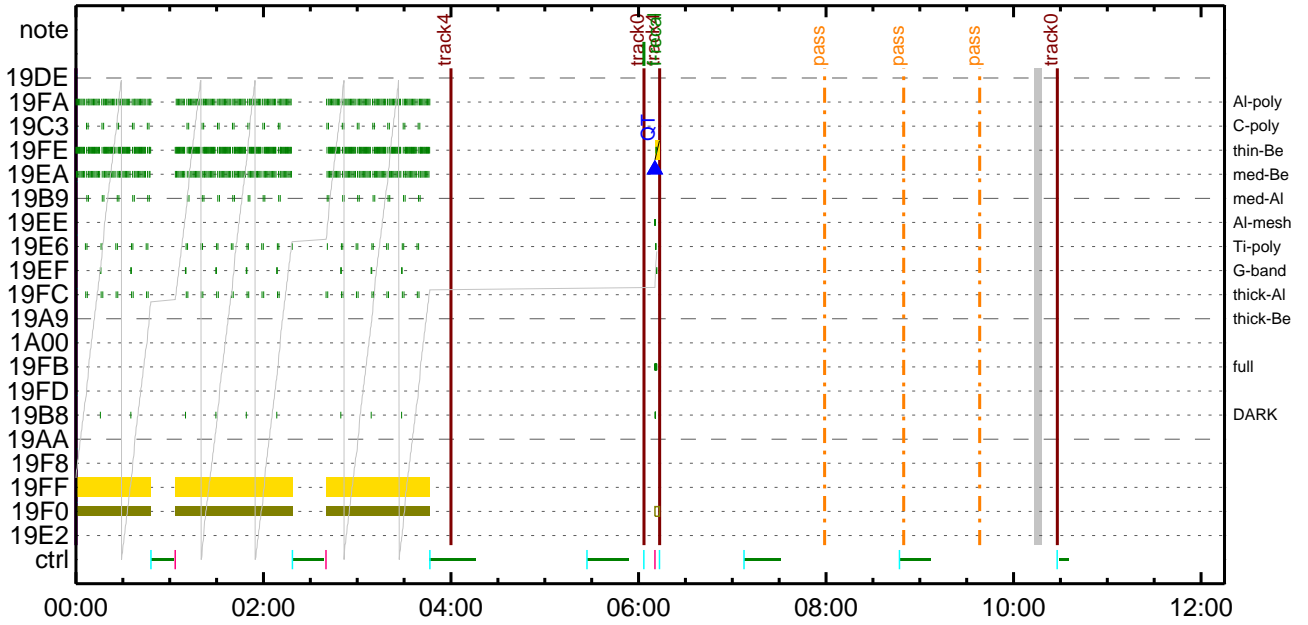
CMDI #0124 2014/03/03



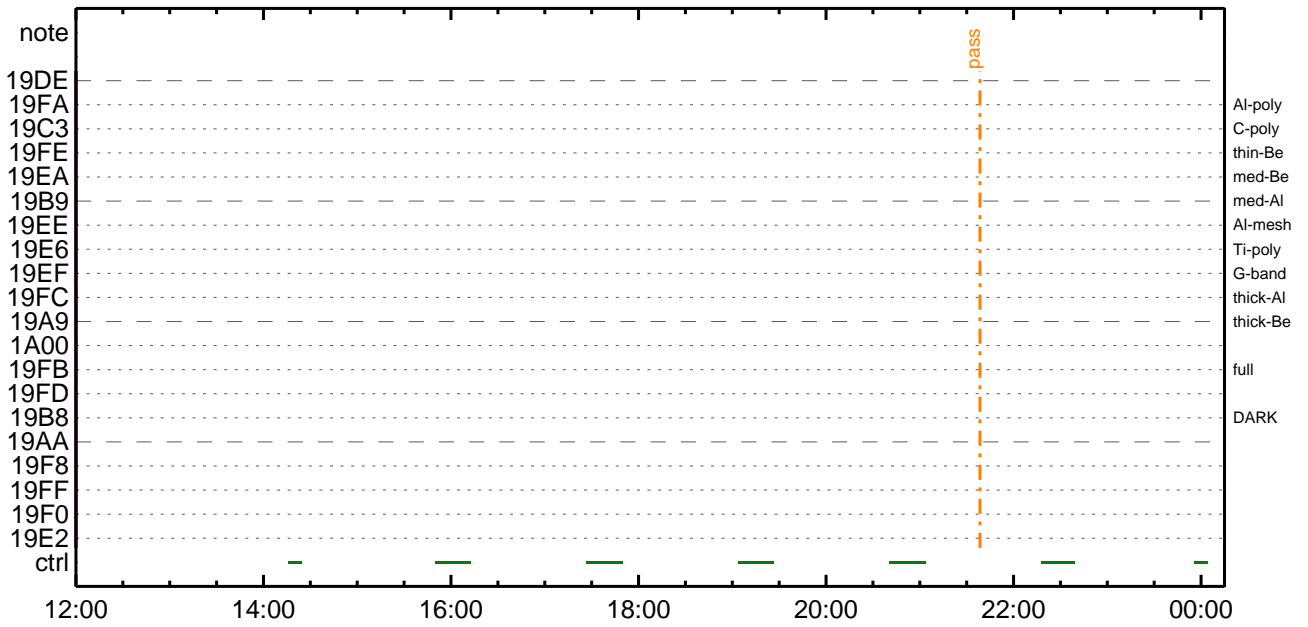
CMDI #0124 2014/03/03



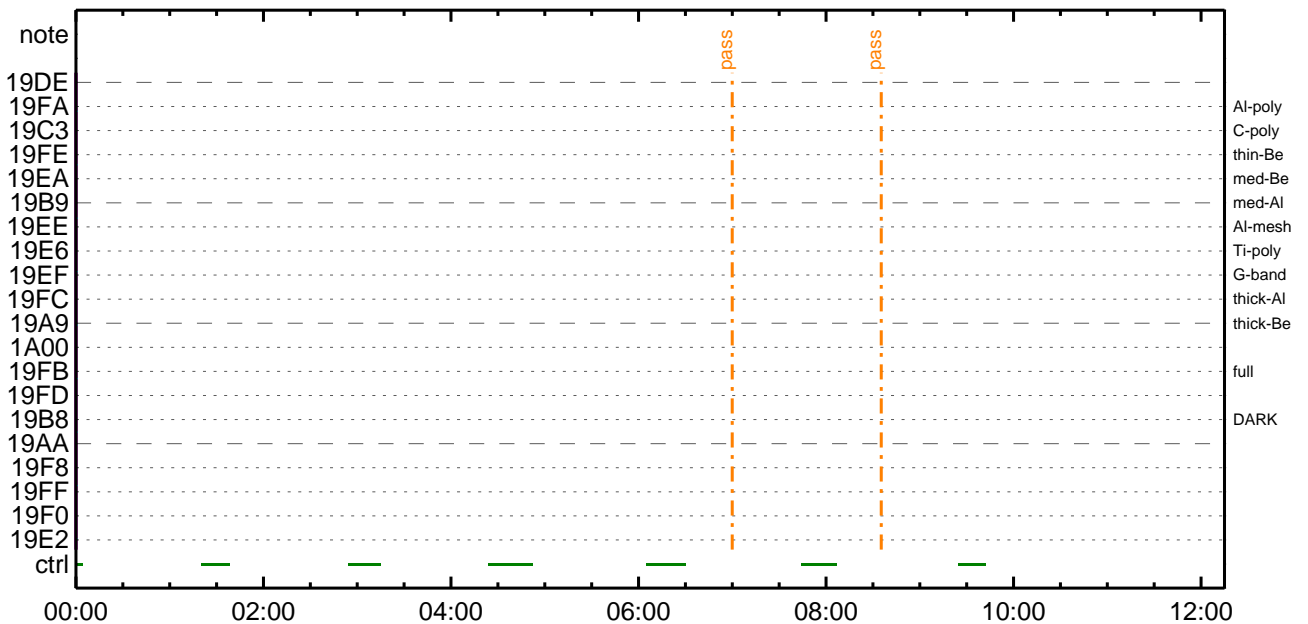
CMDI #0124 2014/03/04



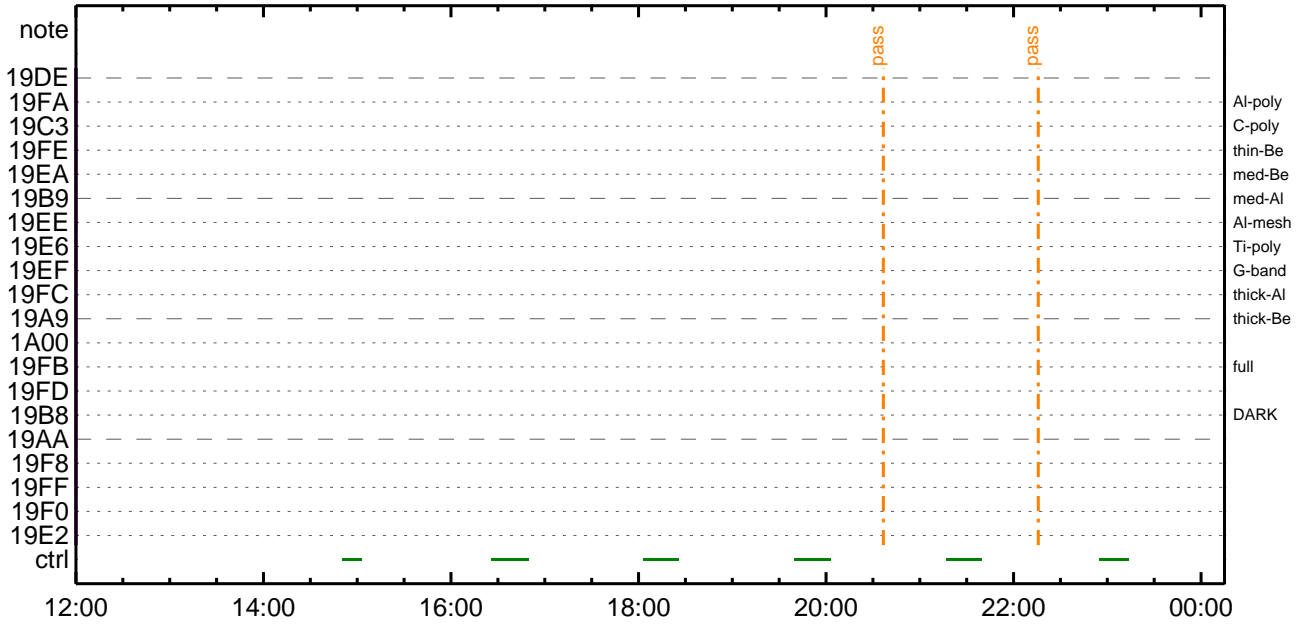
CMDI #0124 2014/03/04



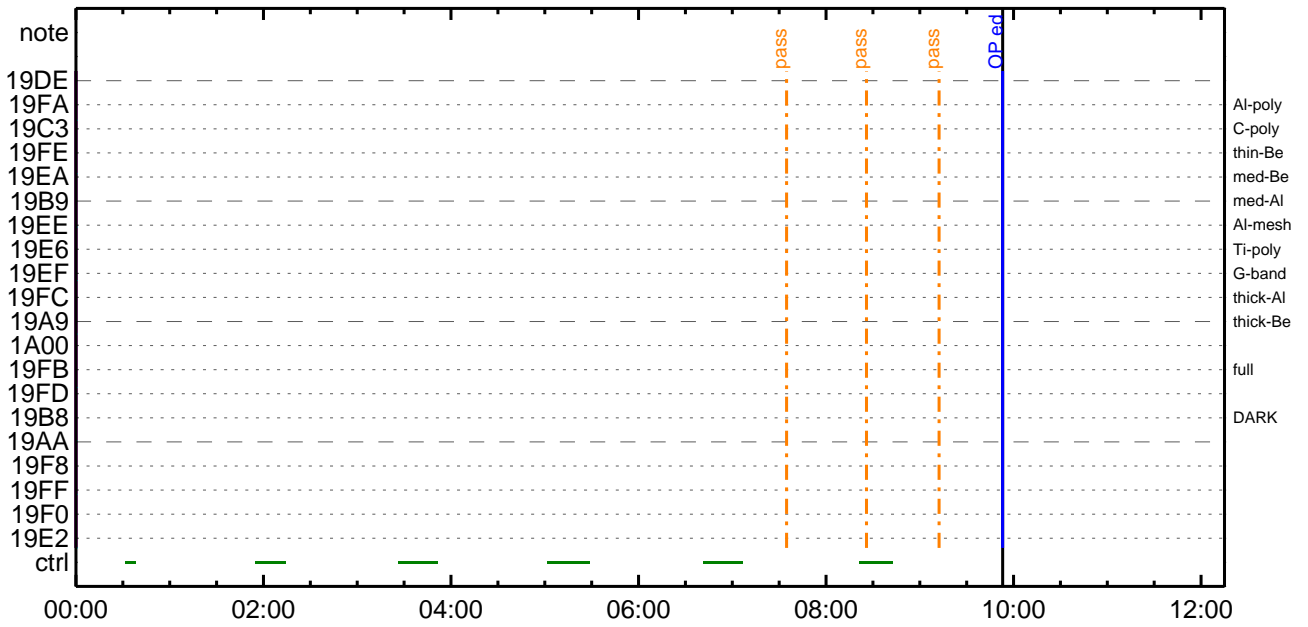
CMDI #0124 2014/03/05



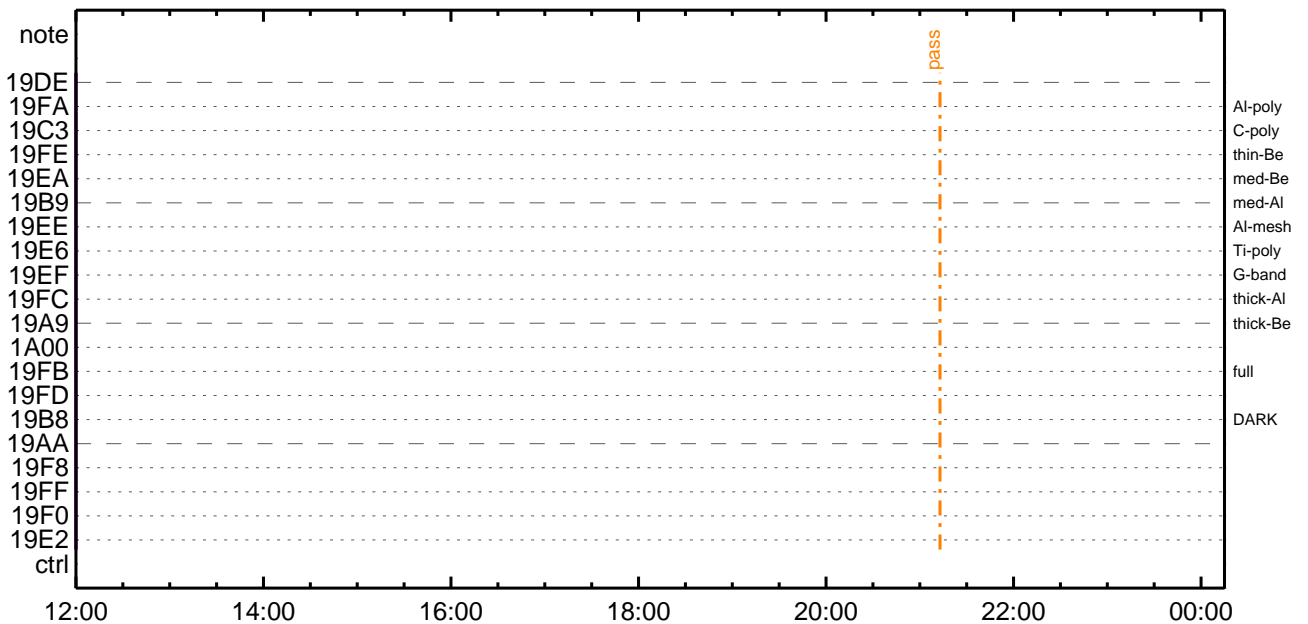
CMDI #0124 2014/03/05



CMDI #0124 2014/03/06



CMDI #0124 2014/03/06



(a) Spacecraft Operation Procedure (real-commands)

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main-239 2014-03-01 13:28:55 289 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSÝÁÝÝÝÁÝ~¼Á»Û;ã
0005 C.
0006 C. ÝÀÝß;¼Ý³ÝÞÝÓÝÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C.  Áí;ËçòÁáµºÆ»Í×ÁÇóÍÝçÝÁÝ×ÝÍ;¼ÝÉ;ËÈÈµ·íÉ;ËóÈ¼ºÇóµ·ç¼í¹çµí;çÀ®, ùµ¹µóµáµáçÁ+ç®µ·óÈµµµ³µÈ; í
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+çµ;ON
0016 C. *****
0017 C.  ºÆÀ, Í×ËÝµáLOSµáµçµí»´Óµð¹íÍ, µ·; çÉÓÍ×µÈXÁÓONµí¹ÓµÈµíµáµµ³µÈ; í
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C.          çç[HK1_XPA_ON/OFF]              EQ      ON
0025 C.          çç[HK1_XPA_PWR_HI/LO]          EQ      HI
0026 C.          çç[HK1_XMOD_ON/OFF]            EQ      ON
0027 C.          çç[HK1_XMOD_QPSK/PM]           EQ      QPSK
0028 C.
0029 . C. XÝÐÝÓÝÉÝÍÝÁÝ~¾ÓÁÖµ·ºÆÀµ·çµ; çºÈ²¼µíºÆÀ, ¼È¼çµð¼Á¹Óµ¹µÈ; í
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼íºÆÀ,
0033 C. *****
0034 C.  º RESTART; ÈPT1; Èµ·çµç¼í¹çµí; çºÈ²¼µí¼Á¹Óµ»µº; çDCBC-150µðçÈµá; í
0035 C.
0036 . C. ;ãPT1ºÆÀ, ³«»Í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC          (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C.          çç[HK1_REP_PT_1/2]              EQ      PT1      (¼Á¹Ö, ;¼Ú)
0043 C.          çç[HK1_REP_STA/STP]            EQ      START   (¼Á¹Ö, ;¼Ú)
0044 C.          çç[HK1_X_VC4_ON/OFF]           EQ      ON       (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãÝçÝÓÝÉÝÈÀÙÁ; ÈÁ·ÁºóÈð; È, áµíºÆÀ, ºÆ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C.          çç[HK1_REP_PT_1/2]              EQ      PT1      (¼Á¹Ö, ;¼Ú)
0050 C.          çç[HK1_REP_STA/STP]            EQ      START   (¼Á¹Ö, ;¼Ú)
0051 C.          çç[HK1_X_VC4_ON/OFF]           EQ      ON       (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1ºÆÀ, µ·¼«ÆºÁá»ßµ·çµ; á; çºÈ²¼µð¼Á¹Óµ¹µÈ; í
0055 C.  ÝçÝÓÝÉÝÈÀÙÁóáÁ·ÁºóÈðµ·¼µç¼í¹çµíºÆÀºÆ³«; çDCBC-150µðçÈµá; í
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼íºÆÀ,
0059 C. *****
0060 C.  º RESTART; ÈPT2; Èµ·çµç¼í¹çµí; çºÈ²¼µí¼Á¹Óµ»µº; çDCBC-151µðçÈµá; í
0061 C.
0062 . C. ;ãPT2ºÆÀ, ³«»Í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC          (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C.          çç[HK1_REP_PT_1/2]              EQ      PT2      (¼Á¹Ö, ;¼Ú)
0069 C.          çç[HK1_REP_STA/STP]            EQ      START   (¼Á¹Ö, ;¼Ú)
0070 C.          çç[HK1_X_VC4_ON/OFF]           EQ      ON       (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãÝçÝÓÝÉÝÈÀÙÁ; ÈÁ·ÁºóÈð; È, áµíºÆÀ, ºÆ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C.          çç[HK1_REP_PT_1/2]              EQ      PT2      (¼Á¹Ö, ;¼Ú)
0076 C.          çç[HK1_REP_STA/STP]            EQ      START   (¼Á¹Ö, ;¼Ú)
0077 C.          çç[HK1_X_VC4_ON/OFF]           EQ      ON       (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DRºÆÀ, Áá»ß; çXÁ+çµ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDRºÆÀ, Áá»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C.          çç[HK1_REP_STA/STP]              EQ      STOP
0087 C.          çç[HK1_S_VC4_ON/OFF]            EQ      OFF
0088 C.          çç[HK1_X_VC4_ON/OFF]            EQ      OFF
0089 C.
0090 C.  ;ãXÁ+çµ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C.          çç[HK1_XMOD_ON/OFF]              EQ      OFF
0095 C.          çç[HK1_XPA_ON/OFF]              EQ      OFF
  
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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOYx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-239:OP
0104 ( )
0105 S. OG og-239:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOYx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOYx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOYx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½A´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOYx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²¬Á÷¿@NG²î½i¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²²ò¿;çSET²E²DUMP²îE±²îY¹²ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²YOYE²òðÁDî¿(UT)
0183 +. TI 2014-03-01 10:01:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2014-03-01 10:01:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2014-03-01 10:01:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2014-03-01 10:05:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          꺍꺍[HK1_TI_CMD_NUM]                EQ      1COUNTUP
0198 C.
0199 C. °È²¼ïîÄè%íîññîîŷÄŷŷÄŷ-¹àìŭ
0200 C.          꺍꺍[HK1_TI_CMD_ENA/DIS]            EQ      ENA
0201 C.          꺍꺍[HK1_TI_CMD_NUM]                EQ      4
0202 C.          꺍꺍[HK1_NEXT_EXEC_PIM]            EQ      DHU
0203 C.          꺍꺍[HK1_NEXT_EXEC_DC]            EQ      0xB3
0204 C.
0205 . C. *****
0206 C. TÎîî°èŷÄŷÔŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          꺍꺍[HK1_DMP_TOP_ADRS_1]            EQ      07
0213 C.          꺍꺍[HK1_DMP_TOP_ADRS_0]            EQ      2B
0214 C.          꺍꺍[HK1_DMP_BLOCK_NUM]            EQ      3
0215 C.          꺍꺍[HK1_DMP_REPEAT_NUM]          EQ      0
0216 C.          꺍꺍[HK1_DMA_DMP_PIM]            EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          꺍꺍[HK1_PKT_FORM_NO]            EQ      7
0220 C.          꺍꺍[HK1_PKT_GEN_TIME]            EQ      0.25 s
0221 C.          꺍꺍[HK1_S_TLM_BIT_RATE]          EQ      32k
0222 C.          꺍꺍[HK1_X_TLM_BIT_RATE]          EQ      4M
0223 C.          꺍꺍[HK1_DMP_CHK_FLG]            EQ      EXEC
0224 C.
0225 . C. ŷÄŷÔŷ×½ª î»ð³ îÇ§
0226 C.          꺍꺍[HK1_DMP_CHK_FLG]            EQ      NON
0227 C.
0228 . C. RAM ID=TI_TBLñî¾È¹ç·è²îOKð³ îÇ§
0229 C.
0230 . C. DHUŷâ;¼ŷÈ;È¾ŷ¼. ŷî;¼ŷÈ;Èððîãñ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          꺍꺍[HK1_PKT_FORM_NO]            EQ      2
0234 C.          꺍꺍[HK1_PKT_GEN_TIME]            EQ      0.5S
0235 C.          꺍꺍[HK1_S_TLM_BIT_RATE]          EQ      32K
0236 C.          꺍꺍[HK1_X_TLM_BIT_RATE]          EQ      4M
0237 C.
0238 . C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2014-03-01 10:05:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC          (21 02)
0247 +. TI 2014-03-01 10:05:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC          (22)
0250 . C.          [ ] [HK1_TI_CMD_NUM]        EQ      2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. SOT TI command set
0256 C. *****
0257 C. Execute, after the success of OP upload.
0258 +. TI 2014-03-01 10:05:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC          (41)
0261 . C. -----
0262 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0263 C. -----
0264 C. ***** SOT END *****
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2014-03-01 10:05:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC          (c3)
0271 . C.          [ ] [HK1_TI_CMD_NUM]        EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 . C. ***** MDP `ûÃîñî»ö¼ŷñèÄðñ¹èDCBC·x²è *****
0276 C. (¼ª°îŷÔŷÄŷÈŷŷŷÈŷ¼ŷçŷèè¾¼ññ¼Å»Ûñ¹è)
0277 . S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 . C. ***** ŷÐŷ¹·î Daily±çîññè'øñ¹èDCBC·x²è *****
0282 . S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 . C. ;ãLOSŷÄŷŷŷÄŷ-¼Å»Û;ã
0287 C.
0288 . C. ***** LOS *****
0289 C.

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(a) Spacecraft Operation Procedure (real-commands)

```
main-240 2014-03-01 13:28:55 82 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY~¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Èø¿øÅø·μ°E»Í×ÁÇøÍYçYÁY×Yí;¼YÉ;ÈÈ%μ·íÍÉ;ÈøE¼°ÇÔø·ø¿¼í¹çøÍ;çÀ®, ùø¹øÈøBøÇÁ+¿®ø·øÊøøø³øÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 . C. Verify EIS in MANUAL mode
0049 . C. Estimated OBSTBL upload time is 39s
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-820:EIS_OBSTBL
0054 ( )
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2014-03-01 10:05:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 . C. ***** MDP `ûÁîøî»ö¼YøÈÁø¹øÈDCBC·x²è *****
0069 C. (¼á°îYÓYÁYÈYB³YÉYáYçYÈøE¼°ÇÔ»Ûø¹øÈ)
0070 . S. DC-BC dcbc-402:DCBC
0071 (MDP_known_event)
0072 C.
0073 C.
0074 . C. ***** YDÝ¹·Ï Daily±¿ÎNøÈ´øø¹øÈDCBC·x²è *****
0075 . S. DC-BC dcbc-153:DCBC
0076 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077 C.
0078 C.
0079 . C. ;ãLOSÁY$YÁY~¼Á»Û;ã
0080 C.
0081 . C. ***** LOS *****
0082 C.
```



```

0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-263:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 C. *****
0120 C. SOT TI command set
0121 C. *****
0122 C. Execute, after the success of TBL upload.
0123 +. TI 2014-03-01 10:05:18.0
0124 DC 07-F0 MDP_SOT_MODE_OBSV
0125 BC (40)
0126 . C. -----
0127 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0128 C. -----
0129 C.
0130 C.
0131 C. ***** XRT START *****
0132 C.
0133 +. DC 07-F0 MDP_XRT_CTRL_MANU
0134 BC (c1)
0135 + DC 07-F0 MDP_XRT_MODE_STBY
0136 BC (c3)
0137 . C. ----- Success Verify ? OK / NG____
0138 C.
0139 C. XRT Obs. Table Upload
0140 . S. RAM ram-291:MDP_OBS_X
0141 ( )
0142 C.
0143 +. DC 07-F0 MDP_DUMP_XRTTBL
0144 BC (84 07 00 00 00 3a d4)
0145 . C. ----- Comparison Check ? OK / ERR ____
0146 C.
0147 C.
0148 +. DC 07-F0 MDP_XRT_ROI_SET
0149 BC (cd 01 b1 b1 04 04)
0150 + DC 07-F0 MDP_XRT_ROI_SET
0151 BC (cd 02 b1 b1 08 08)
0152 + DC 07-F0 MDP_XRT_ROI_SET
0153 BC (cd 03 b1 b1 08 08)
0154 + DC 07-F0 MDP_XRT_ROI_SET
0155 BC (cd 04 b1 b1 06 06)
0156 + DC 07-F0 MDP_XRT_ROI_SET
0157 BC (cd 05 85 83 06 06)
0158 + DC 07-F0 MDP_XRT_ROI_SET
0159 BC (cd 06 85 83 06 06)
0160 + DC 07-F0 MDP_XRT_ROI_SET
0161 BC (cd 07 80 80 20 20)
0162 + DC 07-F0 MDP_XRT_ROI_SET
0163 BC (cd 08 80 80 20 08)
0164 + DC 07-F0 MDP_XRT_ROI_SET
0165 BC (cd 09 80 80 08 20)
0166 + DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 0a 85 83 08 08)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 0f 80 80 06 06)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 10 80 80 08 08)
0172 + DC 07-F0 MDP_XRT_FLD_ENA
0173 BC (d8)
0174 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0175 BC (c8)
0176 + DC 07-F0 MDP_XRT_AEC_RESET
0177 BC (d0)
0178 + DC 07-F0 MDP_XRT_ARS_DIS
0179 BC (d5)
0180 + DC 07-F0 MDP_XRT_FLD_RESET
0181 BC (da)
0182 . C. ----- Success Verify ? OK / NG ____
0183 C.
0184 C.
0185 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0186 C.
0187 +. DC 07-F0 MDP_XRT_MODE_OBSV
0188 BC (c2)
0189 +. TI 2014-03-01 10:05:02.0
0190 DC 07-F0 MDP_XRT_MODE_OBSV
0191 BC (c2)
0192 . C. ----- Success Verify ? OK / NG ____
0193 C.

```

```
0194 C. ***** XRT END *****
0195 C.
0196 . C. ***** MDP 'úÃîï»ô¼ÝðËÂÐ¹æDCBC•x²è *****
0197 C. (¼ã°ïÿÓÿÃÿËÿPÿËÿáÿçÿèæ¼¼¼¼¼»Û¹æ)
0198 . S. DC-BC dcbc-402:DCBC
0199 (MDP_known_event)
0200 C.
0201 C.
0202 . C. ***** ÿÐÿ¹•ï Daily±¿îÑæ'Ø¹æDCBC•x²è *****
0203 . S. DC-BC dcbc-153:DCBC
0204 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0205 C.
0206 C.
0207 . C. ;ãLOSÿÁÿ§ÿÃÿ-¼Â»Û;ã
0208 C.
0209 . C. ***** LOS *****
0210 C.
```

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*** OP Sequence for XRT ***

2014/03/01	10:15:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/01	10:15:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/03/01	10:16:00.5	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	04 00 00 00 00
2014/03/01	10:16:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/03/01	10:16:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/03/01	10:16:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/03/01	10:16:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/03/01	10:16:24.0	XRT_FLD_RESET_431_OG [0x1af]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/01	10:16:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/01	10:16:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/01	10:16:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/01	10:18:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2014/03/01	10:18:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2014/03/01	10:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/03/01	10:26:00.0	XRT_Custom_430_OG [0x1ae]			
2014/03/01	10:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/01	14:06:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/01	14:06:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/01	14:06:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/01	14:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/03/01	14:11:30.0	XRT_Custom_430_OG [0x1ae]			
2014/03/01	14:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/01	15:38:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/01	15:38:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/01	15:38:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/01	15:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/03/01	16:02:00.0	XRT_Custom_430_OG [0x1ae]			
2014/03/01	16:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/01	17:15:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/01	17:15:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/01	17:15:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/01	17:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/03/01	17:38:30.0	XRT_Custom_430_OG [0x1ae]			
2014/03/01	17:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/01	17:55:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/01	17:55:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/03/01	17:56:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2014/03/01	17:56:16.0	XRT_FLD_DIS_434_OG [0x1b2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2014/03/01	17:58:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/03/01	17:58:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/03/01	17:58:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2014/03/01	17:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/01	18:05:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/01	18:05:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/03/01	18:06:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	04 00 00 00 00
2014/03/01	18:06:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/03/01	18:06:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/03/01	18:06:20.0	XRT_AEC_RESET_413_OG [0x19d]			

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2014/03/01	18:06:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
			MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/03/01	18:06:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/03/01	18:08:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09		
2014/03/01	18:08:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02		
2014/03/01	18:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/03/01	18:52:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/03/01	18:52:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/03/01	18:52:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/03/01	18:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/03/01	19:15:30.0	XRT_Custom_430_OG [0x1ae]							
2014/03/01	19:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/03/01	20:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/03/01	20:29:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/03/01	20:29:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/03/01	20:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/03/01	20:52:30.0	XRT_Custom_430_OG [0x1ae]							
2014/03/01	20:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/03/01	22:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/03/01	22:06:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/03/01	22:06:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/03/01	22:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/03/01	22:28:30.0	XRT_Custom_430_OG [0x1ae]							
2014/03/01	22:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/03/01	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/03/01	22:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2014/03/01	23:00:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	03	00	00	00
2014/03/01	23:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/03/01	23:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/03/01	23:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/03/01	23:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/03/01	23:00:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/03/01	23:02:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	03		
2014/03/01	23:02:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02		
2014/03/01	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/03/01	23:44:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/03/01	23:44:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/03/01	23:44:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/03/01	23:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/03/01	23:54:00.0	XRT_Custom_430_OG [0x1ae]							
2014/03/01	23:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/03/02	01:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/03/02	01:09:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/03/02	01:09:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/03/02	01:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/03/02	01:27:00.0	XRT_Custom_430_OG [0x1ae]							
2014/03/02	01:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/03/02	02:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/03/02	02:42:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/03/02	02:42:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							

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2014/03/02	02:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/03/02	03:03:30.0	XRT_Custom_430_OG [0x1ae]					
2014/03/02	03:04:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/03/02	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/03/02	03:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2014/03/02	04:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	04 00 00 00 00	
2014/03/02	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/03/02	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/03/02	04:00:20.0	XRT_AEC_RESET_413_OG [0x19d]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/03/02	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/03/02	04:00:24.0	XRT_FLD_RESET_431_OG [0x1af]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/03/02	04:02:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2014/03/02	04:02:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2014/03/02	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/03/02	04:12:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/03/02	04:12:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/03/02	04:12:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/03/02	04:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/03/02	04:41:00.0	XRT_Custom_430_OG [0x1ae]					
2014/03/02	04:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/03/02	05:52:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/03/02	05:52:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/03/02	05:52:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/03/02	05:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/03/02	06:18:30.0	XRT_Custom_430_OG [0x1ae]					
2014/03/02	06:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/03/02	06:28:24.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/03/02	06:28:26.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/03/02	06:28:30.0	AOCS_ORe-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2014/03/02	06:28:46.0	XRT_FLD_DIS_434_OG [0x1b2]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/03/02	06:31:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/03/02	06:31:26.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/03/02	06:31:28.0	XRT_QT_PROG_SET_447_OG [0x1bf]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2014/03/02	06:31:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/03/02	06:38:24.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/03/02	06:38:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2014/03/02	06:38:30.0	AOCS_ORe-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	04 00 00 00 00	
2014/03/02	06:38:46.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/03/02	06:38:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/03/02	06:38:50.0	XRT_AEC_RESET_413_OG [0x19d]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/03/02	06:38:52.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/03/02	06:38:54.0	XRT_FLD_RESET_431_OG [0x1af]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/03/02	06:41:26.0	XRT_QT_PROG_SET_429_OG [0x1ad]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2014/03/02	06:41:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2014/03/02	06:41:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/03/02	07:33:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/03/02	07:33:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/03/02	07:33:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					

2014/03/02	07:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/03/02	07:55:30.0	XRT_Custom_430_OG [0x1ae]					
2014/03/02	07:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/03/02	09:12:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/03/02	09:12:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/03/02	09:12:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/03/02	09:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/03/02	09:31:00.0	XRT_Custom_430_OG [0x1ae]					
2014/03/02	09:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/03/02	11:29:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/03/02	11:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2014/03/02	11:30:00.0	AOCS_OrE-point_Start_4_OG [0x09a]					
			AOCU_NM	5	02-76	01 00 00 00 00	
2014/03/02	11:30:16.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/03/02	11:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/03/02	11:30:20.0	XRT_AEC_RESET_413_OG [0x19d]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/03/02	11:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/03/02	11:30:24.0	XRT_FLD_RESET_431_OG [0x1af]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/03/02	11:32:56.0	XRT_QT_PROG_SET_406_OG [0x196]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 07	
2014/03/02	11:32:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 02	
2014/03/02	11:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/03/02	13:29:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/03/02	14:39:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/03/02	14:39:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/03/02	14:39:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/03/02	14:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/03/02	16:14:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/03/02	16:14:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/03/02	16:14:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/03/02	16:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/03/02	17:51:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/03/02	17:51:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/03/02	17:51:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/03/02	17:54:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/03/02	18:14:24.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/03/02	18:14:26.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/03/02	18:14:30.0	AOCS_OrE-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2014/03/02	18:14:46.0	XRT_FLD_DIS_434_OG [0x1b2]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/03/02	18:17:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/03/02	18:17:26.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/03/02	18:17:28.0	XRT_QT_PROG_SET_447_OG [0x1bf]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2014/03/02	18:17:30.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/03/02	18:24:24.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/03/02	18:24:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2014/03/02	18:24:30.0	AOCS_OrE-point_Start_1_OG [0x097]					
			AOCU_NM	5	02-76	04 00 00 00 00	
2014/03/02	18:24:46.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/03/02	18:24:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/03/02	18:24:50.0	XRT_AEC_RESET_413_OG [0x19d]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	

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2014/03/02	18:24:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/03/02	18:24:54.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/02	18:27:26.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	09			
2014/03/02	18:27:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2014/03/02	18:27:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/02	19:28:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/02	19:28:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/02	19:28:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/02	19:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/02	19:51:30.0	XRT_Custom_430_OG [0x1ae]							
2014/03/02	19:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/02	21:05:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/02	21:05:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/02	21:05:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/02	21:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/02	21:28:30.0	XRT_Custom_430_OG [0x1ae]							
2014/03/02	21:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/02	22:43:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/02	22:43:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/02	22:43:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/02	22:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/02	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/02	22:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/03/02	23:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	03	00	00	00	00
2014/03/02	23:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/03/02	23:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/03/02	23:00:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/03/02	23:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/03/02	23:00:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/02	23:02:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2014/03/02	23:02:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2014/03/02	23:03:30.0	XRT_Custom_430_OG [0x1ae]							
2014/03/02	23:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/03	00:20:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/03	00:20:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/03	00:20:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/03	00:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/03	00:27:30.0	XRT_Custom_430_OG [0x1ae]							
2014/03/03	00:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/03	01:43:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/03	01:43:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/03	01:43:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/03	01:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/03	02:03:00.5	XRT_Custom_430_OG [0x1ae]							
2014/03/03	02:04:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/03	03:18:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/03	03:18:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/03	03:18:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/03	03:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				

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2014/03/03	03:40:00.0	XRT_Custom_430_OG [0x1ae]			
2014/03/03	03:41:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/03	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/03	03:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/03/03	04:00:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	04 00 00 00 00
2014/03/03	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/03/03	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/03/03	04:00:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/03/03	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/03/03	04:00:24.0	XRT_FLD_RESET_431_OG [0x1af]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/03	04:02:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2014/03/03	04:02:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2014/03/03	04:03:00.5	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/03	04:49:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/03	04:49:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/03	04:49:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/03	04:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/03/03	05:17:30.0	XRT_Custom_430_OG [0x1ae]			
2014/03/03	05:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/03	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/03	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/03/03	06:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2014/03/03	06:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2014/03/03	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/03/03	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/03/03	06:02:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2014/03/03	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/03	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/03	06:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/03/03	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	04 00 00 00 00
2014/03/03	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/03/03	06:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/03/03	06:10:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/03/03	06:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/03/03	06:10:24.0	XRT_FLD_RESET_431_OG [0x1af]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/03	06:12:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2014/03/03	06:12:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2014/03/03	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/03	06:30:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/03	06:30:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/03	06:30:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/03	06:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/03/03	06:55:00.0	XRT_Custom_430_OG [0x1ae]			
2014/03/03	06:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/03	08:10:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/03	08:10:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/03	08:10:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/03	08:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2014/03/03	20:04:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/03	20:04:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/03/03	20:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/03/03	21:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/03/03	21:42:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/03	21:42:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/03/03	21:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/03/03	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/03/03	22:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/03	23:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/03/03	23:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00		
2014/03/03	23:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/03/03	23:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/03/03	23:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/03/03	23:00:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/03/03	23:02:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/03/03	23:02:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2014/03/03	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
2014/03/03	23:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/03/03	23:19:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/03	23:19:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/03/03	23:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/03/03	23:32:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/03/03	23:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]					
2014/03/04	00:48:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/03/04	00:48:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/04	00:48:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/03/04	00:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/03/04	01:02:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/03/04	01:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]					
2014/03/04	02:18:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/03/04	02:18:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/04	02:18:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/03/04	02:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/03/04	02:39:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/03/04	02:40:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]					
2014/03/04	03:46:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/03/04	03:46:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/04	03:46:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/03/04	03:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/03/04	04:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/03/04	05:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	04 00 00 00 00		
2014/03/04	05:27:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/04	05:27:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/03/04	05:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/03/04	06:03:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/03/04	06:03:26.0	XRT_FOCUS_RECALIBRATE_433_OG [0x1b1]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/04	06:03:30.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_RECAL	2	07-F8	78 00		

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2014/03/04	06:07:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	AOCU_NM	5	02-76	00	00	00	00	00
		XRT_FOCUS_POSITION		4	07-F8	22	ff	aa	00	
2014/03/04	06:07:46.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/03/04	06:10:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/03/04	06:10:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/03/04	06:10:28.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2014/03/04	06:10:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/04	06:13:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/04	06:13:30.0	AOCS_ORe-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	04	00	00	00	00
2014/03/04	07:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/04	07:07:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/04	07:07:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/04	07:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/04	08:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/04	08:47:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/04	08:47:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/04	08:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/04	10:27:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/04	10:28:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00	00