

XRT Timeline to be uploaded on 2014/03/04

Period: 2014/03/04 10:18:00 - 2014/03/08 09:27:00

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Normal mode

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XOB #1A00: AR Standard-B(Morphology), Be/thin 5min-cad, multifilter PFB (Al/poly, Be/thin, Be/med), multifilter context, 384x384, 1064x1048, G-band (33r)												
Term	Pointing (x, y)	Comment										
03/04 10:36:00 - 03/04 12:59:54	Track (45.5, -301.2) @ 03/04 10:28:00	# OP start + 10min AR 11991										
03/04 14:27:30 - 03/04 18:05:54	Track (77.9, -301.5) @ 03/04 14:24:30	AR 11991										
03/05 04:03:00 - 03/05 05:36:24	Track (188.4, -303.5) @ 03/05 04:00:00	AR11991										
PROG= 09 Inf.-time(s)												
┌ Subr= 1 1-time(s) 2.0sec												
├ Seqn= 8 1-time(s) 2.0sec												
│ Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
├ Seqn= 24 1-time(s) 2.0sec												
│ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
│ Open/G-band Open/G-band open Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└ Subr= 2 2-time(s) 2.0sec												
├ Seqn= 16 2-time(s) 2.0sec												
│ Open/Ti-poly Open/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
│ Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
│ Al-poly/Open thin-Be/Open close Safe Norm 177ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
│ C-poly/Open C-poly/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
│ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
│ med-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
│ med-Al/Open Open/thick-Al close Safe Norm 2.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
├ Seqn= 20 12-time(s) 300.0sec												
│ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 13.0sec												
│ Al-poly/Open thin-Be/Open close Safe Norm 177ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 13.0sec												
│ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 13.0sec												
│ med-Be/Open Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 13.0sec												
│ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 13.0sec												
│ Al-poly/Open thin-Be/Open close Safe Norm 177ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 13.0sec												
│ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 13.0sec												
│ med-Be/Open Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 13.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #19EE: AR Standard-B(Morphology), Be/thin 48s-cad, multifilter PFB (Al/poly, Be/thin, Be/med), multifilter context, 384x384, 1064x1048, G-band (33r)												
Term	Pointing (x, y)	Comment										
03/04 13:03:00 - 03/04 14:16:00	Track (319.2, -135.3) @ 03/04 13:00:00	IHOP 250 sunspot										
03/04 23:03:00 - 03/05 03:59:54	Track (443.6, -128.9) @ 03/04 23:00:00	IHOP 246 hot plasma										
03/05 23:14:30 - 03/06 04:19:54	Track (621.3, -144.3) @ 03/05 23:00:00	IHOP 246 hot plasma										
PROG= 14 Inf.-time(s)												
┌ Subr= 1 1-time(s) 2.0sec												
├ Seqn= 8 1-time(s) 2.0sec												
│ Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec												
├ Seqn= 24 1-time(s) 2.0sec												
│ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
│ Open/G-band Open/G-band open Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec												
└ Subr= 2 2-time(s) 2.0sec												
├ Seqn= 16 2-time(s) 2.0sec												
│ Open/Ti-poly Open/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
│ Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
│ Al-poly/Open thin-Be/Open close Safe Norm 177ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
│ C-poly/Open C-poly/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
│ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
│ med-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
│ med-Al/Open Open/thick-Al close Safe Norm 2.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec												
├ Seqn= 29 50-time(s) 2.0sec												
│ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 6.0sec												
│ Al-poly/Open thin-Be/Open close Safe Norm 177ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 6.0sec												
│ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 6.0sec												
│ med-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 3 6.0sec												
│ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 6.0sec												
│ Al-poly/Open thin-Be/Open close Safe Norm 177ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 6.0sec												
│ thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 6.0sec												
│ med-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 6.0sec												
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval												

XOB #19FE: Synoptic Q95 2x2 - Al/mesh(3/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(8/362/1443) + Thin												
Term	Pointing (x, y)	Comment										
03/04 18:09:00 - 03/04 18:15:54	Fixed (0.0, 0.0)	synoptic, shifted 6.0 min										
03/05 05:39:30 - 03/05 05:46:24	Fixed (0.0, 0.0)	synoptic, shifted -23.5 min										
03/05 17:42:00 - 03/05 17:48:54	Fixed (0.0, 0.0)	synoptic, shifted -21.0 min										
03/06 04:23:00 - 03/06 04:29:54	Fixed (0.0, 0.0)	synoptic, shifted manually										
PROG= 17 1-time(s)												
┌ Subr= 1 1-time(s) 12.0sec												

Seqn= 35 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	3ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 72 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 67 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19C3: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 45ms-2												
Term		Pointing (x, y)						Comment				
03/05 08:13:30 - 03/05 17:38:54		Fixed (-15.0, -960.0)						HOP81 S-pole day 1				
PROG= 18 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 13 2-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 30 1-time(s) 30.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Subr= 3 30-time(s) 2.0sec												
Seqn= 57 1-time(s) 60.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * * **Flare mode** * * * * *

XOB #19F0: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops-lowered-gband												
Term		Pointing (x, y)						Comment				
03/04 10:36:00 - 03/04 12:59:54		Track (45.5, -301.2) @ 03/04 10:28:00						# OP start + 10min AR 11991				
03/04 13:03:00 - 03/04 14:16:00		Track (319.2, -135.3) @ 03/04 13:00:00						IHOP 250 sunspot				
03/04 14:27:30 - 03/04 18:05:54		Track (77.9, -301.5) @ 03/04 14:24:30						AR 11991				
03/04 23:03:00 - 03/05 03:59:54		Track (443.6, -128.9) @ 03/04 23:00:00						IHOP 246 hot plasma				
03/05 04:03:00 - 03/05 05:36:24		Track (188.4, -303.5) @ 03/05 04:00:00						AR11991				
03/05 08:13:30 - 03/05 17:38:54		Fixed (-15.0, -960.0)						HOP81 S-pole day 1				
03/05 23:14:30 - 03/06 04:19:54		Track (621.3, -144.3) @ 03/05 23:00:00						IHOP 246 hot plasma				
PROG= 02 12-time(s)												
Subr= 1 45-time(s) 10.0sec												
Seqn= 9 1-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 10.0sec												
Seqn= 17 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * * **Active Region Search** * * * * *

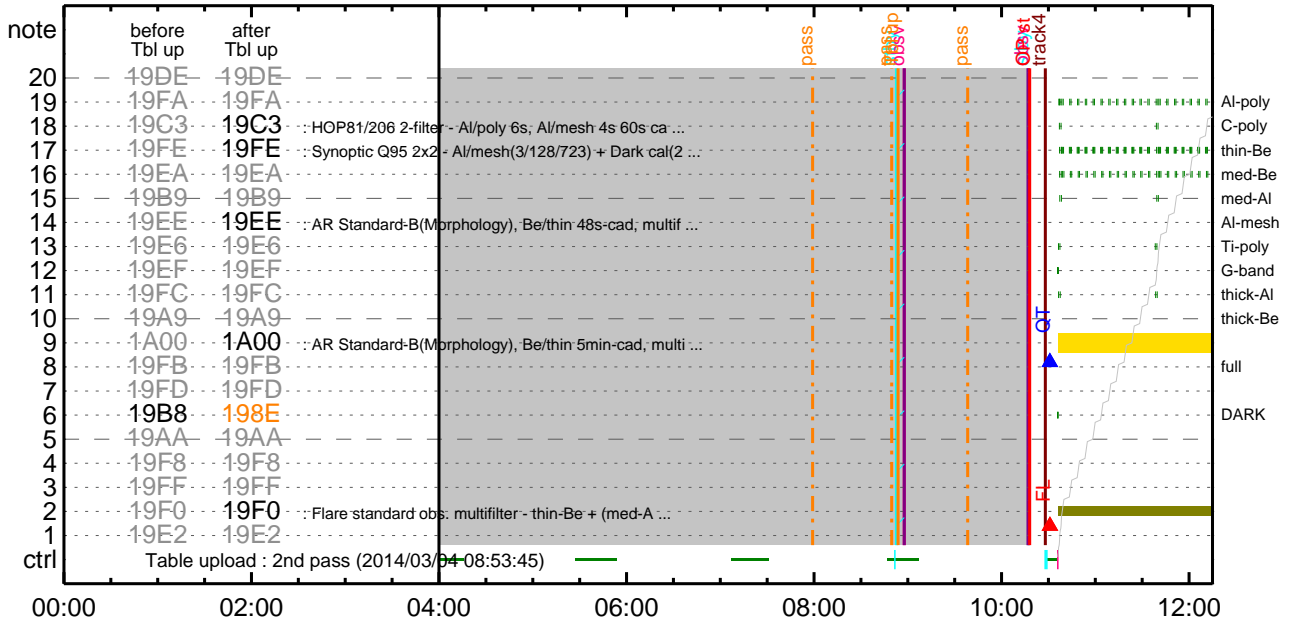
NOT USED

* * * * * **Flare Detection** * * * * *

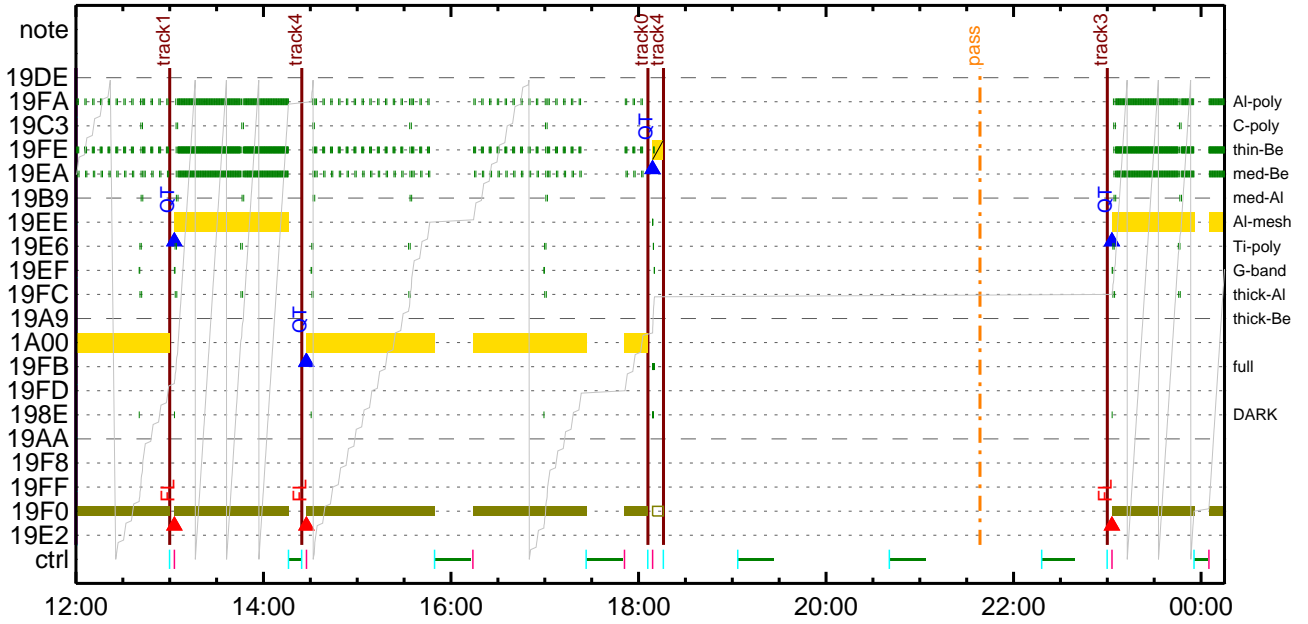
FLD Patrol												
Term		Pointing (x, y)						Comment				
03/04 23:00:16 - 03/05 05:36:46		Track (443.6, -128.9) @ 03/04 23:00:00						IHOP 246 hot plasma				
03/05 08:10:46 - 03/05 17:39:16		Fixed (-15.0, -960.0)						HOP81 S-pole day 1				
03/05 23:00:16 - 03/06 04:20:16		Track (621.3, -144.3) @ 03/05 23:00:00						IHOP 246 hot plasma				

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

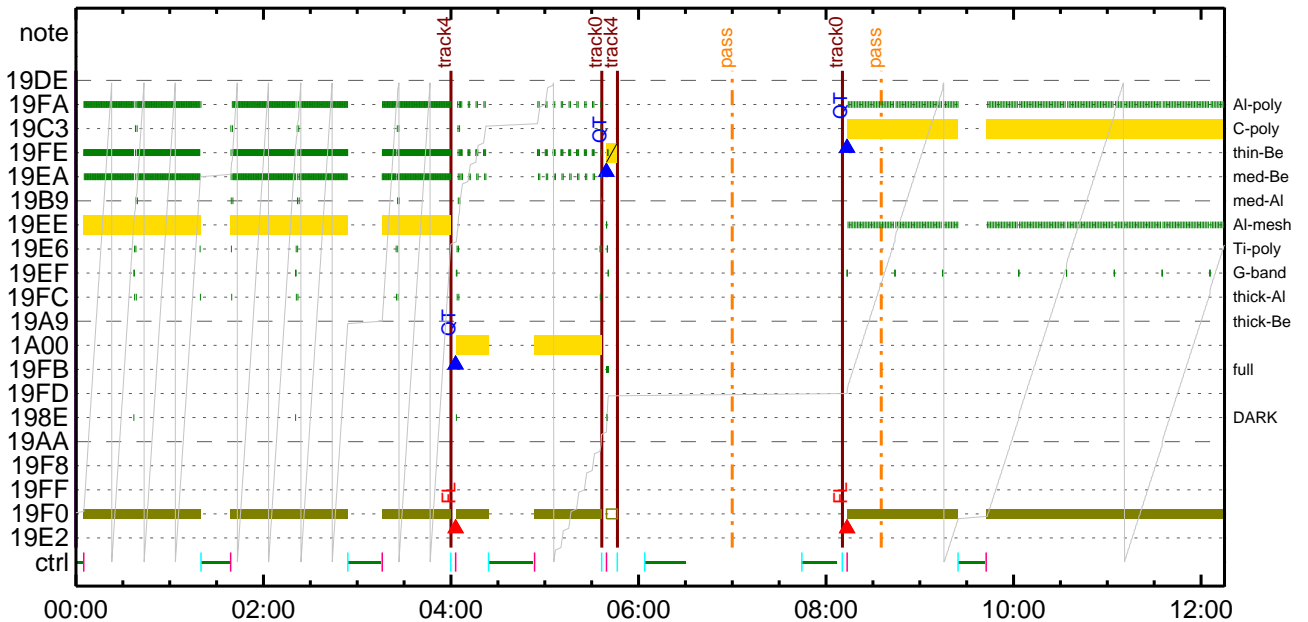
CMDI #0128 2014/03/04



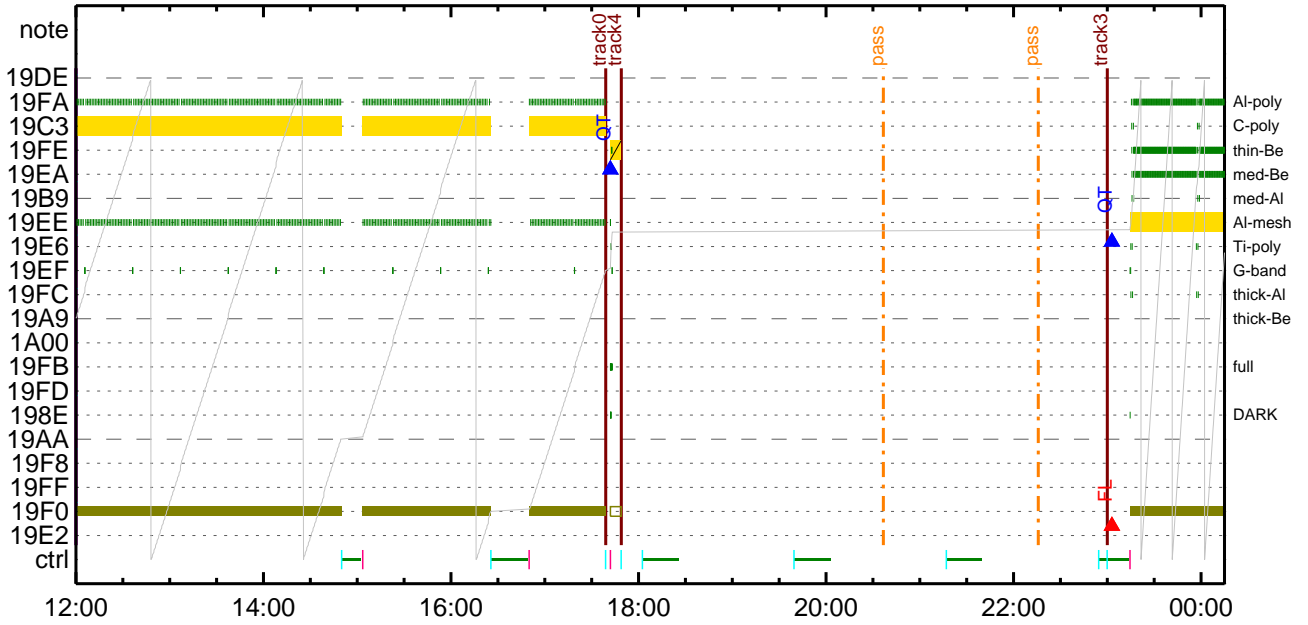
CMDI #0128 2014/03/04



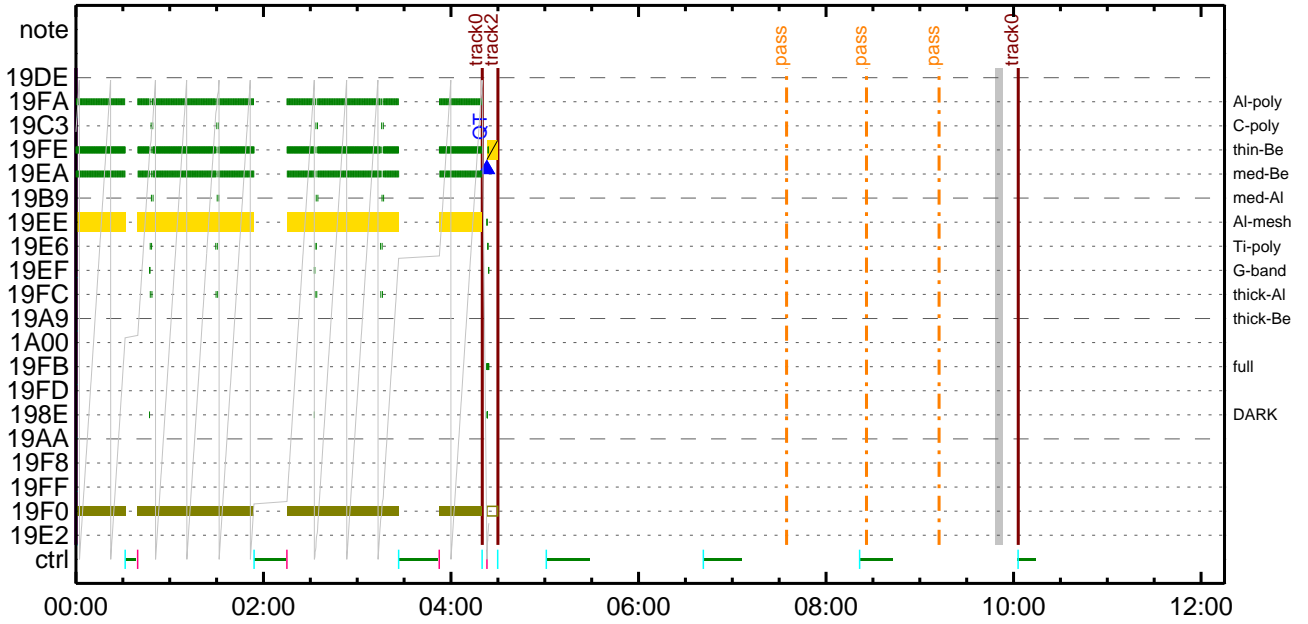
CMDI #0128 2014/03/05



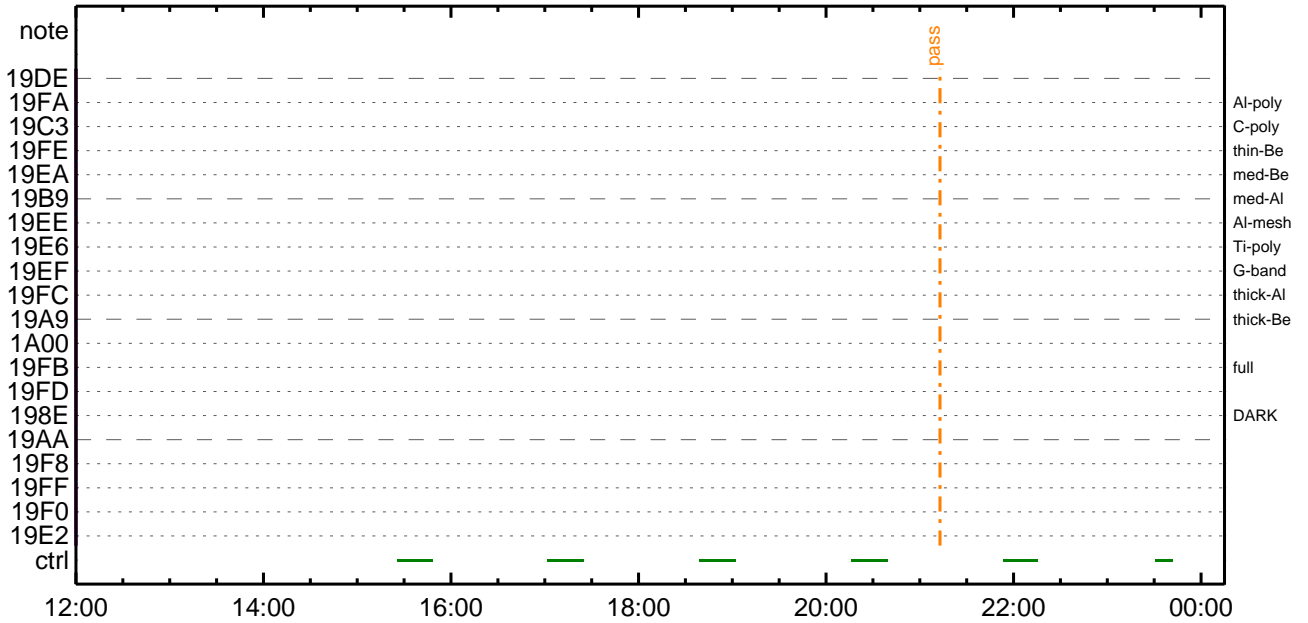
CMDI #0128 2014/03/05



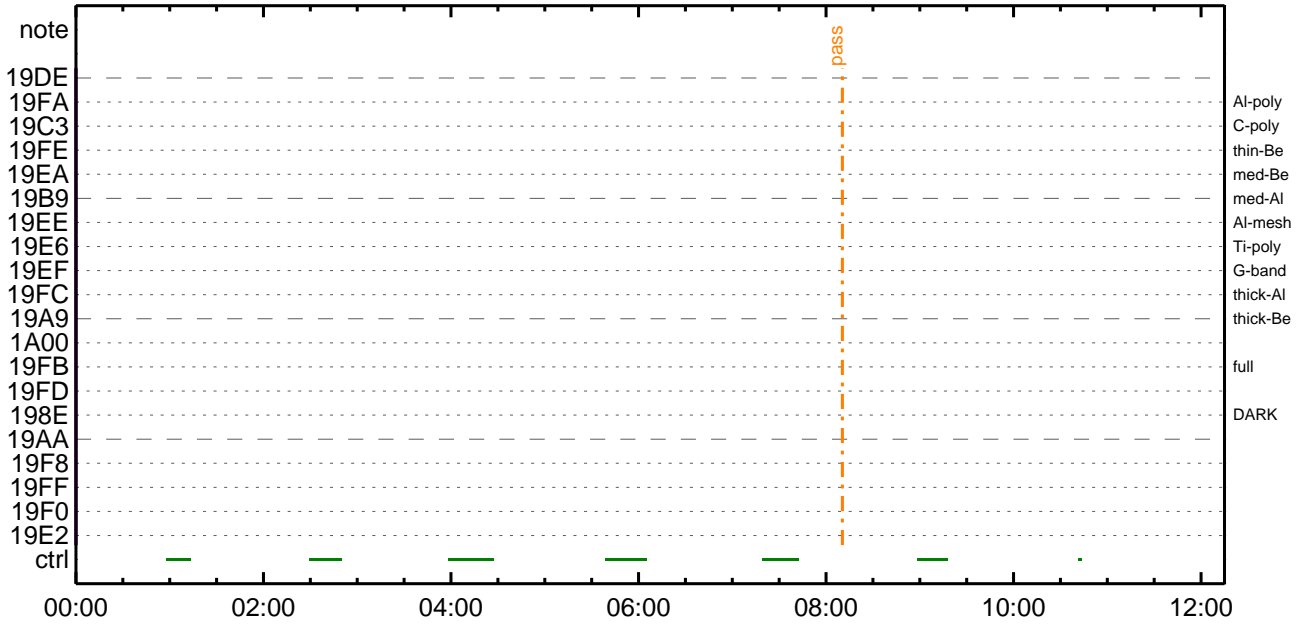
CMDI #0128 2014/03/06



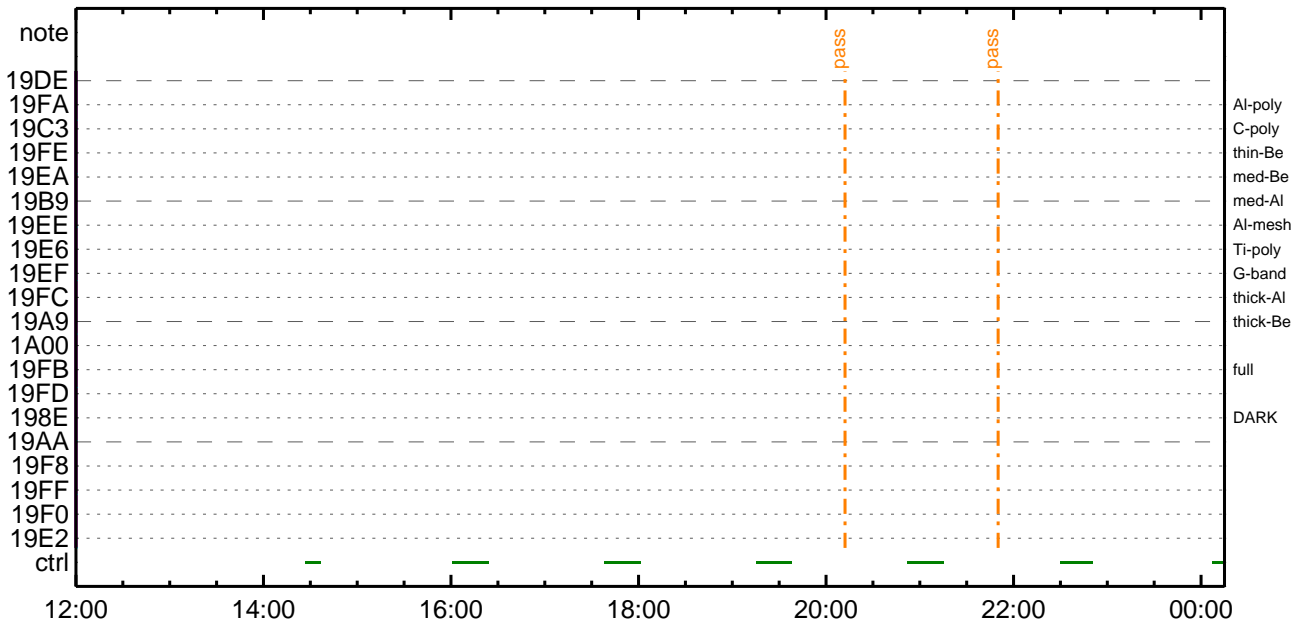
CMDI #0128 2014/03/06



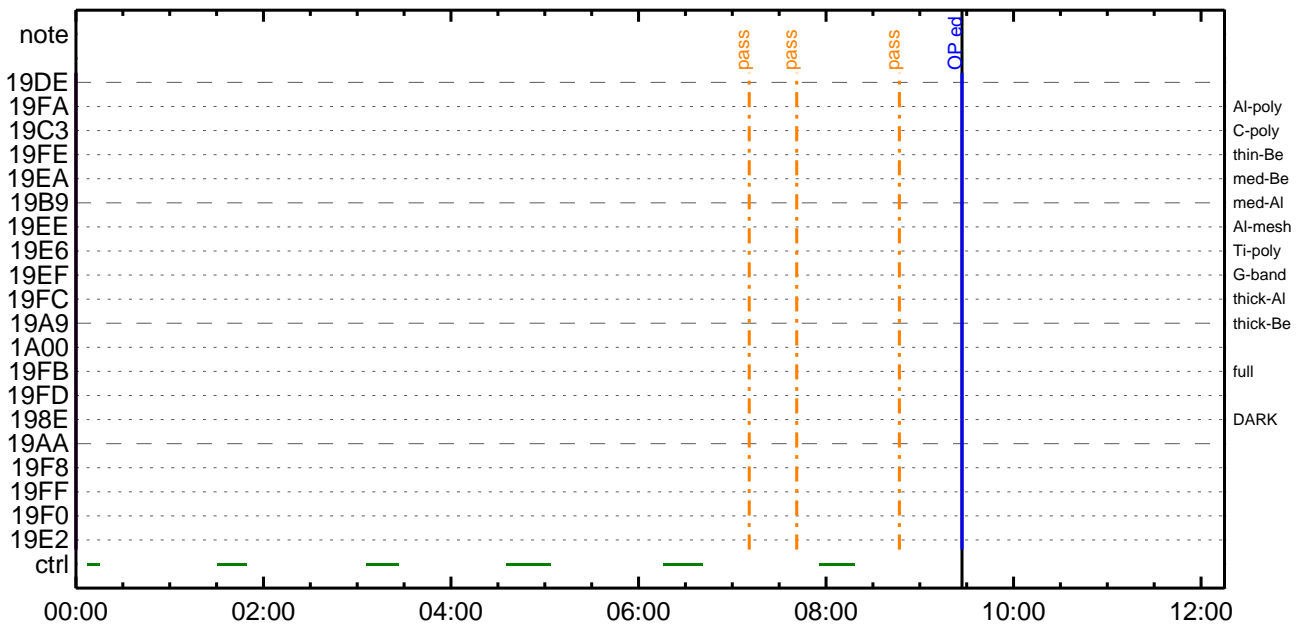
CMDI #0128 2014/03/07



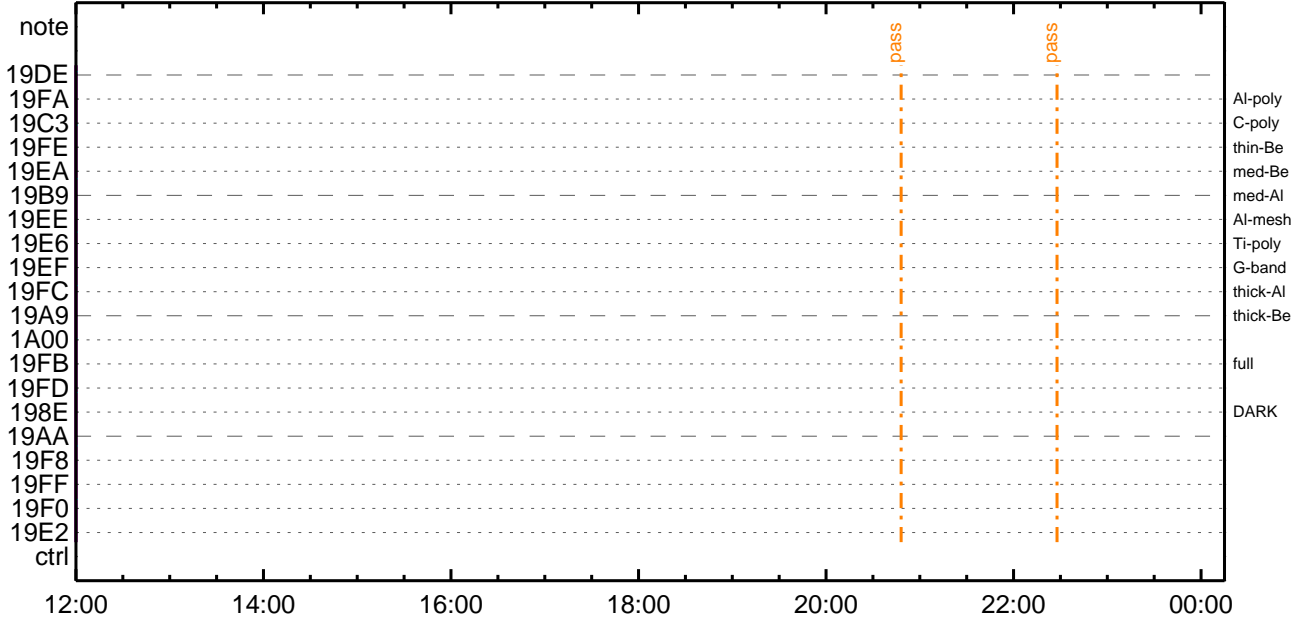
CMDI #0128 2014/03/07



CMDI #0128 2014/03/08



CMDI #0128 2014/03/08



0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YEi;4AY6Yx
0100 C. *****
0101 C.
0102 . C. ;ãOP/OGY1;4YEi;ã
0103 . S. OP op-244:OP
0104 ()
0105 . S. OG og-244:OG
0106 ()
0107 C.
0108 . C. ;ãNMOG&OPfî°è¥ÅY6Yx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 . C. ¥ÅY6Yx½ªî»ð³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 . C. RAM ID=NMOGåî½ª¹ç.ë²îOKð³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 . C. ¥ÅY6Yx½ªî»ð³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 . C. RAM ID=NMOGåî½ª¹ç.ë²îOKð³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 . C. ¥ÅY6Yx½ªî»ð³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 . C. RAM ID=NMOG, RAM ID=OPåî½ª¹ç.ë²îOKð³îÇ§
0165 C.
0166 . C. ***** öÊ²¼ðî½ª¹ç.ë²îOKð³îÇ§ *****
0167 C. DHU¥ã;½YÊ;Ê½Y½;¥i;½YÊ;Ëððîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 . C. NOTICE ; § OPOG UPLOADå-Á÷ç@NGåî½ª¹ç;ç°Ê²¼ðî½ª¹ç@åî½ª¹ç.ë²îOKð³îÇ§; §
0180 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0181 C.
0182 . C. TI¥³¥P¥6YÊðððîã¹ç(UT)
0183 +. TI 2014-03-04 10:13:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2014-03-04 10:13:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2014-03-04 10:13:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

(a) Spacecraft Operation Procedure (real-commands)

```
main-245 2014-03-04 14:38:06 156 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÄY~¼Á»Û;ã
0005 C.
0006 C. YÄY$;¼Y³YFÝÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿òÁò•µ°È»Í×ÁÇóÍY¿YÄY×Yí;¼YÉ;ÈÈ%µ•íÉ;ÈÈ¼°ÇÖò•ò¿¼l¹çòÍ;çÄ®, ùò¹òÈòÈòçÄ+¿®ò•òÈòòò³òÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_ST$1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. ***** AOCS Commands (Orbital Element Update) *****
0046 C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048 BC (16)
0049 + DC 02-8E AOCU_ORB_UPD
0050 C.
0051 C. <A_ORB>[ORBIT] EPC = 8100316.9 +- 1.0 (s) [ ]
0052 C.
0053 . C.
0054 C.
0055 C. ***** XRT START *****
0056 C.
0057 +. DC 07-F0 MDP_XRT_CTRL_MANU
0058 BC (c1)
0059 + DC 07-F0 MDP_XRT_MODE_STBY
0060 BC (c3)
0061 . C. ----- Success Verify ? OK / NG____
0062 C.
0063 C. XRT Obs. Table Upload
0064 . S. RAM ram-291:MDP_OBS_X
0065 ( )
0066 C.
0067 +. DC 07-F0 MDP_DUMP_XRTTBL
0068 BC (84 07 00 00 00 3a d4)
0069 . C. ----- Comparison Check ? OK / ERR ____
0070 C.
0071 C.
0072 +. DC 07-F0 MDP_XRT_ROI_SET
0073 BC (cd 01 b1 b1 04 04)
0074 + DC 07-F0 MDP_XRT_ROI_SET
0075 BC (cd 02 b1 b1 08 08)
0076 + DC 07-F0 MDP_XRT_ROI_SET
0077 BC (cd 03 b1 b1 08 08)
0078 + DC 07-F0 MDP_XRT_ROI_SET
0079 BC (cd 04 b1 b1 06 06)
0080 + DC 07-F0 MDP_XRT_ROI_SET
0081 BC (cd 05 85 83 06 06)
0082 + DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 06 85 83 06 06)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 07 80 80 20 20)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 08 80 80 20 08)
0088 + DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 09 80 80 08 20)
0090 + DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 0a 85 83 08 08)
0092 + DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 0f 80 80 06 06)
0094 + DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 10 80 80 08 08)
```

```

0096 + DC 07-F0 MDP_XRT_FLD_ENA
0097 BC (d8)
0098 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0099 BC (c8)
0100 + DC 07-F0 MDP_XRT_AEC_RESET
0101 BC (d0)
0102 + DC 07-F0 MDP_XRT_ARS_DIS
0103 BC (d5)
0104 + DC 07-F0 MDP_XRT_FLD_RESET
0105 BC (da)
0106 . C. ----- Success Verify ? OK / NG ____
0107 C.
0108 C.
0109 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0110 C.
0111 +. DC 07-F0 MDP_XRT_MODE_OBSV
0112 BC (c2)
0113 +. TI 2014-03-04 10:17:02.0
0114 DC 07-F0 MDP_XRT_MODE_OBSV
0115 BC (c2)
0116 . C. ----- Success Verify ? OK / NG ____
0117 C.
0118 C. ***** XRT END *****
0119 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0120 +. DC 07-FC EIS_MODE_MANU
0121 BC (21 02)
0122 . C. Verify EIS in MANUAL mode
0123 . C. Estimated OBSTBL upload time is 16s
0124 C. *****
0125 C. EIS START OBSTBL LOAD
0126 C. *****
0127 . S. RAM ram-820:EIS_OBSTBL
0128 ( )
0129 +. DC 07-FC EIS_DUMP_OBSTBL
0130 BC (07 07 07 00 00 70 00)
0131 C.
0132 C. Execute, after the success of OBSTBL upload.
0133 C. Set EIS TI-commands
0134 +. TI 2014-03-04 10:17:50.0
0135 DC 07-FC EIS_MODE_CHG_ENA
0136 BC (20)
0137 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0138 C. *****
0139 C. EIS END OBSTBL LOAD
0140 C. *****
0141 C.
0142 . C. ***** MDP `ûÃîñî»ô%ÿñÊÃð¹ñèDCBC•x²è *****
0143 C. (%ã°îÿÓÿÃÿÈÿPÿËÿãÿçÿèñE%¼ñ¼Ã»Ûñ¹ñè)
0144 . S. DC-BC dcbc-402:DCBC
0145 (MDP_known_event)
0146 C.
0147 C.
0148 . C. ***** ÿÐÿ¹.Ï Daily±;îÑñÊ´Øñ¹ñèDCBC•x²è *****
0149 . S. DC-BC dcbc-153:DCBC
0150 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0151 C.
0152 C.
0153 . C. ;ãLOSÿÃÿSÿÿÃÿ~¼Ã»Û;ã
0154 C.
0155 . C. ***** LOS *****
0156 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-246 2014-03-04 14:38:06 187 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY-¼Á»Û;ã
0005 C.
0006 C. YÀYß;¼Y³YFÿÖYÉÁ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;ËççµÁµ•µ°Æ»í×ÁÇçíYçYÄY×Yí;¼YÉ;ËÈÈµ•ííÉ;ËµÈ¼°ÇÖµ•µç¼l¹ççí;çÀ®, ùµ¹µÈµµçÁ+ç®µ•µÈµµµ³µÈ; f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+çµ;ON
0016 C. *****
0017 C. ç" °ÆÀ, í×ËYµáLOSµµççí»p´Öµç¹íí, µ•; çÉÖí×µÈXÁÖONµí¹ÖµÈµíµÈµµµ³µÈ; f
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XYDÿÖYÉYíYÄY-¾ÖÁÖµ-°ÁÁêµ•µçç; ç°È²¼µí°ÆÀ, ¼È½çµç¼Á¹Öµ¹µç; f
0030 C.
0031 . C. *****
0032 C. DR PT1 Áí¼í°ÆÀ,
0033 C. *****
0034 C. ç" RESTART;ËPT1;Ëµ•µçç¼l¹ççí; ç°È²¼µí°ÆÀ¹Öµ»µ°; çDCBC-150µççÈµç; f
0035 C.
0036 . C. ;ãPT1°ÆÀ, ³«»í;ã
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYÖYÆYÈÁÚÁÖ;ËÁ•Á°²öÈð;Ë, áµí°ÆÀ, °Æ³«;ã
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Á¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÀ, µ-¼«Æ°Áá»ßµ•µçç, á; ç°È²¼µç¼Á¹Öµ¹µç; f
0055 C. YçYÖYÆYÈÁÚÁÖµÁ•Á°²öÈðµ-¶áµµ¼l¹ççí°í»µ¹µÈµµµçÁÖµÁ; f
0056 C.
0057 . C. *****
0058 C. DR PT2 Áí¼í°ÆÀ,
0059 C. *****
0060 C. ç" RESTART;ËPT2;Ëµ•µçç¼l¹ççí; ç°È²¼µí°ÆÀ¹Öµ»µ°; çDCBC-151µççÈµç; f
0061 C.
0062 . C. ;ãPT2°ÆÀ, ³«»í;ã
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYÖYÆYÈÁÚÁÖ;ËÁ•Á°²öÈð;Ë, áµí°ÆÀ, °Æ³«;ã
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Á¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Á¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Á¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÀ, Áá»ß;çXÁ+çµ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÀ, Áá»ß;ã
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+çµ;OFF;ã
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

```

0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-264:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Stop SP table >
0120 +. DC 07-F0 MDP_SP_CTRL_MANU
0121 BC (61)
0122 C. -----
0123 C. MDP_SP_CTRL_MODE = MANU [ ]
0124 C. -----
0125 C.
0126 . C. <Upload SP Observation Table>
0127 . S. RAM ram-287:MDP_OBS_S
0128 ( )
0129 C.
0130 . C. < Dump RAMID=MDP_OBS_S >
0131 +. DC 07-F0 MDP_DUMP_SPTBL
0132 BC (83 07 00 00 00 38 b8)
0133 C. -----
0134 C. MDP_OBS_S verify = OK/NG [ ]
0135 C. -----
0136 C.
0137 . C. < Upload DPL table >
0138 C.
0139 C. ¥çYÃ¥×¥í;¼¥ÉñîÁ°ñÈSTS_CHKñðOFFñÈñ¹ñè
0140 C.
0141 . S. RAM ram-271:MDP_DPL
0142 ( )
0143 C.
0144 . C. < Dump RAMID=MDP_DPL >
0145 +. DC 07-F0 MDP_DUMP_FGTBL
0146 BC (82 07 00 38 b8 00 40)
0147 C. -----
0148 C. MDP_DPL verify = OK [ ]
0149 C. -----
0150 C.
0151 C. STS_CHKñðONñÈñ¹ñè
0152 C.
0153 . C. < Update MDP DSC PAR1 >
0154 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0155 BC (4c)
0156 C. MDP_CMD_CODE = F04C0700[ ]
0157 C. MDP_CMD_CNT (count-up 1) [ ]
0158 C. -----
0159 C.
0160 . C.
0161 C. *****
0162 C. SOT TI command set
0163 C. *****
0164 C. Execute, after the success of TBL upload.
0165 +. TI 2014-03-04 10:17:18.0
0166 DC 07-F0 MDP_SOT_MODE_OBSV
0167 BC (40)
0168 C. -----
0169 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0170 C. -----
0171 C.
0172 C.
0173 C. ***** MDP `úÃîñî»ö¼YñÈÁñ¹ñèDCBC•x²è *****
0174 C. (¼ã°îYÓYÃYÈY¥YÈYáYçYèñÈ¼ññ¼Á»Ûñ¹ñè)
0175 . S. DC-BC dcbc-402:DCBC
0176 (MDP_known_event)
0177 C.
0178 C.
0179 . C. ***** YÐY¹•î Daily±¼îññÈ´Øñ¹ñèDCBC•x²è *****
0180 . S. DC-BC dcbc-153:DCBC
0181 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0182 C.
0183 C.
0184 . C. ;ãLOS¥Á¥S¥Ã¥-¼Á»Û;ã
0185 C.
0186 . C. ***** LOS *****
0187 C.

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Mar 04, 14 14:38

XRT_OGLIST_0128.chk

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*** OP Sequence for XRT ***

2014/03/04	10:27:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/04	10:27:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/03/04	10:28:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	04 00 00 00 00
2014/03/04	10:28:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/03/04	10:28:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/03/04	10:28:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/03/04	10:28:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/03/04	10:28:24.0	XRT_FLD_RESET_431_OG [0x1af]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/04	10:29:00.5	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/04	10:29:02.5	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/04	10:29:04.5	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/04	10:30:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2014/03/04	10:30:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2014/03/04	10:32:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/03/04	10:35:00.0	XRT_Custom_430_OG [0x1ae]			
2014/03/04	10:36:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/04	12:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/04	12:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/03/04	13:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	01 00 00 00 00
2014/03/04	13:00:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/03/04	13:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/03/04	13:00:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/03/04	13:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/03/04	13:00:24.0	XRT_FLD_RESET_431_OG [0x1af]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/04	13:02:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2014/03/04	13:02:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2014/03/04	13:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/04	14:16:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/04	14:16:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/04	14:16:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/04	14:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/03/04	14:24:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/04	14:24:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/03/04	14:24:30.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	04 00 00 00 00
2014/03/04	14:24:46.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/03/04	14:24:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/03/04	14:24:50.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/03/04	14:24:52.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/03/04	14:24:54.0	XRT_FLD_RESET_431_OG [0x1af]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/04	14:27:26.0	XRT_QT_PROG_SET_429_OG [0x1ad]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2014/03/04	14:27:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2014/03/04	14:27:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/04	15:49:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/04	15:49:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/04	15:49:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/04	15:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9

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2014/03/04	16:13:00.0	XRT_Custom_430_OG [0x1ae]			
2014/03/04	16:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/04	17:26:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/04	17:26:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/04	17:26:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/04	17:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/03/04	17:50:00.5	XRT_Custom_430_OG [0x1ae]			
2014/03/04	17:51:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/04	18:05:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/04	18:05:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/03/04	18:06:00.0	AOCS_Ore-point_Start_3_OG [0x099]			
		AOCU_NM	5	02-76	00 00 00 00 00
2014/03/04	18:06:16.0	XRT_FLD_DIS_434_OG [0x1b2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2014/03/04	18:08:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/03/04	18:08:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/03/04	18:08:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2014/03/04	18:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/04	18:15:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/04	18:16:00.0	AOCS_Ore-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	04 00 00 00 00
2014/03/04	19:03:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/04	19:03:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/04	19:03:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/04	19:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/03/04	20:40:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/04	20:40:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/04	20:40:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/04	20:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/03/04	22:18:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/04	22:18:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/04	22:18:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/04	22:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/03/04	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/04	22:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/03/04	23:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]			
		AOCU_NM	5	02-76	03 00 00 00 00
2014/03/04	23:00:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/03/04	23:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/03/04	23:00:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/03/04	23:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/03/04	23:00:24.0	XRT_FLD_RESET_431_OG [0x1af]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/04	23:02:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2014/03/04	23:02:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02
2014/03/04	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/04	23:55:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/04	23:55:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/04	23:55:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/04	23:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/03/05	00:04:00.0	XRT_Custom_430_OG [0x1ae]			
2014/03/05	00:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/05	01:20:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1

2014/03/05	01:20:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2014/03/05	01:20:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2014/03/05	01:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2014/03/05	01:38:00.0	XRT_Custom_430_OG [0x1ae]								
2014/03/05	01:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2014/03/05	02:54:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/03/05	02:54:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2014/03/05	02:54:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2014/03/05	02:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2014/03/05	03:15:00.0	XRT_Custom_430_OG [0x1ae]								
2014/03/05	03:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2014/03/05	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/03/05	03:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					
2014/03/05	04:00:00.0	AOCS_Ore-point_Start_1_OG [0x097]								
		AOCU_NM	5	02-76	04 00 00 00 00					
2014/03/05	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2014/03/05	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2014/03/05	04:00:20.0	XRT_AEC_RESET_413_OG [0x19d]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2014/03/05	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2014/03/05	04:00:24.0	XRT_FLD_RESET_431_OG [0x1af]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2014/03/05	04:02:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09					
2014/03/05	04:02:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02					
2014/03/05	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2014/03/05	04:24:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/03/05	04:24:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2014/03/05	04:24:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2014/03/05	04:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2014/03/05	04:52:30.0	XRT_Custom_430_OG [0x1ae]								
2014/03/05	04:53:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2014/03/05	05:36:24.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/03/05	05:36:26.0	XRT_FOCUS_POSITION_403_OG [0x193]								
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00					
2014/03/05	05:36:30.0	AOCS_Ore-point_Start_3_OG [0x099]								
		AOCU_NM	5	02-76	00 00 00 00 00					
2014/03/05	05:36:46.0	XRT_FLD_DIS_434_OG [0x1b2]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					
2014/03/05	05:39:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2014/03/05	05:39:26.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2014/03/05	05:39:28.0	XRT_QT_PROG_SET_447_OG [0x1bf]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11					
2014/03/05	05:39:30.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2014/03/05	05:46:24.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/03/05	05:46:30.0	AOCS_Ore-point_Start_1_OG [0x097]								
		AOCU_NM	5	02-76	04 00 00 00 00					
2014/03/05	06:04:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/03/05	06:04:02.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2014/03/05	06:04:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2014/03/05	06:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2014/03/05	07:44:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/03/05	07:44:32.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2014/03/05	07:44:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2014/03/05	07:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2014/03/05	08:10:24.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/03/05	08:10:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00					

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2014/03/05	08:10:30.0	AOCS_OrE-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	55	58	01	58
2014/03/05	08:10:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0					d8
2014/03/05	08:10:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0					c8
2014/03/05	08:10:50.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0					d0
2014/03/05	08:10:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2014/03/05	08:10:54.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2014/03/05	08:13:26.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 12
2014/03/05	08:13:28.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0					c5 02
2014/03/05	08:13:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2014/03/05	09:24:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2014/03/05	09:24:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2014/03/05	09:24:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2014/03/05	09:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2014/03/05	09:41:30.0	XRT_Custom_430_OG [0x1ae]							
2014/03/05	09:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2014/03/05	14:50:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2014/03/05	14:50:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2014/03/05	14:50:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2014/03/05	14:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2014/03/05	15:02:30.0	XRT_Custom_430_OG [0x1ae]							
2014/03/05	15:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2014/03/05	16:25:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2014/03/05	16:25:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2014/03/05	16:25:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2014/03/05	16:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2014/03/05	16:49:00.0	XRT_Custom_430_OG [0x1ae]							
2014/03/05	16:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2014/03/05	17:38:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2014/03/05	17:38:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa		00
2014/03/05	17:39:00.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	00	00	00	00
2014/03/05	17:39:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0					d9
2014/03/05	17:41:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0					c9
2014/03/05	17:41:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0					d5
2014/03/05	17:41:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0					c4 11
2014/03/05	17:42:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0					c0
2014/03/05	17:48:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2014/03/05	17:49:00.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	04	00	00	00	00
2014/03/05	18:02:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2014/03/05	18:02:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2014/03/05	18:02:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2014/03/05	18:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2014/03/05	19:39:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2014/03/05	19:39:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2014/03/05	19:39:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8
2014/03/05	19:42:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0					e9
2014/03/05	21:17:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0					c1
2014/03/05	21:17:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0					da
2014/03/05	21:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0					e8

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2014/03/05	21:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/03/05	22:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/05	22:54:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/03/05	22:54:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/03/05	22:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/03/05	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/05	22:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/03/05	23:00:00.0	AOCS_OrE-point_Start_4_OG [0x09a]	AOCU_NM	5	02-76	03 00 00 00 00		
2014/03/05	23:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/03/05	23:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/03/05	23:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/03/05	23:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/03/05	23:00:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/03/05	23:02:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e		
2014/03/05	23:02:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 02		
2014/03/05	23:13:30.5	XRT_Custom_430_OG [0x1ae]						
2014/03/05	23:14:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/03/06	00:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/06	00:31:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/03/06	00:31:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/03/06	00:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/03/06	00:38:30.0	XRT_Custom_430_OG [0x1ae]						
2014/03/06	00:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/03/06	01:54:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/06	01:54:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/03/06	01:54:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/03/06	01:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/03/06	02:14:00.0	XRT_Custom_430_OG [0x1ae]						
2014/03/06	02:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/03/06	03:26:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/06	03:26:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/03/06	03:26:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/03/06	03:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/03/06	03:51:30.0	XRT_Custom_430_OG [0x1ae]						
2014/03/06	03:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/03/06	04:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/06	04:19:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2014/03/06	04:20:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00		
2014/03/06	04:20:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2014/03/06	04:22:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2014/03/06	04:22:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/03/06	04:22:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2014/03/06	04:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/03/06	04:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/06	04:30:00.0	AOCS_OrE-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	02 00 00 00 00		
2014/03/06	05:01:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/06	05:01:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/03/06	05:01:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/03/06	05:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]						

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2014/03/06	06:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/06	06:41:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STOP	1	07-F0	da
			MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/06	06:41:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e8
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/06	06:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/03/06	08:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/06	08:21:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STOP	1	07-F0	da
			MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/06	08:21:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STOP	1	07-F0	e8
			MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/06	08:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/03/06	10:02:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
			MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/06	10:03:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00