

XRT Timeline to be uploaded on 2014/03/06

Period: 2014/03/06 09:53:00 - 2014/03/11 09:39:00

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Normal mode

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XOB #19C3: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 45ms-2													
Term	Pointing (x, y)		Comment										
03/06 10:15:30 - 03/06 17:59:54	Fixed (-15.0, -940.0)		# OP start + 10min, HOP206										
03/07 08:03:00 - 03/07 17:38:00	Fixed (-15.0, 880.0)		HOP81 N-pole										
PROG= 18 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 13 2-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec													
└─ Seqn= 30 1-time(s) 30.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
└─ Subr= 3 30-time(s) 2.0sec													
└─ Seqn= 57 1-time(s) 60.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19FE: Synoptic Q95 2x2 - Al/mesh(3/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(8/362/1443) + Thin													
Term	Pointing (x, y)		Comment										
03/06 18:03:00 - 03/06 18:09:54	Fixed (0.0, 0.0)		synoptic										
03/07 06:18:00 - 03/07 06:24:54	Fixed (0.0, 0.0)		synoptic, shifted 15.0 min										
03/07 18:04:30 - 03/07 18:11:24	Fixed (0.0, 0.0)		synoptic, shifted 1.5 min										
03/08 05:51:00 - 03/08 05:57:54	Fixed (0.0, 0.0)		synoptic, shifted -12.0 min										
PROG= 17 1-time(s)													
└─ Subr= 1 1-time(s) 12.0sec													
└─ Seqn= 35 1-time(s) 4.0sec													
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	3ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 72 1-time(s) 4.0sec													
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 67 1-time(s) 2.0sec													
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 6 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A00: AR Standard-B(Morphology), Be/thin 5min-cad, multifilter PFB (Al/poly, Be/thin, Be/med), multifilter context, 384x384, 1064x1048, G-band (33													
Term	Pointing (x, y)		Comment										
03/06 18:13:00 - 03/06 22:29:54	Track (476.3, -318.3)		© 03/06 18:10:00 AR 11991										
03/07 04:28:30 - 03/07 06:14:54	Track (541.9, -324.2)		© 03/07 04:00:00 AR 11991										
03/07 06:28:00 - 03/07 07:59:54	Track (557.3, -325.7)		© 03/07 06:25:00 AR 11991										
PROG= 09 Inf.-time(s)													
└─ Subr= 1 1-time(s) 2.0sec													
└─ Seqn= 8 1-time(s) 2.0sec													
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Seqn= 24 1-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Subr= 2 2-time(s) 2.0sec													
└─ Seqn= 16 2-time(s) 2.0sec													
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Al/Open	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 20 12-time(s) 300.0sec													
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	13.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	13.0sec

thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	13.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	13.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	13.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	13.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	13.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	13.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19EE: AR Standard-B(Morphology), Be/thin 48s-cad, multifilter PFB (Al/poly, Be/thin, Be/med), multifilter context, 384x384, 1064x1048, G-band (33m)

Term	Pointing (x, y)	Comment
03/06 22:33:00 - 03/07 03:58:30	Track (763.0, -163.9) @ 03/06 22:30:00	IHOP 246 hot plasma
03/07 22:58:00 - 03/08 03:59:54	Track (868.7, -188.0) @ 03/07 22:55:00	IHOP 246 hot plasma

PROG= 14 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 8	1-time(s)	2.0sec										
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Seqn= 24	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	2-time(s)	2.0sec										
Seqn= 16	2-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 29	50-time(s)	2.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #19F0: Flare standard obs. multifilter - thin-Be + (med-Al,thick-Be) 384x384 + (Al-poly 512x512 2x2)-no interval context-12 loops-lowered-gband

Term	Pointing (x, y)	Comment
03/06 10:15:30 - 03/06 17:59:54	Fixed (-15.0, -940.0)	# OP start + 10min, HOP206
03/06 18:13:00 - 03/06 22:29:54	Track (476.3, -318.3) @ 03/06 18:10:00	AR 11991
03/06 22:33:00 - 03/07 03:58:30	Track (763.0, -163.9) @ 03/06 22:30:00	IHOP 246 hot plasma
03/07 04:28:30 - 03/07 06:14:54	Track (541.9, -324.2) @ 03/07 04:00:00	AR 11991
03/07 06:28:00 - 03/07 07:59:54	Track (557.3, -325.7) @ 03/07 06:25:00	AR 11991
03/07 08:03:00 - 03/07 17:38:00	Fixed (-15.0, 880.0)	HOP81 N-pole
03/07 22:58:00 - 03/08 03:59:54	Track (868.7, -188.0) @ 03/07 22:55:00	IHOP 246 hot plasma

PROG= 02 12-time(s)

Subr= 1	45-time(s)	10.0sec										
Seqn= 9	1-time(s)	2.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 10	1-time(s)	2.0sec										
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11	1-time(s)	2.0sec										
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2	1-time(s)	10.0sec										
Seqn= 17	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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Flare Detection

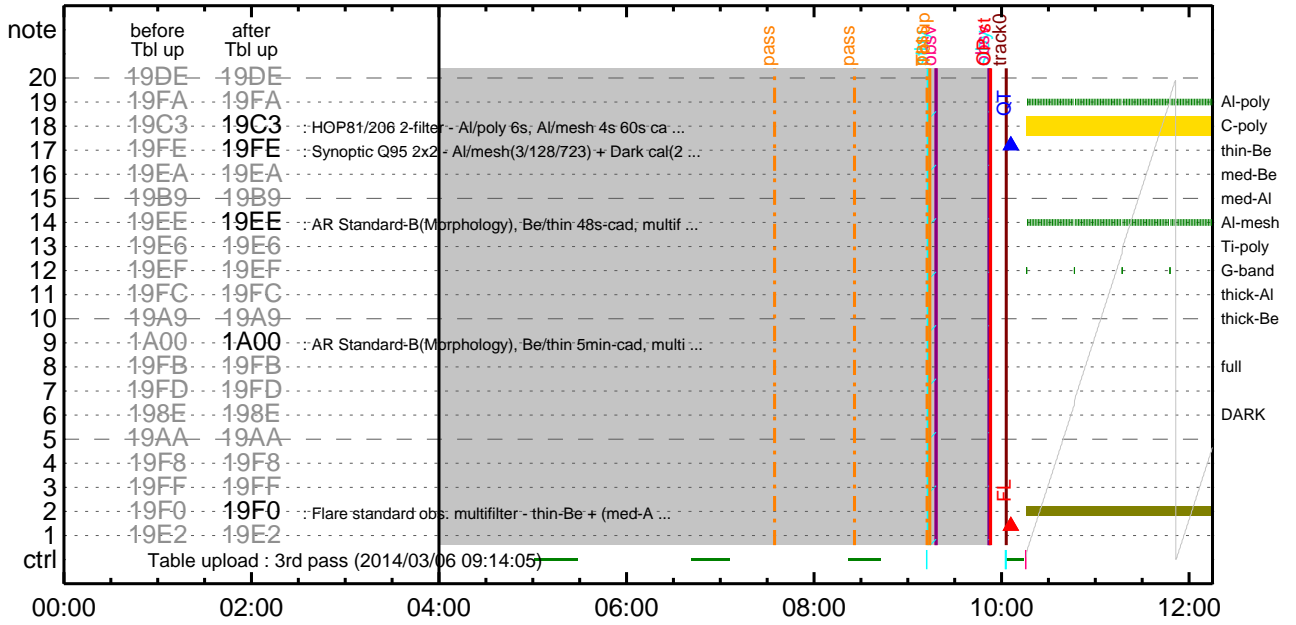
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FLD Patrol

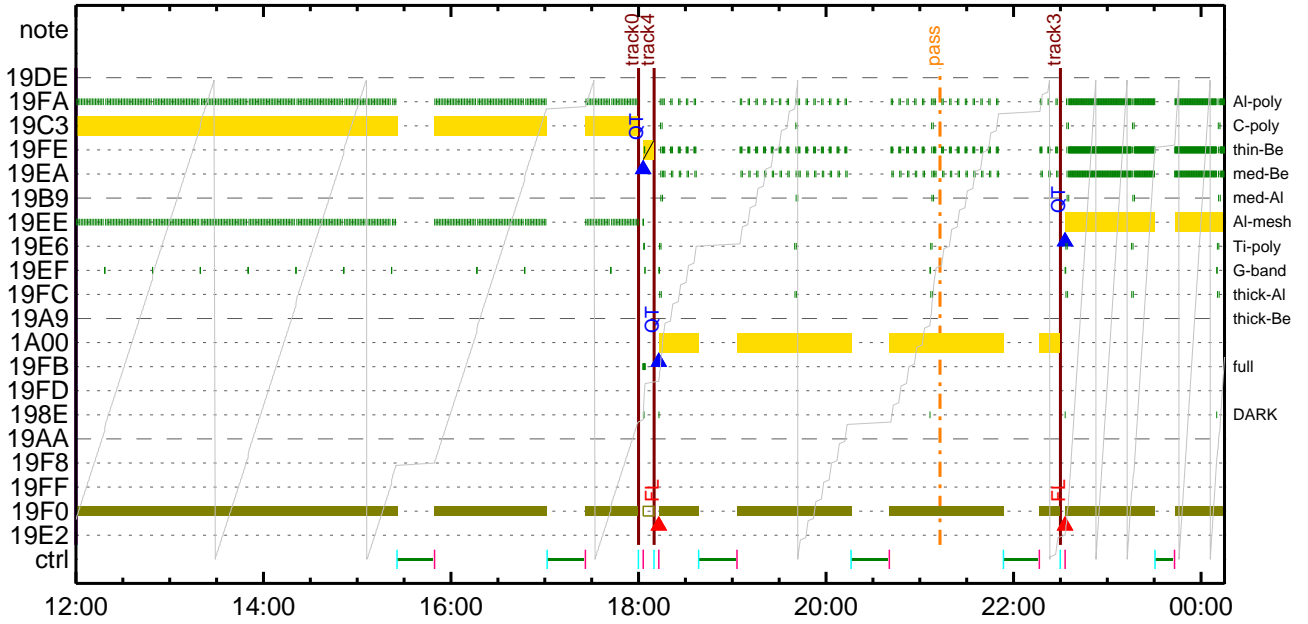
Term	Pointing (x, y)	Comment
03/06 18:10:16 - 03/07 06:15:16	Track (476.3, -318.3) @ 03/06 18:10:00	AR 11991
03/07 06:25:16 - 03/07 18:01:46	Track (557.3, -325.7) @ 03/07 06:25:00	AR 11991
03/07 22:55:16 - 03/08 05:48:16	Track (868.7, -188.0) @ 03/07 22:55:00	IHOP 246 hot plasma

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

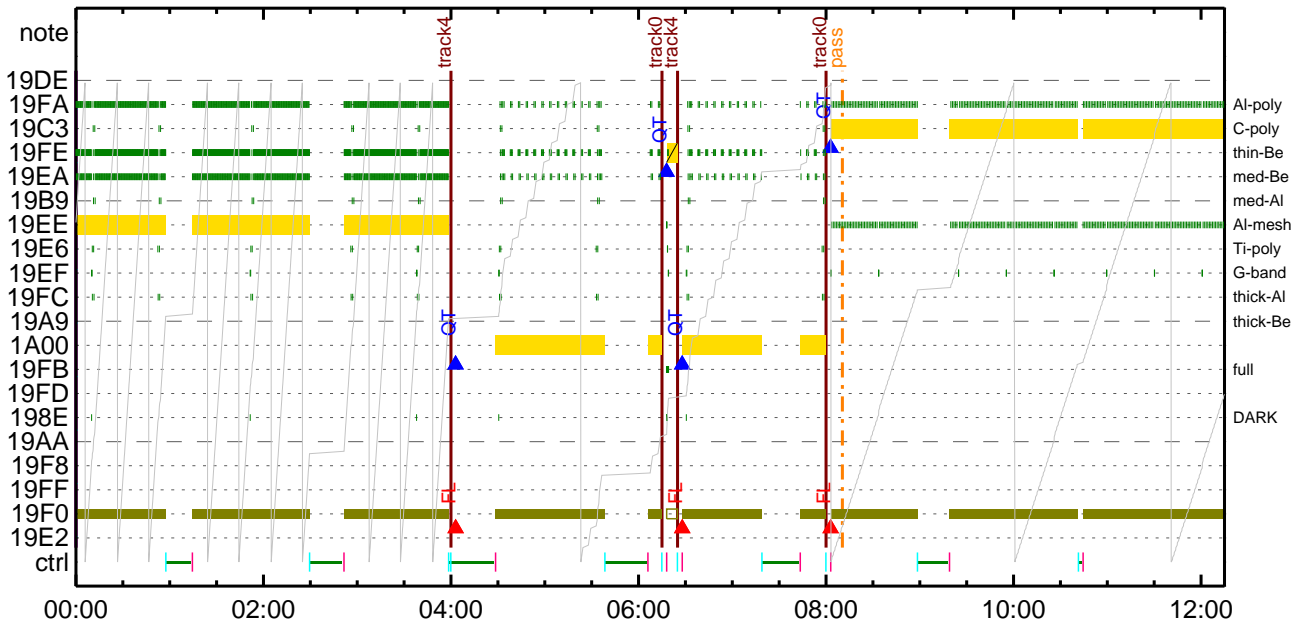
CMDI #0134 2014/03/06



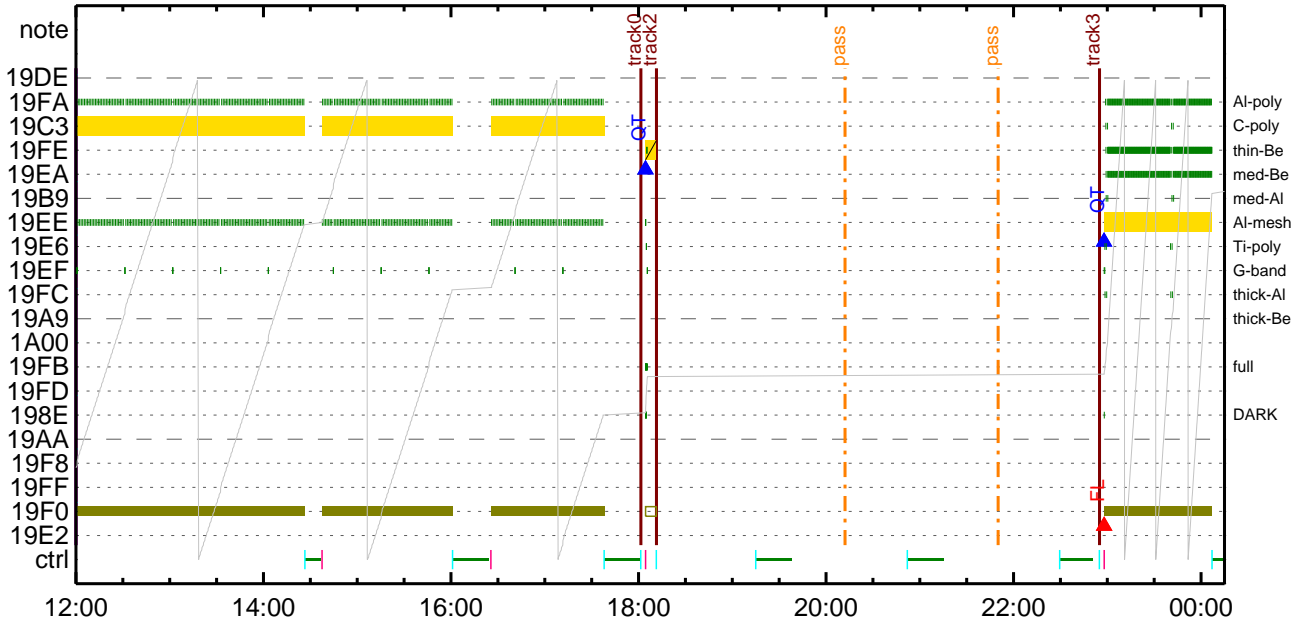
CMDI #0134 2014/03/06



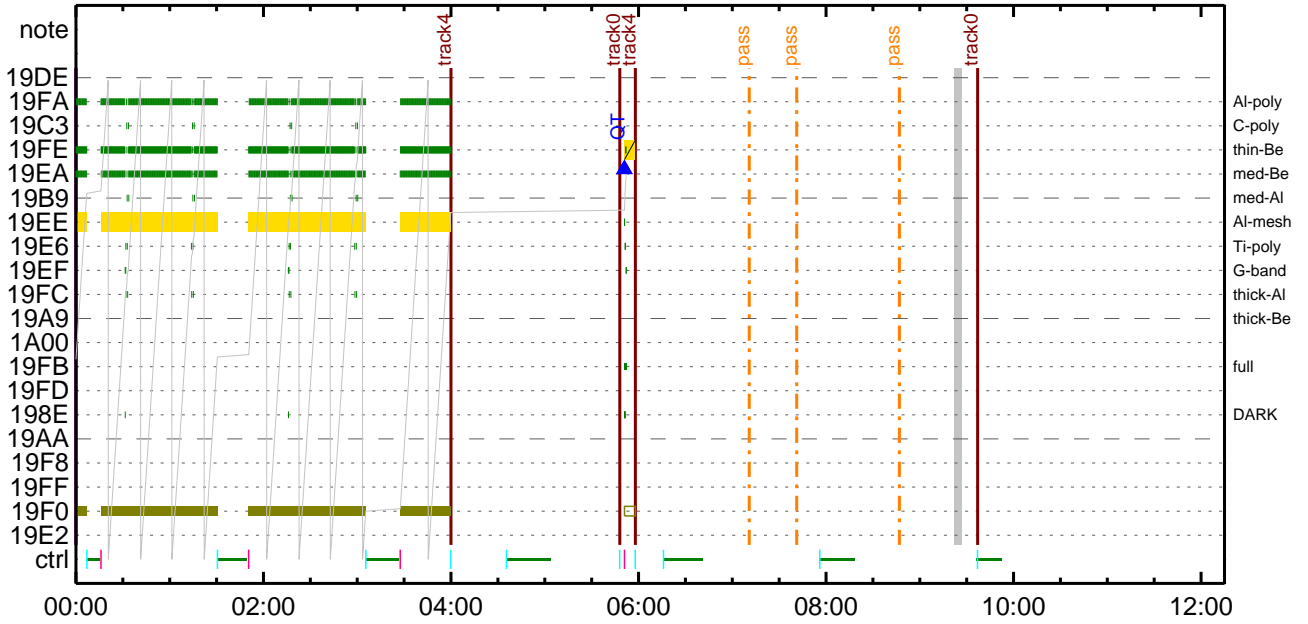
CMDI #0134 2014/03/07



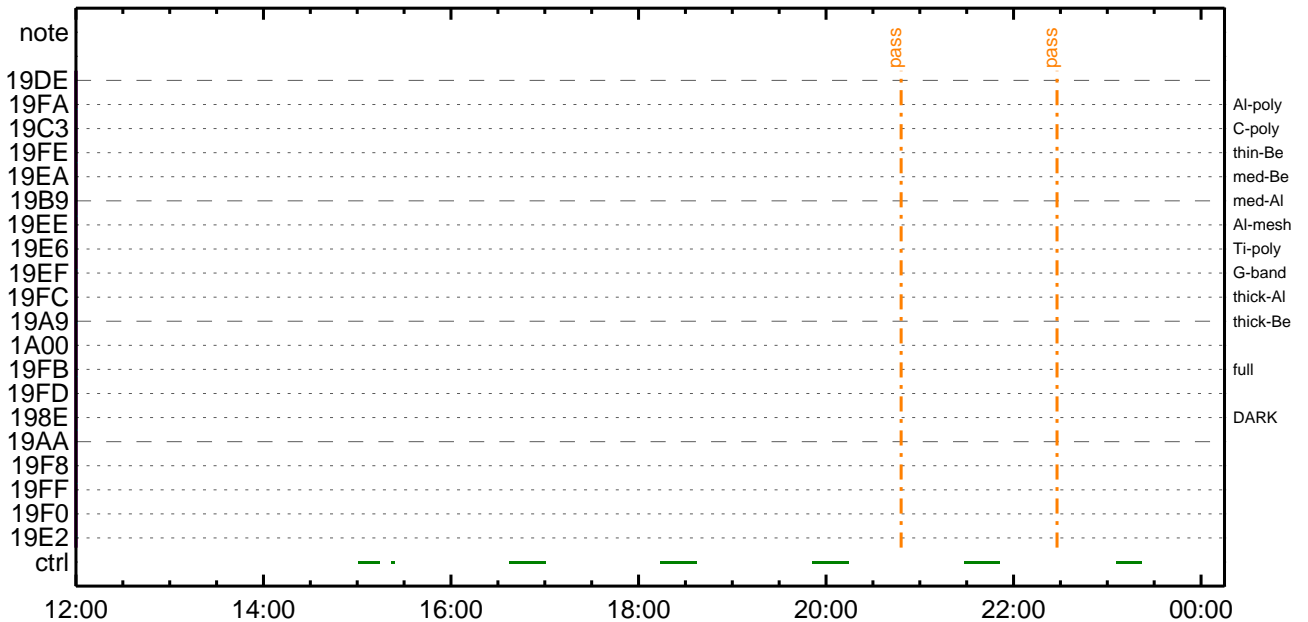
CMDI #0134 2014/03/07



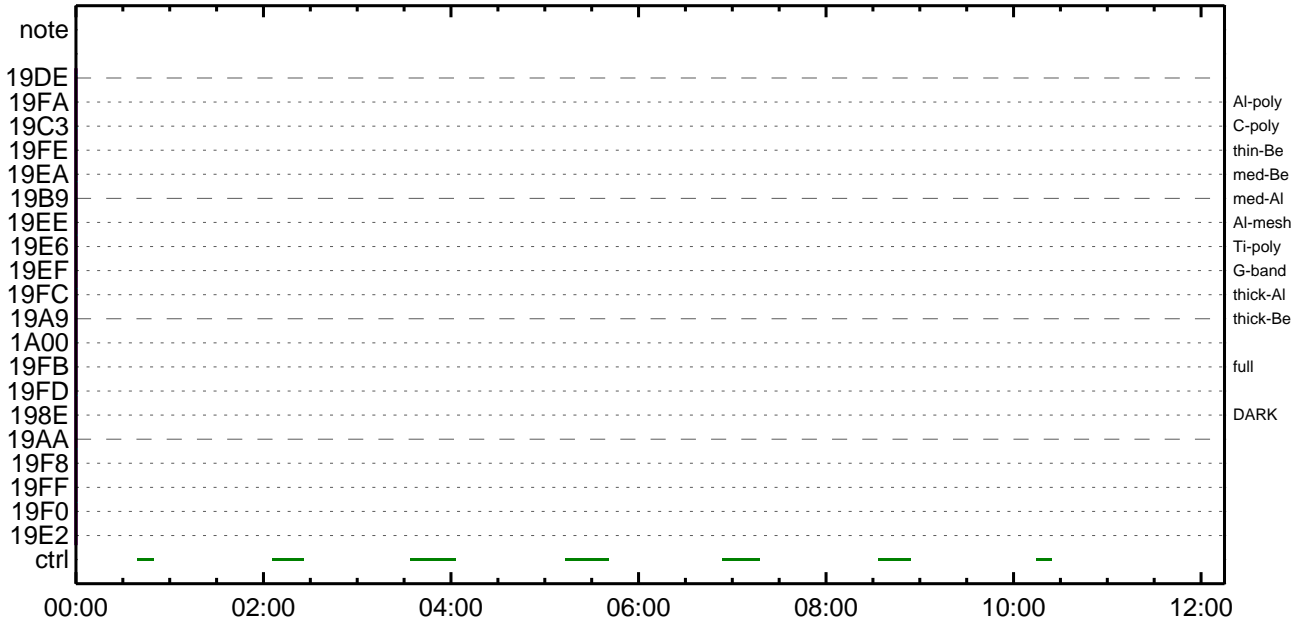
CMDI #0134 2014/03/08



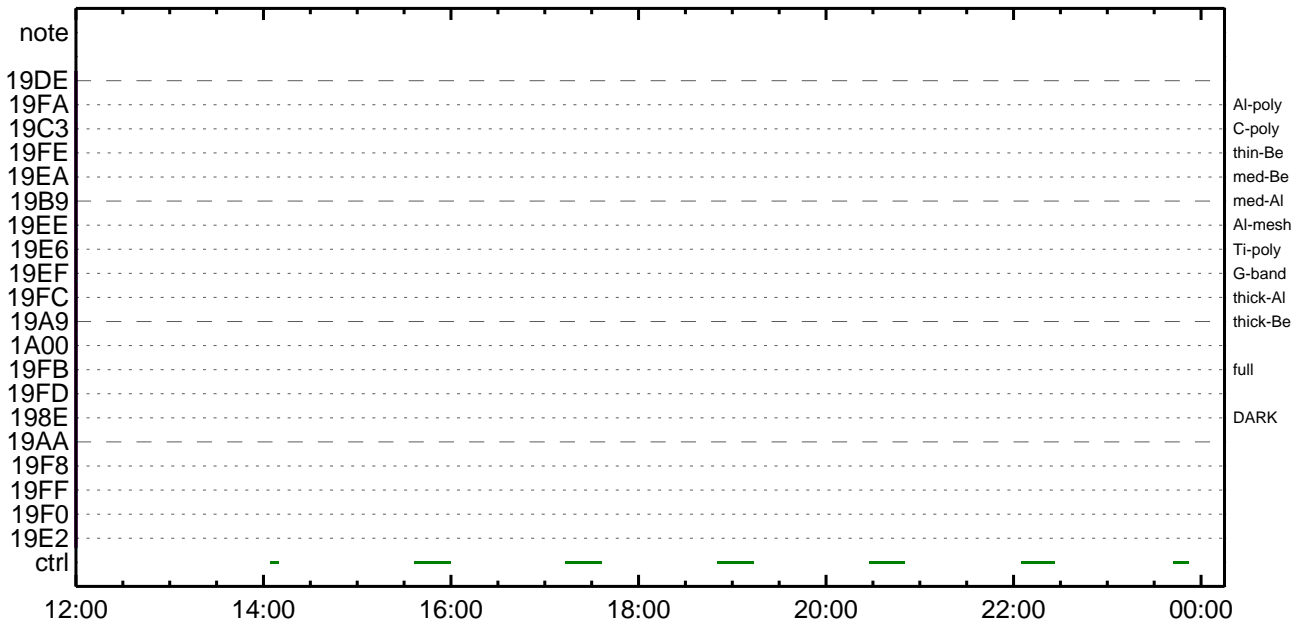
CMDI #0134 2014/03/08



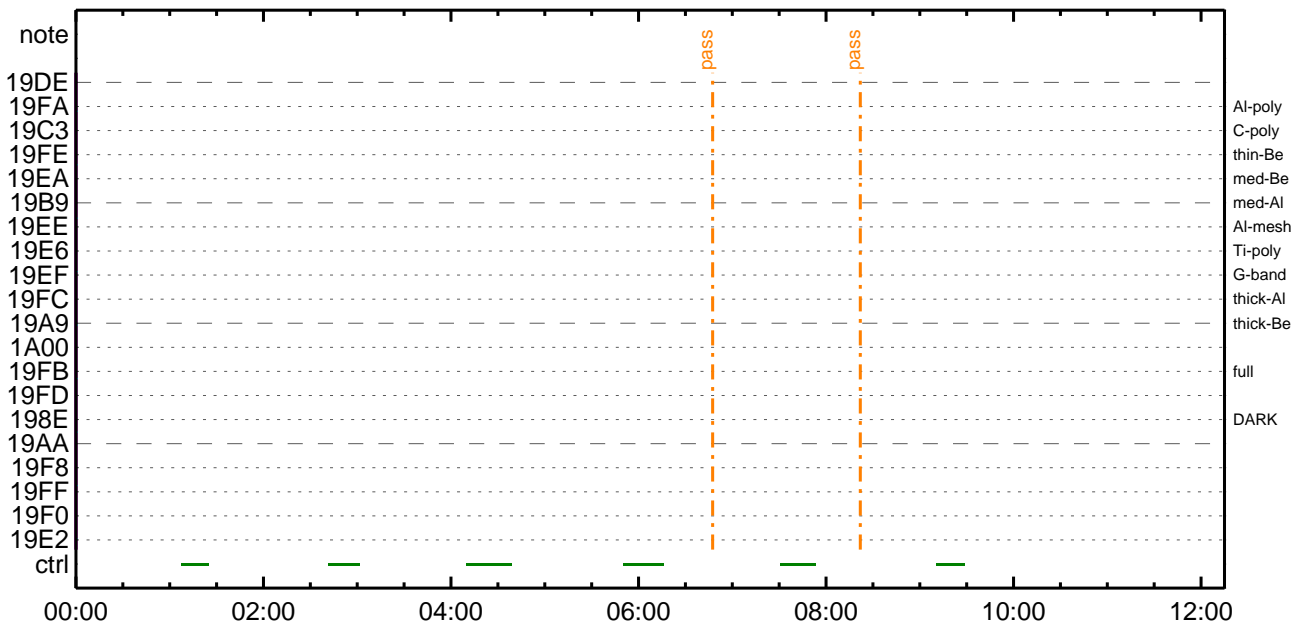
CMDI #0134 2014/03/09



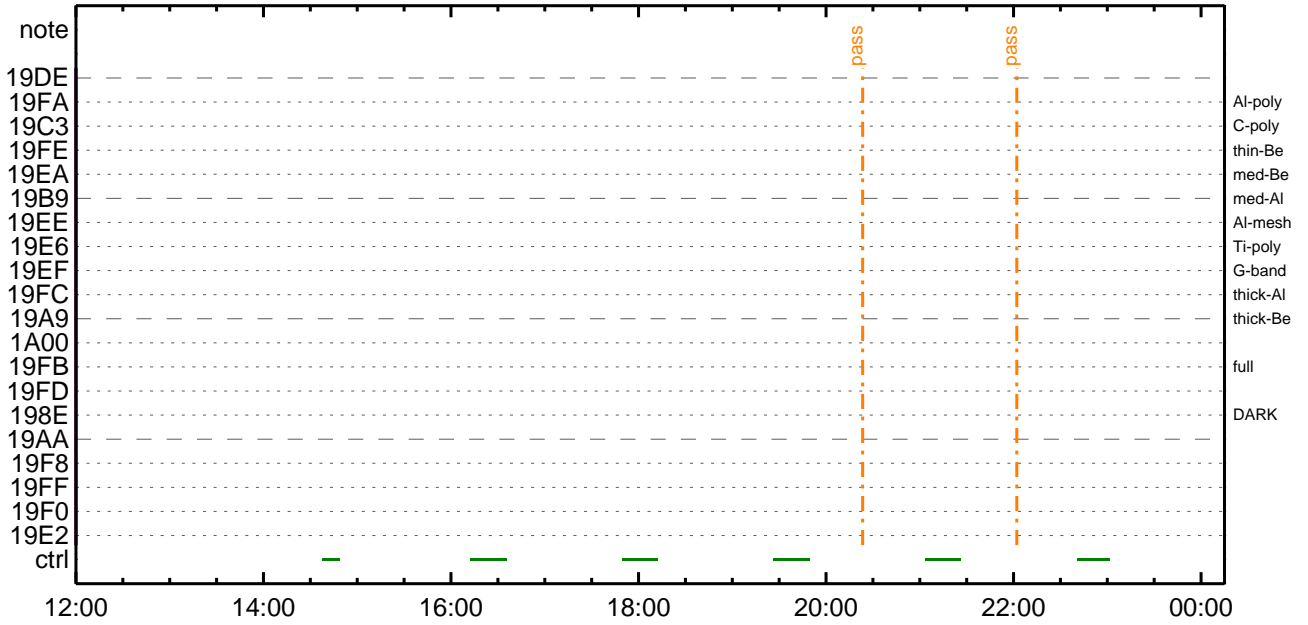
CMDI #0134 2014/03/09



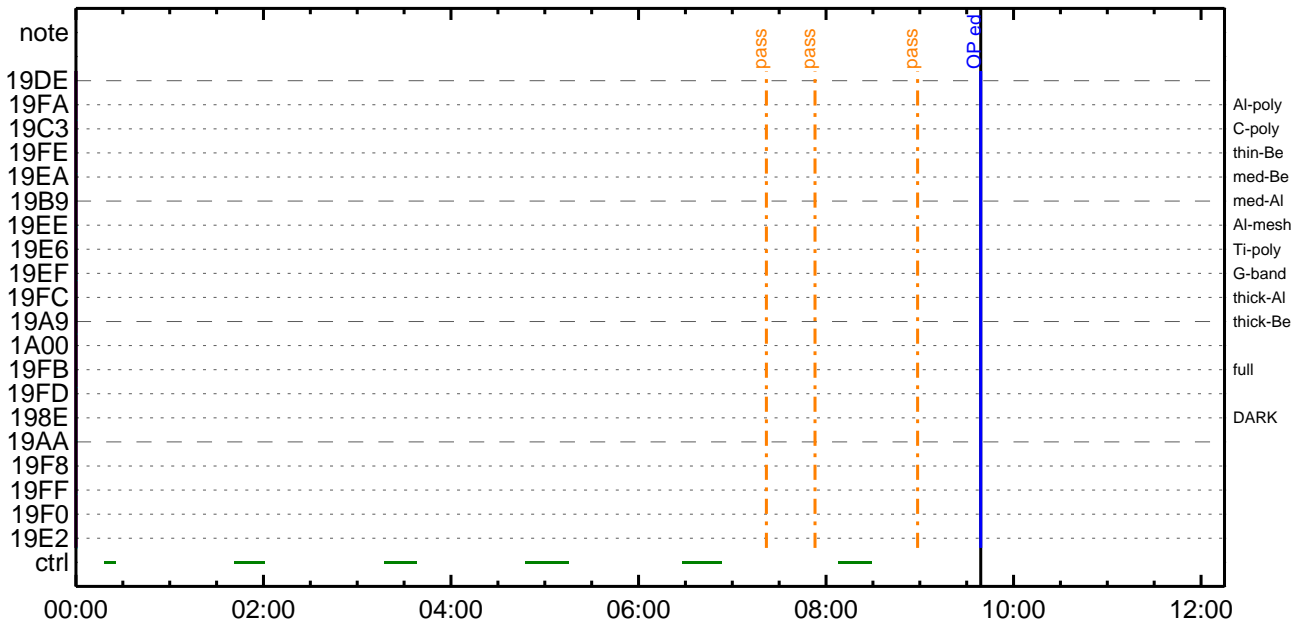
CMDI #0134 2014/03/10



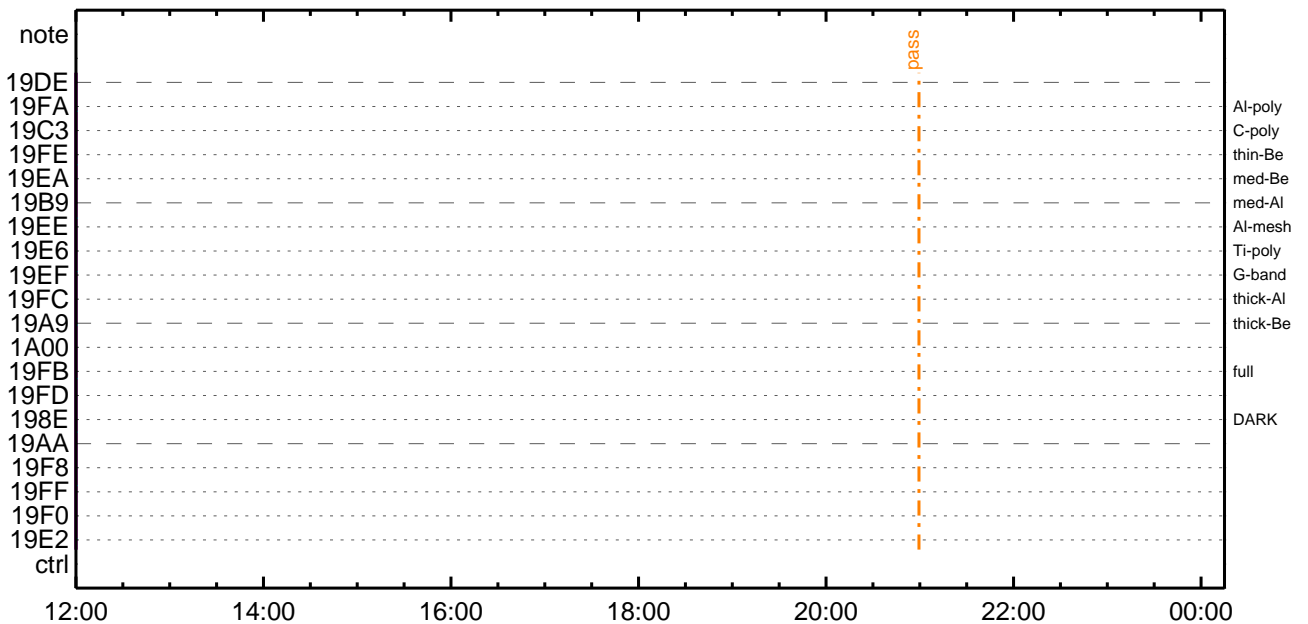
CMDI #0134 2014/03/10



CMDI #0134 2014/03/11



CMDI #0134 2014/03/11




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-249:OP
0104 ( )
0105 S. OG og-249:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ä´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²²ò¿;çSET²EDUMPA²E±²îY¹²ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y²YóYÉòðÁDî¿(UT)
0183 +. TI 2014-03-06 09:48:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2014-03-06 09:48:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2014-03-06 09:48:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2014-03-06 09:52:59.5
0196 DC 01-B2 DHU_OP_START
0197 C. ¢¢[HK1_TI_CMD_NUM] EQ 1COUNTUP
0198 C.
0199 C. °Ê²¼õïÄê%íîñõîŷÅŷ§ŷÅŷ¹âîŭ
0200 C. ¢¢[HK1_TI_CMD_ENA/DIS] EQ ENA
0201 C. ¢¢[HK1_TI_CMD_NUM] EQ 4
0202 C. ¢¢[HK1_NEXT_EXEC_PIM] EQ DHU
0203 C. ¢¢[HK1_NEXT_EXEC_DC] EQ 0xB3
0204 C.
0205 . C. *****
0206 C. TIîŷ°èŷÅŷØŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;\$ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C. ¢¢[HK1_DMP_TOP_ADRS_1] EQ 07
0213 C. ¢¢[HK1_DMP_TOP_ADRS_0] EQ 2B
0214 C. ¢¢[HK1_DMP_BLOCK_NUM] EQ 3
0215 C. ¢¢[HK1_DMP_REPEAT_NUM] EQ 0
0216 C. ¢¢[HK1_DMA_DMP_PIM] EQ DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C. ¢¢[HK1_PKT_FORM_NO] EQ 7
0220 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.25 s
0221 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32k
0222 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0223 C. ¢¢[HK1_DMP_CHK_FLG] EQ EXEC
0224 C.
0225 . C. ŷÅŷØŷ×½ªî»õ³îÇ§
0226 C. ¢¢[HK1_DMP_CHK_FLG] EQ NON
0227 C.
0228 . C. RAM ID=TI_TBLõîŷÅŷ¹çª²îOKõ³îÇ§
0229 C.
0230 . C. DHUŷâ;¼ŷÊ;Ê¼ŷ½. ŷî;¼ŷÊ;Êõðîãõ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C. ¢¢[HK1_PKT_FORM_NO] EQ 2
0234 C. ¢¢[HK1_PKT_GEN_TIME] EQ 0.5S
0235 C. ¢¢[HK1_S_TLM_BIT_RATE] EQ 32K
0236 C. ¢¢[HK1_X_TLM_BIT_RATE] EQ 4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2014-03-06 09:52:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC (41)
0245 . C. -----
0246 C. HK1_TI_CMD_NUM = 1 CNTUP []
0247 C. -----
0248 C. ***** SOT END *****
0249 . C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2014-03-06 09:52:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC (21 02)
0258 +. TI 2014-03-06 09:52:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC (22)
0261 . C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2014-03-06 09:52:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC (c3)
0271 . C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 . C. ***** MDP ´ûÃîãî»õ¼ŷõÊÄõ¹õêDCBC•x²è *****
0276 C. (¼ª°îŷÅŷÊŷŷŷÅŷÇŷÈõÊ¼õõ¼ª»ŭõ¹õè)
0277 . S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 . C. ***** ŷÐŷ¹.î Daily±çîñõË´Øõ¹õêDCBC•x²è *****
0282 . S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 . C. ;ãLOSŷÅŷ§ŷÅŷ¹¼ª»ŭ;ã
0287 C.
0288 . C. ***** LOS *****
0289 C.


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0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-265:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Stop SP table >
0120 +. DC 07-F0 MDP_SP_CTRL_MANU
0121 BC (61)
0122 C. -----
0123 C. MDP_SP_CTRL_MODE = MANU [ ]
0124 C. -----
0125 C.
0126 . C. <Upload SP Observation Table>
0127 . S. RAM ram-288:MDP_OBS_S
0128 ( )
0129 C.
0130 . C. < Dump RAMID=MDP_OBS_S >
0131 +. DC 07-F0 MDP_DUMP_SPTBL
0132 BC (83 07 00 00 00 38 b8)
0133 C. -----
0134 C. MDP_OBS_S verify = OK/NG [ ]
0135 C. -----
0136 C.
0137 C. *****
0138 C. SOT TI command set
0139 C. *****
0140 C. Execute, after the success of TBL upload.
0141 +. TI 2014-03-06 09:52:18.0
0142 DC 07-F0 MDP_SOT_MODE_OBSV
0143 BC (40)
0144 . C. -----
0145 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0146 C. -----
0147 C.
0148 C.
0149 C. ***** XRT START *****
0150 C.
0151 +. DC 07-F0 MDP_XRT_CTRL_MANU
0152 BC (c1)
0153 + DC 07-F0 MDP_XRT_MODE_STBY
0154 BC (c3)
0155 . C. ----- Success Verify ? OK / NG____
0156 C.
0157 C. XRT Obs. Table Upload
0158 . S. RAM ram-291:MDP_OBS_X
0159 ( )
0160 C.
0161 +. DC 07-F0 MDP_DUMP_XRTTBL
0162 BC (84 07 00 00 00 3a d4)
0163 . C. ----- Comparison Check ? OK / ERR ____
0164 C.
0165 C.
0166 +. DC 07-F0 MDP_XRT_ROI_SET
0167 BC (cd 01 b1 b1 04 04)
0168 + DC 07-F0 MDP_XRT_ROI_SET
0169 BC (cd 02 b1 b1 08 08)
0170 + DC 07-F0 MDP_XRT_ROI_SET
0171 BC (cd 03 b1 b1 08 08)
0172 + DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 04 b1 b1 06 06)
0174 + DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 05 85 83 06 06)
0176 + DC 07-F0 MDP_XRT_ROI_SET
0177 BC (cd 06 85 83 08 08)
0178 + DC 07-F0 MDP_XRT_ROI_SET
0179 BC (cd 07 85 83 06 06)
0180 + DC 07-F0 MDP_XRT_ROI_SET
0181 BC (cd 08 80 80 20 20)
0182 + DC 07-F0 MDP_XRT_ROI_SET
0183 BC (cd 09 80 80 20 08)
0184 + DC 07-F0 MDP_XRT_ROI_SET
0185 BC (cd 0a 80 80 08 20)
0186 + DC 07-F0 MDP_XRT_ROI_SET
0187 BC (cd 0f 80 80 06 06)
0188 + DC 07-F0 MDP_XRT_ROI_SET
0189 BC (cd 10 80 80 08 08)
0190 + DC 07-F0 MDP_XRT_FLD_ENA
0191 BC (d8)
0192 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0193 BC (c8)

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0194 + DC 07-F0 MDP_XRT_AEC_RESET
0195 BC (d0)
0196 + DC 07-F0 MDP_XRT_ARS_DIS
0197 BC (d5)
0198 + DC 07-F0 MDP_XRT_FLD_RESET
0199 BC (da)
0200 . C. ----- Success Verify ? OK / NG ____
0201 C.
0202 C.
0203 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0204 C.
0205 +. DC 07-F0 MDP_XRT_MODE_OBSV
0206 BC (c2)
0207 +. TI 2014-03-06 09:52:02.0
0208 DC 07-F0 MDP_XRT_MODE_OBSV
0209 BC (c2)
0210 . C. ----- Success Verify ? OK / NG ____
0211 C.
0212 C. ***** XRT END *****
0213 C.
0214 . C. ***** MDP 'úÃîâî»ö¼ÝâÉÂð¹ªēDCBC•x²è *****
0215 C. (¼â°îÿÓÿÄÿÉÿÞÿËÿÀÿÇÿÈª¼ª¼ª»Ûª¹ªé)
0216 . S. DC-BC dcbc-402:DCBC
0217 (MDP_known_event)
0218 C.
0219 C.
0220 . C. ***** ŸÐŸ¹•İ Daily±¿İÑªÉ'Øª¹ªēDCBC•x²è *****
0221 . S. DC-BC dcbc-153:DCBC
0222 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0223 C.
0224 C.
0225 . C. ;ãLOSŸÁŸŞŸÄŸ~¼Â»Û;ä
0226 C.
0227 . C. ***** LOS *****
0228 C.

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*** OP Sequence for XRT ***

2014/03/06	10:02:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/06	10:02:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/06	10:02:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/06	10:02:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/06	10:02:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/03/06	10:03:00.0	AOCS_Orе-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 53 8d 01 58				
2014/03/06	10:03:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/03/06	10:03:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/03/06	10:03:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/03/06	10:03:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/03/06	10:03:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/06	10:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/06	10:05:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2014/03/06	10:05:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/03/06	10:14:30.0	XRT_Custom_430_OG [0x1ae]							
2014/03/06	10:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/06	15:25:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/06	15:25:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/06	15:25:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/06	15:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/06	15:48:30.0	XRT_Custom_430_OG [0x1ae]							
2014/03/06	15:49:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/06	17:01:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/06	17:01:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/06	17:01:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/06	17:04:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/06	17:25:00.0	XRT_Custom_430_OG [0x1ae]							
2014/03/06	17:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/06	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/06	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/03/06	18:00:00.0	AOCS_Orе-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/03/06	18:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/03/06	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/03/06	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/03/06	18:02:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2014/03/06	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/06	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/06	18:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/03/06	18:10:00.0	AOCS_Orе-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2014/03/06	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/03/06	18:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/03/06	18:10:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/03/06	18:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/03/06	18:10:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/06	18:12:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2014/03/06	18:12:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/03/06	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2014/03/06	18:38:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/06	18:38:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/06	18:38:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/06	18:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/06	19:02:00.0	XRT_Custom_430_OG [0x1ae]							
2014/03/06	19:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/06	20:16:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/06	20:16:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/06	20:16:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/06	20:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/06	20:39:30.0	XRT_Custom_430_OG [0x1ae]							
2014/03/06	20:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/06	21:53:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/06	21:53:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/06	21:53:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/06	21:56:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/06	22:15:30.0	XRT_Custom_430_OG [0x1ae]							
2014/03/06	22:16:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/06	22:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/06	22:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/03/06	22:30:00.0	AOCS_OrE-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2014/03/06	22:30:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/03/06	22:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/03/06	22:30:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/03/06	22:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/03/06	22:30:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/06	22:32:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2014/03/06	22:32:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/03/06	22:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/06	23:30:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/06	23:30:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/06	23:30:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/06	23:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/06	23:42:00.0	XRT_Custom_430_OG [0x1ae]							
2014/03/06	23:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/07	00:57:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/07	00:57:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/07	00:57:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/07	01:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/07	01:13:30.0	XRT_Custom_430_OG [0x1ae]							
2014/03/07	01:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/07	02:29:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/07	02:29:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/07	02:29:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/07	02:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/07	02:50:30.0	XRT_Custom_430_OG [0x1ae]							
2014/03/07	02:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/07	03:58:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/07	03:58:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/07	03:58:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							

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2014/03/07	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/07	03:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/03/07	04:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	04	00	00	00	00
2014/03/07	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/03/07	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/03/07	04:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/03/07	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/03/07	04:00:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/07	04:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/07	04:02:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09			
2014/03/07	04:02:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2014/03/07	04:27:30.0	XRT_Custom_430_OG [0x1ae]								
2014/03/07	04:28:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/07	05:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/07	05:38:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/07	05:38:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/07	05:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/07	06:05:00.0	XRT_Custom_430_OG [0x1ae]								
2014/03/07	06:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/07	06:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/07	06:14:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/03/07	06:15:00.5	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00	00
2014/03/07	06:15:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/03/07	06:17:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/03/07	06:17:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/03/07	06:17:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2014/03/07	06:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/07	06:24:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/07	06:24:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/03/07	06:25:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	04	00	00	00	00
2014/03/07	06:25:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/03/07	06:25:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/03/07	06:25:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/03/07	06:25:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/03/07	06:25:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/07	06:27:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	09			
2014/03/07	06:27:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02			
2014/03/07	06:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/07	07:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/07	07:19:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/07	07:19:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/07	07:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/07	07:42:30.0	XRT_Custom_430_OG [0x1ae]								
2014/03/07	07:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/07	07:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/07	07:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/03/07	08:00:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	b1	cb	01	58
2014/03/07	08:00:16.0	XRT_FLD_ENA_411_OG [0x19b]								

2014/03/07	08:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/03/07	08:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/03/07	08:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/03/07	08:00:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/03/07	08:02:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	12	
2014/03/07	08:02:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	02	
2014/03/07	08:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/03/07	08:58:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/07	08:58:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/03/07	08:58:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/03/07	09:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/03/07	09:18:00.0	XRT_Custom_430_OG [0x1ae]						
2014/03/07	09:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/03/07	10:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/07	10:41:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/03/07	10:41:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/03/07	10:43:30.0	XRT_Custom_430_OG [0x1ae]						
2014/03/07	10:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/03/07	10:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/03/07	14:26:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/07	14:26:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/03/07	14:26:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/03/07	14:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/03/07	14:36:30.0	XRT_Custom_430_OG [0x1ae]						
2014/03/07	14:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/03/07	16:01:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/07	16:01:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/03/07	16:01:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/03/07	16:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/03/07	16:24:30.0	XRT_Custom_430_OG [0x1ae]						
2014/03/07	16:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/03/07	17:38:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/07	17:38:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/03/07	17:38:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/03/07	17:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/03/07	18:01:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/07	18:01:26.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2014/03/07	18:01:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00	
2014/03/07	18:01:46.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2014/03/07	18:04:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2014/03/07	18:04:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/03/07	18:04:28.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11	
2014/03/07	18:04:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/03/07	18:11:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/07	18:11:30.0	AOCS_ORe-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	02	00 00 00 00	
2014/03/07	19:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/03/07	19:15:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/03/07	19:15:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		

2014/03/07	19:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/07	20:52:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/07	20:52:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/07	20:52:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/07	20:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/07	22:29:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/07	22:29:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/07	22:29:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/07	22:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/07	22:54:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/07	22:54:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/03/07	22:55:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2014/03/07	22:55:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/03/07	22:55:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/03/07	22:55:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/03/07	22:55:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/03/07	22:55:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/07	22:57:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2014/03/07	22:57:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 02				
2014/03/07	22:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/08	00:07:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/08	00:07:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/08	00:07:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/08	00:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/08	00:15:00.0	XRT_Custom_430_OG [0x1ae]							
2014/03/08	00:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/08	01:30:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/08	01:30:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/08	01:30:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/08	01:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/08	01:49:30.0	XRT_Custom_430_OG [0x1ae]							
2014/03/08	01:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/08	03:05:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/08	03:05:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/08	03:05:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/08	03:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/08	03:26:30.0	XRT_Custom_430_OG [0x1ae]							
2014/03/08	03:27:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/08	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/08	04:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2014/03/08	04:35:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/08	04:35:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/08	04:35:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/08	04:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/08	05:47:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/08	05:47:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/03/08	05:48:00.5	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/03/08	05:48:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				

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2014/03/08	05:50:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/03/08	05:50:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/03/08	05:50:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2014/03/08	05:51:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/08	05:57:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/08	05:58:00.0	AOCS_OrE-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	04	00 00 00 00			
2014/03/08	06:16:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/08	06:16:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/08	06:16:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/08	06:19:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/08	07:56:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/08	07:56:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/08	07:56:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/08	07:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/08	09:36:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/08	09:37:00.0	AOCS_OrE-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00 00 00 00			