

XRT Timeline to be uploaded on 2014/03/27

Period: 2014/03/27 09:35:00 - 2014/04/01 09:21:00

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Normal mode

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XOB #19E4: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33m

Term	Pointing (x, y)	Comment
03/27 10:00:00 - 03/27 11:44:54	Track (345.6, -159.5) ^{© 03/27 09:45:00}	# OP start + 10min AR 12014
03/28 01:43:00 - 03/28 05:59:54	Track (482.2, -132.2) ^{© 03/28 01:40:00}	AR 12014
03/28 06:13:00 - 03/28 14:10:00	Track (506.2, -169.8) ^{© 03/28 06:10:00}	AR 12014 cont

PROG= 13 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec											
└─	Seqn= 8	2-time(s)	2.0sec										
└─└─	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2	2-time(s)	2.0sec											
└─	Seqn= 24	1-time(s)	2.0sec										
└─└─	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─└─	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─	Seqn= 42	4-time(s)	2.0sec										
└─└─	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─└─	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─└─	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─	Seqn= 62	10-time(s)	180.0sec										
└─└─	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─└─	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─└─	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─└─	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─└─	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─└─	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1A03: AR Standard-B(Morphology), Be/thin 60s-cad, multifilter PFB (Al/poly, Be/thin, Be/med), multifilter context, 384x384, 1064x1048, G-band (33m

Term	Pointing (x, y)	Comment
03/27 11:48:00 - 03/27 14:14:54	Track (360.8, -160.0) ^{© 03/27 11:45:00}	HOP 246 (AR 12014)
03/28 23:03:00 - 03/29 05:29:54	Track (623.2, -180.4) ^{© 03/28 23:00:00}	HOP 246 (AR 12014)

PROG= 03 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec											
└─	Seqn= 8	1-time(s)	2.0sec										
└─└─	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─	Seqn= 24	1-time(s)	2.0sec										
└─└─	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─└─	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	2-time(s)	2.0sec											
└─	Seqn= 16	2-time(s)	2.0sec										
└─└─	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─└─	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─└─	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─└─	C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─└─	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─└─	med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─└─	med-Al/Open	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─	Seqn= 80	50-time(s)	60.0sec										
└─└─	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─└─	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─└─	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─└─	med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #19E3: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33m

Term	Pointing (x, y)	Comment
03/27 14:18:00 - 03/27 17:57:54	Track (382.3, -161.5) ^{© 03/27 14:15:00}	HOP 251 AR 12014
03/28 14:18:00 - 03/28 17:59:54	Track (564.6, -174.8) ^{© 03/28 14:15:00}	HOP 251 (AR 12014)

PROG= 14 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec											
└─	Seqn= 8	2-time(s)	2.0sec										
└─└─	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2	2-time(s)	2.0sec											
└─	Seqn= 24	1-time(s)	2.0sec										
└─└─	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─└─	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─	Seqn= 42	4-time(s)	2.0sec										
└─└─	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─└─	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─└─	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─	Seqn= 62	15-time(s)	120.0sec										
└─└─	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─└─	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19FE: Synoptic Q95 2x2 - Al/mesh(3/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(8/362/1443) + Thin_

Term	Pointing (x, y)	Comment
03/27 18:01:00 - 03/27 18:07:54	Fixed (0.0, 0.0)	synoptic, shifted -2.0 min
03/28 06:03:00 - 03/28 06:09:54	Fixed (0.0, 0.0)	synoptic
03/28 18:03:00 - 03/28 18:09:54	Fixed (0.0, 0.0)	synoptic
03/29 05:33:00 - 03/29 05:39:54	Fixed (0.0, 0.0)	synoptic, shifted -30.0 min

PROG= 17 1-time(s)

- Subr= 1 1-time(s) 12.0sec
 - Seqn= 35 1-time(s) 4.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 3ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 5 1-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
 - Seqn= 72 1-time(s) 4.0sec
 - Open/Ti-poly Open/Ti-poly close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Ti-poly Open/Ti-poly close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 67 1-time(s) 2.0sec
 - thin-Be/Open thin-Be/Open close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - thin-Be/Open thin-Be/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - thin-Be/Open thin-Be/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
 - Seqn= 6 1-time(s) 2.0sec
 - Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
 - Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
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XOB #19C3: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 45ms-2

Term	Pointing (x, y)	Comment
03/27 18:11:00 - 03/28 01:39:54	Fixed (-15.0, -940.0)	HOP 206 (south pole)

PROG= 18 Inf.-time(s)

- Subr= 1 1-time(s) 2.0sec
 - Seqn= 13 2-time(s) 2.0sec
 - Open/G-band Open/G-band close Safe Norm 63ms Obs 1x1 512x512 (1064, 1048) DPCM 0 0 2.0sec
- Subr= 2 1-time(s) 2.0sec
 - Seqn= 30 1-time(s) 30.0sec
 - Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
- Subr= 3 30-time(s) 2.0sec
 - Seqn= 57 1-time(s) 60.0sec
 - Open/Al-mesh Open/Al-mesh close Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec
 - Al-poly/Open Al-poly/Open close Safe Norm 5.66s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
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XOB #19E5: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)

Term	Pointing (x, y)	Comment
03/28 18:13:00 - 03/28 22:59:54	Track (591.8, -177.5) @ 03/28 18:10:00	AR 12014
03/29 05:43:00 - 03/29 08:20:00	Track (666.4, -185.8) @ 03/29 05:40:00	AR 12014

PROG= 06 Inf.-time(s)

- Subr= 1 1-time(s) 2.0sec
 - Seqn= 8 2-time(s) 2.0sec
 - Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
- Subr= 2 2-time(s) 2.0sec
 - Seqn= 24 1-time(s) 2.0sec
 - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
 - Open/G-band Open/G-band open Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
 - Seqn= 42 4-time(s) 2.0sec
 - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
 - Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - Seqn= 62 8-time(s) 240.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
 - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
 - thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
 - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	
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Flare mode

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XOB #19EC: Flare - high cad multifilter (Be/thin, Be/med, Al/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gband (4

Term	Pointing (x, y)	Comment
03/27 10:00:00 - 03/27 11:44:54	Track (345.6, -159.5) ^{Ⓢ 03/27 09:45:00}	# OP start + 10min AR 12014
03/27 11:48:00 - 03/27 14:14:54	Track (360.8, -160.0) ^{Ⓢ 03/27 11:45:00}	HOP 246 (AR 12014)
03/27 14:18:00 - 03/27 17:57:54	Track (382.3, -161.5) ^{Ⓢ 03/27 14:15:00}	HOP 251 AR 12014
03/27 18:11:00 - 03/28 01:39:54	Fixed (-15.0, -940.0)	HOP 206 (south pole)
03/28 01:43:00 - 03/28 05:59:54	Track (482.2, -132.2) ^{Ⓢ 03/28 01:40:00}	AR 12014
03/28 06:13:00 - 03/28 14:10:00	Track (506.2, -169.8) ^{Ⓢ 03/28 06:10:00}	AR 12014 cont
03/28 14:18:00 - 03/28 17:59:54	Track (564.6, -174.8) ^{Ⓢ 03/28 14:15:00}	HOP 251 (AR 12014)
03/28 18:13:00 - 03/28 22:59:54	Track (591.8, -177.5) ^{Ⓢ 03/28 18:10:00}	AR 12014
03/28 23:03:00 - 03/29 05:29:54	Track (623.2, -180.4) ^{Ⓢ 03/28 23:00:00}	HOP 246 (AR 12014)
03/29 05:43:00 - 03/29 08:20:00	Track (666.4, -185.8) ^{Ⓢ 03/29 05:40:00}	AR 12014

PROG= 01 30-time(s)

Subr= 1 30-time(s) 2.0sec												
└─ Seqn= 26 1-time(s) 4.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
└─ Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
└─ Seqn= 15 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

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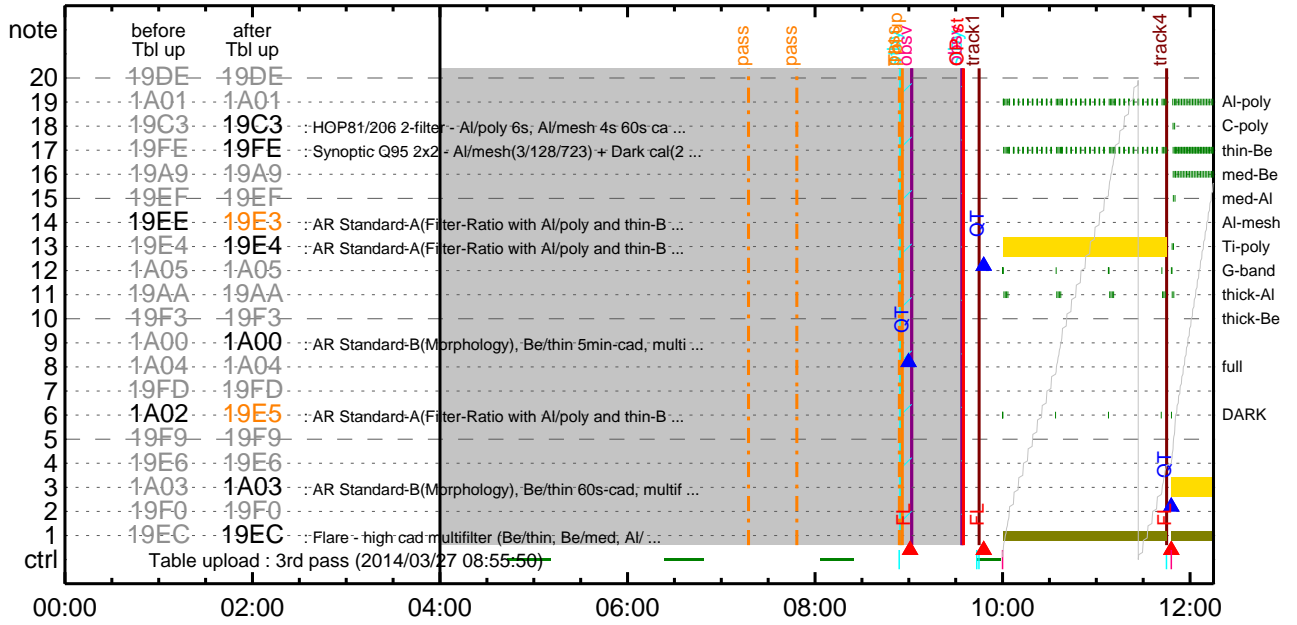
Flare Detection

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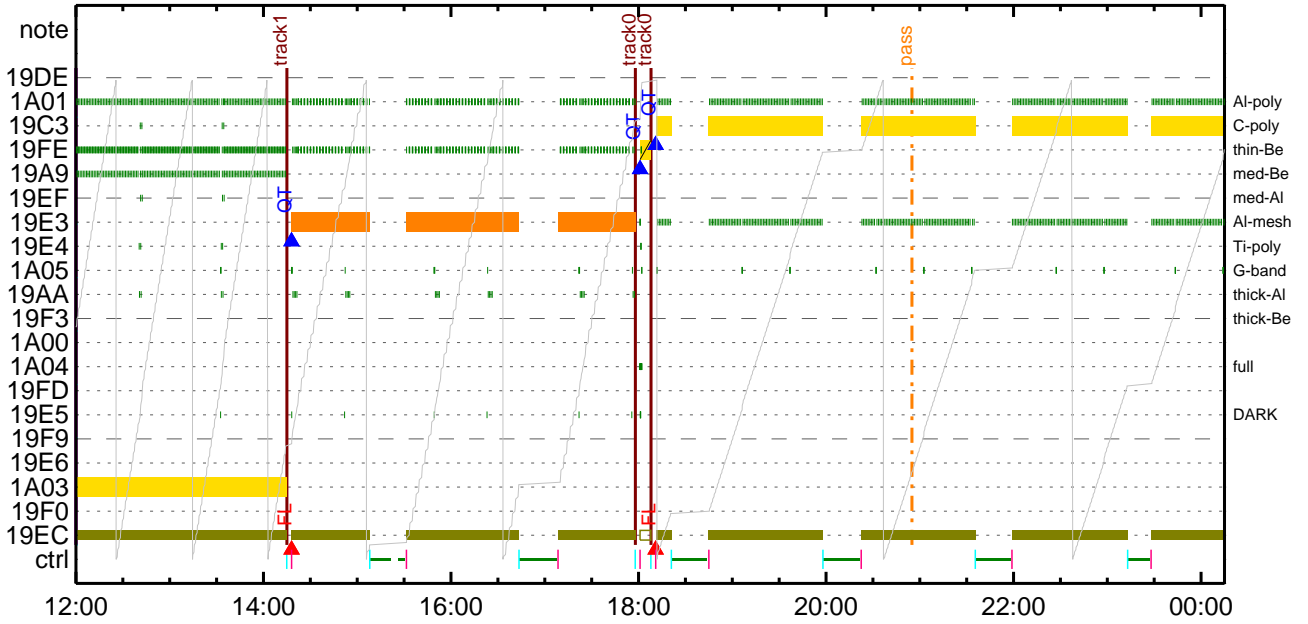
FLD Patrol

Term	Pointing (x, y)	Comment
03/27 18:08:16 - 03/28 06:00:16	Fixed (-15.0, -940.0)	HOP 206 (south pole)
03/28 06:10:16 - 03/28 18:00:16	Track (506.2, -169.8) ^{Ⓢ 03/28 06:10:00}	AR 12014 cont
03/28 18:10:16 - 03/29 05:30:16	Track (591.8, -177.5) ^{Ⓢ 03/28 18:10:00}	AR 12014
03/29 05:40:16 - 04/01 09:21:00	Track (666.4, -185.8) ^{Ⓢ 03/29 05:40:00}	AR 12014
Open/Ti-poly	Open/thick-Al	close Safe Norm 8ms Obs 8x8 Q=50 30sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

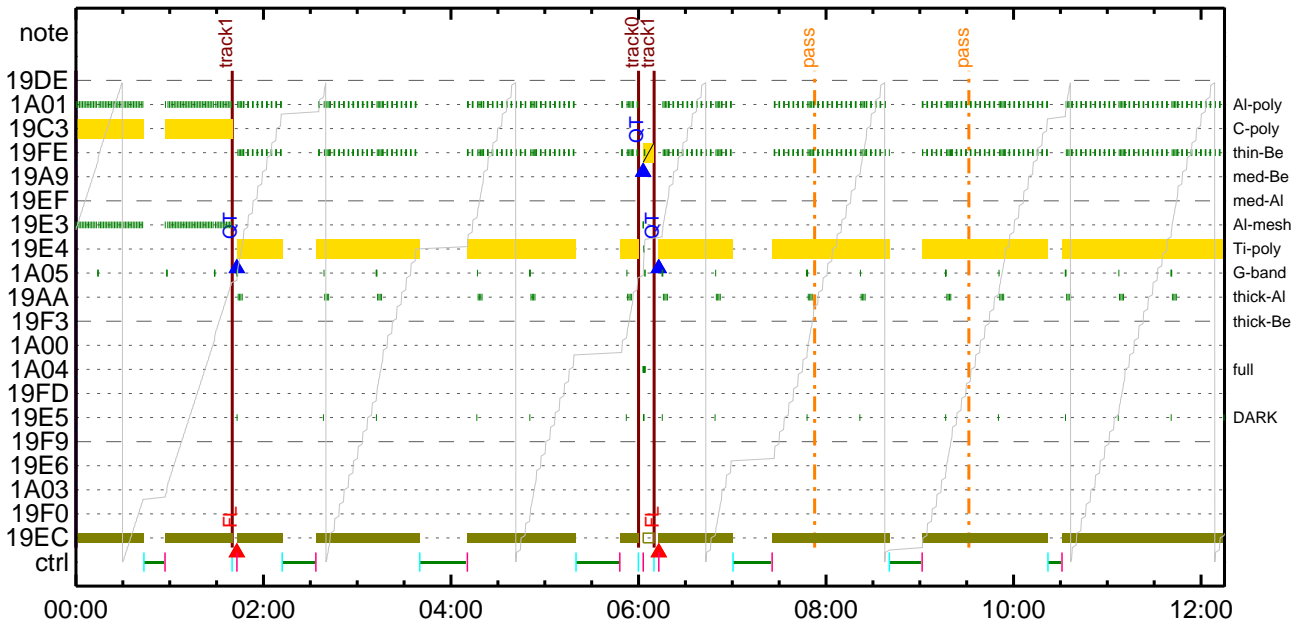
CMDI #0172 2014/03/27



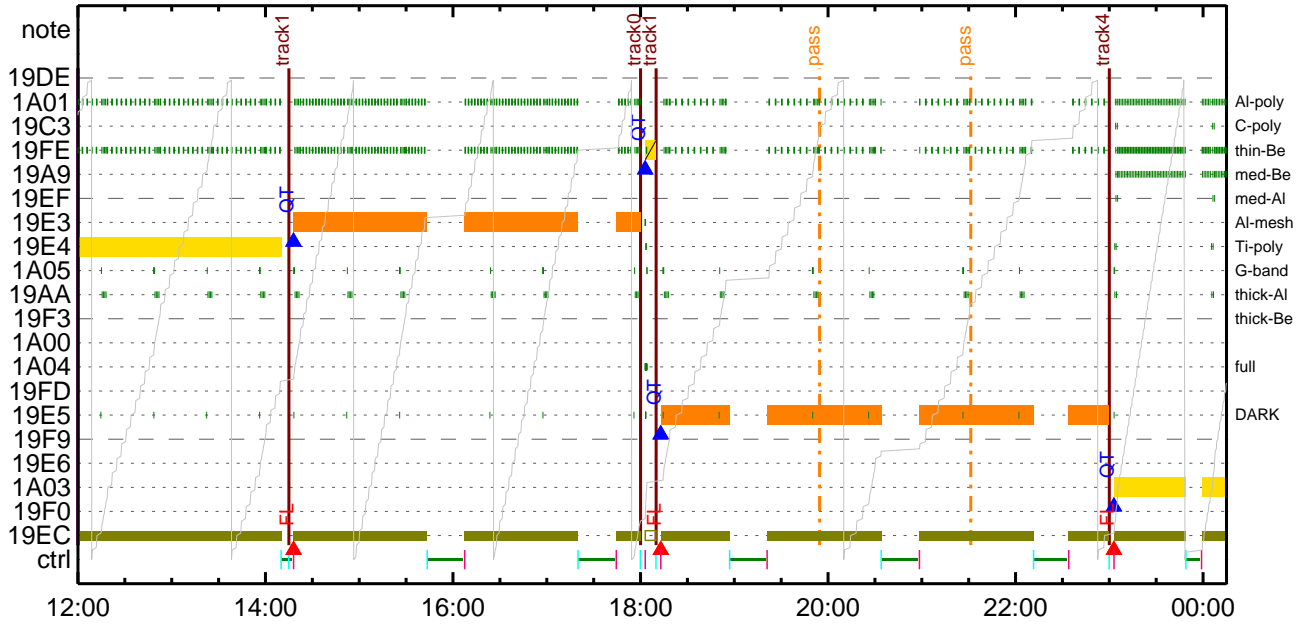
CMDI #0172 2014/03/27



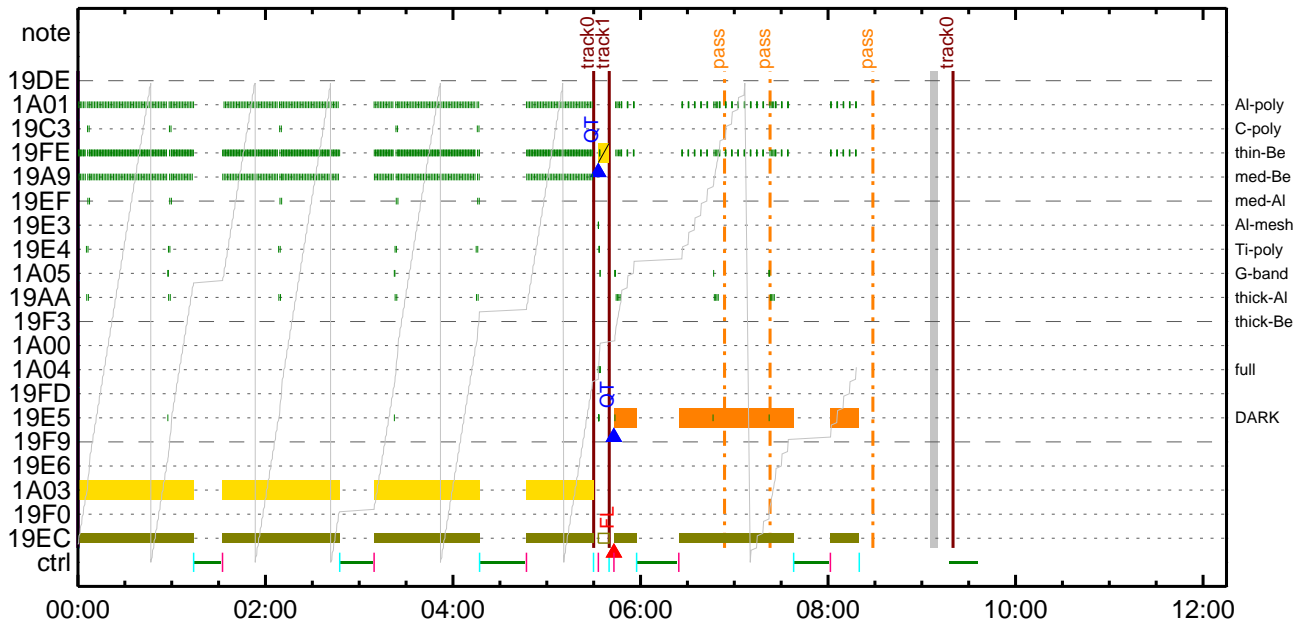
CMDI #0172 2014/03/28



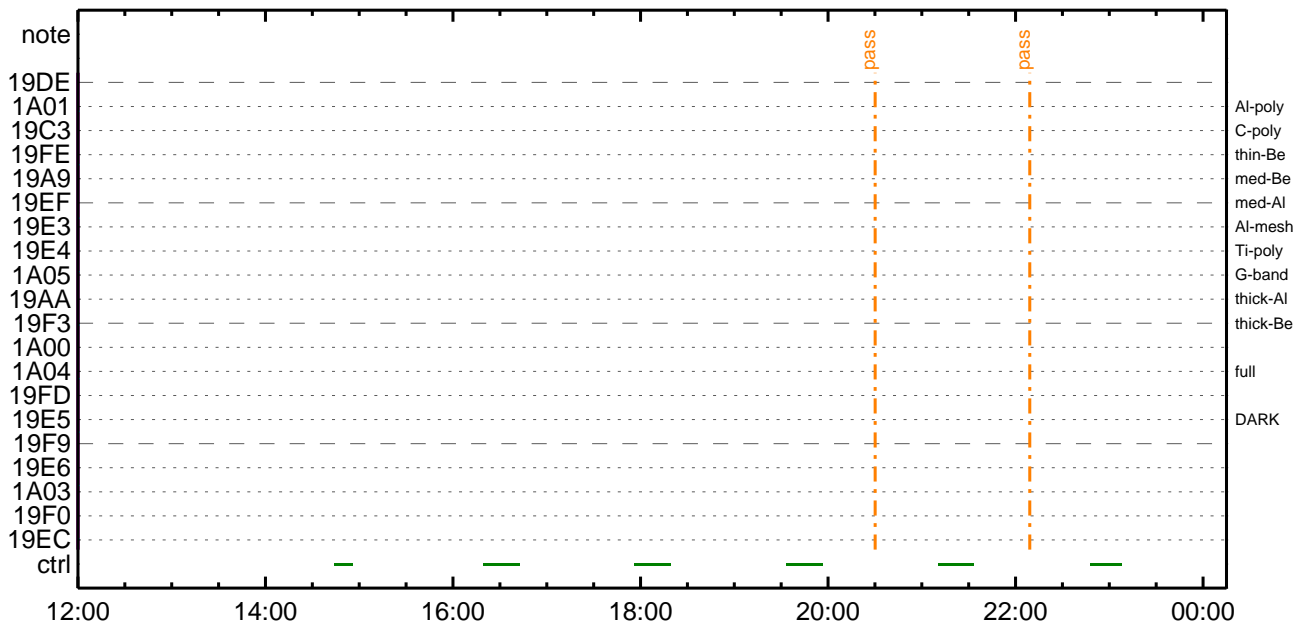
CMDI #0172 2014/03/28



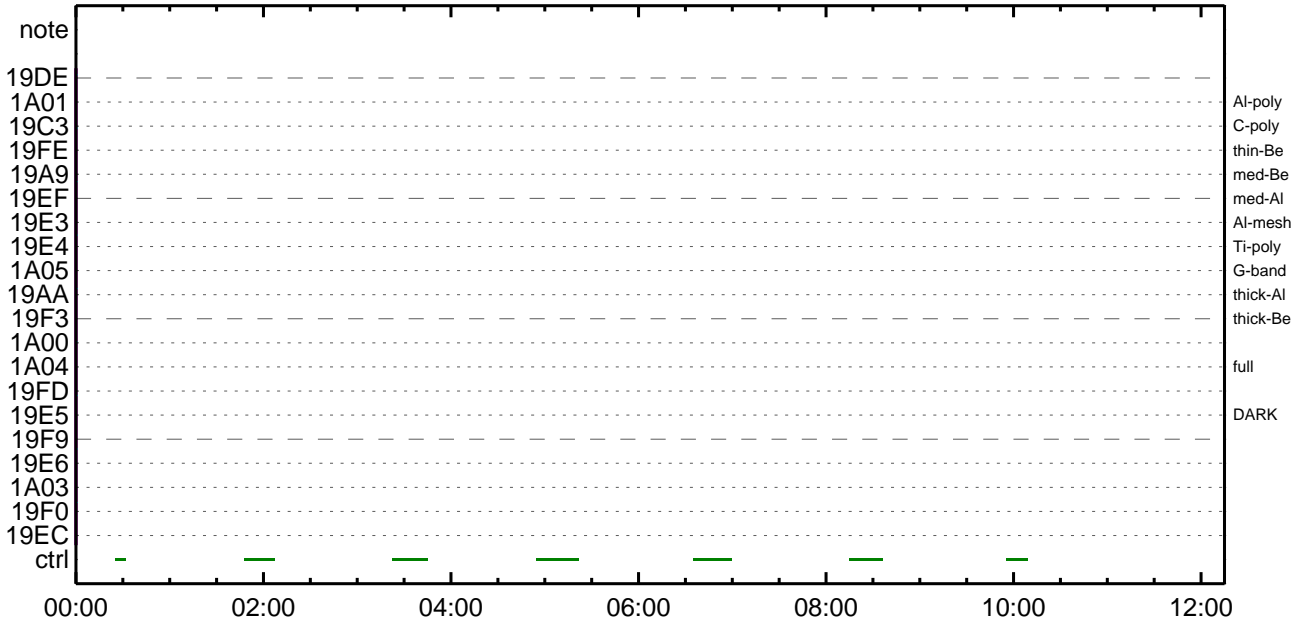
CMDI #0172 2014/03/29



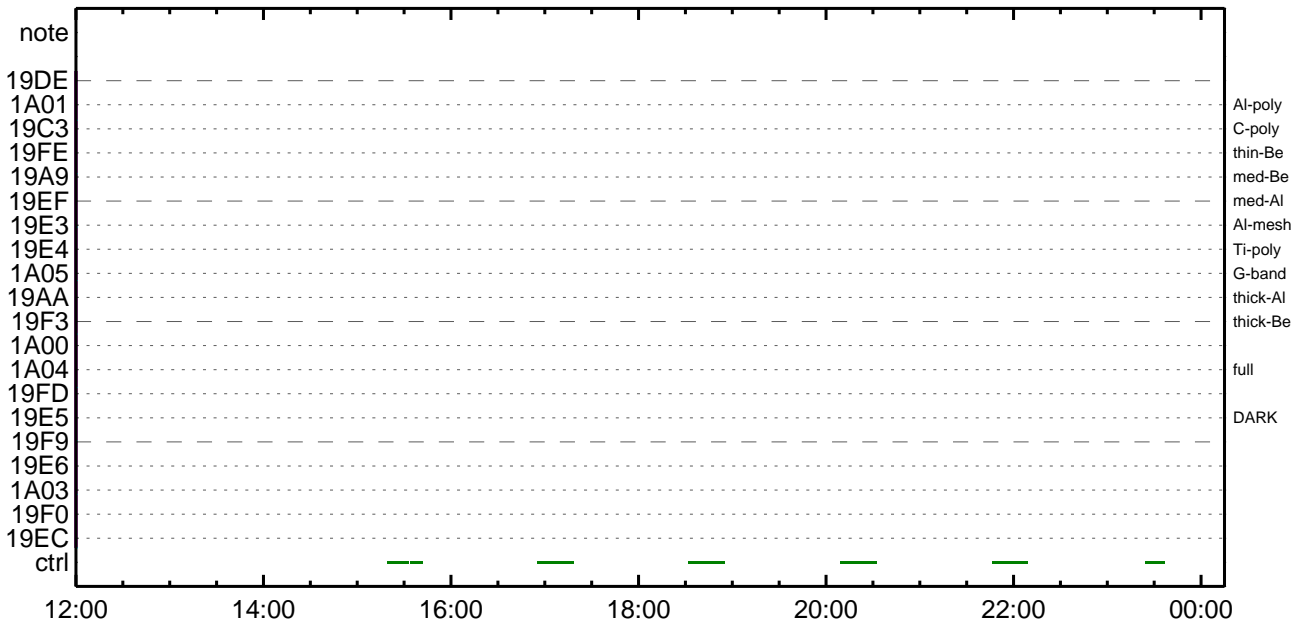
CMDI #0172 2014/03/29



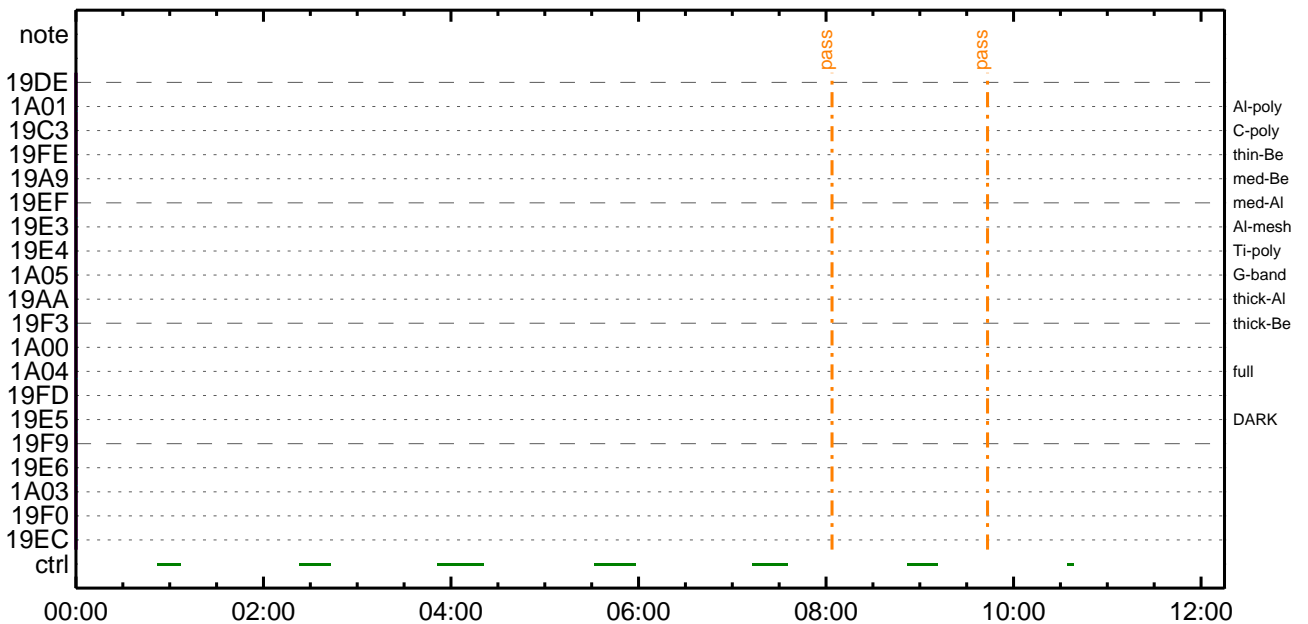
CMDI #0172 2014/03/30



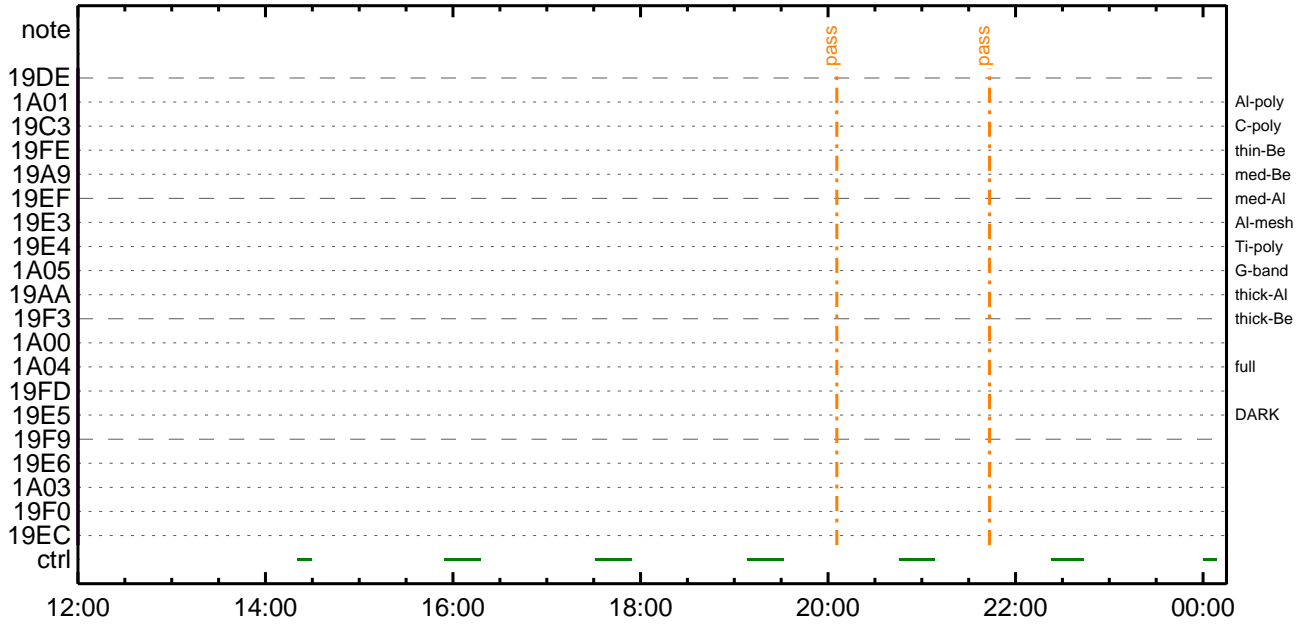
CMDI #0172 2014/03/30



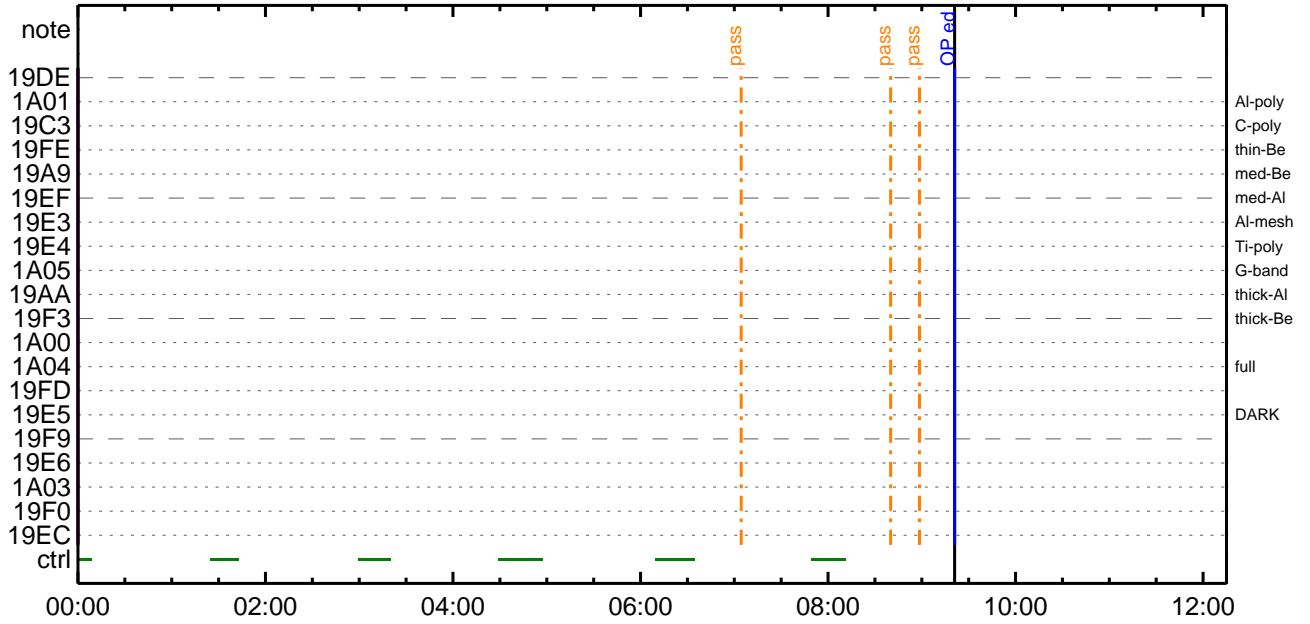
CMDI #0172 2014/03/31



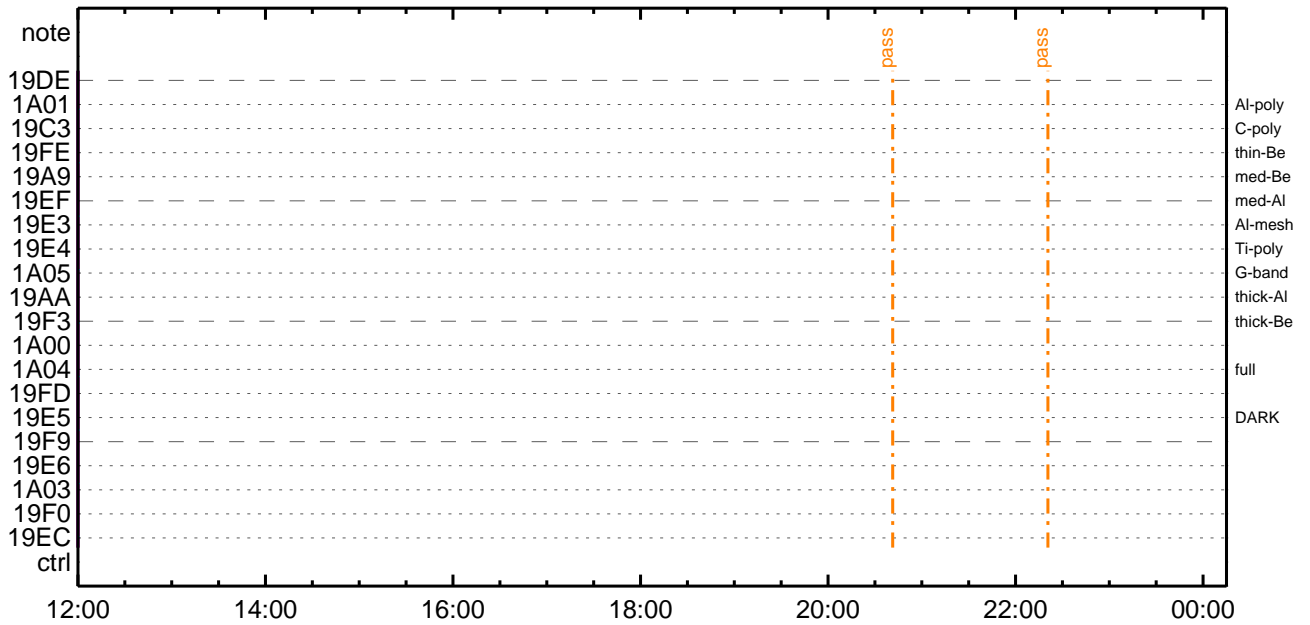
CMDI #0172 2014/03/31



CMDI #0172 2014/04/01



CMDI #0172 2014/04/01




```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YEi;YAY6Yx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YEi;ã
0103 S. OP op-283:OP
0104 ( )
0105 S. OG og-283:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°èYAY6Yx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAY6Yx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½Ê¹ç·è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAY6Yx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½Ê¹ç·è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAY6Yx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½Ê¹ç·è²îOKò³îÇ§
0165 C.
0166 C. ***** °Ê²¼òî½Ä´¶Ä°òÊÊ¬ò°Ã÷¿@ (¼âµ-YAY6Yx½ê½çòðÄÖËæç¼ª°²òè¼i¹çòçòâ) *****
0167 C. DHUªâ;½YEi;Ê½Y½;Yi;½YEi;Êòðîã²¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Ã÷¿@NG²î½î¹ç;ç°Ê²¼òîTI-CMDÃ÷¿@²î½Ä¹Ò²·²Ê²²²³²Ê;£
0180 C. ²²²¿;çSET²ÊDUMP²îÆ±°²îYÑY¹²ç¹Ò²|²³²Ê;£
0181 C.
0182 C. TIY³Y½Y6YË²òðÄî¿¿(UT)
0183 +. TI 2014-03-27 09:30:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2014-03-27 09:30:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2014-03-27 09:30:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-284 2014-03-27 13:24:16 82 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY-¼Ä»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿µÄµ°Æ»Í×ÁÇµÍYçYÄY×Yí;¼YÉ;ÈÈ%µ•íÉ;ÈÈ%°ÇÔµ•µ¿¼l¹çµÍ;çÄ®, ùµ¹µèµµµÇÁ+¿®µ•µÈµµµ³µÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCS Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCS Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047 BC (21 02)
0048 . C. Verify EIS in MANUAL mode
0049 . C. Estimated OBSTBL upload time is 0ms
0050 C. *****
0051 C. EIS START OBSTBL LOAD
0052 C. *****
0053 . S. RAM ram-821:EIS_OBSTBL
0054 ( )
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056 BC (07 07 07 00 00 70 00)
0057 C.
0058 C. Execute, after the success of OBSTBL upload.
0059 C. Set EIS TI-commands
0060 +. TI 2014-03-27 09:34:50.0
0061 DC 07-FC EIS_MODE_CHG_ENA
0062 BC (20)
0063 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064 C. *****
0065 C. EIS END OBSTBL LOAD
0066 C. *****
0067 C.
0068 . C. ***** MDP `ûÄîõî»ó¼YµÈÄµ¹µèDCBC•x²è *****
0069 C. (¼á°íYÓYÄYÈYÞYÉYáYçYèµÈ%¼µ¼Ä»Ûµ¹µè)
0070 . S. DC-BC dcbc-402:DCBC
0071 (MDP_known_event)
0072 C.
0073 C.
0074 . C. ***** YDÝ¹.İ Daily±¿ÍÑµÈ´Øµ¹µèDCBC•x²è *****
0075 . S. DC-BC dcbc-153:DCBC
0076 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077 C.
0078 C.
0079 . C. ;ãLOSÁYŞYÄY-¼Ä»Û;ã
0080 C.
0081 . C. ***** LOS *****
0082 C.
```



```

0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 85 83 08 08)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 20)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 20 08)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 80 80 08 20)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 + DC 07-F8 XRT_FOCUS_POSITION
0141 BC (22 fe 97 00)
0142 + DC 07-F0 MDP_XRT_FLD_ENA
0143 BC (d8)
0144 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0145 BC (c8)
0146 + DC 07-F0 MDP_XRT_AEC_RESET
0147 BC (d0)
0148 + DC 07-F0 MDP_XRT_ARS_DIS
0149 BC (d5)
0150 + DC 07-F0 MDP_XRT_FLD_RESET
0151 BC (da)
0152 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0153 BC (c4 09)
0154 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0155 BC (c5 01)
0156 . C. ----- Success Verify ? OK / NG ____
0157 C.
0158 C.
0159 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0160 C.
0161 +. DC 07-F0 MDP_XRT_MODE_OBSV
0162 BC (c2)
0163 +. TI 2014-03-27 09:34:02.0
0164 DC 07-F0 MDP_XRT_MODE_OBSV
0165 BC (c2)
0166 . C. ----- Success Verify ? OK / NG ____
0167 C.
0168 C. ***** XRT END *****
0169 . C. *****
0170 C. SOT table upload
0171 C. *****
0172 . C. < Stop FG table >
0173 +. DC 07-F0 MDP_FG_CTRL_MANU
0174 BC (51)
0175 . C. -----
0176 C. MDP_FG_CTRL_MODE = MANU [ ]
0177 C. -----
0178 C.
0179 . C. <Upload FG Observation Table>
0180 . S. RAM ram-265:MDP_OBS_F
0181 ( )
0182 C.
0183 . C. < Dump RAMID=MDP_OBS_F >
0184 +. DC 07-F0 MDP_DUMP_FGTBL
0185 BC (82 07 00 00 00 38 b8)
0186 C. -----
0187 C. MDP_OBS_F verify = OK/NG [ ]
0188 C. -----
0189 C.
0190 . C. < Stop SP table >
0191 +. DC 07-F0 MDP_SP_CTRL_MANU
0192 BC (61)
0193 C. -----

```

```
0194 C. MDP_SP_CTRL_MODE = MANU [ ]
0195 C. -----
0196 C.
0197 . C. <Upload SP Observation Table>
0198 . S. RAM ram-285:MDP_OBS_S
0199 ( )
0200 C.
0201 . C. < Dump RAMID=MDP_OBS_S >
0202 +. DC 07-F0 MDP_DUMP_SPTBL
0203 BC (83 07 00 00 00 38 b8)
0204 C. -----
0205 C. MDP_OBS_S verify = OK/NG [ ]
0206 C. -----
0207 C.
0208 C. *****
0209 C. SOT TI command set
0210 C. *****
0211 C. Execute, after the success of TBL upload.
0212 +. TI 2014-03-27 09:34:18.0
0213 DC 07-F0 MDP_SOT_MODE_OBSV
0214 BC (40)
0215 C. -----
0216 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0217 C. -----
0218 C.
0219 C.
0220 . C. ***** MDP `úÃîñî»ô¼ÝðÊÂðñ¹æDCBC•x²è *****
0221 C. (¼ãºîÝÓÝÄÝÈÝÞÝÈÝÁÝçÝèñÈ¼ñ¼Ã»Ûñ¹æè)
0222 . S. DC-BC dcbc-402:DCBC
0223 (MDP_known_event)
0224 C.
0225 C.
0226 . C. ***** ÝÐÝ¹·Ï Daily±¿ÎÑñÈ´Øñ¹æDCBC•x²è *****
0227 . S. DC-BC dcbc-153:DCBC
0228 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0229 C.
0230 C.
0231 . C. ;ãLOSÝÁÝ§ÝÄÝ´¼Ã»Û;ã
0232 C.
0233 . C. ***** LOS *****
0234 C.
```

Mar 27, 14 13:24

XRT_OGLIST_0172.chk

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*** OP Sequence for XRT ***

2014/03/27	09:43:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/27	09:43:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/27	09:43:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/27	09:44:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/27	09:44:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/03/27	09:45:00.5	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2014/03/27	09:45:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/03/27	09:45:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/03/27	09:45:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/03/27	09:45:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/03/27	09:45:24.0	XRT_FLD_RESET_431_OG [0x1af]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/27	09:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/03/27	09:47:56.0	XRT_QT_PROG_SET_401_OG [0x191]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0d
2014/03/27	09:47:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/03/27	09:59:00.0	XRT_Custom_430_OG [0x1ae]			
2014/03/27	10:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/27	11:44:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/27	11:44:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/03/27	11:45:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	04 00 00 00 00
2014/03/27	11:45:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/03/27	11:45:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/03/27	11:45:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/03/27	11:45:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/03/27	11:45:24.0	XRT_FLD_RESET_431_OG [0x1af]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/27	11:47:56.0	XRT_QT_PROG_SET_422_OG [0x1a6]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2014/03/27	11:47:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/03/27	11:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/27	14:14:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/27	14:14:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/03/27	14:15:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2014/03/27	14:15:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/03/27	14:15:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/03/27	14:15:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/03/27	14:15:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/03/27	14:15:24.0	XRT_FLD_RESET_431_OG [0x1af]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/27	14:17:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2014/03/27	14:17:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/03/27	14:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/27	15:08:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/27	15:08:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/27	15:08:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/27	15:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/03/27	15:30:30.0	XRT_Custom_430_OG [0x1ae]			
2014/03/27	15:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/27	16:43:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/27	16:43:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/27	16:43:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			

2014/03/27	16:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/03/27	17:07:30.0	XRT_Custom_430_OG [0x1ae]					
2014/03/27	17:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/03/27	17:57:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/03/27	17:57:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/03/27	17:58:00.0	AOCS_ORe-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2014/03/27	17:58:16.0	XRT_FLD_DIS_434_OG [0x1b2]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/03/27	18:00:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/03/27	18:00:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/03/27	18:00:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2014/03/27	18:01:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/03/27	18:07:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/03/27	18:07:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2014/03/27	18:08:00.0	AOCS_ORe-point_Start_4_OG [0x09a]					
			AOCU_NM	5	02-76	00 53 8d 01 58	
2014/03/27	18:08:16.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/03/27	18:08:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/03/27	18:08:20.0	XRT_AEC_RESET_413_OG [0x19d]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/03/27	18:08:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/03/27	18:08:24.0	XRT_FLD_RESET_431_OG [0x1af]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/03/27	18:10:56.0	XRT_QT_PROG_SET_445_OG [0x1bd]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 12	
2014/03/27	18:10:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 01	
2014/03/27	18:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/03/27	18:21:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/03/27	18:21:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/03/27	18:21:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/03/27	18:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/03/27	18:44:00.5	XRT_Custom_430_OG [0x1ae]					
2014/03/27	18:45:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/03/27	19:58:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/03/27	19:58:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/03/27	19:58:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/03/27	20:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/03/27	20:21:30.0	XRT_Custom_430_OG [0x1ae]					
2014/03/27	20:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/03/27	21:35:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/03/27	21:35:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/03/27	21:35:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/03/27	21:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/03/27	21:58:00.0	XRT_Custom_430_OG [0x1ae]					
2014/03/27	21:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/03/27	23:13:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/03/27	23:13:02.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/03/27	23:13:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/03/27	23:16:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/03/27	23:27:00.0	XRT_Custom_430_OG [0x1ae]					
2014/03/27	23:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/03/28	00:43:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/03/28	00:43:32.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/03/28	00:43:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					

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2014/03/28	00:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/28	00:56:00.0	XRT_Custom_430_OG [0x1ae]								
2014/03/28	00:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/28	01:39:54.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/28	01:39:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/03/28	01:40:00.0	AOCS_ORe-point_Start_5_OG [0x09b]								
			AOCU_NM	5	02-76	01	fc	e6	ff	1b
2014/03/28	01:40:16.0	XRT_FLD_ENA_411_OG [0x19b]								
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/03/28	01:40:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/03/28	01:40:20.0	XRT_AEC_RESET_413_OG [0x19d]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/03/28	01:40:22.0	XRT_ARS_DIS_423_OG [0x1a7]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/03/28	01:40:24.0	XRT_FLD_RESET_431_OG [0x1af]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/28	01:42:56.0	XRT_QT_PROG_SET_401_OG [0x191]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2014/03/28	01:42:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]								
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2014/03/28	01:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/28	02:12:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/28	02:12:02.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/28	02:12:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/28	02:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/28	02:32:30.0	XRT_Custom_430_OG [0x1ae]								
2014/03/28	02:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/28	03:40:00.0	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/28	03:40:02.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/28	03:40:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/28	03:43:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/28	04:09:30.0	XRT_Custom_430_OG [0x1ae]								
2014/03/28	04:10:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/28	05:20:00.5	XRT_CTRL_MANU_400_OG [0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/28	05:20:03.0	XRT_FLD_RESET_415_OG [0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/28	05:20:05.0	XRT_PREFLR_STRT_418_OG [0x1a2]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/28	05:23:15.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/28	05:47:00.0	XRT_Custom_430_OG [0x1ae]								
2014/03/28	05:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/28	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/28	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]								
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/03/28	06:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]								
			AOCU_NM	5	02-76	00	00	00	00	00
2014/03/28	06:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]								
			MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/03/28	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/03/28	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/03/28	06:02:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2014/03/28	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/28	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/28	06:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/03/28	06:10:00.0	AOCS_ORe-point_Start_1_OG [0x097]								
			AOCU_NM	5	02-76	01	00	00	00	00
2014/03/28	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]								
			MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/03/28	06:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/03/28	06:10:20.0	XRT_AEC_RESET_413_OG [0x19d]								
			MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/03/28	06:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]								
			MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/03/28	06:10:24.0	XRT_FLD_RESET_431_OG [0x1af]								
			MDP_XRT_FLD_RESET	1	07-F0	da				

2014/03/28	06:12:56.0	XRT_QT_PROG_SET_401_OG [0x191]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d			
2014/03/28	06:12:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2014/03/28	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/28	07:00:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/28	07:00:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/28	07:00:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/28	07:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/28	07:24:30.0	XRT_Custom_430_OG [0x1ae]							
2014/03/28	07:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/28	08:40:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/28	08:40:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/28	08:40:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/28	08:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/28	09:00:30.5	XRT_Custom_430_OG [0x1ae]							
2014/03/28	09:01:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/28	10:22:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/28	10:22:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/28	10:22:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/28	10:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/28	10:30:00.0	XRT_Custom_430_OG [0x1ae]							
2014/03/28	10:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/28	14:10:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/28	14:10:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/28	14:10:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/28	14:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/28	14:14:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/28	14:14:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2014/03/28	14:15:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00 00 00 00			
2014/03/28	14:15:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/03/28	14:15:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/03/28	14:15:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/03/28	14:15:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/03/28	14:15:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/28	14:17:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2014/03/28	14:17:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2014/03/28	14:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/28	15:43:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/28	15:43:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/28	15:43:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/28	15:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/28	16:06:30.0	XRT_Custom_430_OG [0x1ae]							
2014/03/28	16:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/28	17:20:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/03/28	17:20:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/03/28	17:20:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/03/28	17:23:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/03/28	17:43:30.5	XRT_Custom_430_OG [0x1ae]							
2014/03/28	17:44:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/03/28	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2014/03/28	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00
2014/03/28	18:00:00.0	AOCs_OrE-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00	00	00	00 00
2014/03/28	18:00:16.0	XRT_FLD_DIS_434_OG [0x1b2] MDP_XRT_FLD_DIS	1	07-F0		d9		
2014/03/28	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0		c9		
2014/03/28	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5		
2014/03/28	18:02:58.0	XRT_QT_PROG_SET_447_OG [0x1bf] MDP_XRT_QT_PROG_SET	2	07-F0		c4	11	
2014/03/28	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2014/03/28	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2014/03/28	18:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2014/03/28	18:10:00.0	AOCs_OrE-point_Start_1_OG [0x097] AOCU_NM	5	02-76	01	00	00	00 00
2014/03/28	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0		d8		
2014/03/28	18:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2014/03/28	18:10:20.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0		d0		
2014/03/28	18:10:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5		
2014/03/28	18:10:24.0	XRT_FLD_RESET_431_OG [0x1af] MDP_XRT_FLD_RESET	1	07-F0		da		
2014/03/28	18:12:56.0	XRT_QT_PROG_SET_441_OG [0x1b9] MDP_XRT_QT_PROG_SET	2	07-F0		c4	06	
2014/03/28	18:12:58.0	XRT_FL_PROG_SET_448_OG [0x1c0] MDP_XRT_FL_PROG_SET	2	07-F0		c5	01	
2014/03/28	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2014/03/28	18:57:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2014/03/28	18:57:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2014/03/28	18:57:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2014/03/28	19:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2014/03/28	19:20:00.0	XRT_Custom_430_OG [0x1ae]						
2014/03/28	19:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2014/03/28	20:34:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2014/03/28	20:34:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2014/03/28	20:34:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2014/03/28	20:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2014/03/28	20:57:30.0	XRT_Custom_430_OG [0x1ae]						
2014/03/28	20:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2014/03/28	22:11:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2014/03/28	22:11:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da		
2014/03/28	22:11:34.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0		e8		
2014/03/28	22:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0		e9		
2014/03/28	22:33:00.5	XRT_Custom_430_OG [0x1ae]						
2014/03/28	22:34:00.5	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2014/03/28	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1		
2014/03/28	22:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00
2014/03/28	23:00:00.0	AOCs_OrE-point_Start_2_OG [0x098] AOCU_NM	5	02-76	04	00	00	00 00
2014/03/28	23:00:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0		d8		
2014/03/28	23:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8		
2014/03/28	23:00:20.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0		d0		
2014/03/28	23:00:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5		
2014/03/28	23:00:24.0	XRT_FLD_RESET_431_OG [0x1af] MDP_XRT_FLD_RESET	1	07-F0		da		
2014/03/28	23:02:56.0	XRT_QT_PROG_SET_422_OG [0x1a6] MDP_XRT_QT_PROG_SET	2	07-F0		c4	03	
2014/03/28	23:02:58.0	XRT_FL_PROG_SET_448_OG [0x1c0] MDP_XRT_FL_PROG_SET	2	07-F0		c5	01	
2014/03/28	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0		c0		
2014/03/28	23:49:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0		c1		

2014/03/28	23:49:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/28	23:49:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/28	23:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/03/28	23:58:00.0	XRT_Custom_430_OG [0x1ae]			
2014/03/28	23:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/29	01:14:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/29	01:14:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/29	01:14:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/29	01:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/03/29	01:31:30.0	XRT_Custom_430_OG [0x1ae]			
2014/03/29	01:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/29	02:47:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/29	02:47:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/29	02:47:34.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/29	02:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/03/29	03:08:30.0	XRT_Custom_430_OG [0x1ae]			
2014/03/29	03:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/29	04:17:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/29	04:17:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/29	04:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/29	04:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/03/29	04:46:00.0	XRT_Custom_430_OG [0x1ae]			
2014/03/29	04:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/29	05:29:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/29	05:29:56.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/03/29	05:30:00.0	AOCS_Orе-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00 00 00 00 00
2014/03/29	05:30:16.0	XRT_FLD_DIS_434_OG [0x1b2] MDP_XRT_FLD_DIS	1	07-F0	d9
2014/03/29	05:32:54.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/03/29	05:32:56.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2014/03/29	05:32:58.0	XRT_QT_PROG_SET_447_OG [0x1bf] MDP_XRT_QT_PROG_SET	2	07-F0	c4 11
2014/03/29	05:33:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/29	05:39:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/29	05:39:56.0	XRT_FOCUS_POSITION_410_OG [0x19a] XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/03/29	05:40:00.0	AOCS_Orе-point_Start_1_OG [0x097] AOCU_NM	5	02-76	01 00 00 00 00
2014/03/29	05:40:16.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2014/03/29	05:40:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/03/29	05:40:20.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0	d0
2014/03/29	05:40:22.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2014/03/29	05:40:24.0	XRT_FLD_RESET_431_OG [0x1af] MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/29	05:42:56.0	XRT_QT_PROG_SET_441_OG [0x1b9] MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2014/03/29	05:42:58.0	XRT_FL_PROG_SET_448_OG [0x1c0] MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/03/29	05:43:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/29	05:57:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/03/29	05:57:32.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da
2014/03/29	05:57:34.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/03/29	06:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/03/29	06:23:30.0	XRT_Custom_430_OG [0x1ae]			
2014/03/29	06:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/03/29	07:38:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1

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2014/03/29	07:38:02.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2014/03/29	07:38:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/03/29	07:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/03/29	08:00:30.0	XRT_Custom_430_OG [0x1ae]				
2014/03/29	08:01:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/03/29	08:20:00.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/03/29	09:20:00.0	AOCS_ORe-point_Start_3_OG [0x099]				
		AOCU_NM	5	02-76	00 00 00 00 00	