

XRT Timeline to be uploaded on 2014/04/03

Period: 2014/04/03 11:06:00 - 2014/04/08 10:52:00

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Normal mode

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XOB #19FD: AR standard-B (thin-Be) with PFB, 384 FOV at 1064x1048, thin-Be, thick-Al, and Al/Poly context, With G-band (33ms/45ms leak), 10s cad, lowe														
Term		Pointing (x, y)						Comment						
04/03 12:03:06 - 04/03 12:59:54		Track (363.0, -114.1) ^{© 04/03 11:16:00}						# OP start + 10min, AR 12021 for HOP 247						
PROG= 07 1-time(s)														
└─ Subr= 1 1-time(s) 2.0sec														
└─ Seqn= 8 2-time(s) 2.0sec														
		Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2 2-time(s) 2.0sec														
└─ Seqn= 24 1-time(s) 2.0sec														
		Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
		Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 42 2-time(s) 2.0sec														
		Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
		thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
		Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 87 75-time(s) 2.0sec														
		thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	10.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A08: CCD Monitor During Bakeout - G-Band 33ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term		Pointing (x, y)						Comment						
04/03 15:03:00 - 04/03 15:09:54		Fixed (-528.4, -528.4)						XRT post bake out obs (1/4)						
PROG= 05 1-time(s)														
└─ Subr= 1 1-time(s) 12.0sec														
└─ Seqn= 88 1-time(s) 12.0sec														
		Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
		Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
		Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
		Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec														
└─ Seqn= 23 2-time(s) 2.0sec														
		Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 3 2-time(s) 2.0sec														
└─ Seqn= 12 1-time(s) 2.0sec														
		Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 14 1-time(s) 2.0sec														
		Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A09: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms-2

Term		Pointing (x, y)						Comment						
04/03 15:13:00 - 04/03 15:19:54		Fixed (528.4, -528.4)						XRT post bake out obs (2/4)						
PROG= 02 1-time(s)														
└─ Subr= 1 1-time(s) 12.0sec														
└─ Seqn= 31 1-time(s) 12.0sec														
		Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
		Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
		Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
		Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec														
└─ Seqn= 23 2-time(s) 2.0sec														
		Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 3 2-time(s) 2.0sec														
└─ Seqn= 12 1-time(s) 2.0sec														
		Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 14 1-time(s) 2.0sec														
		Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A0A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms-2

Term		Pointing (x, y)						Comment						
04/03 15:23:00 - 04/03 15:29:54		Fixed (528.4, 528.4)						XRT post bake out obs (3/4)						
PROG= 15 1-time(s)														
└─ Subr= 1 1-time(s) 12.0sec														
└─ Seqn= 81 1-time(s) 12.0sec														
		Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
		Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
		Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
		Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec

Subr= 2	1-time(s)	2.0sec																		
└─ Seqn= 23	2-time(s)	2.0sec																		
└─ Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Subr= 3	2-time(s)	2.0sec																		
└─ Seqn= 12	1-time(s)	2.0sec																		
└─ Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048	(1024, 1024)	DPCM	0	0	2.0sec							
└─ Seqn= 14	1-time(s)	2.0sec																		
└─ Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048	(1024, 1024)	Q=95	0	0	2.0sec							
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)		Comp.	AEC Buffer	Interval								

XOB #1A0B: CCD Monitor During Bakeout - G-Band 33ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment
04/03 15:33:00 - 04/03 16:05:30	Fixed (-528.4, 528.4)	XRT post bake out obs (4/4)
PROG= 20 1-time(s)		
Subr= 1	1-time(s)	12.0sec
└─ Seqn= 28	1-time(s)	12.0sec
└─ Open/G-band	Open/G-band	open
└─ Open/G-band	Open/G-band	open
└─ Open/thick-Be	Open/thick-Be	close
└─ Open/thick-Be	Open/thick-Be	close
Subr= 2	1-time(s)	2.0sec
└─ Seqn= 23	2-time(s)	2.0sec
└─ Open/Al-mesh	Open/Ti-poly	close
└─ Open/Ti-poly	Open/thick-Al	close
Subr= 3	2-time(s)	2.0sec
└─ Seqn= 12	1-time(s)	2.0sec
└─ Open/G-band	Open/G-band	close
└─ Seqn= 14	1-time(s)	2.0sec
└─ Open/G-band	Open/G-band	open
Default Filter	Thicker Filter	VLS
mode	image	Exp.
CCD	Bin	ROI: size (center)
Comp.	AEC Buffer	Interval

XOB #19FE: Synoptic Q95 2x2 - Al/mesh(3/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(8/362/1443) + Thin...

Term	Pointing (x, y)	Comment
04/03 16:54:00 - 04/03 17:42:00	Fixed (0.0, 0.0)	* HOP 130 (3/15), synoptic shifted manually
04/04 05:55:30 - 04/04 06:02:24	Fixed (0.0, 0.0)	synoptic, shifted -7.5 min
04/04 17:58:00 - 04/04 18:04:54	Fixed (0.0, 0.0)	synoptic, shifted -5.0 min
04/05 06:03:00 - 04/05 06:09:54	Fixed (0.0, 0.0)	synoptic
PROG= 17 1-time(s)		
Subr= 1	1-time(s)	12.0sec
└─ Seqn= 35	1-time(s)	4.0sec
└─ Open/Al-mesh	Open/Al-mesh	close
└─ Open/Al-mesh	Open/Al-mesh	close
└─ Open/Al-mesh	Open/Al-mesh	close
└─ Seqn= 5	1-time(s)	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close
└─ Open/Ti-poly	Open/thick-Al	close
└─ Open/Ti-poly	Open/thick-Al	close
└─ Open/Ti-poly	Open/thick-Al	close
└─ Open/Ti-poly	Open/thick-Al	close
└─ Open/Ti-poly	Open/thick-Al	close
└─ Seqn= 72	1-time(s)	4.0sec
└─ Open/Ti-poly	Open/Ti-poly	close
└─ Open/Ti-poly	Open/Ti-poly	close
└─ Open/Ti-poly	Open/Ti-poly	close
└─ Seqn= 67	1-time(s)	2.0sec
└─ thin-Be/Open	thin-Be/Open	close
└─ thin-Be/Open	thin-Be/Open	close
└─ thin-Be/Open	thin-Be/Open	close
└─ Seqn= 6	1-time(s)	2.0sec
└─ Open/G-band	Open/G-band	open
└─ Open/G-band	Open/G-band	close
Default Filter	Thicker Filter	VLS
mode	image	Exp.
CCD	Bin	ROI: size (center)
Comp.	AEC Buffer	Interval

XOB #19E3: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)

Term	Pointing (x, y)	Comment
04/03 21:20:30 - 04/03 23:14:54	Fixed (-887.0, -363.0)	* HOP 253
04/04 11:03:08 - 04/04 15:00:00	Track (-466.2, 283.1) ^{Ⓜ 04/04 06:02:30}	AR 12027, HOP 245 (11-15UT)
04/04 20:48:00 - 04/04 22:59:54	Fixed (-887.0, -363.0)	HOP 253
04/04 23:03:00 - 04/05 03:37:00	Track (-326.7, 289.8) ^{Ⓜ 04/04 23:00:00}	AR 12027, HOP 245 (23-04UT)
PROG= 14 Inf.-time(s)		
Subr= 1	1-time(s)	2.0sec
└─ Seqn= 8	2-time(s)	2.0sec
└─ Open/G-band	Open/G-band	close
Subr= 2	2-time(s)	2.0sec
└─ Seqn= 24	1-time(s)	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close
└─ Open/G-band	Open/G-band	open

Seqn= 42		4-time(s)		2.0sec											
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec			
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec			
Seqn= 62		15-time(s)		120.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec			
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec			
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec			
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval				

XOB #19E6: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33m)													
Term	Pointing (x, y)		Comment										
04/04 04:03:00 - 04/04 05:52:24	Track (-482.3, 282.2)	@ 04/04 04:00:00	AR 12027 tracking									
04/04 06:05:30 - 04/04 11:00:00	Track (-466.2, 283.1)	@ 04/04 06:02:30	AR 12027 , HOP 245 (11-15UT)									
04/04 15:03:06 - 04/04 17:54:54	Track (-466.2, 283.1)	@ 04/04 06:02:30	AR 12027 , HOP 245 (11-15UT)									
04/04 18:08:00 - 04/04 20:44:54	Track (-368.2, 288.1)	@ 04/04 18:05:00	AR 12027 tracking									
04/05 04:08:00 - 04/05 05:59:54	Track (-326.7, 289.8)	@ 04/04 23:00:00	AR 12027, HOP 245 (23-04UT)									
04/05 06:13:00 - 04/05 09:20:00	Track (-264.9, 291.7)	@ 04/05 06:10:00	AR 12027 tracking									

PROG= 04		Inf.-time(s)													
Subr= 1		1-time(s)		2.0sec											
Seqn= 8		2-time(s)		2.0sec											
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec			
Subr= 2		2-time(s)		2.0sec											
Seqn= 24		1-time(s)		2.0sec											
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec			
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec			
Seqn= 42		4-time(s)		2.0sec											
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec			
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec			
Seqn= 62		8-time(s)		270.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec			
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec			
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec			
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec			
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval				

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Flare mode

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XOB #19EC: Flare - high cad multifilter (Be/thin, Be/med, Al/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gband (43m)													
Term	Pointing (x, y)		Comment										
04/03 12:03:06 - 04/03 12:59:54	Track (363.0, -114.1)	@ 04/03 11:16:00	# OP start + 10min, AR 12021 for HOP 247									
04/03 21:20:30 - 04/03 23:14:54	Fixed (-887.0, -363.0)		* HOP 253									
04/04 04:03:00 - 04/04 05:52:24	Track (-482.3, 282.2)	@ 04/04 04:00:00	AR 12027 tracking									
04/04 06:05:30 - 04/04 11:00:00	Track (-466.2, 283.1)	@ 04/04 06:02:30	AR 12027 , HOP 245 (11-15UT)									
04/04 11:03:08 - 04/04 15:00:00	Track (-466.2, 283.1)	@ 04/04 06:02:30	AR 12027 , HOP 245 (11-15UT)									
04/04 15:03:06 - 04/04 17:54:54	Track (-466.2, 283.1)	@ 04/04 06:02:30	AR 12027 , HOP 245 (11-15UT)									
04/04 18:08:00 - 04/04 20:44:54	Track (-368.2, 288.1)	@ 04/04 18:05:00	AR 12027 tracking									
04/04 20:48:00 - 04/04 22:59:54	Fixed (-887.0, -363.0)		HOP 253									
04/04 23:03:00 - 04/05 03:37:00	Track (-326.7, 289.8)	@ 04/04 23:00:00	AR 12027, HOP 245 (23-04UT)									
04/05 04:08:00 - 04/05 05:59:54	Track (-326.7, 289.8)	@ 04/04 23:00:00	AR 12027, HOP 245 (23-04UT)									
04/05 06:13:00 - 04/05 09:20:00	Track (-264.9, 291.7)	@ 04/05 06:10:00	AR 12027 tracking									

PROG= 01		30-time(s)													
Subr= 1		30-time(s)		2.0sec											
Seqn= 26		1-time(s)		4.0sec											
thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Subr= 2		1-time(s)		2.0sec											
Seqn= 10		1-time(s)		2.0sec											
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec			
Seqn= 11		1-time(s)		2.0sec											
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec			
Seqn= 15		1-time(s)		2.0sec											
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec			
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec			
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval				

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Active Region Search

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NOT USED

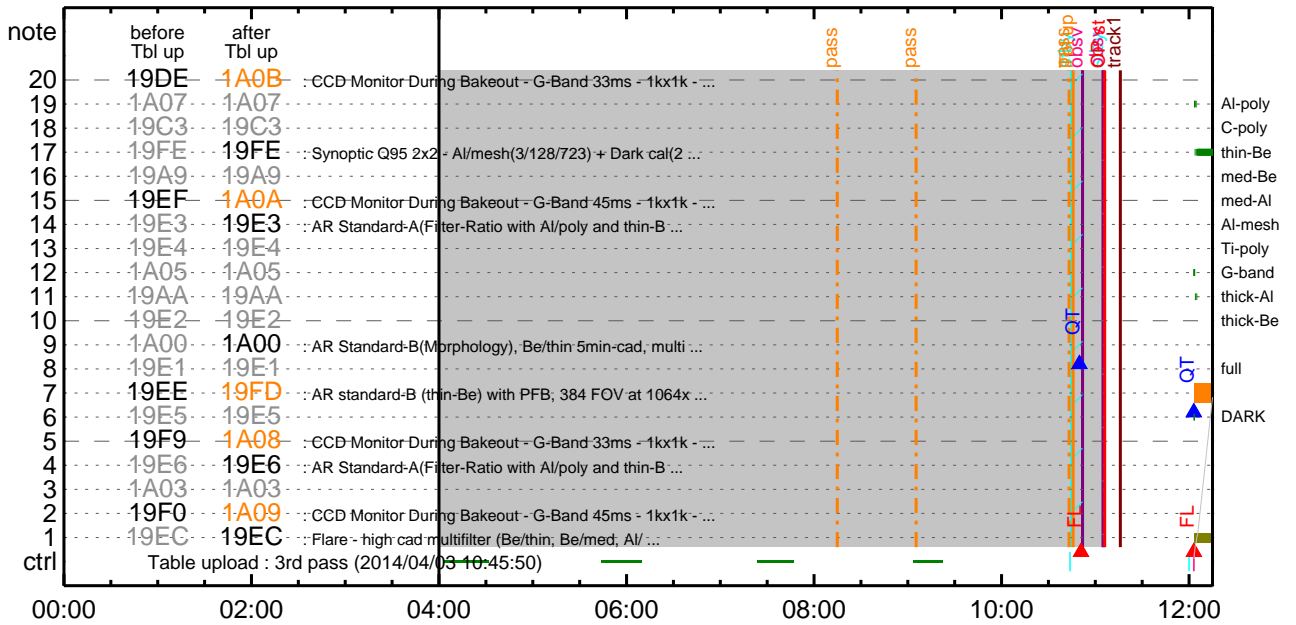
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Flare Detection

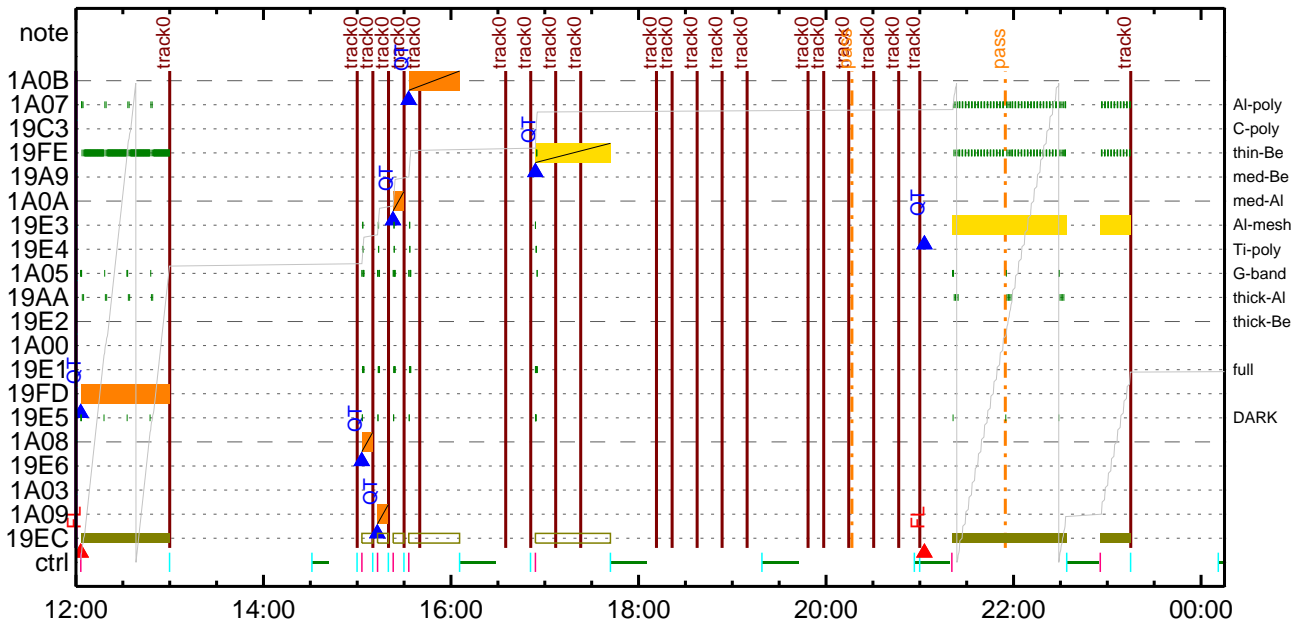
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FLD Patrol												
Term		Pointing (x, y)						Comment				
04/03 21:00:16 - 04/04 05:52:46		Fixed (-887.0, -363.0)						* HOP 253				
04/04 06:02:46 - 04/04 17:55:16		Track (-466.2, 283.1) @ 04/04 06:02:30						AR 12027 , HOP 245 (11-15UT)				
04/04 18:05:16 - 04/05 06:00:16		Track (-368.2, 288.1) @ 04/04 18:05:00						AR 12027 tracking				
04/05 06:10:16 - 04/08 10:52:00		Track (-264.9, 291.7) @ 04/05 06:10:00						AR 12027 tracking				
Open/Ti-poly		Open/thick-Al		close	Safe	Norm	8ms	Obs	8x8		Q=50	80sec
Default Filter		Thicker Filter		VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

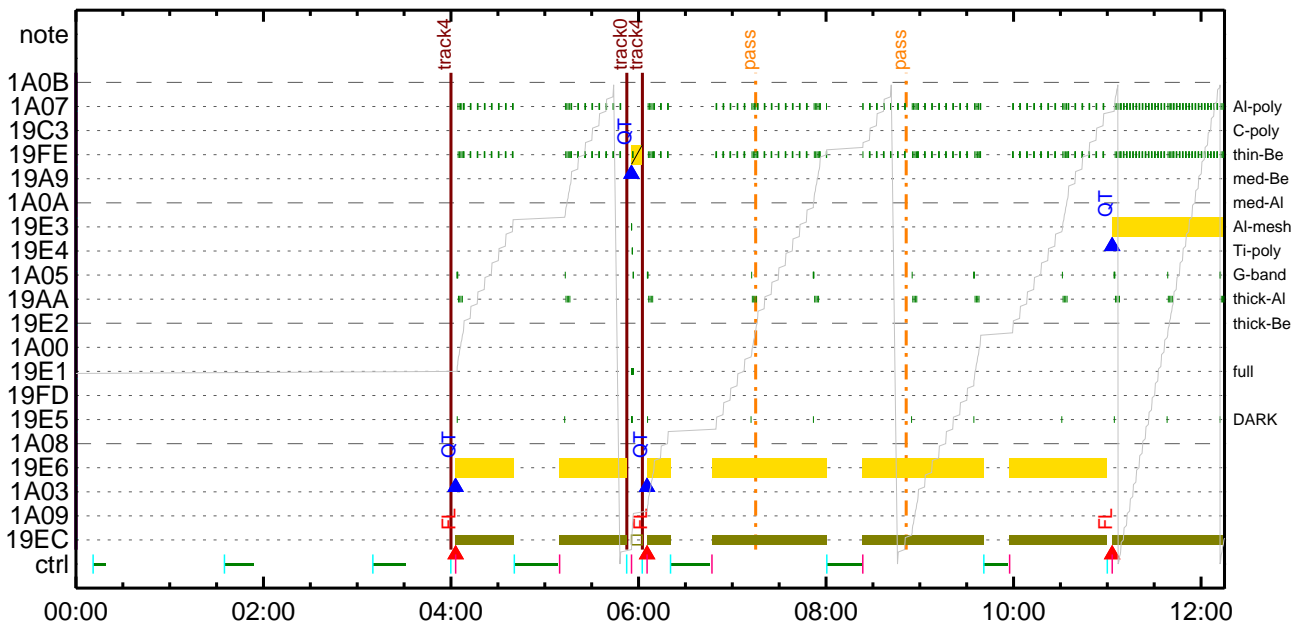
CMDI #0193 2014/04/03



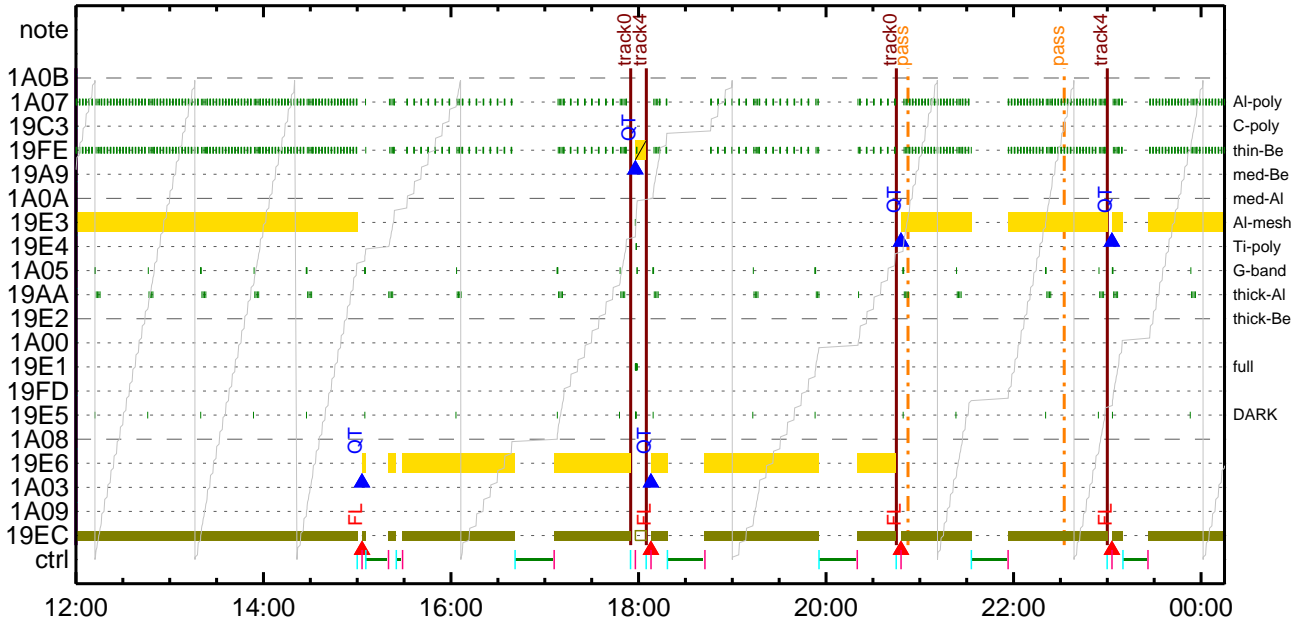
CMDI #0193 2014/04/03



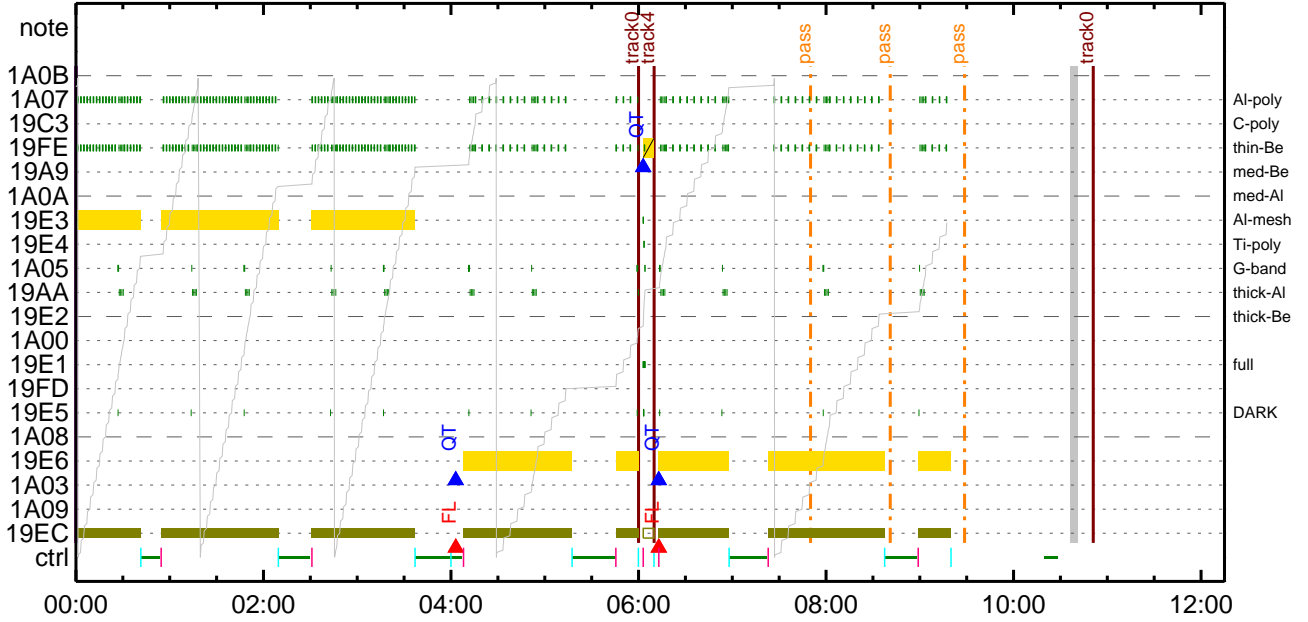
CMDI #0193 2014/04/04



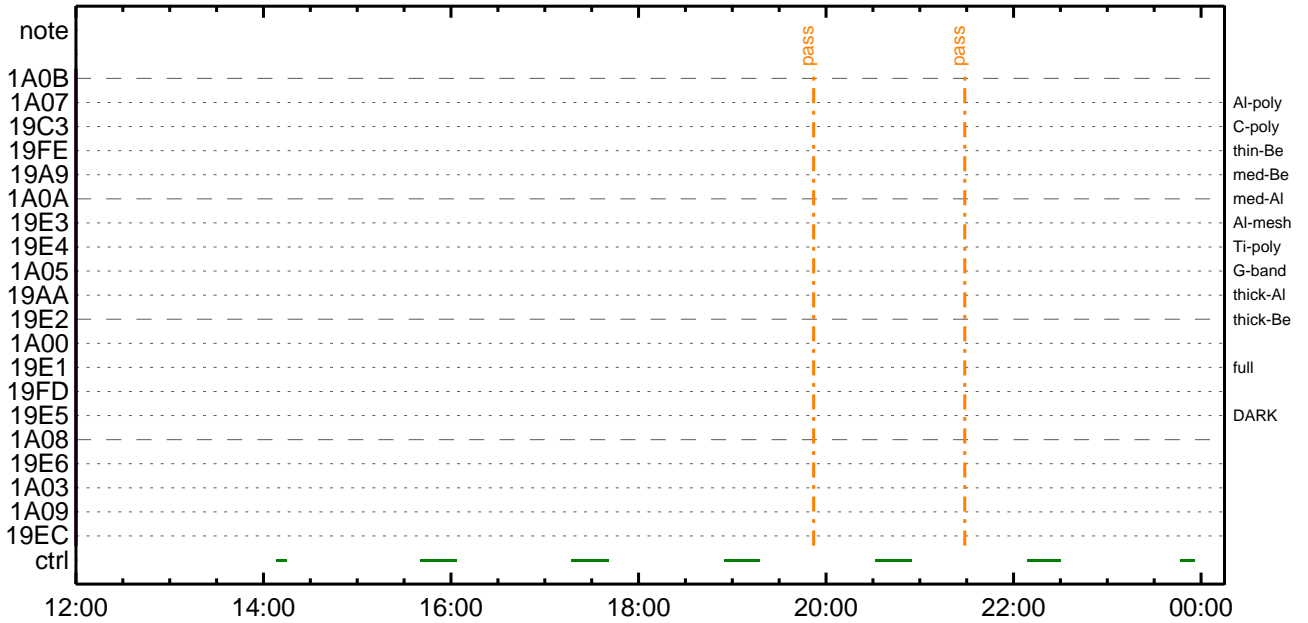
CMDI #0193 2014/04/04



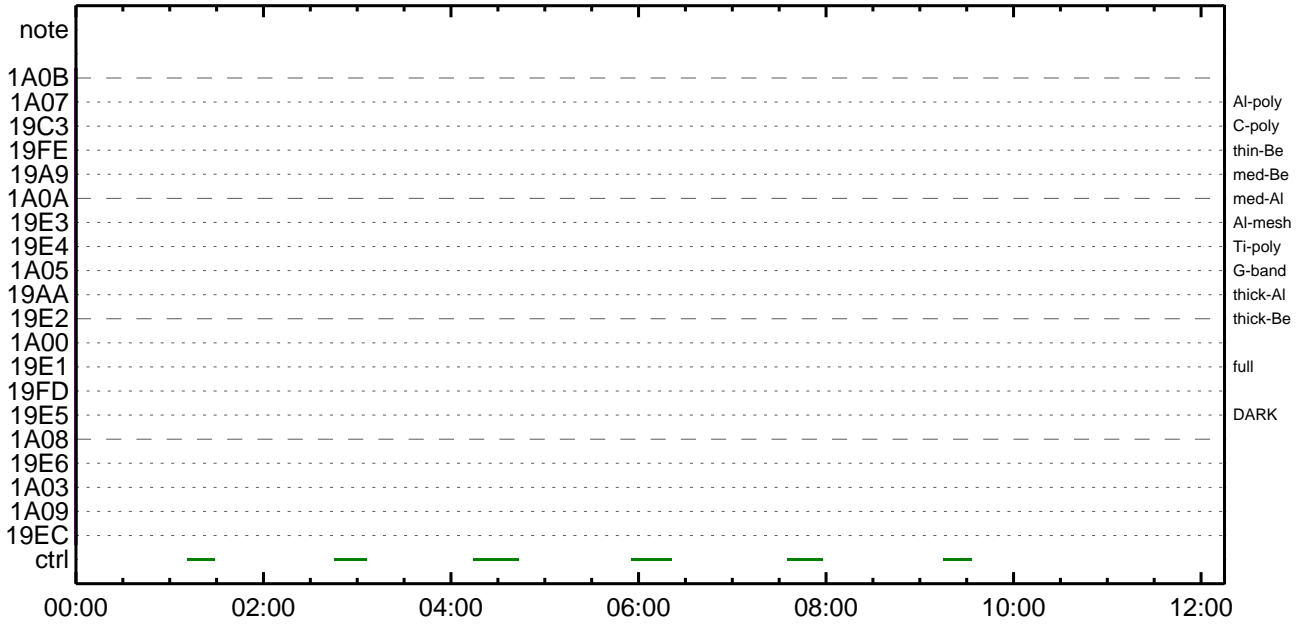
CMDI #0193 2014/04/05



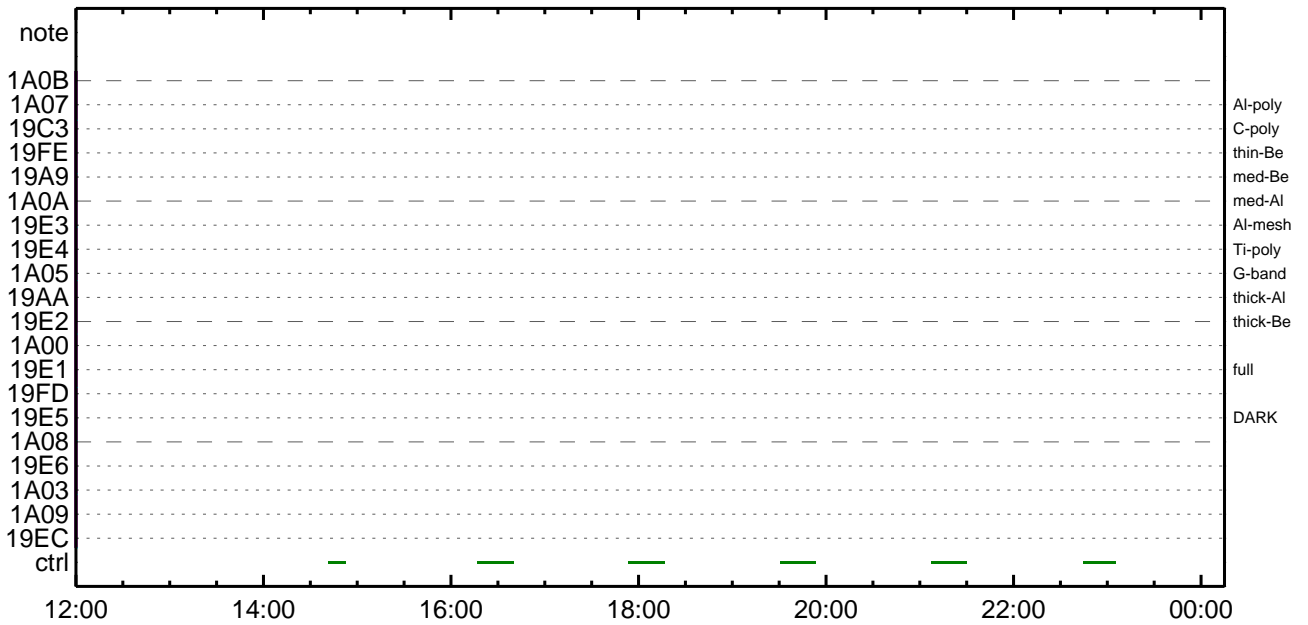
CMDI #0193 2014/04/05



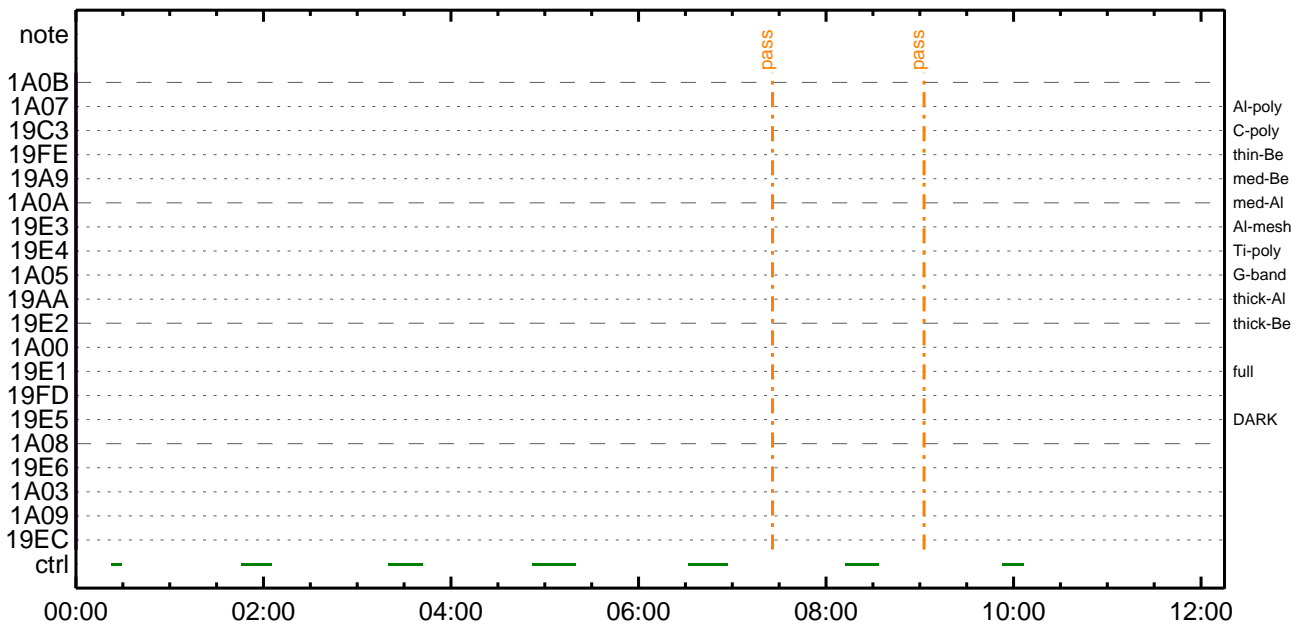
CMDI #0193 2014/04/06



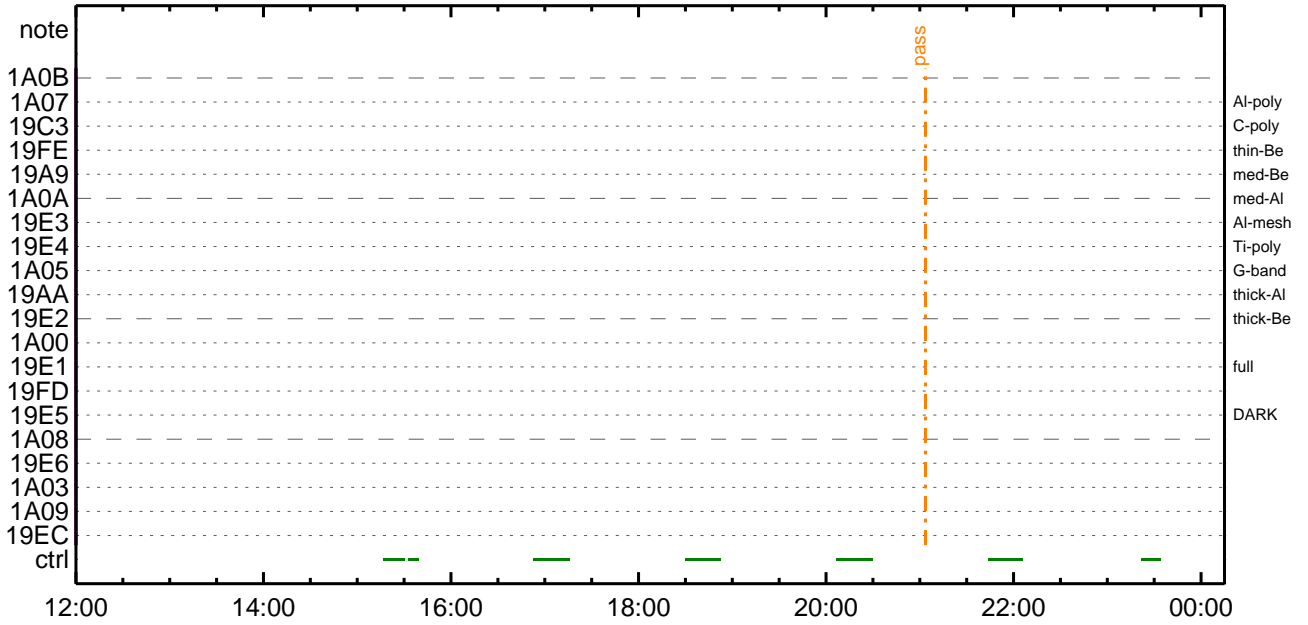
CMDI #0193 2014/04/06



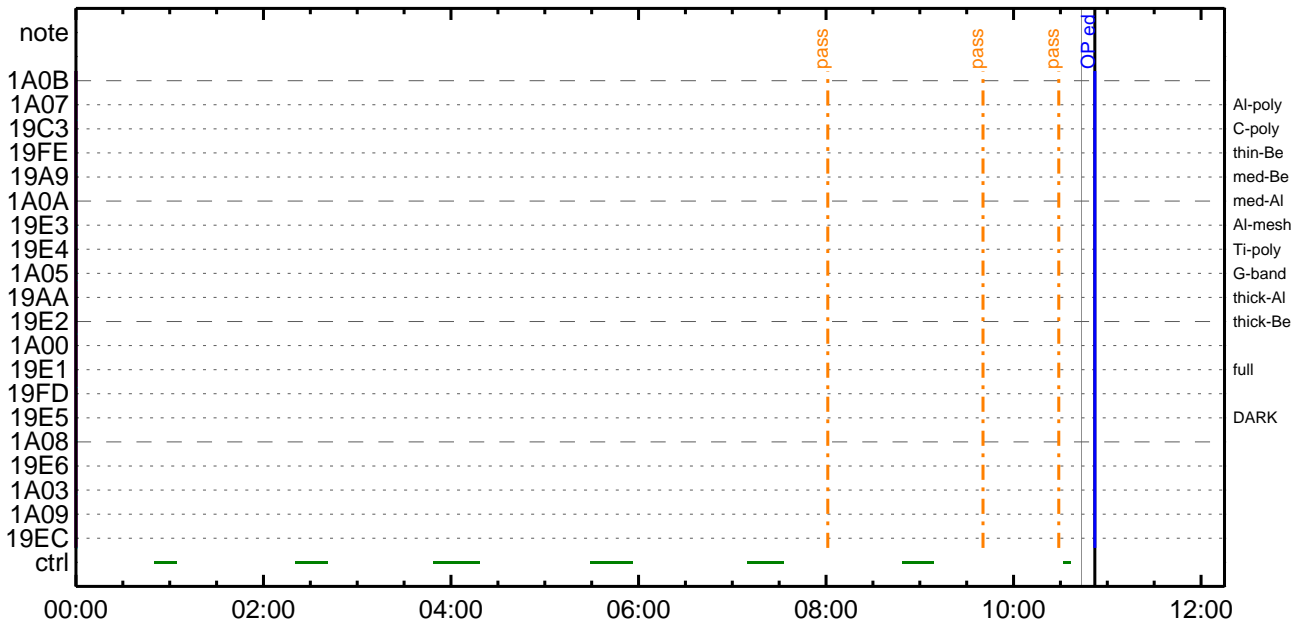
CMDI #0193 2014/04/07



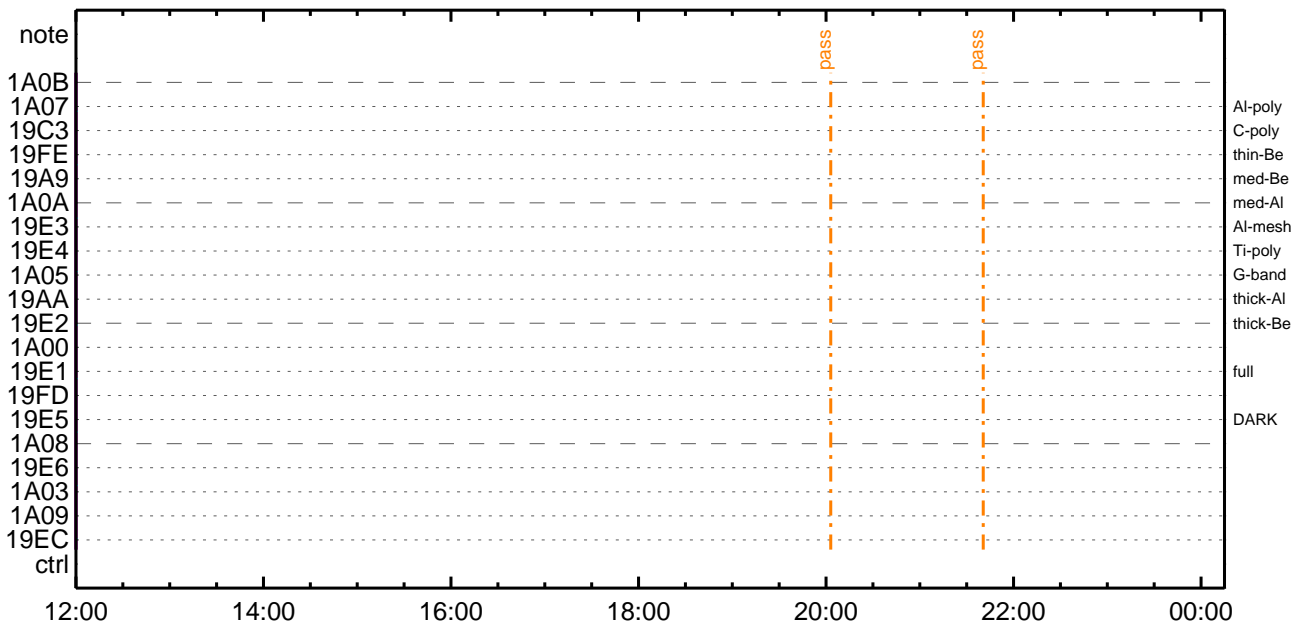
CMDI #0193 2014/04/07



CMDI #0193 2014/04/08



CMDI #0193 2014/04/08




```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-298:OP
0104 ( )
0105 S. OG og-298:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGñî½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGñî½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPñî½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** òE²¼òî½Ä´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD¬-Á÷¿@NGñî½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@ñî½Á¹Òª•ñEòòò³òE;f
0180 C. òEò¿;çSETòEDUMPAîE±°îYñY¹ç¹Òª|ò³òE;f
0181 C.
0182 C. TIY³YpYóYEòðÁDî¿(UT)
0183 +. TI 2014-04-03 11:01:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2014-04-03 11:01:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2014-04-03 11:01:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```

```

0194 C.
0195 +. TI 2014-04-03 11:05:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄë%îíñαîî¥Á¥§¥Á¥-¹àîÛ
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]        EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°è¥Á¥Ö¥×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]       EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]        EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]        EQ      EXEC
0224 C.
0225 C. ¥Á¥Ö¥×½ªî»αð³îÇ§
0226 C.          çç[HK1_DMP_CHK_FLG]        EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαîî¼Ê¹ç•è²îOKαð³îÇ§
0229 C.
0230 C. DHU¥â;¼¥Ê;Ê¼¥½. ¥î;¼¥Ê;Êαðîãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]        EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]     EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2014-04-03 11:05:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2014-04-03 11:05:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2014-04-03 11:05:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2014-04-03 11:05:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´úÃîãî»ö¼ÝαÊÄα¹αèDCBC•x²è *****
0276 C. (¼á°îî¥Á¥Ê¥¥¥¥¥¥¥¥¥¥¥¥¥¥¥èè¼αα¼Á»Ûα¹αè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ¥Ð¥¹•î Daily±çîñαè'Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOS¥Á¥§¥Á¥-¼Á»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-299 2014-04-03 13:05:19 82 33 SOLAR-B MAIN //
0001   C.
0002   . C. ***** AOS *****
0003   C.
0004   . C. ;ãAOSYÁYŞYÄY-¼Á»Û;ã
0005   C.
0006   C. YÀYB;¼Y³YFÝÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008   C.
0009   . C. ***** AOCs : Reload orbital element (send every contact) *****
0010   C.  Áí;È□¿□Å□•µ°Æ»Í×ÁÇ□íYçYÄY×Yí;¼YÉ;ÈÈ%µ•íÉ;È□È¼°ÇÖ□•□¿¼l¹ç□Í;çÀ®, ù□¹□è□È□È□ÇÁ+¿®□•□È□□□³□È;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012   C.
0013   C.
0014   . C. ***** AOCs Commands (Tracking Curve Upload) *****
0015   C. Upload the Orbit Element and the Target Attitude
0016   C. RAM-ID:TARGET_ATT
0017   . S. RAM ram-150:TARGET_ATT
0018   ( )
0019   C.
0020   C.
0021   C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023   BC (07 00 00 00 18 00)
0024   C.
0025   C. <A_STs1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026   C.
0027   C.
0028   C. Change the TLMFormatNo for the AOCs Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030   BC (04 0b f8)
0031   C.
0032   C. Wait for AOCSDUMP to end
0033   C.
0034   . C. Check the dump memory
0035   C.
0036   C. Result = OK [ ]
0037   C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039   BC (02 0a f8)
0040   C.
0041   C. <A_***>[TLM STS] FMT = 2 [ ]
0042   C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044   . C.
0045   C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_MANU
0047   BC (21 02)
0048   . C. Verify EIS in MANUAL mode
0049   . C. Estimated OBSTBL upload time is 59s
0050   C. *****
0051   C. EIS START OBSTBL LOAD
0052   C. *****
0053   . S. RAM ram-820:EIS_OBSTBL
0054   ( )
0055 +. DC 07-FC EIS_DUMP_OBSTBL
0056   BC (07 07 07 00 00 70 00)
0057   C.
0058   C. Execute, after the success of OBSTBL upload.
0059   C. Set EIS TI-commands
0060 +. TI 2014-04-03 11:05:50.0
0061   DC 07-FC EIS_MODE_CHG_ENA
0062   BC (20)
0063   . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0064   C. *****
0065   C. EIS END OBSTBL LOAD
0066   C. *****
0067   C.
0068   . C. ***** MDP `ûÁî□í»ô¼Y□ÈÁ□¹□èDCBC•×²è *****
0069   C. (¼á°íYÓYÁYÈYÞYÉYáYçYè□È¼¼¼¼¼»Û□¹□è)
0070   . S. DC-BC dcbc-402:DCBC
0071   (MDP_known_event)
0072   C.
0073   C.
0074   . C. ***** YDÝ¹.İ Daily±¿ÍÑ□È`Ø□¹□èDCBC•×²è *****
0075   . S. DC-BC dcbc-153:DCBC
0076   (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0077   C.
0078   C.
0079   . C. ;ãLOSÁYŞYÄY-¼Á»Û;ã
0080   C.
0081   . C. ***** LOS *****
0082   C.
```

(a) Spacecraft Operation Procedure (real-commands)

```

main-300 2014-04-03 13:05:19 158 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY~¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YFYóYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;Ë¿øÁB•µ°È»Í×ÁÇøÍYçYÁY×Yí;¼YÉ;ËÈ%µ•íÉ;ËBÈ¼°ÇÓø•ø¿¼l¹çøÍ;çÁ®, ùø¹øÈøBøÇÁ+¿®ø•øÈøøø³øÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-267:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Stop SP table >
0036 +. DC 07-F0 MDP_SP_CTRL_MANU
0037 BC (61)
0038 C. -----
0039 C. MDP_SP_CTRL_MODE = MANU [ ]
0040 C. -----
0041 C.
0042 . C. <Upload SP Observation Table>
0043 . S. RAM ram-286:MDP_OBS_S
0044 ( )
0045 C.
0046 . C. < Dump RAMID=MDP_OBS_S >
0047 +. DC 07-F0 MDP_DUMP_SPTBL
0048 BC (83 07 00 00 00 38 b8)
0049 C. -----
0050 C. MDP_OBS_S verify = OK/NG [ ]
0051 C. -----
0052 C.
0053 C. *****
0054 C. SOT TI command set
0055 C. *****
0056 C. Execute, after the success of TBL upload.
0057 +. TI 2014-04-03 11:05:18.0
0058 DC 07-F0 MDP_SOT_MODE_OBSV
0059 BC (40)
0060 . C. -----
0061 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0062 C. -----
0063 C.
0064 C.
0065 C. ***** XRT START *****
0066 C.
0067 +. DC 07-F0 MDP_XRT_CTRL_MANU
0068 BC (c1)
0069 + DC 07-F0 MDP_XRT_MODE_STBY
0070 BC (c3)
0071 . C. ----- Success Verify ? OK / NG____
0072 C.
0073 C. XRT Obs. Table Upload
0074 . S. RAM ram-291:MDP_OBS_X
0075 ( )
0076 C.
0077 +. DC 07-F0 MDP_DUMP_XRTTBL
0078 BC (84 07 00 00 00 3a d4)
0079 . C. ----- Comparison Check ? OK / ERR _____
0080 C.
0081 C.
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 01 b1 b1 04 04)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 02 b1 b1 08 08)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 03 b1 b1 08 08)
0088 + DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 04 b1 b1 06 06)
0090 + DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 05 85 83 06 06)
0092 + DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 06 85 83 06 06)
0094 + DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 07 85 83 08 08)

```

```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 08 c0 c0 10 10)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 09 80 80 20 20)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 0a 40 c0 10 10)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 0b 40 40 10 10)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 0c c0 40 10 10)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0d 80 80 20 08)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 0e 80 80 08 20)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 0f 80 80 06 06)
0112 + DC 07-F0 MDP_XRT_ROI_SET
0113 BC (cd 10 80 80 08 08)
0114 + DC 07-F8 XRT_FOCUS_POSITION
0115 BC (22 fe 97 00)
0116 + DC 07-F0 MDP_XRT_FLD_ENA
0117 BC (d8)
0118 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0119 BC (c8)
0120 + DC 07-F0 MDP_XRT_AEC_RESET
0121 BC (d0)
0122 + DC 07-F0 MDP_XRT_ARS_DIS
0123 BC (d5)
0124 + DC 07-F0 MDP_XRT_FLD_RESET
0125 BC (da)
0126 + DC 07-F0 MDP_XRT_QT_PROG_SET
0127 BC (c4 09)
0128 + DC 07-F0 MDP_XRT_FL_PROG_SET
0129 BC (c5 01)
0130 . C. ----- Success Verify ? OK / NG ____
0131 C.
0132 C.
0133 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0134 C.
0135 + DC 07-F0 MDP_XRT_MODE_OBSV
0136 BC (c2)
0137 + TI 2014-04-03 11:05:02.0
0138 DC 07-F0 MDP_XRT_MODE_OBSV
0139 BC (c2)
0140 . C. ----- Success Verify ? OK / NG ____
0141 C.
0142 C. ***** XRT END *****
0143 C.
0144 . C. ***** MDP ^ûÃîñÍ»ö%ÝñĒĀðñ¹ñĒDCBC•x²è *****
0145 C. (%á°îÿÓÿĀÿĒÿpÿĒÿáÿçÿĒĒ%¼ññ¼Ā»Ûñ¹ñĒ)
0146 . S. DC-BC dcbc-402:DCBC
0147 (MDP_known_event)
0148 C.
0149 C.
0150 . C. ***** ÿDÿ¹•Ī Daily±¿ĪññĒ´Øñ¹ñĒDCBC•x²è *****
0151 . S. DC-BC dcbc-153:DCBC
0152 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0153 C.
0154 C.
0155 . C. ;ãLOSÿĀÿ$ÿĀÿ~¼Ā»Û;ä
0156 C.
0157 . C. ***** LOS *****
0158 C.
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*** OP Sequence for XRT ***

2014/04/03	11:16:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2014/04/03	12:00:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/03	12:00:02.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/04/03	12:00:22.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/04/03	12:00:24.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/04/03	12:00:26.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/04/03	12:00:28.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/03	12:00:30.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/03	12:03:02.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2014/04/03	12:03:04.0	XRT_FL_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2014/04/03	12:03:06.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/03	12:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/03	13:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	ff	1b	ad	82
2014/04/03	14:31:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/03	14:31:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/03	14:31:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/03	14:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/03	14:59:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/03	15:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00	2e	f9	2e	f9
2014/04/03	15:02:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/04/03	15:02:52.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	05			
2014/04/03	15:02:54.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/04/03	15:02:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/04/03	15:02:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/03	15:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/03	15:09:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/03	15:10:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00	2e	f9	d1	07
2014/04/03	15:12:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/04/03	15:12:52.0	XRT_QT_PROG_SET_414_OG [0x19e]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2014/04/03	15:12:54.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/04/03	15:12:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/04/03	15:12:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/03	15:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/03	15:19:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/03	15:20:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	00	d1	07	d1	07
2014/04/03	15:22:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/04/03	15:22:52.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0f			
2014/04/03	15:22:54.0	XRT_FLD_DIS_404_OG [0x194]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/04/03	15:22:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/04/03	15:22:58.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/03	15:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/03	15:29:54.0	XRT_CTRL_MANU_439_OG [0x1b7]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/03	15:30:00.0	AOCS_Ore-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00	d1	07	2e	f9
2014/04/03	15:32:32.0	XRT_FOCUS_POSITION_442_OG [0x1ba]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/04/03	15:32:52.0	XRT_QT_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2014/04/03	15:32:54.0	XRT_FLD_DIS_404_OG [0x194]							

2014/04/03	15:32:56.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9				
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/04/03	15:32:58.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/03	15:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/03	15:40:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 00 00	ac	cd		
2014/04/03	16:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/03	16:05:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/03	16:05:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/03	16:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/03	16:35:00.0	AOCS_ORe-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 00 00	d6	67		
2014/04/03	16:50:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/03	16:50:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff	aa	00		
2014/04/03	16:51:00.0	AOCS_ORe-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00 00 00	00	00		
2014/04/03	16:51:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/04/03	16:53:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/04/03	16:53:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/03	16:53:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2014/04/03	16:54:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/03	17:07:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00 00 00	29	99		
2014/04/03	17:23:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	00 00 00	53	33		
2014/04/03	17:42:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/03	17:42:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/03	17:42:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/03	17:45:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/03	18:11:30.0	AOCS_ORe-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00 d6	36	b7 8e		
2014/04/03	18:21:30.0	AOCS_ORe-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00 b4	b5	db 75		
2014/04/03	18:37:30.0	AOCS_ORe-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00 ac	5b	00 00		
2014/04/03	18:53:30.0	AOCS_ORe-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00 b4	b5	24 8b		
2014/04/03	19:09:30.0	AOCS_ORe-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	00 d6	36	48 72		
2014/04/03	19:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/03	19:19:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/03	19:19:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/03	19:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/03	19:48:30.0	AOCS_ORe-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	00 29	ca	b7 8e		
2014/04/03	19:58:30.0	AOCS_ORe-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00 4b	4b	db 75		
2014/04/03	20:14:30.0	AOCS_ORe-point_Start_19_OG [0x0a9]	AOCU_NM	5	02-76	00 53	a5	00 00		
2014/04/03	20:30:30.0	AOCS_ORe-point_Start_20_OG [0x0aa]	AOCU_NM	5	02-76	00 4b	4b	24 8b		
2014/04/03	20:46:30.0	AOCS_ORe-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00 29	db	48 72		
2014/04/03	20:56:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/03	20:56:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/03	20:56:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/03	20:59:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/03	20:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/03	20:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe	97	00		
2014/04/03	21:00:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	00 20	41	4e d9		
2014/04/03	21:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/04/03	21:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				

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2014/04/03	21:00:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/04/03	21:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/03	21:00:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/03	21:02:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2014/04/03	21:02:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2014/04/03	21:19:30.0	XRT_Custom_430_OG [0x1ae]							
2014/04/03	21:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/03	22:34:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/03	22:34:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/03	22:34:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/03	22:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/03	22:54:30.0	XRT_Custom_430_OG [0x1ae]							
2014/04/03	22:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/03	23:14:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/03	23:15:00.0	AOCS_Ore-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00	ad	cb	fe	a8
2014/04/04	00:11:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/04	00:11:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/04	00:11:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/04	00:14:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/04	01:35:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/04	01:35:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/04	01:35:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/04	01:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/04	03:10:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/04	03:10:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/04	03:10:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/04	03:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/04	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/04	03:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/04/04	04:00:00.0	AOCS_Ore-point_Start_24_OG [0x0ae]							
		AOCU_NM	5	02-76	04	00	00	00	00
2014/04/04	04:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/04/04	04:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/04/04	04:00:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/04/04	04:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/04	04:00:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/04	04:02:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2014/04/04	04:02:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2014/04/04	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/04	04:40:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/04	04:40:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/04	04:40:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/04	04:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/04	05:08:30.0	XRT_Custom_430_OG [0x1ae]							
2014/04/04	05:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/04	05:52:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/04	05:52:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/04/04	05:52:30.0	AOCS_Ore-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	00	00	00	00
2014/04/04	05:52:46.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				

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2014/04/04	05:55:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/04/04	05:55:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/04/04	05:55:28.0	XRT_QT_PROG_SET_447_OG [0x1bf]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2014/04/04	05:55:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/04/04	06:02:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/04/04	06:02:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2014/04/04	06:02:30.0	AOCS_OrE-point_Start_24_OG [0x0ae]	AOCU_NM	5	02-76	04	00 00 00 00
2014/04/04	06:02:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/04/04	06:02:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/04/04	06:02:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/04/04	06:02:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/04/04	06:02:54.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/04/04	06:05:26.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
2014/04/04	06:05:28.0	XRT_FL_PROG_SET_448_OG [0x1c0]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	01
2014/04/04	06:05:30.5	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/04/04	06:20:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/04/04	06:20:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/04/04	06:20:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/04/04	06:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/04/04	06:46:00.0	XRT_Custom_430_OG [0x1ae]					
2014/04/04	06:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/04/04	08:00:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/04/04	08:00:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/04/04	08:00:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/04/04	08:03:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/04/04	08:22:30.0	XRT_Custom_430_OG [0x1ae]					
2014/04/04	08:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/04/04	09:41:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/04/04	09:41:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/04/04	09:41:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/04/04	09:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/04/04	09:56:30.0	XRT_Custom_430_OG [0x1ae]					
2014/04/04	09:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/04/04	11:00:00.5	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/04/04	11:00:04.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2014/04/04	11:00:24.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/04/04	11:00:26.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/04/04	11:00:28.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/04/04	11:00:30.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/04/04	11:00:32.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/04/04	11:03:04.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e
2014/04/04	11:03:06.0	XRT_FL_PROG_SET_448_OG [0x1c0]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	01
2014/04/04	11:03:08.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/04/04	15:00:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/04/04	15:00:02.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2014/04/04	15:00:22.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/04/04	15:00:24.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/04/04	15:00:26.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	

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2014/04/04	15:00:28.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/04	15:00:30.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/04	15:03:02.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2014/04/04	15:03:04.0	XRT_FL_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2014/04/04	15:03:06.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/04	15:05:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/04	15:05:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/04	15:05:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/04	15:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/04	15:19:00.0	XRT_Custom_430_OG [0x1ae]							
2014/04/04	15:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/04	15:25:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/04	15:25:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/04	15:25:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/04	15:28:00.0	XRT_Custom_430_OG [0x1ae]							
2014/04/04	15:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/04	15:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/04	16:41:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/04	16:41:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/04	16:41:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/04	16:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/04	17:05:00.0	XRT_Custom_430_OG [0x1ae]							
2014/04/04	17:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/04	17:54:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/04	17:54:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/04/04	17:55:00.0	AOCs_OrE-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00	00	00	00	00
2014/04/04	17:55:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/04/04	17:57:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/04/04	17:57:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/04	17:57:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2014/04/04	17:58:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/04	18:04:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/04	18:04:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/04/04	18:05:00.0	AOCs_OrE-point_Start_24_OG [0x0ae]							
		AOCU_NM	5	02-76	04	00	00	00	00
2014/04/04	18:05:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/04/04	18:05:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/04/04	18:05:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/04/04	18:05:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/04	18:05:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/04	18:07:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2014/04/04	18:07:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2014/04/04	18:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/04	18:18:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/04	18:18:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/04	18:18:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/04	18:21:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/04	18:41:30.0	XRT_Custom_430_OG [0x1ae]							
2014/04/04	18:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/04	19:55:30.0	XRT_CTRL_MANU_400_OG [0x190]							

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2014/04/04	19:55:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/04	19:55:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/04	19:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/04	20:19:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/04	20:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/04	20:44:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/04	20:44:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/04/04	20:45:00.0	AOCS_Ore-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	00 20 41 4e d9				
2014/04/04	20:45:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/04/04	20:45:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/04/04	20:45:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/04/04	20:45:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/04	20:45:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/04	20:47:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2014/04/04	20:47:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01				
2014/04/04	20:48:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/04	21:33:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/04	21:33:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/04	21:33:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/04	21:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/04	21:55:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/04	21:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/04	22:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/04	22:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/04/04	23:00:00.0	AOCS_Ore-point_Start_24_OG [0x0ae]	AOCU_NM	5	02-76	04 00 00 00 00				
2014/04/04	23:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/04/04	23:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/04/04	23:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/04/04	23:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/04	23:00:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/04	23:02:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2014/04/04	23:02:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01				
2014/04/04	23:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/04	23:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/04	23:10:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/04	23:10:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/04	23:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/04	23:25:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/04	23:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/05	00:41:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/05	00:41:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/05	00:41:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/05	00:44:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/05	00:53:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/05	00:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/05	02:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/05	02:09:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/05	02:09:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				

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2014/04/05	02:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/05	02:30:00.0	XRT_Custom_430_OG [0x1ae]							
2014/04/05	02:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/05	03:37:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/05	03:37:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/05	03:37:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/05	03:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/05	04:00:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/05	04:00:02.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/04/05	04:00:22.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/04/05	04:00:24.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/04/05	04:00:26.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/04/05	04:00:28.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/05	04:00:30.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/05	04:03:02.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2014/04/05	04:03:04.0	XRT_FL_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01				
2014/04/05	04:07:00.5	XRT_Custom_430_OG [0x1ae]							
2014/04/05	04:08:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/05	05:17:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/05	05:17:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/05	05:17:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/05	05:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/05	05:44:30.0	XRT_Custom_430_OG [0x1ae]							
2014/04/05	05:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/05	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/05	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/04/05	06:00:00.0	AOCs_OrE-point_Start_9_OG [0x09f]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/04/05	06:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/04/05	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/04/05	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/05	06:02:58.0	XRT_QT_PROG_SET_447_OG [0x1bf]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11				
2014/04/05	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/05	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/05	06:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/04/05	06:10:00.0	AOCs_OrE-point_Start_24_OG [0x0ae]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2014/04/05	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/04/05	06:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/04/05	06:10:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/04/05	06:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/05	06:10:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/05	06:12:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2014/04/05	06:12:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01				
2014/04/05	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/05	06:58:00.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/05	06:58:02.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/05	06:58:04.5	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/05	07:01:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/05	07:22:00.0	XRT_Custom_430_OG [0x1ae]							
2014/04/05	07:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							

2014/04/05	08:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0						
			MDP_XRT_CTRL_MANU	1	07-F0	c1						
2014/04/05	08:37:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da						
2014/04/05	08:37:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8						
2014/04/05	08:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9						
2014/04/05	08:58:00.0	XRT_Custom_430_OG [0x1ae]										
2014/04/05	08:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0						
2014/04/05	09:20:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1						
2014/04/05	10:51:00.0	AOCS_ORe-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00 00 00 00 00						