

XRT Timeline to be uploaded on 2014/04/19

Period: 2014/04/19 11:18:00 - 2014/04/24 10:14:00

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Normal mode

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XOB #1A11: AR Standard-B(Morphology), Be-thin w/ 10s cad and Be-med PFB, multifilter context w/ 20 min cad, G-band test w/ 60 min cad, 384x384, 1064

Term	Pointing (x, y)	Comment
04/19 11:31:00 - 04/19 13:59:54	Track (-2.6, -201.0) @ 04/19 11:28:00	# OP start + 10min IHOP 247, Hot plage near AR 12035
04/19 14:36:30 - 04/19 17:19:00	Track (644.8, -252.6) @ 04/19 14:00:00	AR 12036
04/19 18:13:00 - 04/20 04:09:54	Track (669.4, -255.1) @ 04/19 18:10:00	AR cont and ,IHOP 245 AR 12036 at UT23:00-03:00

PROG= 09 Inf.-time(s)

Subr=	1-time(s)	2.0sec										
Subr= 1	1-time(s)	2.0sec										
Seqn= 8	1-time(s)	2.0sec										
Open/G-band	Open/G-band close		Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Seqn= 24	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al close		Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band open		Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2	3-time(s)	2.0sec										
Seqn= 16	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al close		Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al close		Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open close		Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open	C-poly/thick-Al close		Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open close		Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open close		Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	Open/thick-Al close		Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 92	110-time(s)	2.0sec										
thin-Be/Open	med-Be/Open close		Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	5.0sec
med-Be/Open	Open/thick-Al close		Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	5.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #1A10: Synoptic Q95 2x2 - Al/mesh(2/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(5/256/1443) + Thin

Term	Pointing (x, y)	Comment
04/19 18:03:00 - 04/19 18:09:54	Fixed (0.0, 0.0)	synoptic
04/20 04:13:00 - 04/20 04:19:54	Fixed (0.0, 0.0)	synoptic, shifted -120.0 min

PROG= 03 1-time(s)

Subr=	1-time(s)	12.0sec										
Subr= 1	1-time(s)	12.0sec										
Seqn= 35	1-time(s)	4.0sec										
Open/Al-mesh	Open/Al-mesh close		Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh close		Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh close		Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al close		Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al close		Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al close		Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al close		Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al close		Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 72	1-time(s)	4.0sec										
Open/Ti-poly	Open/Ti-poly close		Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly close		Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly close		Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 67	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open close		Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open close		Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open close		Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6	1-time(s)	2.0sec										
Open/G-band	Open/G-band open		Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band close		Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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Flare mode

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XOB #19EC: Flare - high cad multifilter (Be/thin, Be/med, Al/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gband (4

Term	Pointing (x, y)	Comment
04/19 11:31:00 - 04/19 13:59:54	Track (-2.6, -201.0) @ 04/19 11:28:00	# OP start + 10min IHOP 247, Hot plage near AR 12035
04/19 14:36:30 - 04/19 17:19:00	Track (644.8, -252.6) @ 04/19 14:00:00	AR 12036
04/19 18:13:00 - 04/20 04:09:54	Track (669.4, -255.1) @ 04/19 18:10:00	AR cont and ,IHOP 245 AR 12036 at UT23:00-03:00

PROG= 01 30-time(s)

Subr=	30-time(s)	2.0sec										
Subr= 1	30-time(s)	2.0sec										
Seqn= 26	1-time(s)	4.0sec										
thin-Be/Open	med-Be/Open close		Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al close		Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be close		Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2	1-time(s)	2.0sec										
Seqn= 10	1-time(s)	2.0sec										
med-Al/Open	med-Al/thick-Al close		Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be close		Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec

Seqn= 11	1-time(s)	2.0sec												
└─ Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec		
Seqn= 15	1-time(s)	2.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec		
└─ Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval			

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Active Region Search

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NOT USED

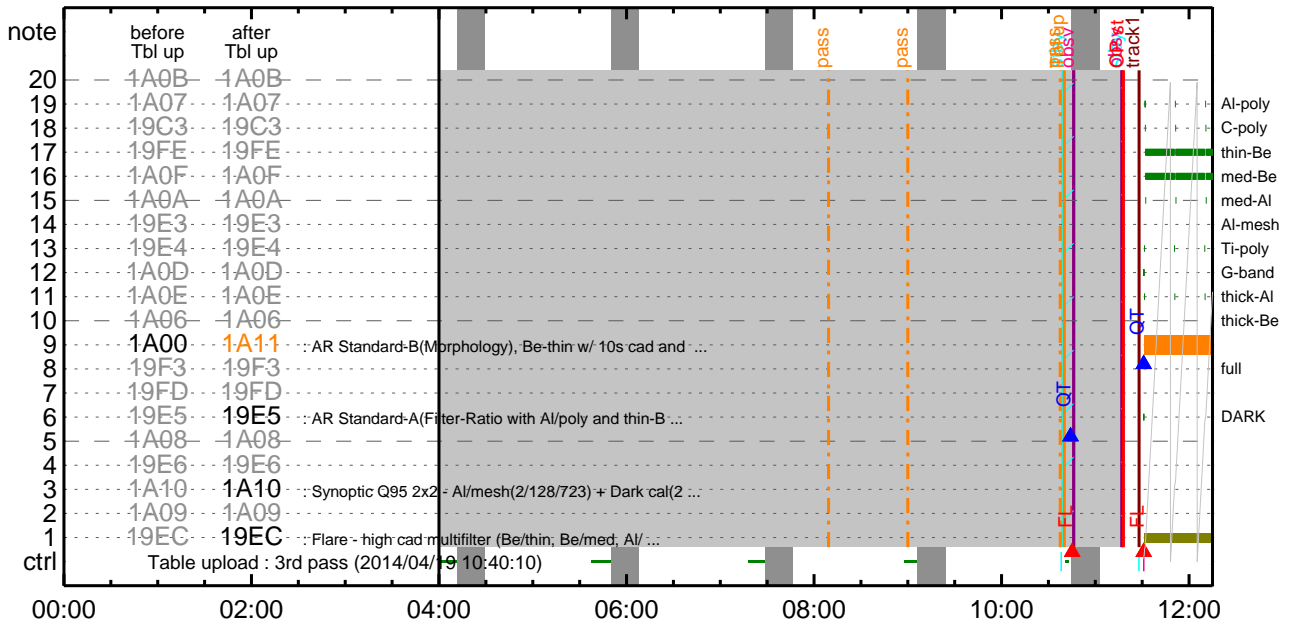
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Flare Detection

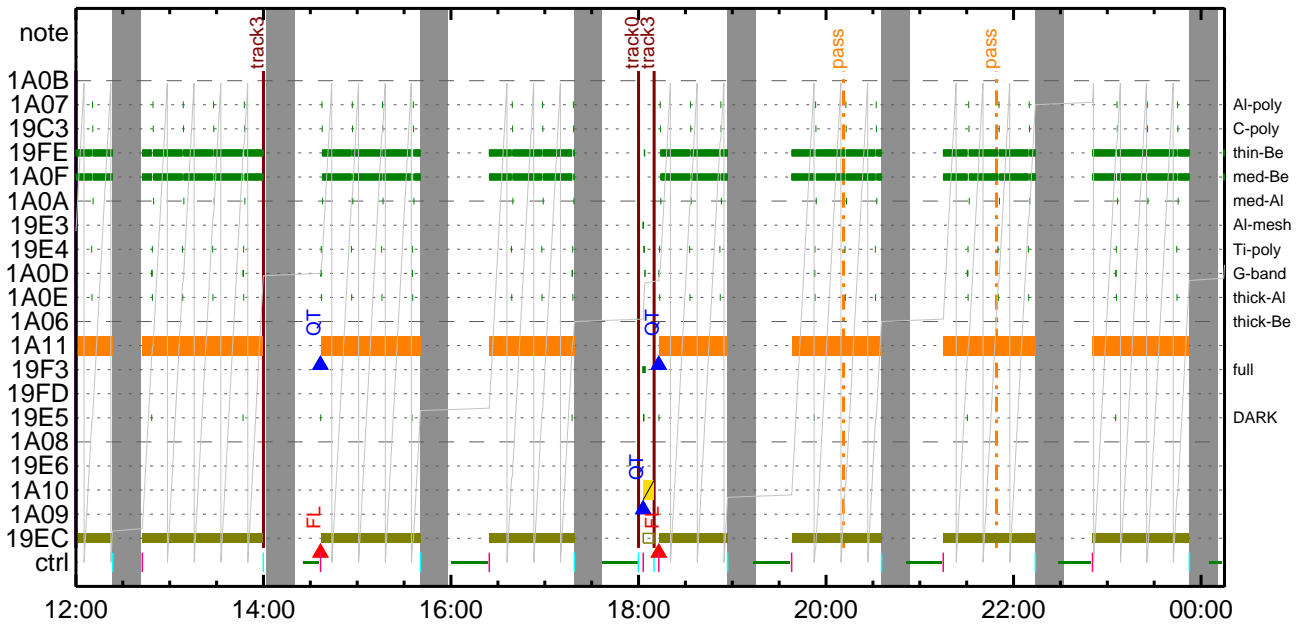
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FLD Patrol													
Term		Pointing (x, y)					Comment						
04/19 18:10:16 - 04/20 04:10:16		Track (669.4, -255.1) ^{© 04/19 18:10:00}					AR cont and ,IHOP 245 AR 12036 at UT23:00-03:00						
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8					Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval		

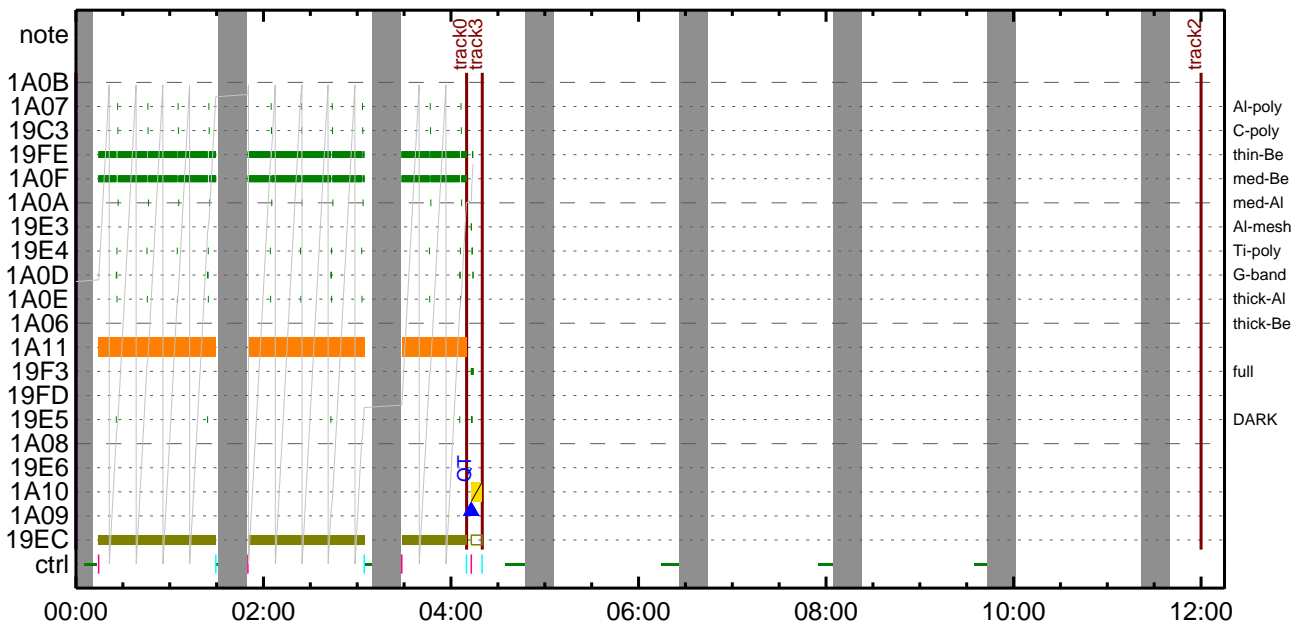
CMDI #0226 2014/04/19



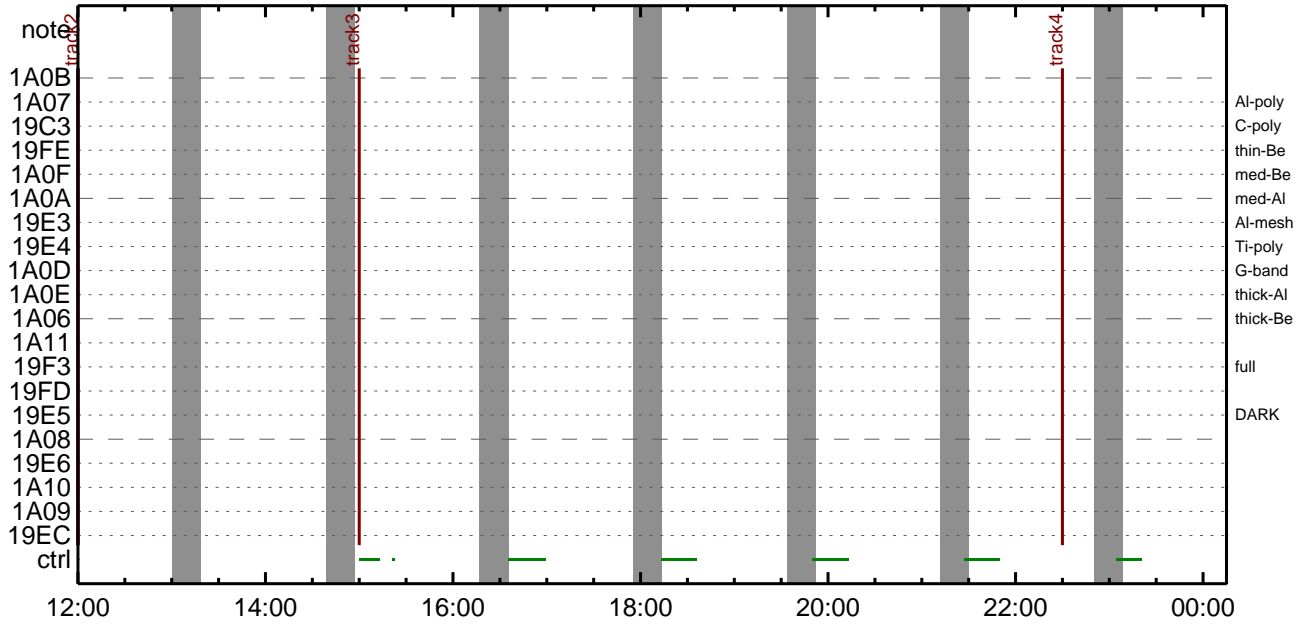
CMDI #0226 2014/04/19



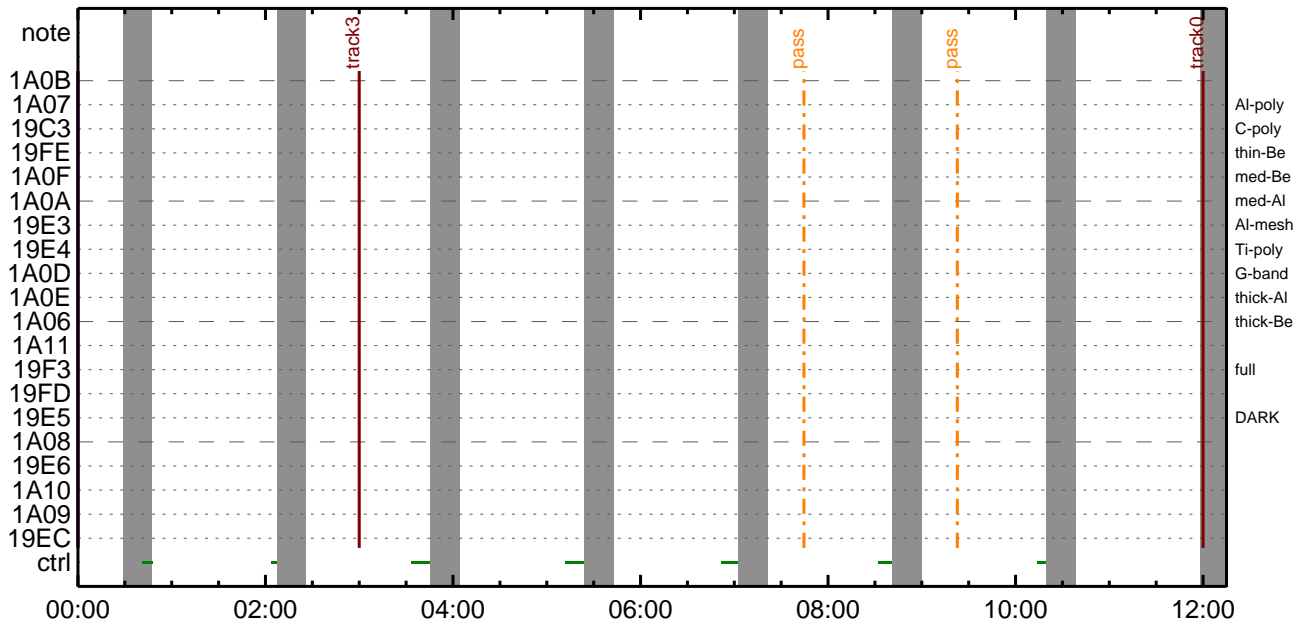
CMDI #0226 2014/04/20



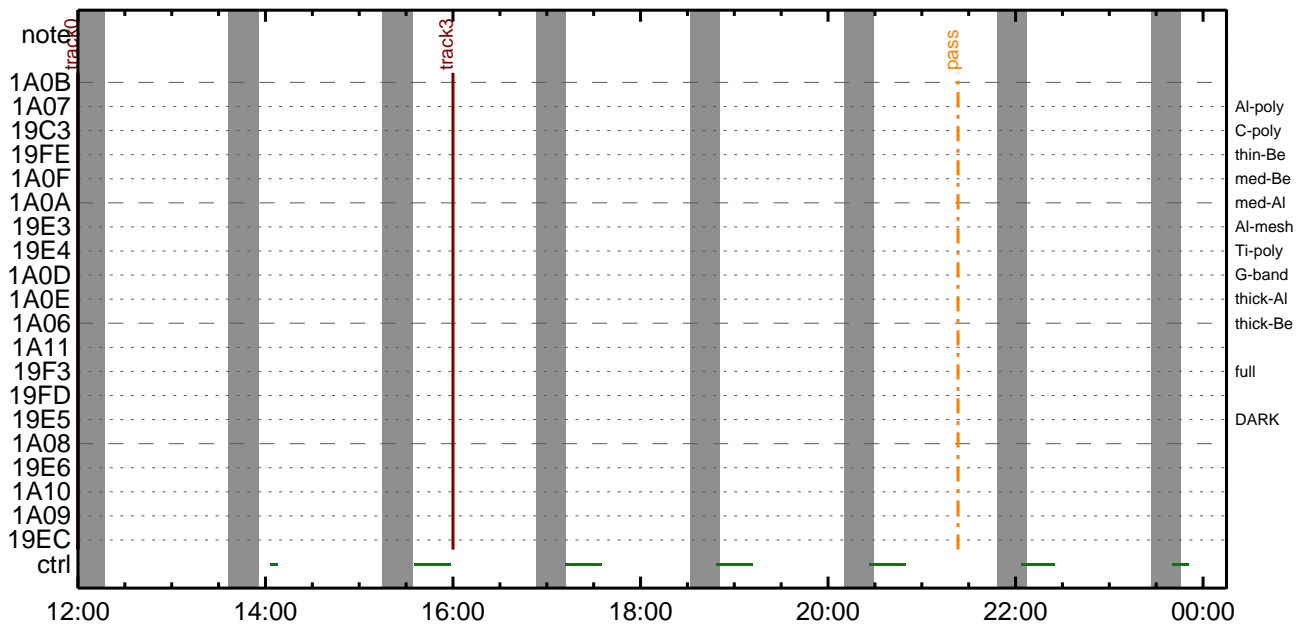
CMDI #0226 2014/04/20



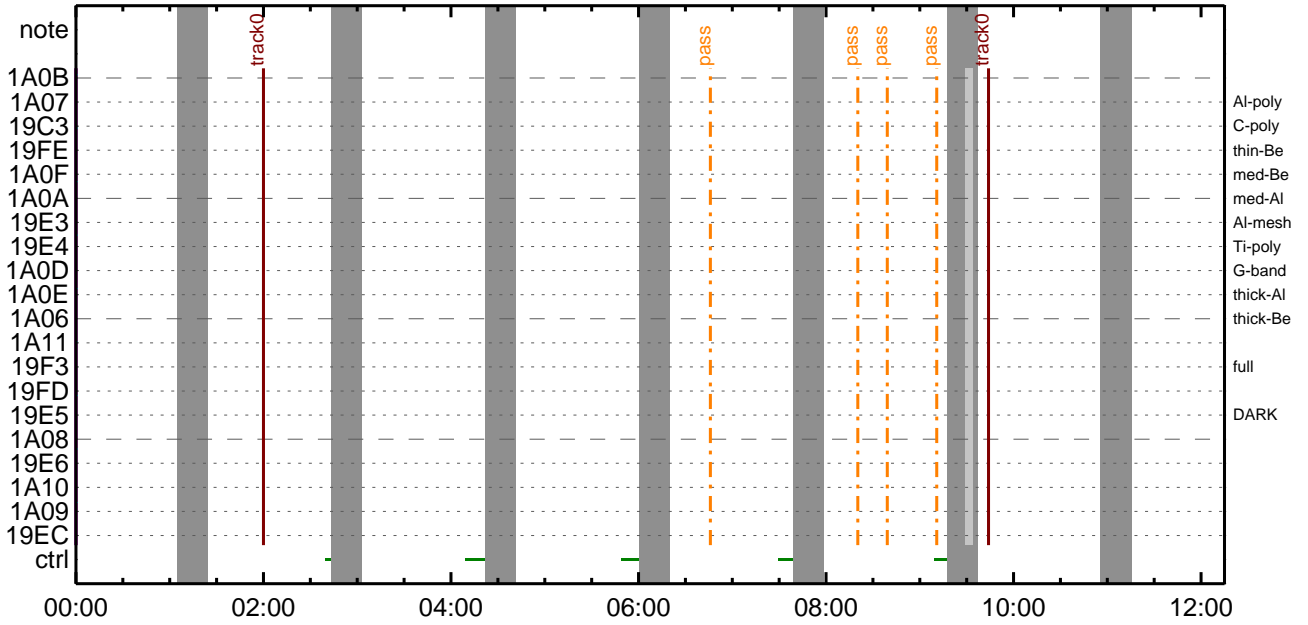
CMDI #0226 2014/04/21



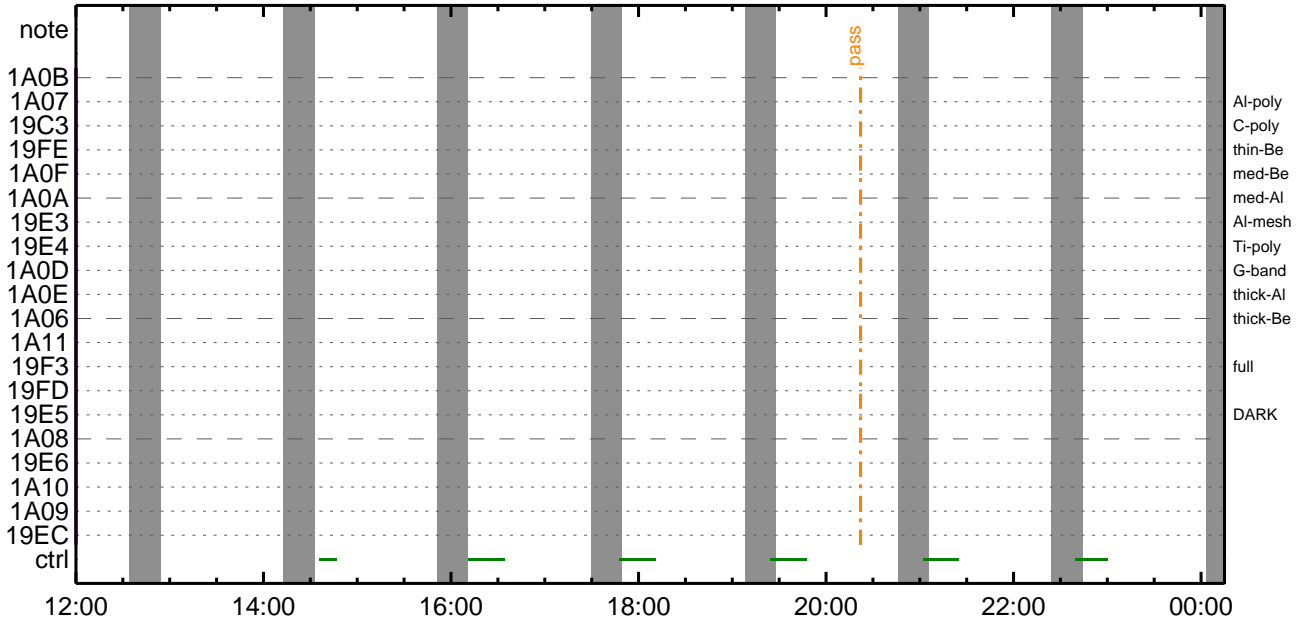
CMDI #0226 2014/04/21



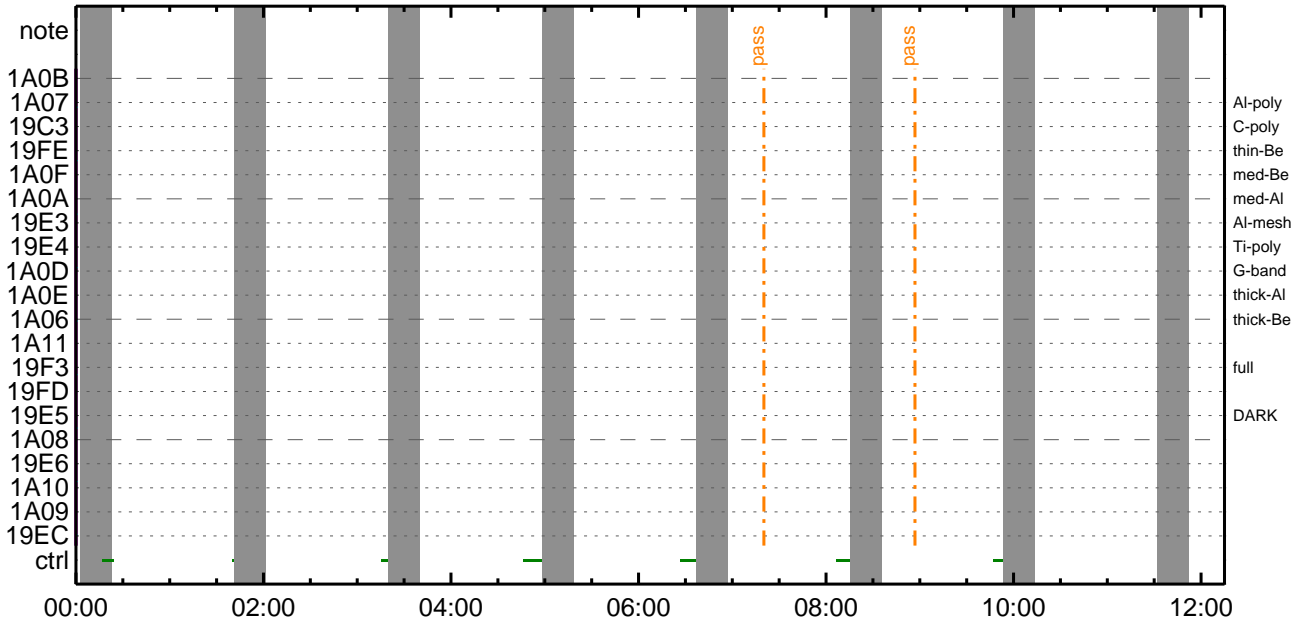
CMDI #0226 2014/04/22



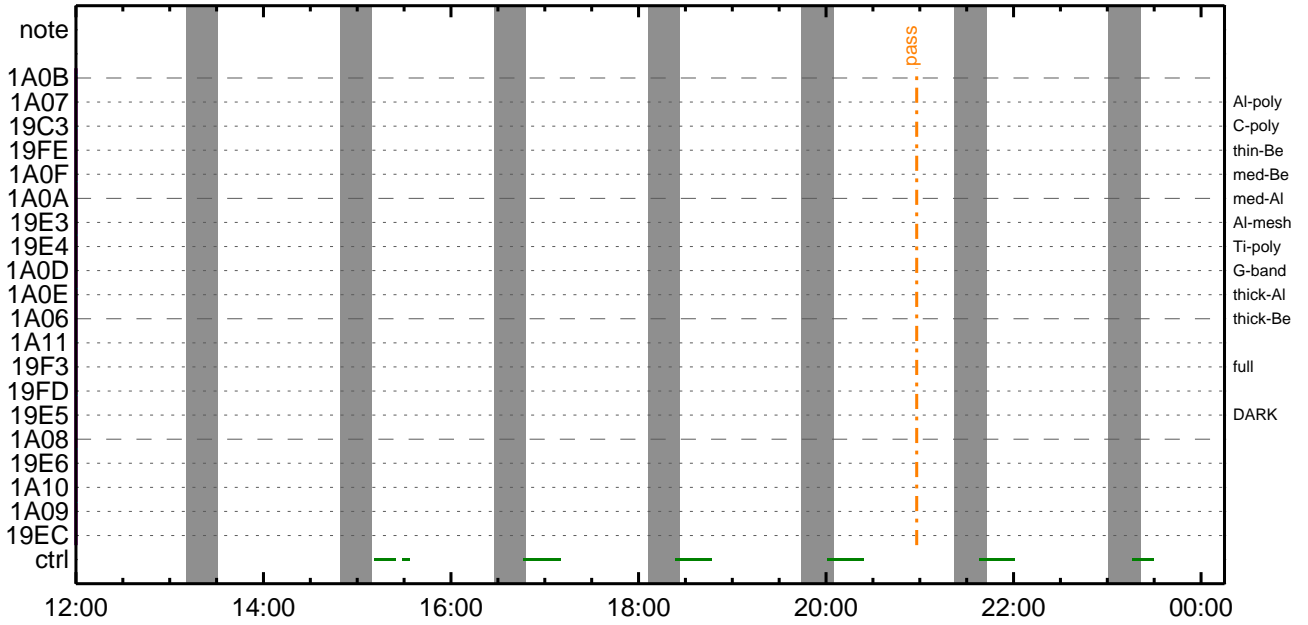
CMDI #0226 2014/04/22



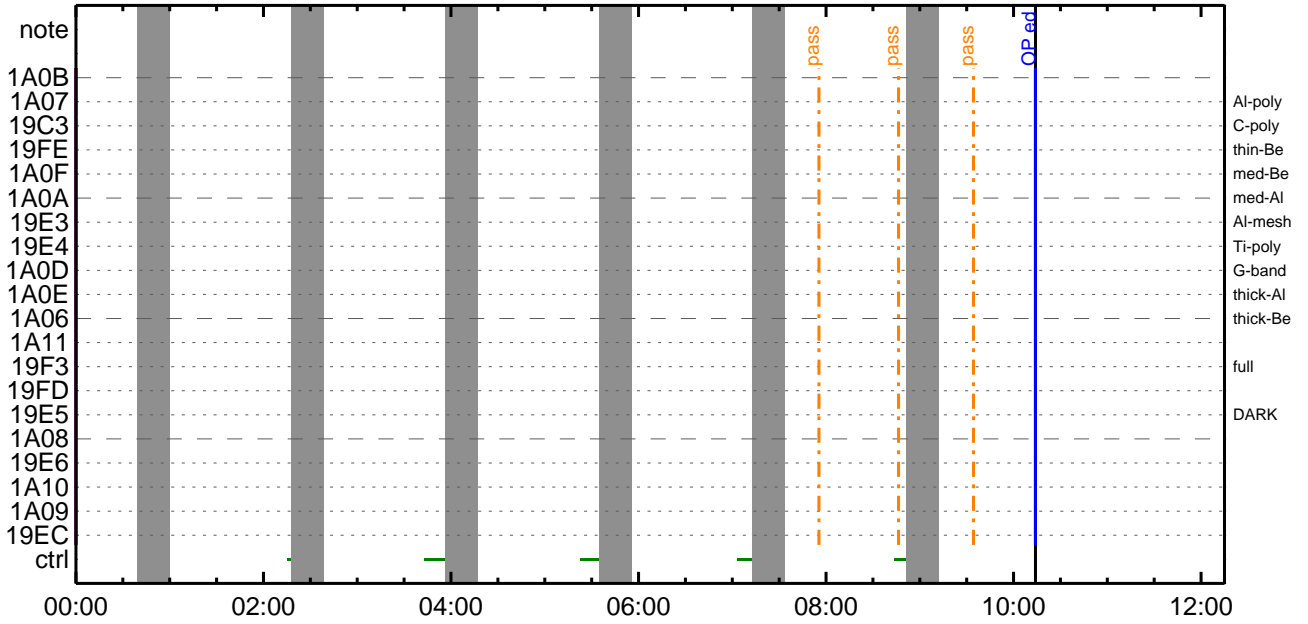
CMDI #0226 2014/04/23



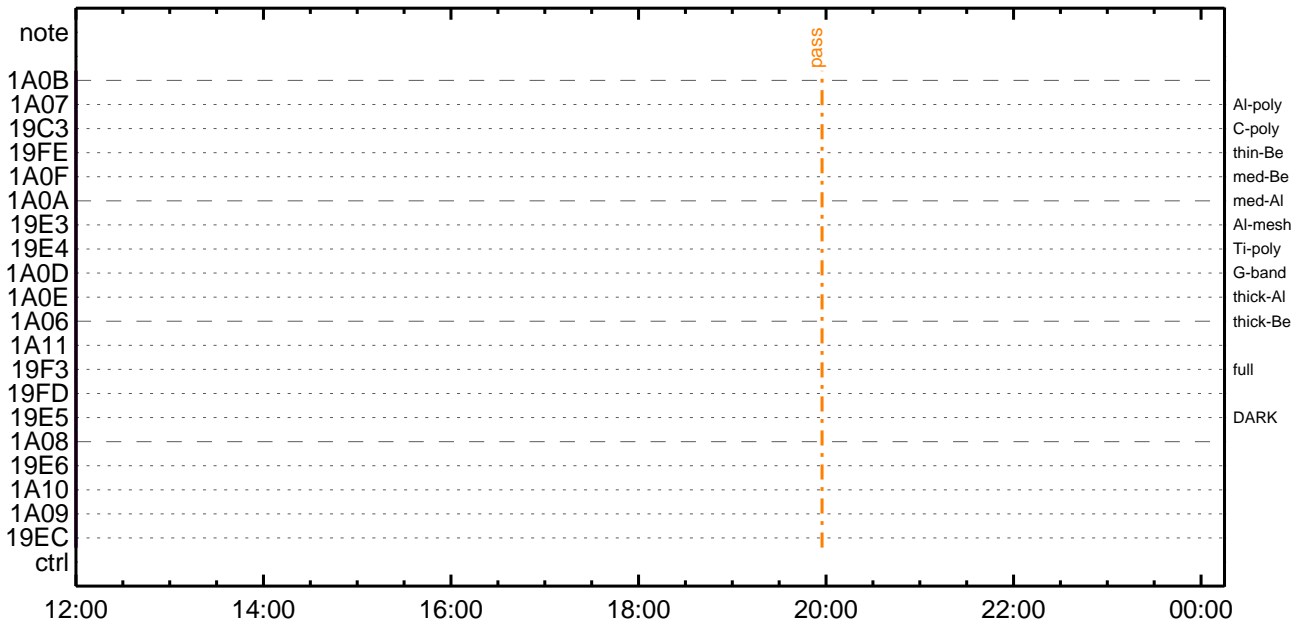
CMDI #0226 2014/04/23



CMDI #0226 2014/04/24



CMDI #0226 2014/04/24




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;ã
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-331:OP
0104 ( )
0105 S. OG og-331:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPîî°è¥ÅYóYx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. ðð[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. ðð[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. ðð[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. ðð[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. ðð[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. ðð[HK1_PKT_FORM_NO] EQ 7
0120 C. ðð[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. ðð[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. ðð[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. ðð[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. ¥ÅYóYx½ªî»ðð³îÇ§
0125 C. ðð[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGðîî½Ê¹Ç•è²îOKðð³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. ðð[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. ðð[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. ðð[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. ðð[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. ðð[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. ðð[HK1_PKT_FORM_NO] EQ 7
0139 C. ðð[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. ðð[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. ðð[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. ðð[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. ¥ÅYóYx½ªî»ðð³îÇ§
0144 C. ðð[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGðîî½Ê¹Ç•è²îOKðð³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. ðð[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. ðð[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. ðð[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. ðð[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. ðð[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. ðð[HK1_PKT_FORM_NO] EQ 7
0158 C. ðð[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. ðð[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. ðð[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. ðð[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. ¥ÅYóYx½ªî»ðð³îÇ§
0163 C. ðð[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG,RAM ID=OPðîî½Ê¹Ç•è²îOKðð³îÇ§
0165 C.
0166 C. ***** òÊ²¼òîî½Ê¹Ç•è²îOKðð³îÇ§ *****
0167 C. DHUYã;4YE;ã;½Y½;¥î;½YE;Êòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. ðð[HK1_PKT_FORM_NO] EQ 2
0171 C. ðð[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. ðð[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. ðð[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADð-Á÷¿@NGðîî½Ê¹Ç;ç°Ê²¼òîî½Ê¹Ç-Ôð÷¿@îî½Ê¹Çðð³îÇ;§
0180 C. ððð¿;çSETðEDUMPAî±±°îYÑY¹ç¹Òð|ð³ðÊ;§
0181 C.
0182 C. TIY³YpYóYÊðððÁDî¿¿(UT)
0183 +. TI 2014-04-19 11:13:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. ðð[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2014-04-19 11:13:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. ðð[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2014-04-19 11:13:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. ðð[HK1_TI_CMD_NUM] EQ 1COUNTUP
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0194 C.
0195 +. TI 2014-04-19 11:17:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄë%îíñαîŷÄŷ§ŷÄŷ-¹àîü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ      ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ      4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]        EQ      0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC      (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ      07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ      2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ      3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ      0
0216 C.          çç[HK1_DMA_DMP_PIM]        EQ      DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC      (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ      7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0223 C.          çç[HK1_DMP_CHK_FLG]       EQ      EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»αò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]        EQ      NON
0227 C.
0228 C. RAM ID=TI_TBLαîî¾È¹ç•è²îOKαò³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÉ;È¼ŷ¼. ŷî;¼ŷÈ;Èαòîãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC      (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ      2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ      0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]    EQ      32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]    EQ      4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2014-04-19 11:17:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC      (41)
0245 C. -----
0246 C.   HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2014-04-19 11:17:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC      (21 02)
0258 +. TI 2014-04-19 11:17:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC      (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2014-04-19 11:17:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC      (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîαî»ö¼ŷαÈÄα¹αèDCBC•x²è *****
0276 C. (¼á°îŷÖŷÄŷÈŷŷŷÄŷçŷèè¼αα¼Ä»Ûα¹αè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷĐŷ¹•î Daily±çîñαè'Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ä»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

```



```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 08 80 80 20 20)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 09 80 80 20 08)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 0a 80 80 08 20)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 0f 80 80 06 06)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 10 80 80 08 08)
0106 + DC 07-F8 XRT_FOCUS_POSITION
0107 BC (22 fe 97 00)
0108 + DC 07-F0 MDP_XRT_FLD_ENA
0109 BC (d8)
0110 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0111 BC (c8)
0112 + DC 07-F0 MDP_XRT_AEC_RESET
0113 BC (d0)
0114 + DC 07-F0 MDP_XRT_ARS_DIS
0115 BC (d5)
0116 + DC 07-F0 MDP_XRT_FLD_RESET
0117 BC (da)
0118 + DC 07-F0 MDP_XRT_QT_PROG_SET
0119 BC (c4 06)
0120 + DC 07-F0 MDP_XRT_FL_PROG_SET
0121 BC (c5 01)
0122 . C. ----- Success Verify ? OK / NG ____
0123 C.
0124 C.
0125 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0126 C.
0127 + DC 07-F0 MDP_XRT_MODE_OBSV
0128 BC (c2)
0129 + TI 2014-04-19 11:17:02.0
0130 DC 07-F0 MDP_XRT_MODE_OBSV
0131 BC (c2)
0132 . C. ----- Success Verify ? OK / NG ____
0133 C.
0134 C. ***** XRT END *****
0135 C.
0136 . C. ***** MDP `uAÎaî»ô%YnEÂDn¹nèDCBC•x²è *****
0137 C. (%â°îYÔYÄYÈYÏYËYâYçYènE%¼n¼A»Ûn¹nè)
0138 . S. DC-BC dcbc-402:DCBC
0139 (MDP_known_event)
0140 C.
0141 C.
0142 . C. ***** YDÿ¹.Ï Daily±;îÑnÈ´Øn¹nèDCBC•x²è *****
0143 . S. DC-BC dcbc-153:DCBC
0144 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0145 C.
0146 C.
0147 . C. ;ãLOSÿÁÿSÿYÄÿ¼A»Û;ã
0148 C.
0149 . C. ***** LOS *****
0150 C.
```

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*** OP Sequence for XRT ***

2014/04/19	11:27:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/19	11:27:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/04/19	11:28:00.0	AOCS_ORe-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2014/04/19	11:28:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/04/19	11:28:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/04/19	11:28:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/04/19	11:28:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/19	11:28:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/19	11:30:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2014/04/19	11:30:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01				
2014/04/19	11:31:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/19	12:23:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/19	12:23:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/19	12:23:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/19	12:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/19	12:41:30.0	XRT_Custom_430_OG [0x1ae]							
2014/04/19	12:42:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/19	13:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/19	13:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/04/19	14:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2014/04/19	14:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/04/19	14:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/04/19	14:00:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/04/19	14:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/19	14:00:24.0	XRT_FLD_RESET_426_OG [0x1aa]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/19	14:36:26.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 09				
2014/04/19	14:36:28.0	XRT_FL_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01				
2014/04/19	14:36:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/19	15:40:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/19	15:40:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/19	15:40:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/19	15:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/19	16:23:30.0	XRT_Custom_430_OG [0x1ae]							
2014/04/19	16:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/19	17:19:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/19	17:19:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/19	17:19:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/19	17:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/19	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/19	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/04/19	18:00:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/04/19	18:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/04/19	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/04/19	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/19	18:02:58.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2014/04/19	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/19	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							

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2014/04/19	18:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_FOCUS_POSITION		4	07-F8	22 fe 97 00
2014/04/19	18:10:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	03 00 00 00 00
2014/04/19	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2014/04/19	18:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/04/19	18:10:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2014/04/19	18:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/04/19	18:10:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/04/19	18:12:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2014/04/19	18:12:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/04/19	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/04/19	18:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/04/19	18:57:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/04/19	18:57:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/04/19	19:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/04/19	19:37:00.0	XRT_Custom_430_OG [0x1ae]				
2014/04/19	19:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/04/19	20:35:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/04/19	20:35:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/04/19	20:35:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/04/19	20:38:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/04/19	21:14:00.0	XRT_Custom_430_OG [0x1ae]				
2014/04/19	21:15:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/04/19	22:14:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/04/19	22:14:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/04/19	22:14:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/04/19	22:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/04/19	22:49:30.0	XRT_Custom_430_OG [0x1ae]				
2014/04/19	22:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/04/19	23:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/04/19	23:52:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/04/19	23:52:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/04/19	23:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/04/20	00:13:30.0	XRT_Custom_430_OG [0x1ae]				
2014/04/20	00:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/04/20	01:29:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/04/20	01:29:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/04/20	01:29:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/04/20	01:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/04/20	01:49:00.5	XRT_Custom_430_OG [0x1ae]				
2014/04/20	01:50:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/04/20	03:04:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/04/20	03:04:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/04/20	03:04:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/04/20	03:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/04/20	03:27:30.0	XRT_Custom_430_OG [0x1ae]				
2014/04/20	03:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/04/20	04:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/04/20	04:09:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/04/20	04:10:00.0	AOCS_OrE-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00

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2014/04/20	04:10:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/04/20	04:12:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/04/20	04:12:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/20	04:12:58.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2014/04/20	04:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/20	04:19:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/20	04:20:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2014/04/20	04:20:00.5	XRT_TCIB_XRT_S_HTR_A_ENA_428_OG [0x1ac]							
		TCIB_XRT_S_HTR_A_ENA	0	04-BC					
2014/04/20	12:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2014/04/20	15:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2014/04/20	22:30:00.0	AOCS_ORe-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2014/04/21	03:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2014/04/21	12:00:00.0	AOCS_ORe-point_Start_6_OG [0x09c]							
		AOCU_NM	5	02-76	00 ae 4e 01 58				
2014/04/21	16:00:00.0	AOCS_ORe-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2014/04/22	02:00:00.0	AOCS_ORe-point_Start_7_OG [0x09d]							
		AOCU_NM	5	02-76	00 1a 24 b1 69				
2014/04/22	09:44:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 00 00 00 00				