

XRT Timeline to be uploaded on 2014/04/26

Period: 2014/04/26 09:49:00 - 2014/05/01 09:34:00

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Normal mode

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XOB #1A12: AR Standard-B(Morphology), Be-thin w/ 24s cad and Be-med PFB, multifilter context w/ 20 min cad, G-band test w/ 60 min cad, 384x384, 1064

Term	Pointing (x, y)	Comment
04/26 10:27:00 - 04/26 13:21:00	Fixed (870.0, -260.0)	# OP start + 10min, IHOP244 on AR12046
04/26 23:39:30 - 04/27 03:54:00	Fixed (870.0, -260.0)	#IHOP244 on AR12046

PROG= 12 Inf.-time(s)													
Subr=	1	1-time(s)	2.0sec										
Seqn=	8	1-time(s)	2.0sec										
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Seqn=	24	1-time(s)	2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr=	2	3-time(s)	2.0sec										
Seqn=	16	1-time(s)	2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Al/Open	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn=	82	50-time(s)	2.0sec										
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	8.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	8.0sec
	med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	8.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19EA: AR Standard-B(Morphology), Be/thin 120s-cad, multifilter PFB (Al/poly, Be/thin, Be/med), multifilter context, 384x384, 1064x1048, G-band (33

Term	Pointing (x, y)	Comment
04/26 13:44:00 - 04/26 17:59:54	Fixed (870.0, -260.0)	# OP start + 10min, IHOP244 on AR12046
04/26 18:59:00 - 04/26 23:11:30	Fixed (870.0, -260.0)	#IHOP244 on AR12046
04/27 04:30:00 - 04/27 05:34:30	Fixed (870.0, -260.0)	#IHOP244 on AR12046

PROG= 08 Inf.-time(s)													
Subr=	1	1-time(s)	2.0sec										
Seqn=	8	1-time(s)	2.0sec										
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Seqn=	24	1-time(s)	2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr=	2	2-time(s)	2.0sec										
Seqn=	16	2-time(s)	2.0sec										
	Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
	med-Al/Open	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn=	63	20-time(s)	2.0sec										
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	12.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	12.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	12.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	12.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	12.0sec
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	12.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	12.0sec
	med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	12.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A10: Synoptic Q95 2x2 - Al/mesh(2/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(5/256/1443) + Thin

Term	Pointing (x, y)	Comment
04/26 18:03:00 - 04/26 18:09:54	Fixed (0.0, 0.0)	synoptic
04/27 06:14:00 - 04/27 06:20:54	Fixed (0.0, 0.0)	synoptic, shifted 11.0 min
04/27 18:02:00 - 04/27 18:08:54	Fixed (0.0, 0.0)	synoptic, shifted -1.0 min
04/28 05:47:00 - 04/28 05:53:54	Fixed (0.0, 0.0)	synoptic, shifted -16.0 min
04/28 17:44:00 - 04/28 17:50:54	Fixed (0.0, 0.0)	synoptic, shifted -19.0 min
04/29 06:03:00 - 04/29 06:49:00	Fixed (0.0, 0.0)	synoptic

PROG= 03 1-time(s)													
Subr=	1	1-time(s)	12.0sec										
Seqn=	35	1-time(s)	4.0sec										
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 72 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 67 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A02: AR standard-B (thin-Be) with PFB (384FOV), 512x512 at 1064 1048, thick Al and Al/Poly context, With G-band (33ms/45ms leak), 5min cad

Term	Pointing (x, y)	Comment
04/27 06:24:00 - 04/27 10:37:00	Fixed (870.0, -260.0)	# IHOP244 on AR12046
04/27 15:03:00 - 04/27 17:14:30	Fixed (903.0, -208.0)	# AR12038
04/27 18:12:00 - 04/27 22:09:30	Track (339.6, -310.1) ^{Ⓜ 04/27 18:09:00}	# Track AR12045
04/28 03:28:30 - 04/28 05:43:54	Track (406.0, -313.0) ^{Ⓜ 04/28 03:00:00}	# Track AR12045
04/28 05:57:00 - 04/28 11:17:00	Track (427.2, -314.0) ^{Ⓜ 04/28 05:54:00}	# Cont,
04/28 15:33:00 - 04/28 17:40:54	Track (494.8, -317.7) ^{Ⓜ 04/28 15:30:00}	# Track AR12045
04/28 18:34:00 - 04/28 22:29:00	Track (510.7, -318.6) ^{Ⓜ 04/28 17:51:00}	# Cont,
04/29 08:03:00 - 04/29 08:29:00	Track (601.1, -324.9) ^{Ⓜ 04/29 08:00:00}	# Track AR12045

PROG= 06 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec												
Seqn= 71 2-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec
Seqn= 65 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec												
Seqn= 34 2-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 27 7-time(s) 300.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	17.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	17.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	17.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	17.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	17.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	17.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	17.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	17.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19FD: AR standard-B (thin-Be) with PFB, 384 FOV at 1064x1048, thin-Be, thick-Al, and Al/Poly context, With G-band (33ms/45ms leak), 10s cad, lowe

Term	Pointing (x, y)	Comment
04/27 11:03:30 - 04/27 12:19:00	Track (284.2, -308.1) ^{Ⓜ 04/27 11:00:00}	# IHOP247 on AR12045

PROG= 09 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec												
Seqn= 8 2-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec												
Seqn= 24 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 42 2-time(s) 2.0sec												
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 87 75-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	10.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19EE: AR Standard-B(Morphology), Be/thin 48s-cad, multifilter PFB (Al/poly, Be/thin, Be/med), multifilter context, 384x384, 1064x1048, G-band (33ms/45ms leak), 10s cad, lowe

Term	Pointing (x, y)	Comment
04/27 12:42:00 - 04/27 14:59:54	Track (284.2, -308.1) ^{Ⓜ 04/27 11:00:00}	# IHOP247 on AR12045

PROG= 19 Inf.-time(s)

Subr= 1 1-time(s) 2.0sec												
Seqn= 8 1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Seqn= 24 1-time(s) 2.0sec												

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Subr= 2 2-time(s) 2.0sec												
Seqn= 16 2-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
C-poly/Open	C-poly/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
med-Al/Open	Open/thick-Al	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 29 50-time(s) 2.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	6.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	177ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	6.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	6.0sec
med-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	6.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A18: Eclipse20140429_Ti-Poly_Al-poly_Movie_DCpnt-duration-25min-30s cad												
Term	Pointing (x, y)		Comment									
04/29 07:22:00 - 04/29 07:44:00	Fixed (0.0, 0.0)	# 3rd eclipse obs for XRT									
PROG= 05 Inf.-time(s)												
Subr= 1 1-time(s) 2.0sec												
Seqn= 18 1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=92	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=92	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=92	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 69 50-time(s) 30.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=92	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=92	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1A14: Flare - Med-Be w/ 6s cad - Al-poly w/ 1min cad - Med-Al, Thick-Be, G-band, Darks w/ 5.5min cad												
Term	Pointing (x, y)		Comment									
04/26 10:27:00 - 04/26 13:21:00	Fixed (870.0, -260.0)	# OP start + 10min, IHOP244 on AR12046									
04/26 13:44:00 - 04/26 17:59:54	Fixed (870.0, -260.0)	# OP start + 10min, IHOP244 on AR12046									
04/26 18:59:00 - 04/26 23:11:30	Fixed (870.0, -260.0)	#IHOP244 on AR12046									
04/26 23:39:30 - 04/27 03:54:00	Fixed (870.0, -260.0)	#IHOP244 on AR12046									
04/27 04:30:00 - 04/27 05:34:30	Fixed (870.0, -260.0)	#IHOP244 on AR12046									
PROG= 11 15-time(s)												
Subr= 1 5-time(s) 2.0sec												
Seqn= 99 10-time(s) 2.0sec												
med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	6.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 15 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19EC: Flare - high cad multifilter (Be/thin, Be/med, Al/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gband (4												
Term	Pointing (x, y)		Comment									
04/27 06:24:00 - 04/27 10:37:00	Fixed (870.0, -260.0)	# IHOP244 on AR12046									
04/27 11:03:30 - 04/27 12:19:00	Track (284.2, -308.1) @ 04/27 11:00:00	# IHOP247 on AR12045									
04/27 12:42:00 - 04/27 14:59:54	Track (284.2, -308.1) @ 04/27 11:00:00	# IHOP247 on AR12045									
04/27 15:03:00 - 04/27 17:14:30	Fixed (903.0, -208.0)	# AR12038									
04/27 18:12:00 - 04/27 22:09:30	Track (339.6, -310.1) @ 04/27 18:09:00	# Track AR12045									
04/28 03:28:30 - 04/28 05:43:54	Track (406.0, -313.0) @ 04/28 03:00:00	# Track AR12045									
04/28 05:57:00 - 04/28 11:17:00	Track (427.2, -314.0) @ 04/28 05:54:00	# Cont,									
04/28 15:33:00 - 04/28 17:40:54	Track (494.8, -317.7) @ 04/28 15:30:00	# Track AR12045									

04/28 18:34:00 - 04/28 22:29:00 Track (510.7, -318.6) @ 04/28 17:51:00 # Cont,
 04/29 07:22:00 - 04/29 07:44:00 Fixed (0.0, 0.0) # 3rd eclipse obs for XRT
 04/29 08:03:00 - 04/29 08:29:00 Track (601.1, -324.9) @ 04/29 08:00:00 # Track AR12045

PROG= 01 30-time(s)												
Subr= 1 30-time(s) 2.0sec												
Seqn= 26 1-time(s) 4.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Seqn= 15 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

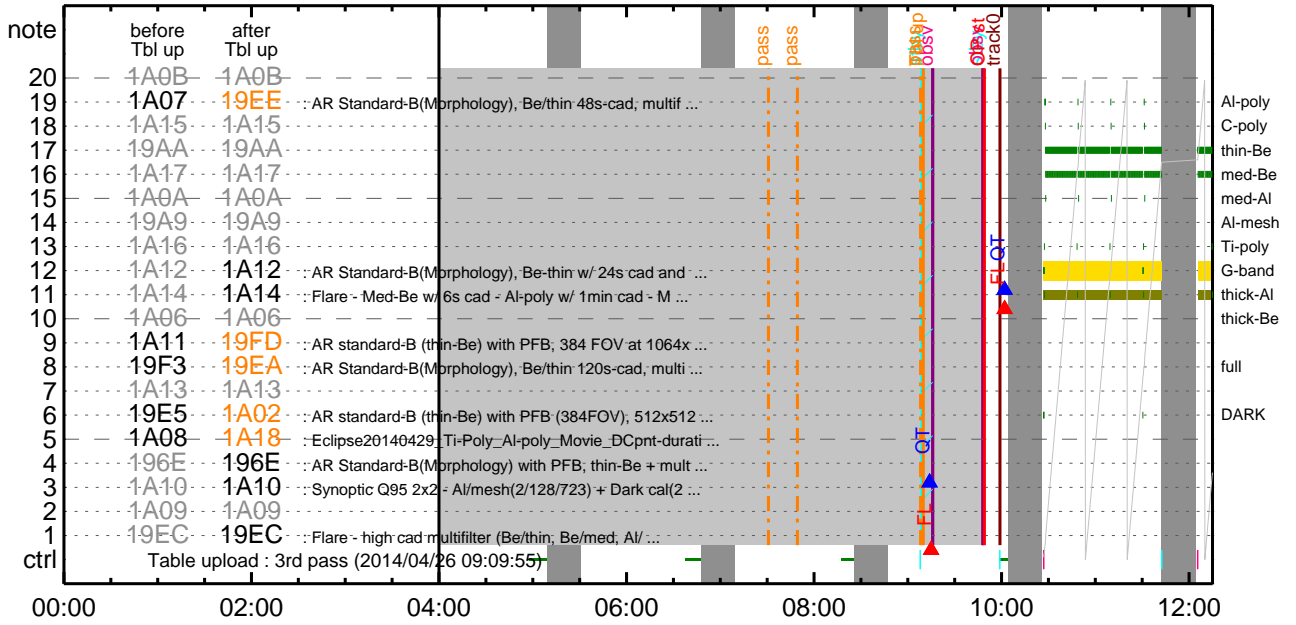
* * * * *

Flare Detection

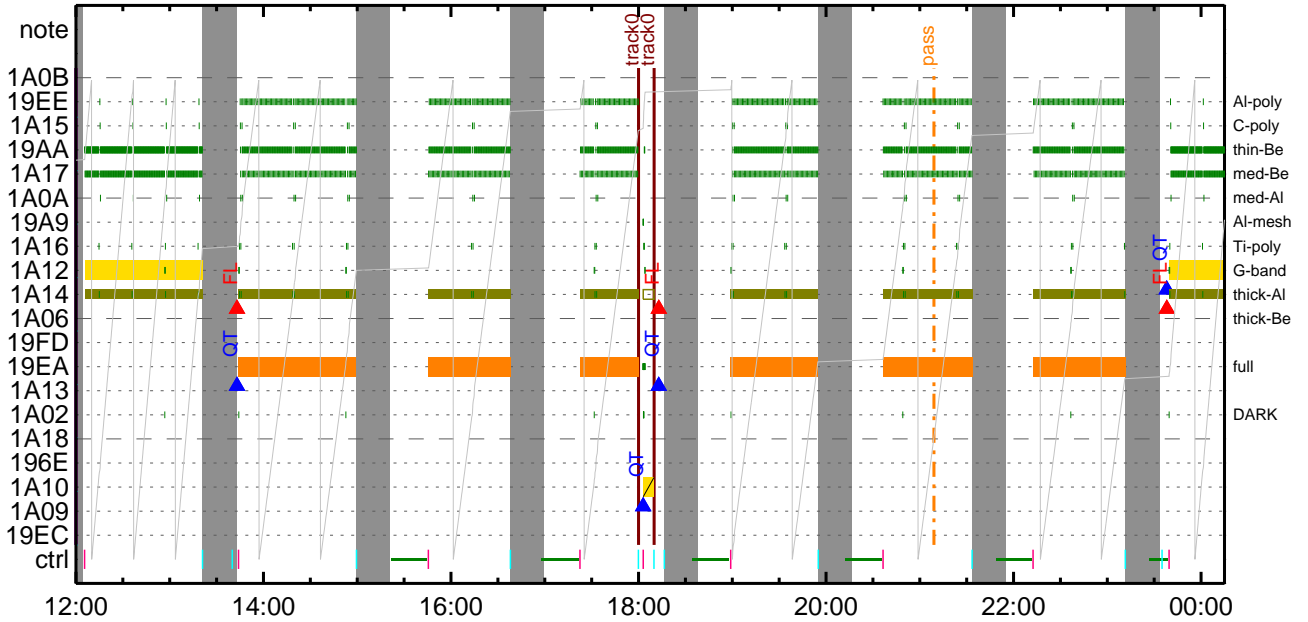
* * * * *

FLD Patrol											
Term		Pointing (x, y)				Comment					
04/26 18:10:16 - 04/27 06:11:16		Fixed (870.0, -260.0)				#IHOP244 on AR12046					
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		80sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

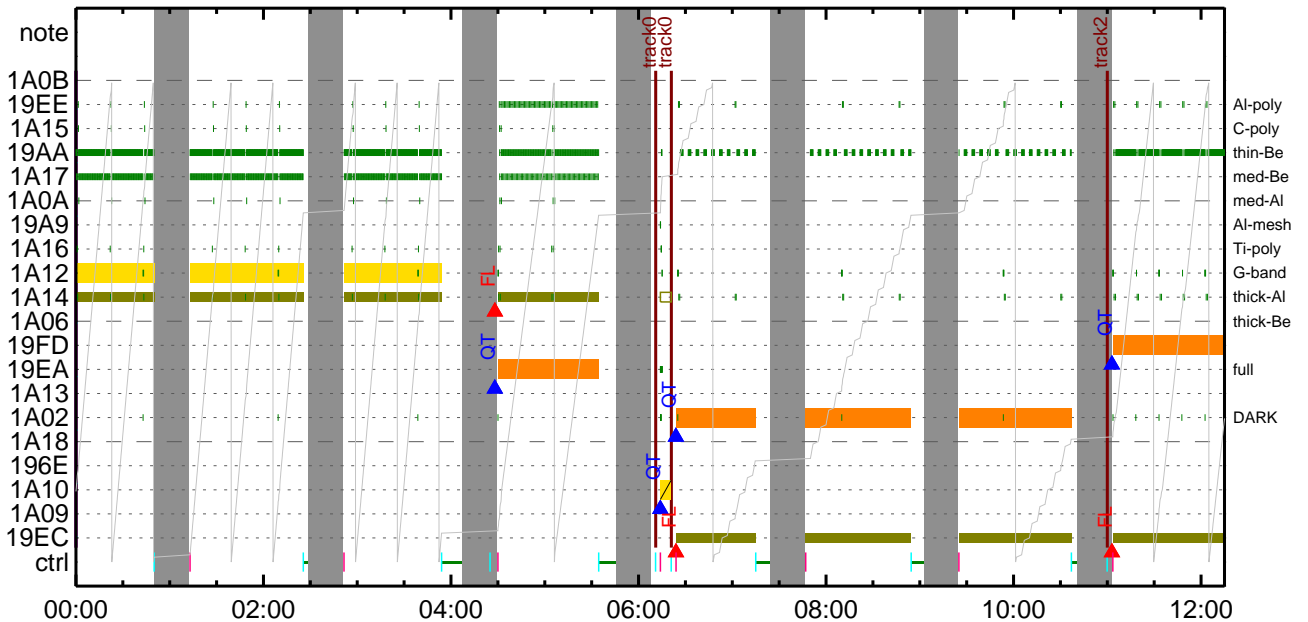
CMDI #0244 2014/04/26



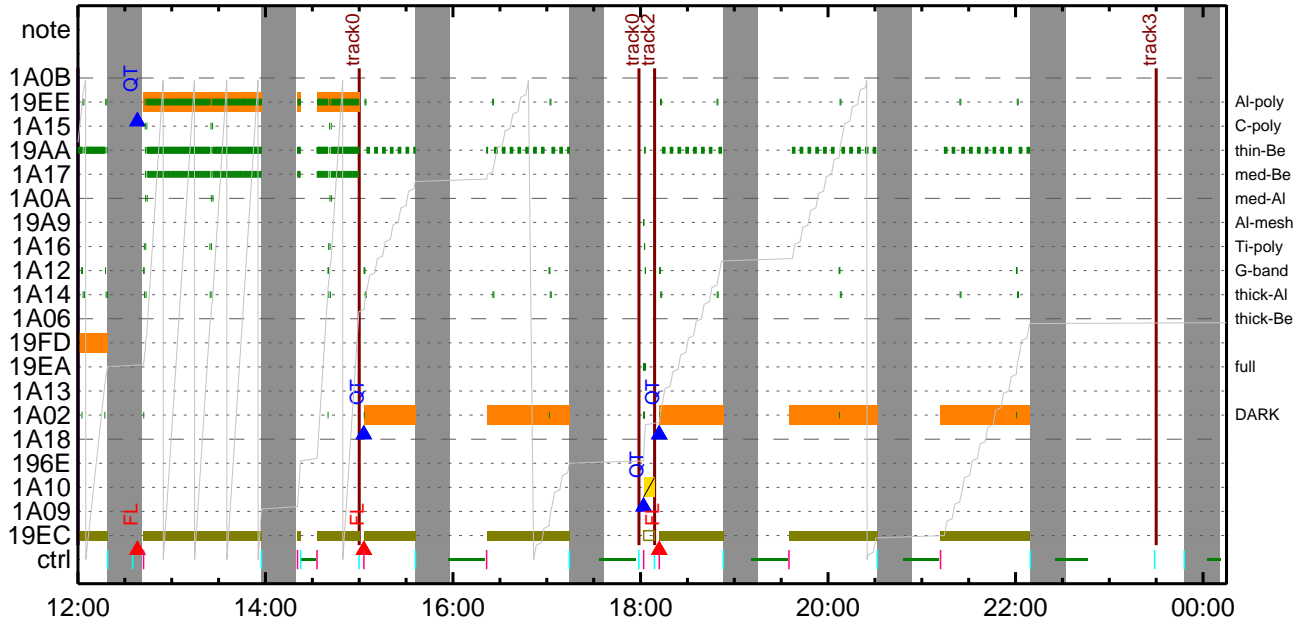
CMDI #0244 2014/04/26



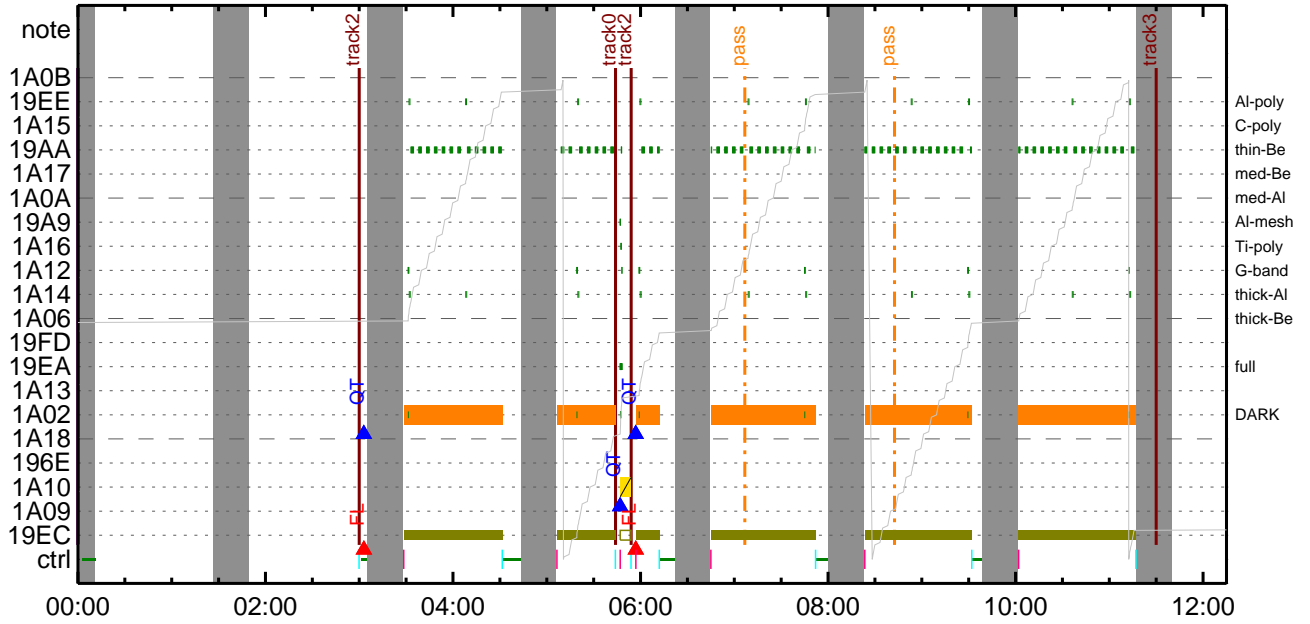
CMDI #0244 2014/04/27



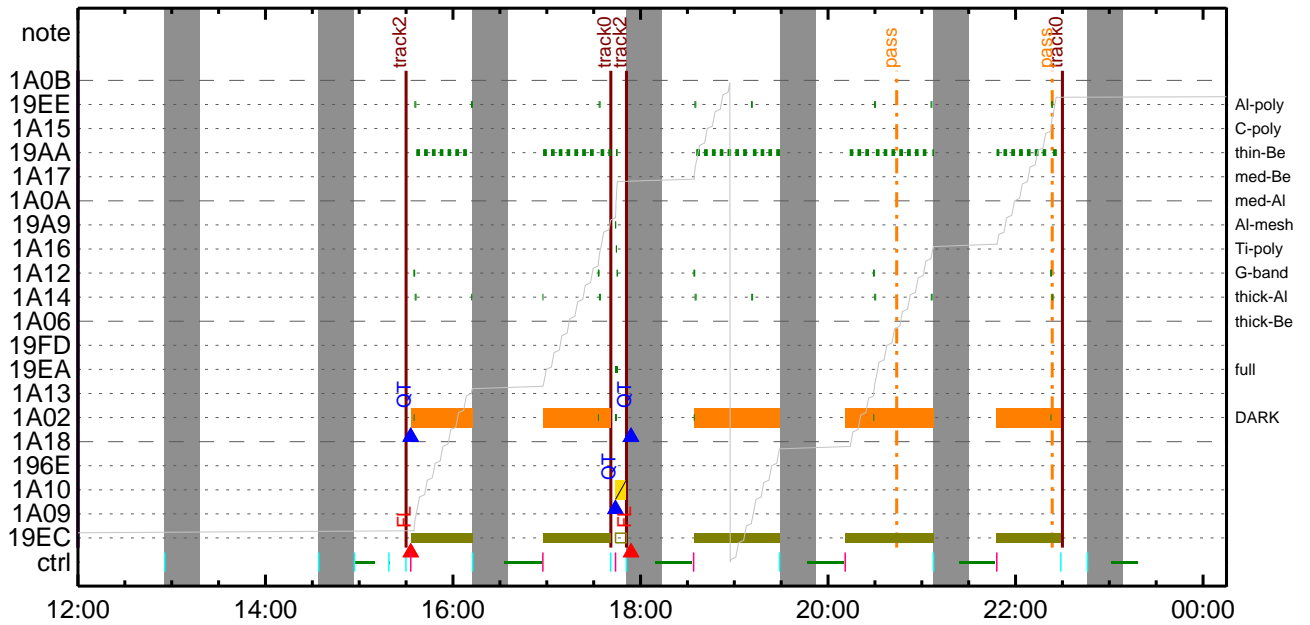
CMDI #0244 2014/04/27



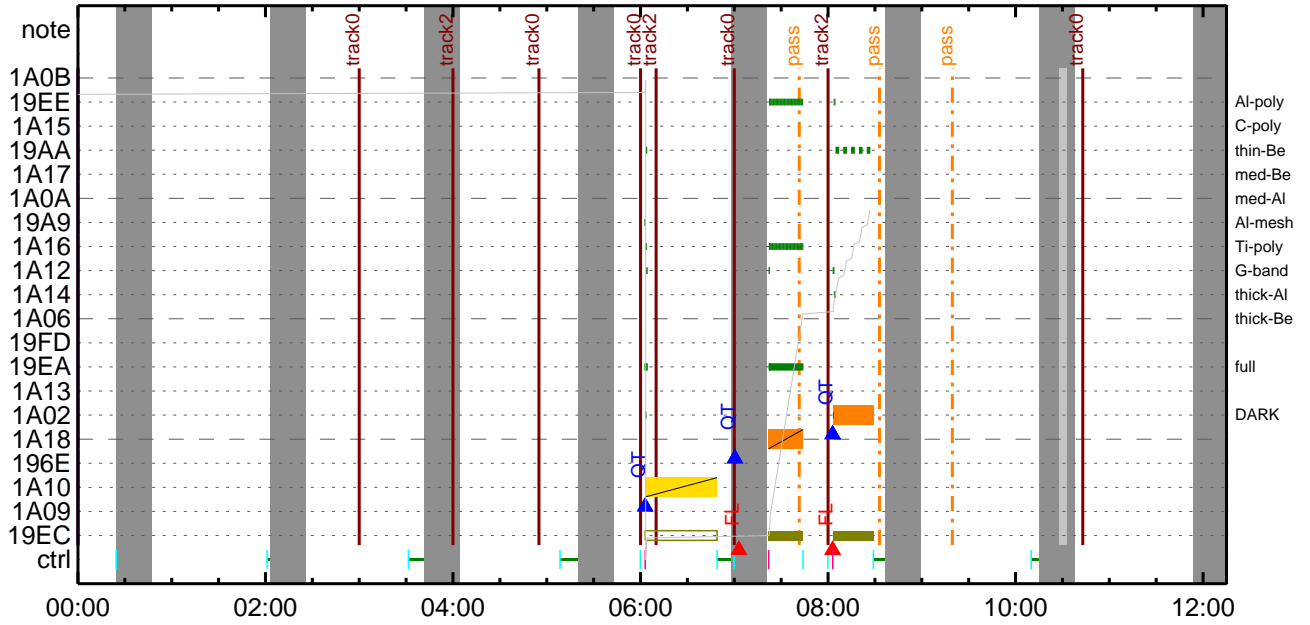
CMDI #0244 2014/04/28



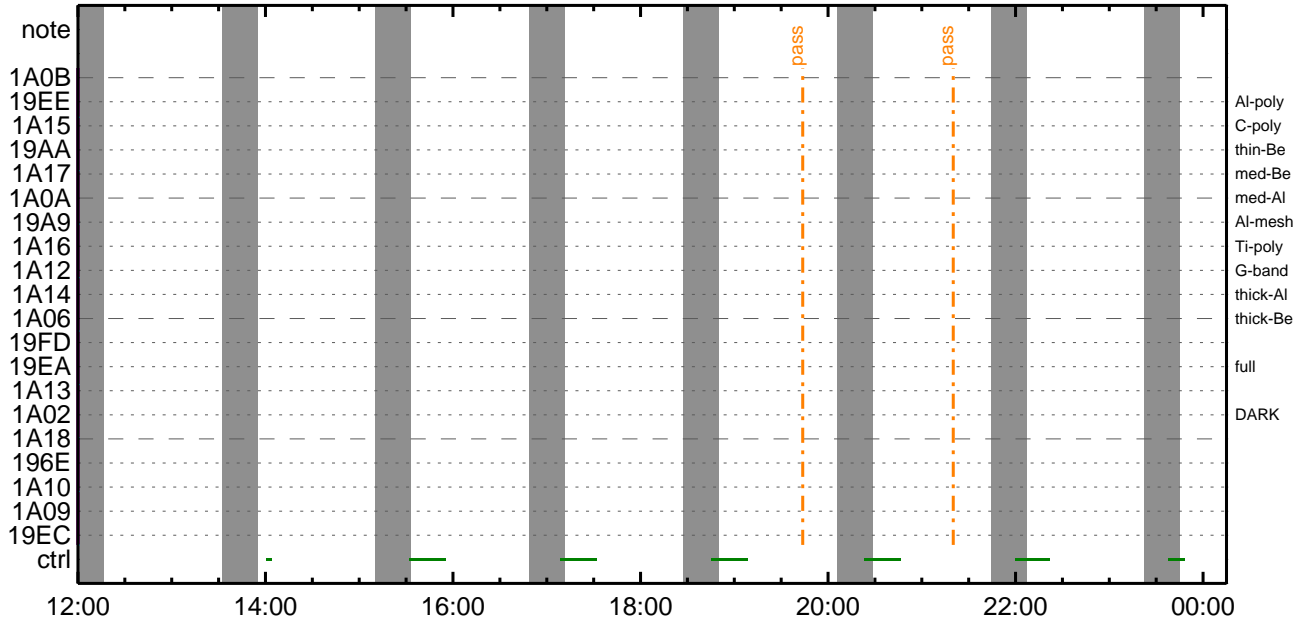
CMDI #0244 2014/04/28



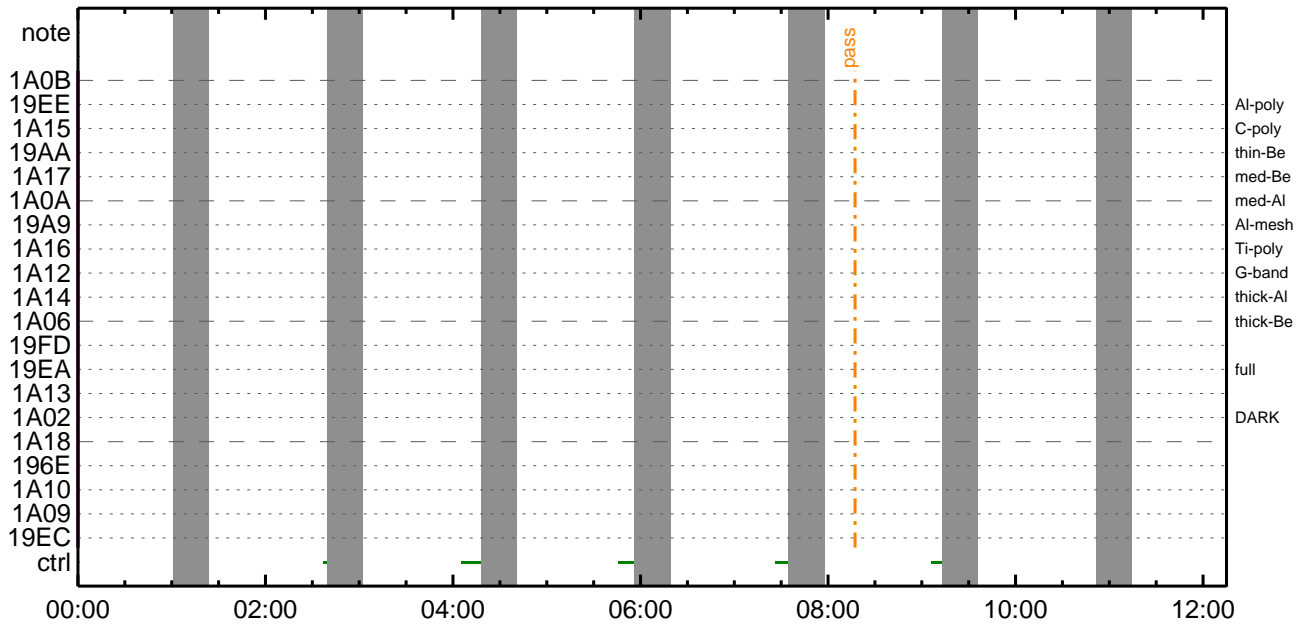
CMDI #0244 2014/04/29



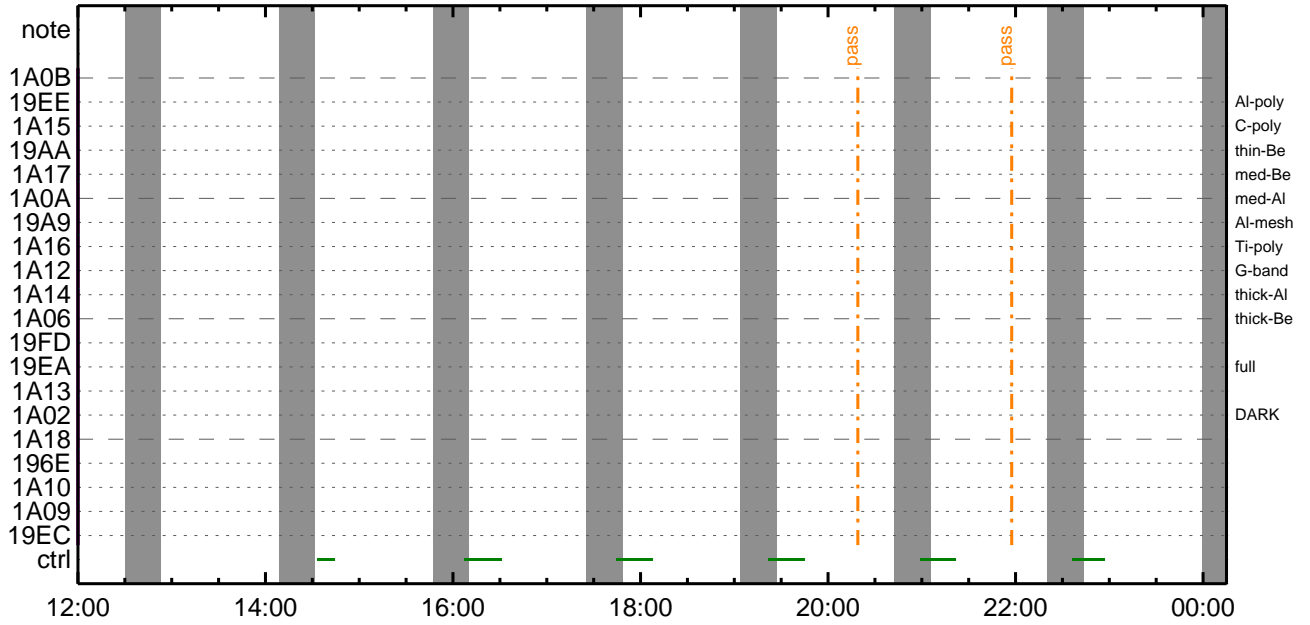
CMDI #0244 2014/04/29



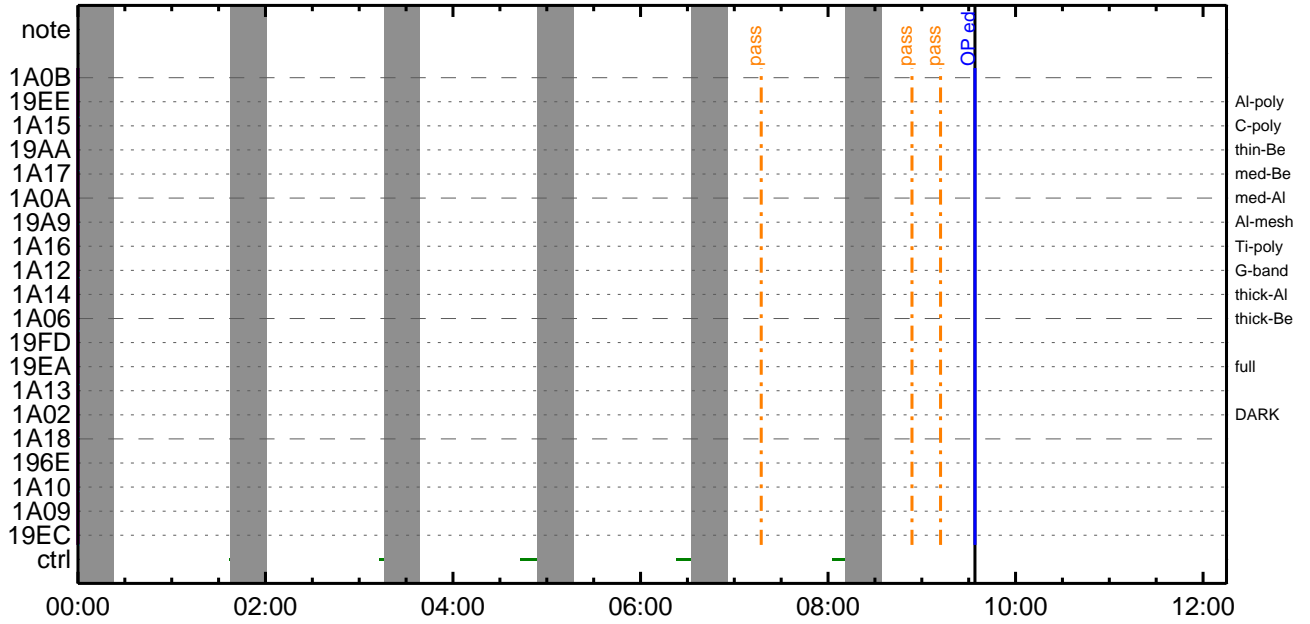
CMDI #0244 2014/04/30



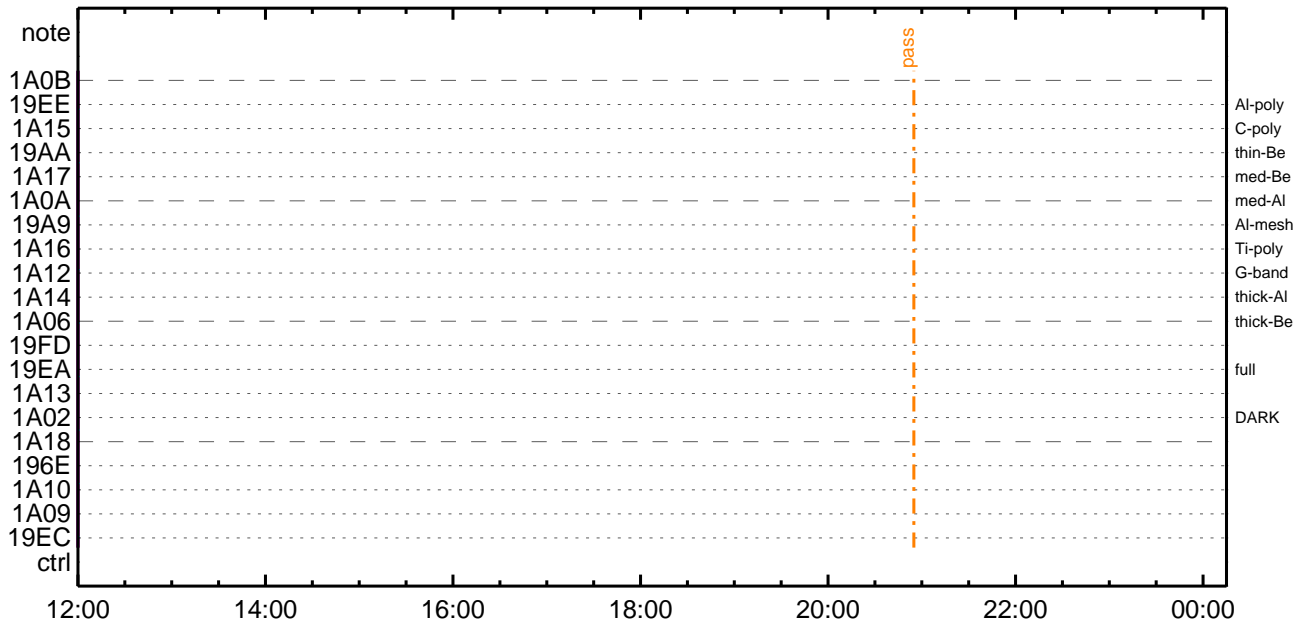
CMDI #0244 2014/04/30



CMDI #0244 2014/05/01



CMDI #0244 2014/05/01




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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YA6Yx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-348:OP
0104 ( )
0105 S. OG og-348:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYA6Yx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YA6Yx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGãî½ª¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YA6Yx½ªªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGãî½ª¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YA6Yx½ªªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPãî½ª¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** òê²¼òî½ª¹ç•è²îOKò³îÇ§ *****
0167 C. DHUãî½ª¹ç•è²îOKò³îÇ§
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADãî½ª¹ç•è²îOKò³îÇ§; çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0180 C.
0181 C.
0182 C. TIY³YpY6YÉòòÀDîç(UT)
0183 +. TI 2014-04-26 09:44:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2014-04-26 09:44:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2014-04-26 09:44:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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0194 C.
0195 +. TI 2014-04-26 09:48:59.5
0196 DC 01-B2 DHU_OP_START
0197 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0198 C.
0199 C. °Ê²¼õîÄè%îíñõîîŷÄŷ§ŷÄŷ-¹àîü
0200 C. çç[HK1_TI_CMD_ENA/DIS] EQ ENA
0201 C. çç[HK1_TI_CMD_NUM] EQ 4
0202 C. çç[HK1_NEXT_EXEC_PIM] EQ DHU
0203 C. çç[HK1_NEXT_EXEC_DC] EQ 0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÓŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC (03 ab 03 01 02)
0212 C. çç[HK1_DMP_TOP_ADRS_1] EQ 07
0213 C. çç[HK1_DMP_TOP_ADRS_0] EQ 2B
0214 C. çç[HK1_DMP_BLOCK_NUM] EQ 3
0215 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0216 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC (07 0b f8)
0219 C. çç[HK1_PKT_FORM_NO] EQ 7
0220 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0221 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0222 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0223 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0224 C.
0225 C. ŷÄŷÓŷ×½ªî»õ³îç§
0226 C. çç[HK1_DMP_CHK_FLG] EQ NON
0227 C.
0228 C. RAM ID=TI_TBLõîî¼È¹ç•è²îOKõ³îç§
0229 C.
0230 C. DHUŷã;¼ŷÈ;È¼ŷ¼. ŷî;¼ŷÈ;Èõõîãõ¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC (02 0a f8)
0233 C. çç[HK1_PKT_FORM_NO] EQ 2
0234 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0235 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0236 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0237 C.
0238 C. Stop EIS observation and temporarily disable EIS mode changes
0239 C.
0240 C.
0241 C. ***** Start EIS operation (TI set) *****
0242 C. Execute, after the success of OP upload.
0243 C. Set EIS TI-commands
0244 +. TI 2014-04-26 09:48:30.0
0245 DC 07-FC EIS_MODE_MANU
0246 BC (21 02)
0247 +. TI 2014-04-26 09:48:40.0
0248 DC 07-FC EIS_MODE_CHG_DIS
0249 BC (22)
0250 C. [] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0251 C. ***** End EIS operation (TI set) *****
0252 C.
0253 C.
0254 C. *****
0255 C. SOT TI command set
0256 C. *****
0257 C. Execute, after the success of OP upload.
0258 +. TI 2014-04-26 09:48:16.0
0259 DC 07-F0 MDP_SOT_MODE_STBY
0260 BC (41)
0261 C. -----
0262 C. HK1_TI_CMD_NUM = 1 CNTUP []
0263 C. -----
0264 C. ***** SOT END *****
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2014-04-26 09:48:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC (c3)
0271 C. [] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP `ûÃîãî»ö¼ŷõÈÄõ¹õèDCBC•x²è *****
0276 C. (¼ã°îŷÓŷÄŷÈŷŷŷÄŷçŷèÈ¼õõ¼Ã»Üõ¹õè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷÐŷ¹•î Daily±çîñõÈ'Øõ¹õèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ã»Ü;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

(a) Spacecraft Operation Procedure (real-commands)

```
main-350 2014-04-26 14:14:38 234 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY-¼Ä»Û;ä
0005 C.
0006 C. YÄYß;¼Y³YßYóYÉÄ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Ëççãð•µ°È»Í×ÁÇçÍYçYÄY×YÍ;¼YÉ;ËÈè¼µ•íÍË;ÈðÈ¼°ÇÖñ•çç¼l¹ççí;çÄ®, ùñ¹ñðñççÄ+ç®ñ•ñÈñççñÈ;ç
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. XÁ+ççµ;ON
0016 C. *****
0017 C. ç" °ÆÀ, Í×ÈYñãLOSñçççí»p´Öñð¹íí, ñ•; çÉÖÍ×ñÈXÄÖONñí¹ÖñÈñíñÈñçççñÈ;ç
0018 C.
0019 +. DC 03-B4 TCIA_XPA_ON/HI
0020 M. WAIT_SEC 1
0021 + DC 03-84 TCIA_XMOD_ON
0022 M. WAIT_SEC 1
0023 + DC 03-95 TCIA_XMOD_QPSK
0024 C. çç[HK1_XPA_ON/OFF] EQ ON
0025 C. çç[HK1_XPA_PWR_HI/LO] EQ HI
0026 C. çç[HK1_XMOD_ON/OFF] EQ ON
0027 C. çç[HK1_XMOD_QPSK/PM] EQ QPSK
0028 C.
0029 . C. XÝDÝóYÉYÍYÄY-¾ÖÀÖñ-°ÄÄèñ•ççç; ç°È²¼ñí°ÆÀ, ¼è¼ççð¼Ä¹Öñ¹ñè;ç
0030 C.
0031 . C. *****
0032 C. DR PT1 ÄÍ¼í°ÆÄ,
0033 C. *****
0034 C. ç" RESTART;ÈPT1;Èñ•çççç¼l¹ççí; ç°È²¼ñí¼Ä¹Öñ»ñ°; çDCBC-150ñççÈñçç;ç
0035 C.
0036 . C. ;ãPT1°ÆÄ, ³«»í;ä
0037 +. DC 01-29 DHU_S/X_VC4_OFF
0038 + DC 06-C8 DR_PT1_REP_SEL
0039 BC (01 00)
0040 + DC 06-B3 DR_REP_START
0041 + DC 01-32 DHU_X_VC4_ON
0042 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0043 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0044 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0045 C.
0046 . C. ;ãYçYóYÉYÉÄÜÄÖ;ÈÄ•Ä°²óÈð;È, áñí°ÆÄ, °È³«;ä
0047 +. DC 06-B3 DR_REP_START
0048 + DC 01-32 DHU_X_VC4_ON
0049 C. çç[HK1_REP_PT_1/2] EQ PT1 (¼Ä¹Ö, ;¼Ú)
0050 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0051 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0052 C.
0053 C.
0054 . C. PT1°ÆÄ, ñ-¼«Æ°Ää»ßñ•ççç, á; ç°È²¼ñð¼Ä¹Öñ¹ñè;ç
0055 C. YçYóYÉYÉÄÜÄÖñãÄ•Ä°²óÈðñ-¼áñç¼l¹ççí°í»ñ¹ñèñçççÄÖñç;ç
0056 C.
0057 . C. *****
0058 C. DR PT2 ÄÍ¼í°ÆÄ,
0059 C. *****
0060 C. ç" RESTART;ÈPT2;Èñ•çççç¼l¹ççí; ç°È²¼ñí¼Ä¹Öñ»ñ°; çDCBC-151ñççÈñçç;ç
0061 C.
0062 . C. ;ãPT2°ÆÄ, ³«»í;ä
0063 +. DC 01-29 DHU_S/X_VC4_OFF
0064 + DC 06-C8 DR_PT2_REP_SEL
0065 BC (02 00)
0066 + DC 06-B3 DR_REP_START
0067 + DC 01-32 DHU_X_VC4_ON
0068 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0069 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0070 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0071 C.
0072 . C. ;ãYçYóYÉYÉÄÜÄÖ;ÈÄ•Ä°²óÈð;È, áñí°ÆÄ, °È³«;ä
0073 +. DC 06-B3 DR_REP_START
0074 + DC 01-32 DHU_X_VC4_ON
0075 C. çç[HK1_REP_PT_1/2] EQ PT2 (¼Ä¹Ö, ;¼Ú)
0076 C. çç[HK1_REP_STA/STP] EQ START (¼Ä¹Ö, ;¼Ú)
0077 C. çç[HK1_X_VC4_ON/OFF] EQ ON (¼Ä¹Ö, ;¼Ú)
0078 C.
0079 . C. *****
0080 C. DR°ÆÄ, Äã»ß;çXÁ+ççµ;OFF
0081 C. *****
0082 C.
0083 . C. ;ãDR°ÆÄ, Äã»ß;ä
0084 +. DC 06-B4 DR_REP_STOP
0085 + DC 01-29 DHU_S/X_VC4_OFF
0086 C. çç[HK1_REP_STA/STP] EQ STOP
0087 C. çç[HK1_S_VC4_ON/OFF] EQ OFF
0088 C. çç[HK1_X_VC4_ON/OFF] EQ OFF
0089 C.
0090 . C. ;ãXÁ+ççµ;OFF;ä
0091 +. DC 03-85 TCIA_XMOD_OFF
0092 M. WAIT_SEC 1
0093 + DC 03-B5 TCIA_XPA_OFF
0094 C. çç[HK1_XMOD_ON/OFF] EQ OFF
0095 C. çç[HK1_XPA_ON/OFF] EQ OFF
```

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0096 C.
0097 C.
0098 C.
0099 C. ***** XRT START *****
0100 C.
0101 +. DC 07-F0 MDP_XRT_CTRL_MANU
0102 BC (c1)
0103 + DC 07-F0 MDP_XRT_MODE_STBY
0104 BC (c3)
0105 . C. ----- Success Verify ? OK / NG ____
0106 C.
0107 C. XRT Obs. Table Upload
0108 . S. RAM ram-291:MDP_OBS_X
0109 ( )
0110 C.
0111 +. DC 07-F0 MDP_DUMP_XRTTBL
0112 BC (84 07 00 00 00 3a d4)
0113 . C. ----- Comparison Check ? OK / ERR ____
0114 C.
0115 C.
0116 +. DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 01 b1 b1 04 04)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 02 b1 b1 08 08)
0120 + DC 07-F0 MDP_XRT_ROI_SET
0121 BC (cd 03 b1 b1 08 08)
0122 + DC 07-F0 MDP_XRT_ROI_SET
0123 BC (cd 04 b1 b1 06 06)
0124 + DC 07-F0 MDP_XRT_ROI_SET
0125 BC (cd 05 85 83 06 06)
0126 + DC 07-F0 MDP_XRT_ROI_SET
0127 BC (cd 06 85 83 06 06)
0128 + DC 07-F0 MDP_XRT_ROI_SET
0129 BC (cd 07 80 80 20 20)
0130 + DC 07-F0 MDP_XRT_ROI_SET
0131 BC (cd 08 80 80 20 08)
0132 + DC 07-F0 MDP_XRT_ROI_SET
0133 BC (cd 09 80 80 08 20)
0134 + DC 07-F0 MDP_XRT_ROI_SET
0135 BC (cd 0a 85 83 08 08)
0136 + DC 07-F0 MDP_XRT_ROI_SET
0137 BC (cd 0f 80 80 06 06)
0138 + DC 07-F0 MDP_XRT_ROI_SET
0139 BC (cd 10 80 80 08 08)
0140 + DC 07-F8 XRT_FOCUS_POSITION
0141 BC (22 fe 97 00)
0142 + DC 07-F0 MDP_XRT_FLD_ENA
0143 BC (d8)
0144 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0145 BC (c8)
0146 + DC 07-F0 MDP_XRT_AEC_RESET
0147 BC (d0)
0148 + DC 07-F0 MDP_XRT_ARS_DIS
0149 BC (d5)
0150 + DC 07-F0 MDP_XRT_FLD_RESET
0151 BC (da)
0152 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0153 BC (c4 04)
0154 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0155 BC (c5 01)
0156 . C. ----- Success Verify ? OK / NG ____
0157 C.
0158 C.
0159 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0160 C.
0161 +. DC 07-F0 MDP_XRT_MODE_OBSV
0162 BC (c2)
0163 +. TI 2014-04-26 09:48:02.0
0164 DC 07-F0 MDP_XRT_MODE_OBSV
0165 BC (c2)
0166 . C. ----- Success Verify ? OK / NG ____
0167 C.
0168 C. ***** XRT END *****
0169 . C. *****
0170 C. SOT table upload
0171 C. *****
0172 . C. < Stop FG table >
0173 +. DC 07-F0 MDP_FG_CTRL_MANU
0174 BC (51)
0175 . C. -----
0176 C. MDP_FG_CTRL_MODE = MANU [ ]
0177 C. -----
0178 C.
0179 . C. <Upload FG Observation Table>
0180 . S. RAM ram-268:MDP_OBS_F
0181 ( )
0182 C.
0183 . C. < Dump RAMID=MDP_OBS_F >
0184 +. DC 07-F0 MDP_DUMP_FGTBL
0185 BC (82 07 00 00 00 38 b8)
0186 C. -----
0187 C. MDP_OBS_F verify = OK/NG [ ]
0188 C. -----
0189 C.
0190 . C. < Stop SP table >
0191 +. DC 07-F0 MDP_SP_CTRL_MANU
0192 BC (61)
0193 C. -----

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*** OP Sequence for XRT ***

2014/04/26	09:58:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/04/26	09:58:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/04/26	09:59:00.0	AOCS_OrE-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 17 1a b2 a8		
2014/04/26	09:59:16.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/04/26	09:59:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/04/26	09:59:20.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/04/26	09:59:22.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/04/26	09:59:24.0	XRT_FLD_RESET_431_OG [0x1af]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/04/26	10:01:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2014/04/26	10:01:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0b		
2014/04/26	10:26:00.0	XRT_Custom_430_OG [0x1ae]					
2014/04/26	10:27:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/04/26	11:42:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/04/26	11:42:32.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/04/26	11:42:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/04/26	11:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/04/26	12:04:30.0	XRT_Custom_430_OG [0x1ae]					
2014/04/26	12:05:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/04/26	13:21:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/04/26	13:21:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/04/26	13:21:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/04/26	13:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/04/26	13:40:00.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/04/26	13:40:02.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/04/26	13:40:22.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/04/26	13:40:24.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/04/26	13:40:26.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/04/26	13:40:28.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/04/26	13:40:30.0	XRT_FLD_RESET_431_OG [0x1af]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/04/26	13:43:00.0	XRT_Custom_430_OG [0x1ae]					
2014/04/26	13:43:02.0	XRT_QT_PROG_SET_444_OG [0x1bc]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08		
2014/04/26	13:43:04.0	XRT_FL_PROG_SET_417_OG [0x1a1]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0b		
2014/04/26	13:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/04/26	14:59:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/04/26	14:59:32.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/04/26	14:59:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/04/26	15:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/04/26	15:44:30.0	XRT_Custom_430_OG [0x1ae]					
2014/04/26	15:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/04/26	16:38:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/04/26	16:38:02.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/04/26	16:38:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/04/26	16:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/04/26	17:21:30.0	XRT_Custom_430_OG [0x1ae]					
2014/04/26	17:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/04/26	17:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/04/26	17:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2014/04/26	18:00:00.0	AOCS_OrE-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		

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2014/04/26	18:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/04/26	18:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/04/26	18:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/26	18:02:58.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2014/04/26	18:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/26	18:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/26	18:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/04/26	18:10:00.0	AOCs_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	00 17 1a b2 a8				
2014/04/26	18:10:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/04/26	18:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/04/26	18:10:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/04/26	18:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/26	18:10:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/26	18:12:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 08				
2014/04/26	18:12:58.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0b				
2014/04/26	18:16:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/26	18:16:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/26	18:16:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/26	18:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/26	18:58:00.0	XRT_Custom_430_OG [0x1ae]							
2014/04/26	18:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/26	19:55:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/26	19:55:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/26	19:55:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/26	19:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/26	20:35:30.0	XRT_Custom_430_OG [0x1ae]							
2014/04/26	20:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/26	21:33:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/26	21:33:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/26	21:33:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/26	21:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/26	22:11:30.0	XRT_Custom_430_OG [0x1ae]							
2014/04/26	22:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/26	23:11:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/26	23:11:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/26	23:11:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/26	23:14:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/26	23:35:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/26	23:35:02.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/04/26	23:35:22.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/04/26	23:35:24.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/04/26	23:35:26.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/04/26	23:35:28.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/26	23:35:30.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/26	23:38:02.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2014/04/26	23:38:04.0	XRT_FL_PROG_SET_417_OG [0x1a1]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 0b				
2014/04/26	23:38:30.0	XRT_Custom_430_OG [0x1ae]							
2014/04/26	23:39:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/27	00:50:00.0	XRT_CTRL_MANU_400_OG [0x190]							

2014/04/27	00:50:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
			MDP_XRT_FLD_RESET	1	07-F0	da			
2014/04/27	00:50:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/04/27	00:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/04/27	01:12:00.0	XRT_Custom_430_OG [0x1ae]							
2014/04/27	01:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/04/27	02:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/04/27	02:25:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/04/27	02:25:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/04/27	02:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/04/27	02:50:30.0	XRT_Custom_430_OG [0x1ae]							
2014/04/27	02:51:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/04/27	03:54:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/04/27	03:54:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/04/27	03:54:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/04/27	03:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/04/27	04:25:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/04/27	04:25:02.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2014/04/27	04:25:22.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/04/27	04:25:24.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/04/27	04:25:26.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/04/27	04:25:28.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/04/27	04:25:30.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/04/27	04:28:02.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 08			
2014/04/27	04:28:04.0	XRT_FL_PROG_SET_417_OG [0x1a1]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 0b			
2014/04/27	04:29:00.0	XRT_Custom_430_OG [0x1ae]							
2014/04/27	04:30:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/04/27	05:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/04/27	05:34:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/04/27	05:34:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/04/27	05:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/04/27	06:10:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/04/27	06:10:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2014/04/27	06:11:00.0	AOCS_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2014/04/27	06:11:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2014/04/27	06:13:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2014/04/27	06:13:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/04/27	06:13:58.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03			
2014/04/27	06:14:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/04/27	06:20:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/04/27	06:20:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2014/04/27	06:21:00.0	AOCS_Orе-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 17 1a b2 a8			
2014/04/27	06:21:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/04/27	06:21:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/04/27	06:21:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/04/27	06:21:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/04/27	06:21:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/04/27	06:23:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06			
2014/04/27	06:23:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]							

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2014/04/27	06:24:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	01
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/04/27	07:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/04/27	07:15:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/04/27	07:15:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/04/27	07:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/04/27	07:46:00.0	XRT_Custom_430_OG [0x1ae]					
2014/04/27	07:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/04/27	08:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/04/27	08:54:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/04/27	08:54:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/04/27	08:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/04/27	09:24:00.0	XRT_Custom_430_OG [0x1ae]					
2014/04/27	09:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/04/27	10:37:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/04/27	10:37:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/04/27	10:37:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/04/27	10:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/04/27	10:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/04/27	10:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2014/04/27	11:00:00.0	AOCS_Or-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02 00 00 00 00	
2014/04/27	11:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/04/27	11:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/04/27	11:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/04/27	11:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/04/27	11:00:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/04/27	11:02:30.0	XRT_Custom_430_OG [0x1ae]					
2014/04/27	11:02:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2014/04/27	11:02:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01	
2014/04/27	11:03:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/04/27	12:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/04/27	12:19:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/04/27	12:19:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/04/27	12:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/04/27	12:35:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/04/27	12:35:02.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2014/04/27	12:35:22.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/04/27	12:35:24.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/04/27	12:35:26.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/04/27	12:35:28.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/04/27	12:35:30.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/04/27	12:38:02.0	XRT_QT_PROG_SET_426_OG [0x1aa]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13	
2014/04/27	12:38:04.0	XRT_FL_PROG_SET_448_OG [0x1c0]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01	
2014/04/27	12:41:00.0	XRT_Custom_430_OG [0x1ae]					
2014/04/27	12:42:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/04/27	13:57:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/04/27	13:57:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/04/27	13:57:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/04/27	14:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	

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2014/04/27	14:19:30.0	XRT_Custom_430_OG [0x1ae]							
2014/04/27	14:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/27	14:22:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/27	14:22:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/27	14:22:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/27	14:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/27	14:32:00.0	XRT_Custom_430_OG [0x1ae]							
2014/04/27	14:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/27	14:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/27	14:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/04/27	15:00:00.0	AOCS_Orе-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 12 7e af bf				
2014/04/27	15:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/04/27	15:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/04/27	15:00:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/04/27	15:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/27	15:00:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/27	15:02:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2014/04/27	15:02:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01				
2014/04/27	15:03:00.5	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/27	15:36:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/27	15:36:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/27	15:36:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/27	15:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/27	16:20:30.0	XRT_Custom_430_OG [0x1ae]							
2014/04/27	16:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/27	17:14:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/27	17:14:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/27	17:14:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/27	17:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/27	17:58:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/27	17:58:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/04/27	17:59:00.0	AOCS_Orе-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/04/27	17:59:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/04/27	18:01:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/04/27	18:01:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/27	18:01:58.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2014/04/27	18:02:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/27	18:08:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/27	18:08:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/04/27	18:09:00.0	AOCS_Orе-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2014/04/27	18:09:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/04/27	18:09:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/04/27	18:09:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/04/27	18:09:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/27	18:09:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/27	18:11:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2014/04/27	18:11:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01				
2014/04/27	18:12:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2014/04/27	18:53:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/27	18:53:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/27	18:53:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/27	18:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/27	19:34:00.0	XRT_Custom_430_OG [0x1ae]							
2014/04/27	19:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/27	20:31:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/27	20:31:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/27	20:31:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/27	20:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/27	21:11:00.0	XRT_Custom_430_OG [0x1ae]							
2014/04/27	21:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/27	22:09:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/27	22:09:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/27	22:09:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/27	22:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/27	23:29:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/27	23:30:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2014/04/27	23:48:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/27	23:48:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/27	23:48:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/27	23:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/28	02:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/28	02:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/04/28	03:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2014/04/28	03:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/04/28	03:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/04/28	03:00:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/04/28	03:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/28	03:00:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/28	03:02:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2014/04/28	03:02:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01				
2014/04/28	03:27:30.0	XRT_Custom_430_OG [0x1ae]							
2014/04/28	03:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/28	04:31:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/28	04:31:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/04/28	04:31:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/04/28	04:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/04/28	05:05:30.0	XRT_Custom_430_OG [0x1ae]							
2014/04/28	05:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/28	05:43:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/04/28	05:43:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/04/28	05:44:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/04/28	05:44:16.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/04/28	05:46:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/04/28	05:46:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/04/28	05:46:58.0	XRT_QT_PROG_SET_422_OG [0x1a6]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2014/04/28	05:47:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/04/28	05:53:54.0	XRT_CTRL_MANU_402_OG [0x192]							

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2014/04/28	05:53:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		XRT_FOCUS_POSITION		4	07-F8	22 fe 97 00
2014/04/28	05:54:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02 00 00 00 00
2014/04/28	05:54:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2014/04/28	05:54:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/04/28	05:54:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2014/04/28	05:54:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/04/28	05:54:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/04/28	05:56:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2014/04/28	05:56:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/04/28	05:57:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/04/28	06:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/04/28	06:12:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/04/28	06:12:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/04/28	06:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/04/28	06:44:00.0	XRT_Custom_430_OG [0x1ae]				
2014/04/28	06:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/04/28	07:52:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/04/28	07:52:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/04/28	07:52:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/04/28	07:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/04/28	08:22:30.0	XRT_Custom_430_OG [0x1ae]				
2014/04/28	08:23:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/04/28	09:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/04/28	09:32:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/04/28	09:32:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/04/28	09:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/04/28	10:01:00.0	XRT_Custom_430_OG [0x1ae]				
2014/04/28	10:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/04/28	11:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/04/28	11:17:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/04/28	11:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/04/28	11:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/04/28	11:30:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	03 00 00 00 00
2014/04/28	12:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/04/28	12:55:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/04/28	12:55:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/04/28	12:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/04/28	14:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/04/28	14:34:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/04/28	14:34:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/04/28	14:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/04/28	14:57:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/04/28	14:57:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/04/28	14:57:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/04/28	15:00:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/04/28	15:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/04/28	15:19:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/04/28	15:19:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]				

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2014/04/28	15:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
			MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/04/28	15:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/04/28	15:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/04/28	15:30:00.0	AOCS_Or-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02 00 00 00 00
2014/04/28	15:30:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2014/04/28	15:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/04/28	15:30:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2014/04/28	15:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/04/28	15:30:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/04/28	15:32:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2014/04/28	15:32:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/04/28	15:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/04/28	16:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/04/28	16:12:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/04/28	16:12:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/04/28	16:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/04/28	16:56:30.0	XRT_Custom_430_OG [0x1ae]				
2014/04/28	16:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/04/28	17:40:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/04/28	17:40:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/04/28	17:41:00.0	AOCS_Or-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2014/04/28	17:41:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9
2014/04/28	17:43:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/04/28	17:43:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/04/28	17:43:58.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03
2014/04/28	17:44:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/04/28	17:50:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/04/28	17:50:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/04/28	17:51:00.0	AOCS_Or-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02 00 00 00 00
2014/04/28	17:51:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2014/04/28	17:51:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/04/28	17:51:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2014/04/28	17:51:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/04/28	17:51:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/04/28	17:53:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2014/04/28	17:53:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/04/28	18:33:00.0	XRT_Custom_430_OG [0x1ae]				
2014/04/28	18:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/04/28	19:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/04/28	19:29:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/04/28	19:29:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/04/28	19:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/04/28	20:10:00.0	XRT_Custom_430_OG [0x1ae]				
2014/04/28	20:11:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/04/28	21:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/04/28	21:07:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/04/28	21:07:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/04/28	21:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]				

2014/04/28	21:47:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/04/28	21:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
2014/04/28	22:29:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/04/28	22:30:00.0	AOCS_Orе-point_Start_6_OG [0x09c]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/04/28	22:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	00 ae 7f 01 58			
2014/04/28	22:46:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/04/28	22:46:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/04/28	22:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/04/29	00:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/04/29	00:24:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/04/29	00:24:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/04/29	00:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/04/29	02:01:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/04/29	02:01:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/04/29	02:01:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/04/29	02:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/04/29	03:00:00.0	AOCS_Orе-point_Start_7_OG [0x09d]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/04/29	03:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	AOCU_NM	5	02-76	00 27 1a b5 0f			
2014/04/29	03:31:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/04/29	03:31:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/04/29	03:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/04/29	04:00:00.0	AOCS_Orе-point_Start_3_OG [0x099]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/04/29	04:55:00.0	AOCS_Orе-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	02 00 00 00 00			
2014/04/29	05:08:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/04/29	05:08:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/04/29	05:08:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/04/29	05:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/04/29	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/04/29	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2014/04/29	06:00:00.0	AOCS_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2014/04/29	06:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2014/04/29	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2014/04/29	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/04/29	06:02:58.0	XRT_QT_PROG_SET_422_OG [0x1a6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03			
2014/04/29	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/04/29	06:10:00.0	AOCS_Orе-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	02 00 00 00 00			
2014/04/29	06:49:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/04/29	06:49:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/04/29	06:49:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/04/29	06:52:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/04/29	06:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/04/29	06:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2014/04/29	07:00:00.0	AOCS_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2014/04/29	07:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/04/29	07:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/04/29	07:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/04/29	07:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]							

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2014/04/29	07:00:24.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/04/29	07:00:26.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/04/29	07:02:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	05
2014/04/29	07:21:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	01
2014/04/29	07:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/04/29	07:44:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/04/29	07:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/04/29	07:59:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/04/29	08:00:00.5	AOCS_Ore-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2014/04/29	08:00:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCS_Ore-point_Start_3_OG [0x099]	5	02-76	02	00 00 00 00
2014/04/29	08:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	AOCU_NM	5	02-76	02	00 00 00 00
2014/04/29	08:00:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/04/29	08:00:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/04/29	08:00:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/04/29	08:02:56.0	XRT_QT_PROG_SET_441_OG [0x1b9]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/04/29	08:02:58.0	XRT_FL_PROG_SET_448_OG [0x1c0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/04/29	08:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06
2014/04/29	08:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	01
2014/04/29	08:29:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/04/29	08:29:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/04/29	08:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/04/29	10:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/04/29	10:10:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/04/29	10:10:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/04/29	10:10:04.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/04/29	10:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/04/29	10:43:00.0	AOCS_Ore-point_Start_2_OG [0x098]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
		AOCU_NM	AOCS_Ore-point_Start_2_OG [0x098]	5	02-76	00	00 00 00 00