

XRT Timeline to be uploaded on 2014/05/31

Period: 2014/05/31 10:51:00 - 2014/06/09 11:54:00

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Normal mode

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XOB #19A1: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 40s cad With G-band and VLS closed												
Term	Pointing (x, y)				Comment							
05/31 11:08:00 - 05/31 17:57:24	Track (302.0, -175.1) [Ⓢ] 05/31 11:01:00				# OP start + 10min AR12075							
05/31 18:09:30 - 06/01 06:15:54	Track (361.6, -175.9) [Ⓢ] 05/31 18:07:30				AR12075 Cont.							
06/01 06:29:00 - 06/01 17:56:54	Track (460.5, -177.3) [Ⓢ] 06/01 06:26:00				AR12075 Cont.							
06/01 18:10:00 - 06/02 01:59:54	Track (548.3, -178.7) [Ⓢ] 06/01 18:07:00				AR12075 cont.							
06/02 02:23:00 - 06/02 02:59:00	Track (605.9, -179.6) [Ⓢ] 06/02 02:20:00				AR12075 cont.							
PROG= 06 Inf.-time(s)												
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 68 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Seqn= 2 4-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/thick-Al	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 4 52-time(s) 40.0sec												
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─ Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
└─ Open/Ti-poly	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #199D: Synoptic Q95 2x2 - Al/mesh(12/128/512) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(64/256/1024) + G-b												
Term	Pointing (x, y)				Comment							
05/31 18:00:30 - 05/31 18:06:24	Fixed (0.0, 0.0)				synoptic, shifted -2.5 min							
06/01 06:19:00 - 06/01 06:25:54	Fixed (0.0, 0.0)				synoptic, shifted 16.0 min							
06/01 18:00:00 - 06/01 18:06:54	Fixed (0.0, 0.0)				synoptic, shifted -3.0 min							
06/02 02:03:00 - 06/02 02:19:54	Fixed (0.0, 0.0)				synoptic, shifted -18.5 min							
06/05 10:19:00 - 06/05 10:25:56	Fixed (0.0, 0.0)				Synoptic after the XRT CCD bakeout							
06/05 17:46:00 - 06/05 17:52:54	Fixed (0.0, 0.0)				synoptic, shifted -17.0 min							
06/06 06:05:00 - 06/06 06:58:00	Fixed (0.0, 0.0)				synoptic, shifted 2.0 min							
06/06 18:01:00 - 06/06 18:07:54	Fixed (0.0, 0.0)				synoptic, shifted -2.0 min							
06/07 05:30:00 - 06/07 05:36:54	Fixed (0.0, 0.0)				synoptic, shifted -33.0 min							
PROG= 12 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 3 1-time(s) 4.0sec												
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	12ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec												
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─ Seqn= 17 1-time(s) 4.0sec												
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	32ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 7 1-time(s) 2.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
└─ Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A08: CCD Monitor During Bakeout - G-Band 33ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms												
Term	Pointing (x, y)				Comment							
06/05 09:03:00 - 06/05 09:09:54	Fixed (-528.4, -528.4)				Quadra Pointing 1							
PROG= 02 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 88 1-time(s) 12.0sec												
└─ Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
└─ Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
└─ Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (1536, 1536)	Q=98	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 23 2-time(s) 2.0sec												
└─ Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3 1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 12 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 14 1-time(s) 2.0sec												
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A09: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms-2

Term	Pointing (x, y)	Comment										
06/05 09:13:00 - 06/05 09:19:54	Fixed (528.4, -528.4)	Quadra Pointing 2										
PROG= 16 1-time(s)												
Subr= 1 1-time(s) 12.0sec												
Seqn= 31 1-time(s) 12.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	1024x1024 (512, 1536)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (512, 1536)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 23 2-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3 2-time(s) 2.0sec												
Seqn= 12 1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 14 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A0A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms-2

Term	Pointing (x, y)	Comment										
06/05 09:23:00 - 06/05 09:29:54	Fixed (528.4, 528.4)	Quadra Pointing 3										
PROG= 17 1-time(s)												
Subr= 1 1-time(s) 12.0sec												
Seqn= 81 1-time(s) 12.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	1024x1024 (512, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (512, 512)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 23 2-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3 2-time(s) 2.0sec												
Seqn= 12 1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 14 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A0B: CCD Monitor During Bakeout - G-Band 33ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment										
06/05 09:33:00 - 06/05 09:41:00	Fixed (-528.4, 528.4)	Quadra Pointing 4										
PROG= 09 1-time(s)												
Subr= 1 1-time(s) 12.0sec												
Seqn= 28 1-time(s) 12.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	1024x1024 (1536, 512)	Q=90	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Dark	32ms	Obs	1x1	1024x1024 (1536, 512)	Q=98	0	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 23 2-time(s) 2.0sec												
Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Subr= 3 2-time(s) 2.0sec												
Seqn= 12 1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 14 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #19A0: HOP81/206 2-filter - Ti/poly 4s, Al/mesh 4s, G-band - 384x384 45ms with G-band VLS closed 33ms

Term	Pointing (x, y)	Comment
06/05 10:29:00 - 06/05 17:42:54	Fixed (-9.0, 866.0)	HOP81 Day1 North Pole
06/06 10:03:00 - 06/06 17:57:54	Fixed (-9.0, -964.0)	HOP81 Day2 South pole
PROG= 04 Inf.-time(s)		

Subr= 1	1-time(s)	2.0sec																		
└ Seqn= 74	2-time(s)	2.0sec																		
└ Open/G-band	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0	0	2.0sec							
Subr= 2	1-time(s)	2.0sec																		
└ Seqn= 30	1-time(s)	30.0sec																		
└ Open/G-band	Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec							
Subr= 3	30-time(s)	2.0sec																		
└ Seqn= 76	2-time(s)	30.0sec																		
└ Open/Al-mesh	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec							
└ Open/Ti-poly	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0	0	2.0sec							
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval									

XOB #1A27: G-Band Alignment with North Pole Q90 2x2(G-band only) - 8msec - 5min cadence - Partial Sun-wNGT-FullCCD																				
Term			Pointing (x, y)							Comment										
06/05 18:08:00 - 06/05 19:52:54			Fixed (0.0, 930.0)							Coalignment Program N-limb										
PROG= 13 1-time(s)																				
└ Subr= 1 1-time(s) 360.0sec																				
└ Seqn= 77 24-time(s) 300.0sec																				
└ Open/G-band	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec							
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval									

XOB #1A28: G-Band Alignment with East limb Q90 2x2 (G-band only) - 8msec - 8 min cadence-wNGT-FullCCD																				
Term			Pointing (x, y)							Comment										
06/05 20:08:00 - 06/05 21:52:54			Fixed (-945.0, 0.0)							Coalignment Program E-limb										
PROG= 11 1-time(s)																				
└ Subr= 1 1-time(s) 360.0sec																				
└ Seqn= 59 15-time(s) 480.0sec																				
└ Open/G-band	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec							
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval									

XOB #1998: AR Standard-A(Filter-Ratio) with PFB, FW1=Open, 384x384 at 1064 1048, 60s cad With G-band and VLS closed																				
Term			Pointing (x, y)							Comment										
06/05 21:56:30 - 06/06 06:01:54			Track (1.6, -114.4) @ 06/05 21:53:00							AR obs.										
06/06 07:35:30 - 06/06 09:59:56			Track (89.2, -115.1) @ 06/06 07:30:00							AR Obs.										
06/06 18:11:00 - 06/07 05:26:54			Track (185.1, -116.0) @ 06/06 18:08:00							AR obs.										
06/07 05:40:00 - 06/07 09:15:00			Track (286.3, -116.9) @ 06/07 05:37:00							AR obs.										
PROG= 07 Inf.-time(s)																				
└ Subr= 2 1-time(s) 2.0sec																				
└ Seqn= 68 1-time(s) 2.0sec																				
└ Open/Ti-poly	Open/thick-Al	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec							
└ Open/G-band	Open/G-band	Open/G-band	open	Safe	Norm	12ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec							
└ Open/G-band	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec							
└ Seqn= 2 4-time(s) 2.0sec																				
└ Open/Ti-poly	Open/thick-Al	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec							
└ Open/thick-Al	Open/thick-Be	Open/thick-Be	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec							
└ Seqn= 4 52-time(s) 60.0sec																				
└ Open/Al-mesh	Open/thick-Al	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec							
└ Open/Ti-poly	Open/thick-Be	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec							
└ Open/Al-mesh	Open/thick-Al	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec							
└ Open/Ti-poly	Open/thick-Be	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec							
└ Open/Al-mesh	Open/thick-Al	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec							
└ Open/Ti-poly	Open/thick-Be	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec							
└ Open/Al-mesh	Open/thick-Al	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec							
└ Open/Ti-poly	Open/thick-Be	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	3	2.0sec							
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval									

* * * * * Flare mode * * * * *

XOB #199C: Flare Standard Obs. with eruptions mode-A (FW1=Open and VLS-Closed)																				
Term			Pointing (x, y)							Comment										
05/31 11:08:00 - 05/31 17:57:24			Track (302.0, -175.1) @ 05/31 11:01:00							# OP start + 10min AR12075										
05/31 18:09:30 - 06/01 06:15:54			Track (361.6, -175.9) @ 05/31 18:07:30							AR12075 Cont.										
06/01 06:29:00 - 06/01 17:56:54			Track (460.5, -177.3) @ 06/01 06:26:00							AR12075 Cont.										
06/01 18:10:00 - 06/02 01:59:54			Track (548.3, -178.7) @ 06/01 18:07:00							AR12075 cont.										
06/02 02:23:00 - 06/02 02:59:00			Track (605.9, -179.6) @ 06/02 02:20:00							AR12075 cont.										
06/05 10:29:00 - 06/05 17:42:54			Fixed (-9.0, 866.0)							HOP81 Day1 North Pole										
06/05 21:56:30 - 06/06 06:01:54			Track (1.6, -114.4) @ 06/05 21:53:00							AR obs.										
06/06 07:35:30 - 06/06 09:59:56			Track (89.2, -115.1) @ 06/06 07:30:00							AR Obs.										
06/06 10:03:00 - 06/06 17:57:54			Fixed (-9.0, -964.0)							HOP81 Day2 South pole										
06/06 18:11:00 - 06/07 05:26:54			Track (185.1, -116.0) @ 06/06 18:08:00							AR obs.										
06/07 05:40:00 - 06/07 09:15:00			Track (286.3, -116.9) @ 06/07 05:37:00							AR obs.										
PROG= 08 1-time(s)																				
└ Subr= 1 30-time(s) 20.0sec																				
└ Seqn= 55 1-time(s) 2.0sec																				
└ Open/thick-Al	Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec							
└ Open/thick-Be	Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec							

Seqn= 9 1-time(s) 2.0sec		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
		Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec														
Seqn= 39 1-time(s) 2.0sec		Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec														
Seqn= 55 1-time(s) 2.0sec		Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
		Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 70 1-time(s) 2.0sec		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
		Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec														
Seqn= 39 1-time(s) 2.0sec		Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec														
Seqn= 55 1-time(s) 2.0sec		Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
		Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 70 1-time(s) 2.0sec		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
		Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec														
Seqn= 39 1-time(s) 2.0sec		Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec														
Seqn= 55 1-time(s) 2.0sec		Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
		Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 70 1-time(s) 2.0sec		Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
		Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec														
Seqn= 1 1-time(s) 2.0sec		Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
		Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
		Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
		Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 4 24-time(s) 600.0sec														
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

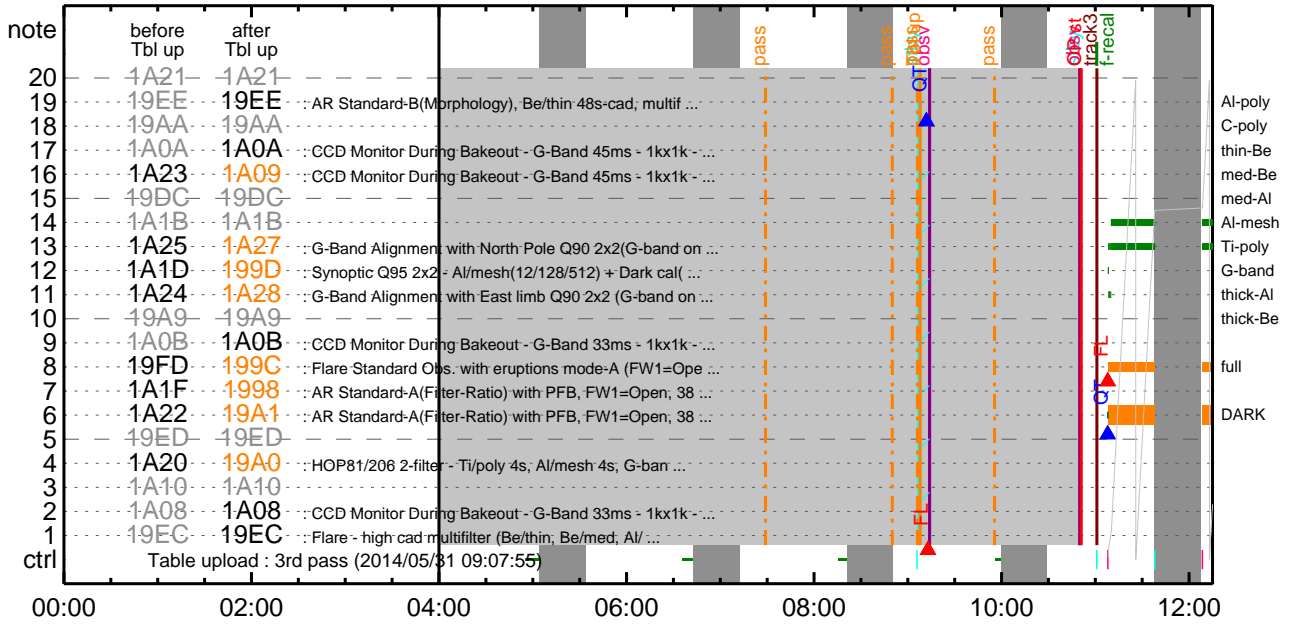
* * * * *

Flare Detection

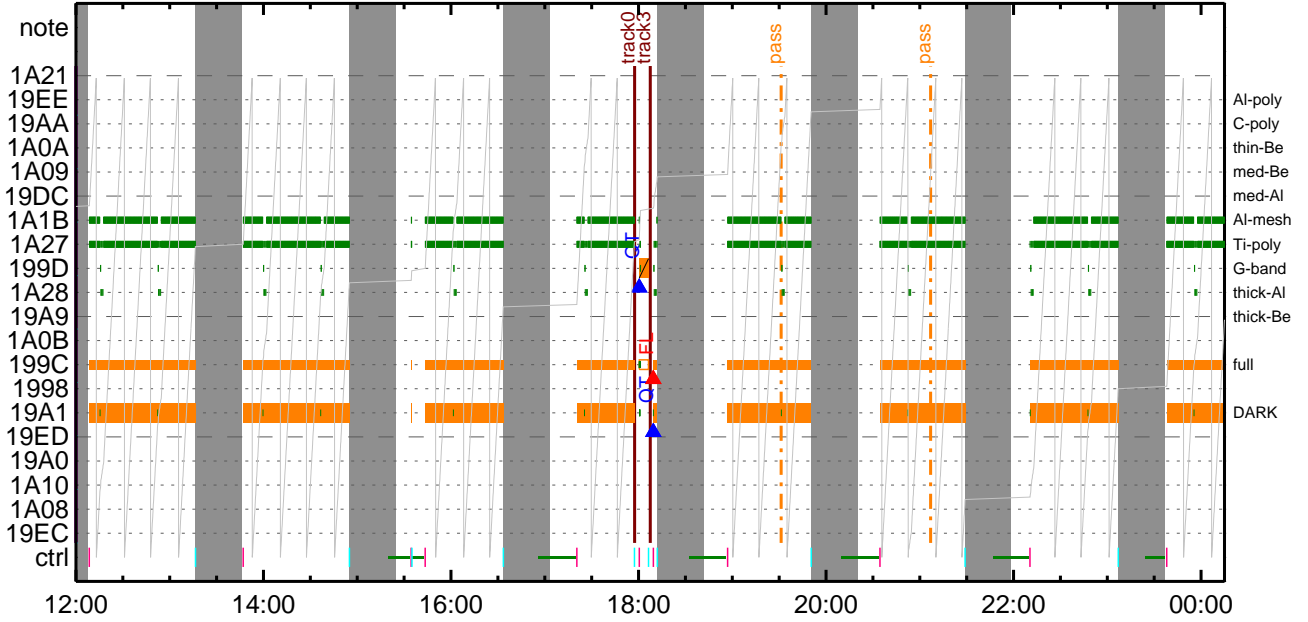
* * * * *

FLD Patrol													
Term		Pointing (x, y)						Comment					
05/31 18:06:46 - 06/01 06:16:16		Fixed (0.0, 0.0)						synoptic, shifted -2.5 min					
06/01 06:26:16 - 06/01 17:57:16		Track (460.5, -177.3) @ 06/01 06:26:00						AR12075 Cont.					
06/01 18:07:16 - 06/02 02:00:16		Track (548.3, -178.7) @ 06/01 18:07:00						AR12075 cont.					
06/02 02:20:16 - 06/05 09:02:54		Track (605.9, -179.6) @ 06/02 02:20:00						AR12075 cont.					
06/05 10:26:18 - 06/05 17:43:16		Fixed (-9.0, 866.0)						HOP81 Day1 North Pole					
06/05 21:53:16 - 06/06 06:02:16		Track (1.6, -114.4) @ 06/05 21:53:00						AR obs.					
06/06 07:30:16 - 06/06 17:58:16		Track (89.2, -115.1) @ 06/06 07:30:00						AR Obs.					
06/06 18:08:16 - 06/07 05:27:16		Track (185.1, -116.0) @ 06/06 18:08:00						AR obs.					
06/07 05:37:16 - 06/09 11:54:00		Track (286.3, -116.9) @ 06/07 05:37:00						AR obs.					
		Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec
		Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

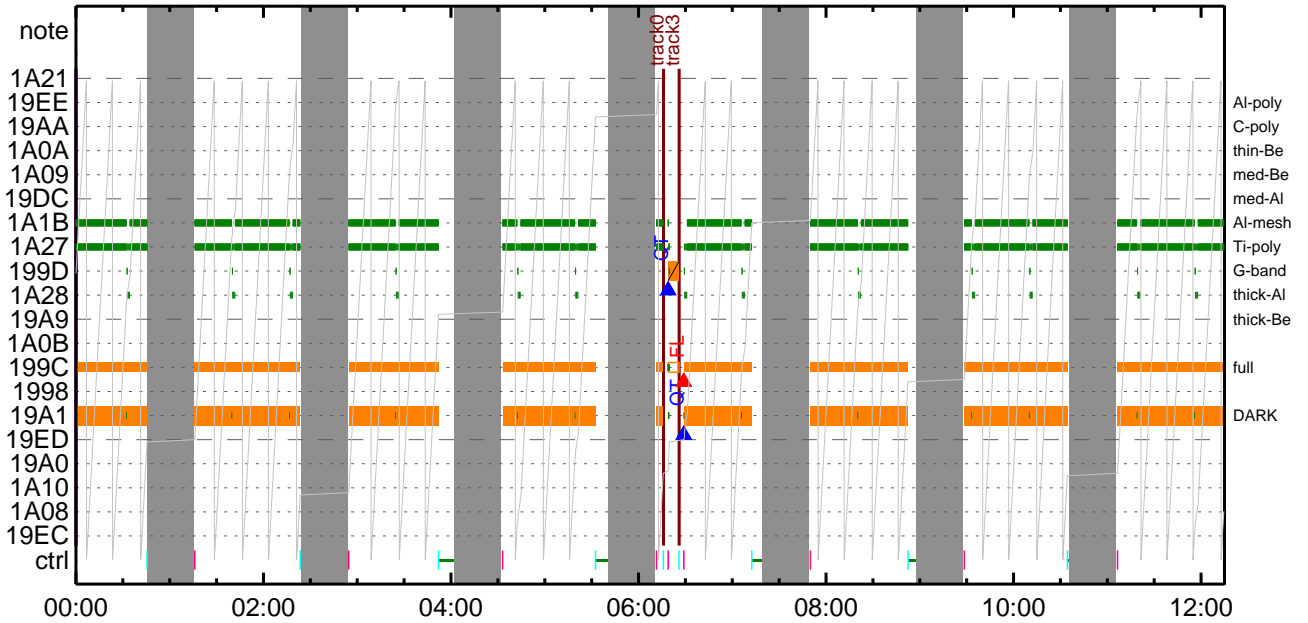
CMDI #0317 2014/05/31



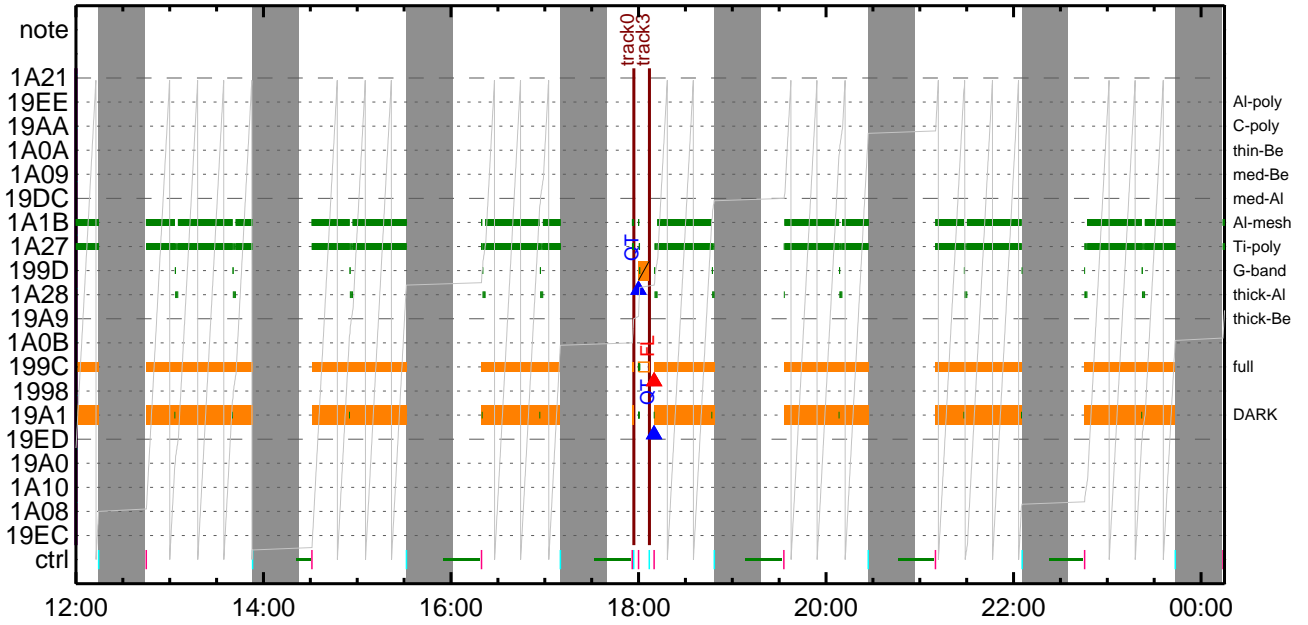
CMDI #0317 2014/05/31



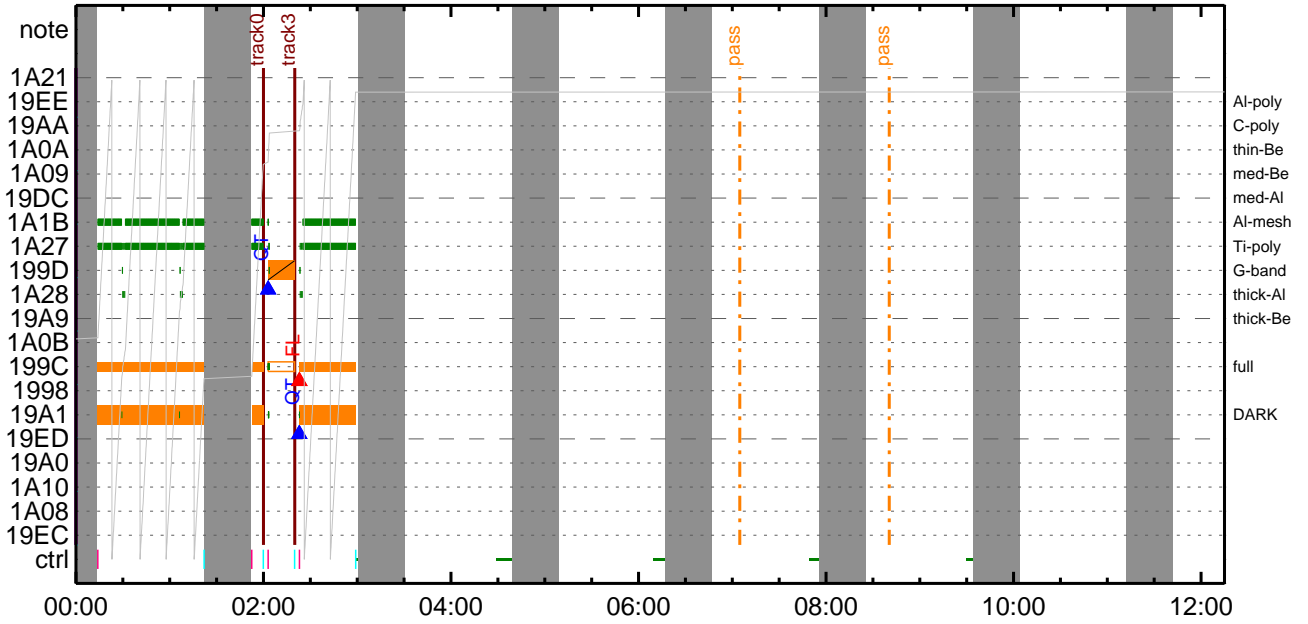
CMDI #0317 2014/06/01



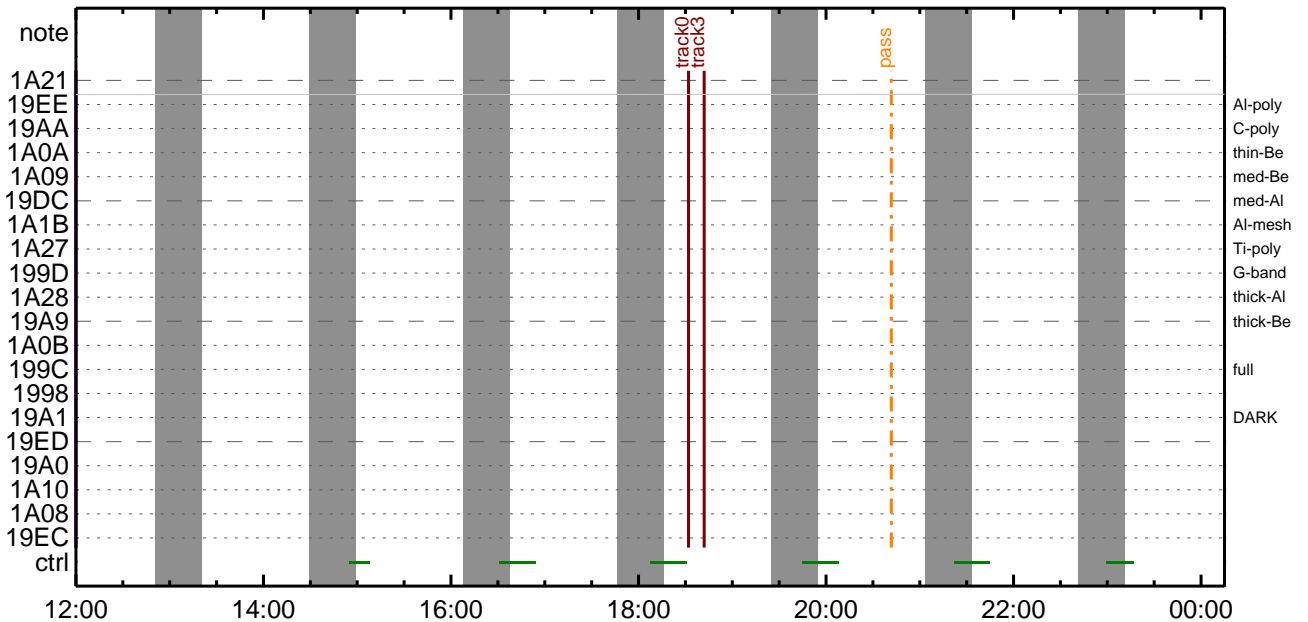
CMDI #0317 2014/06/01



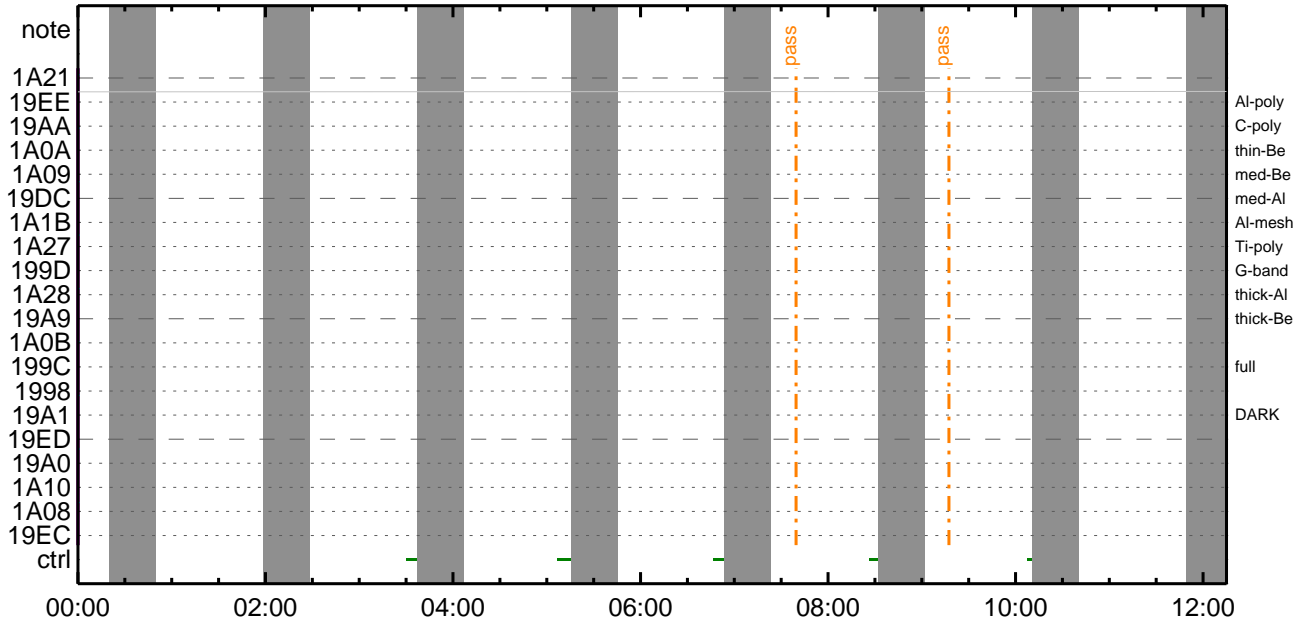
CMDI #0317 2014/06/02



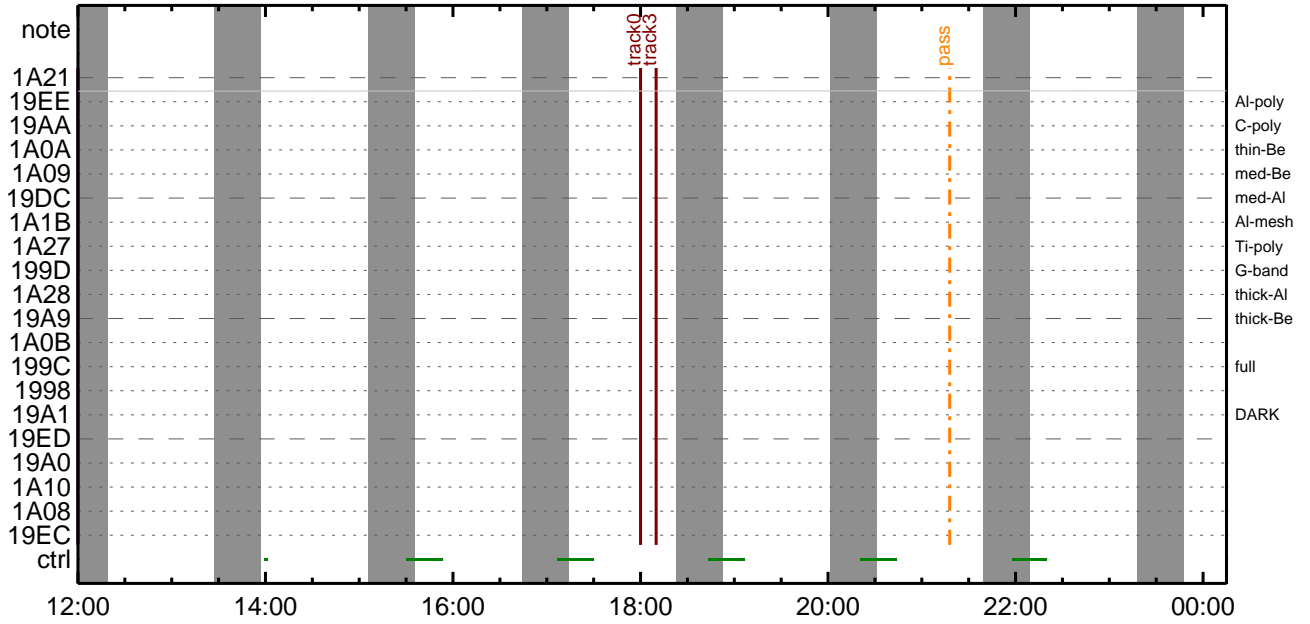
CMDI #0317 2014/06/02



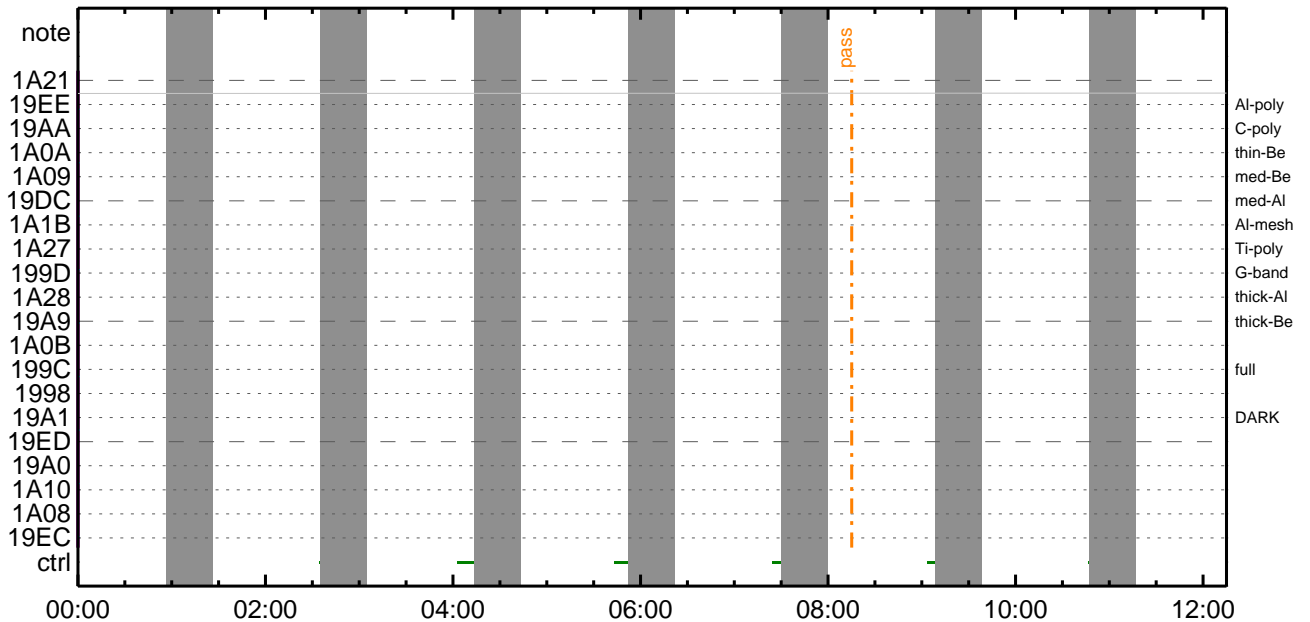
CMDI #0317 2014/06/03



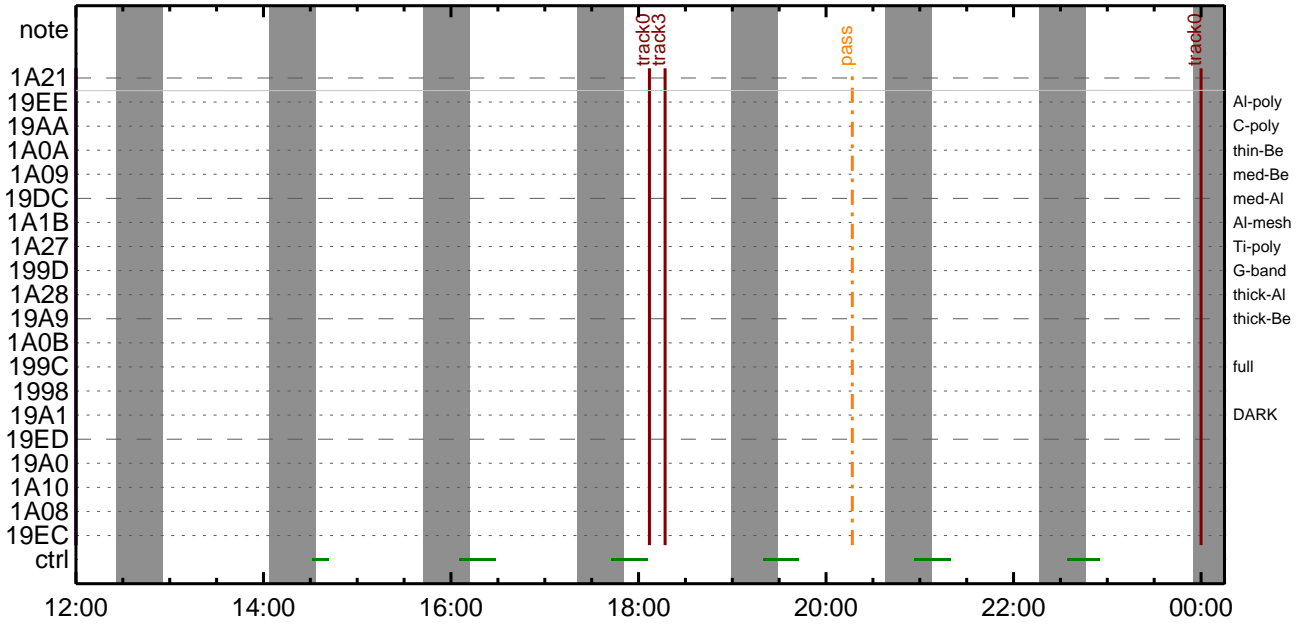
CMDI #0317 2014/06/03



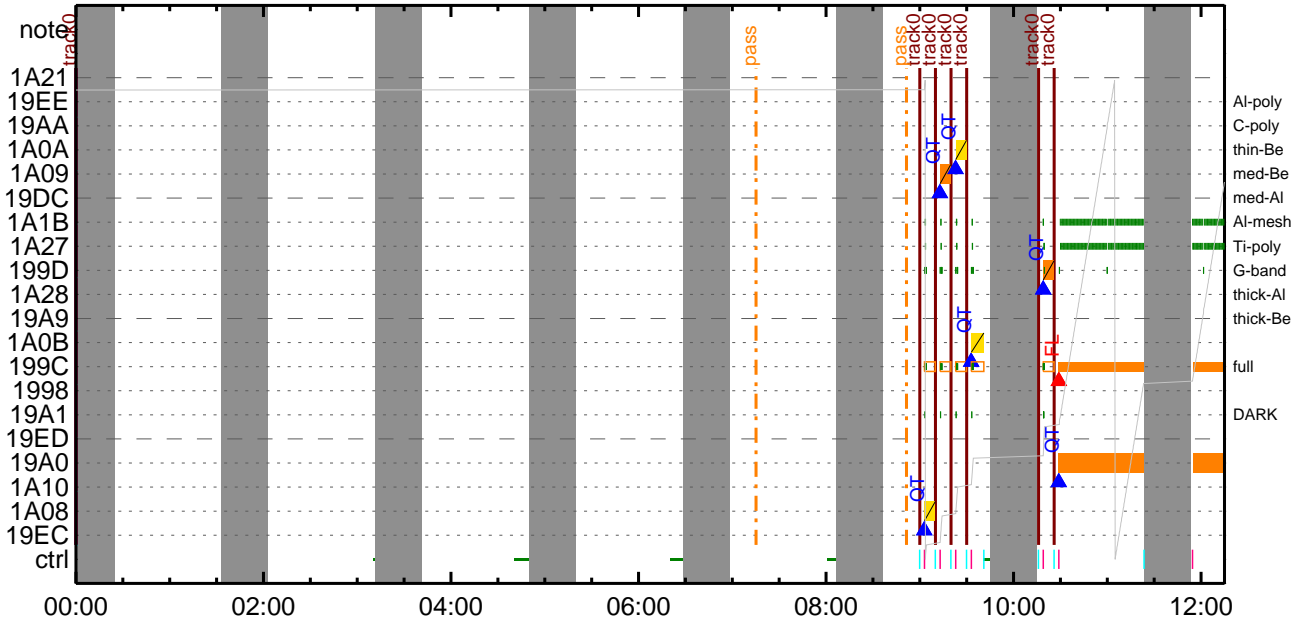
CMDI #0317 2014/06/04



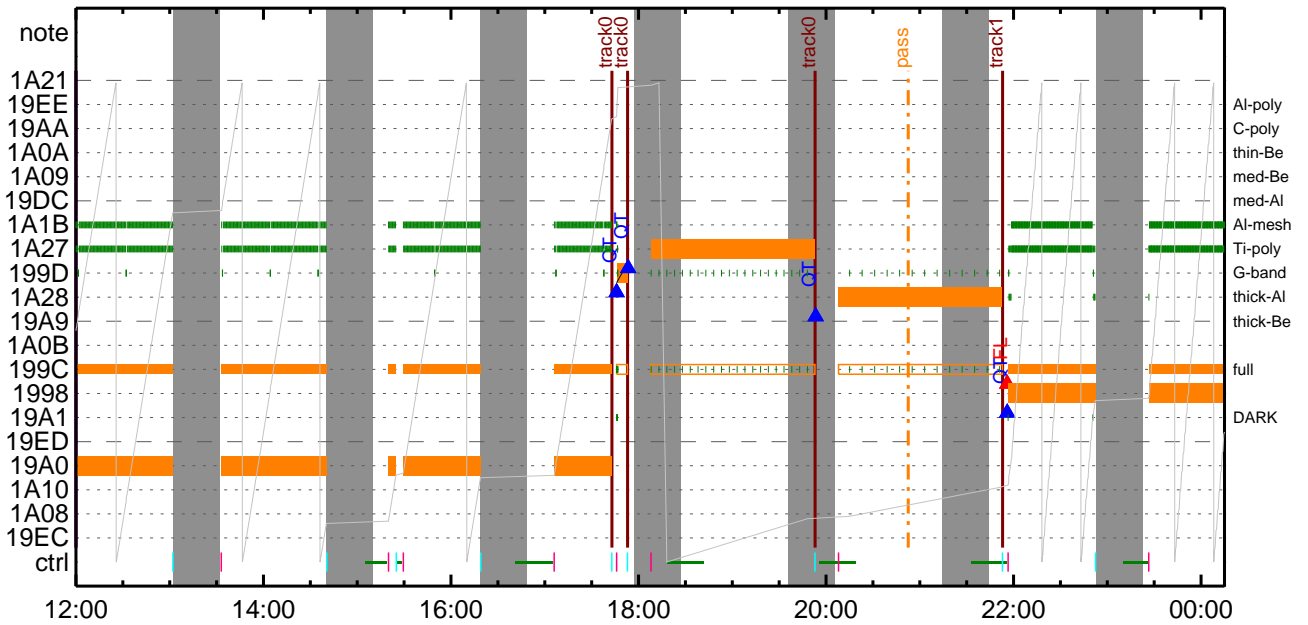
CMDI #0317 2014/06/04



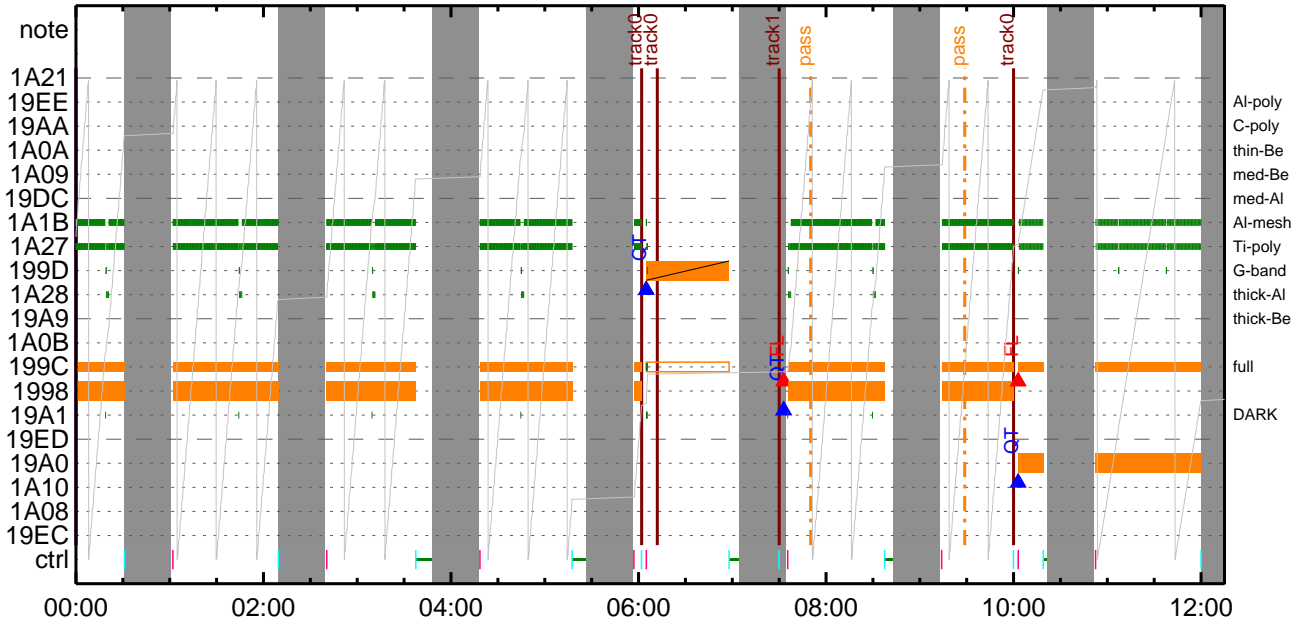
CMDI #0317 2014/06/05



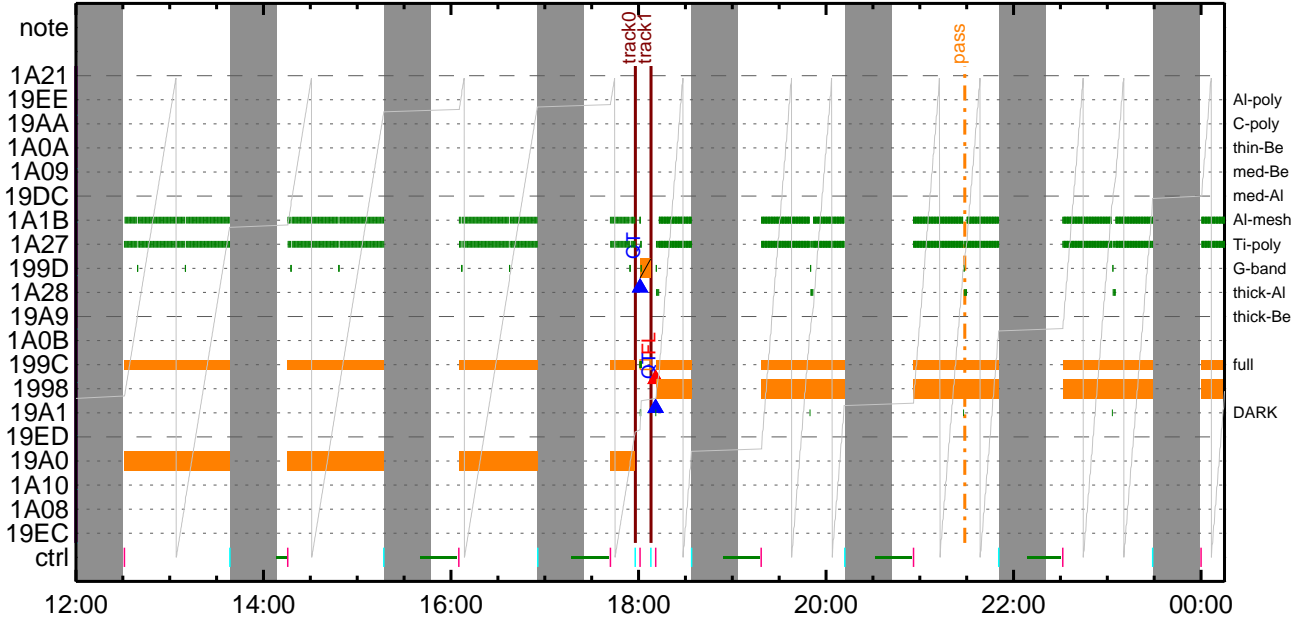
CMDI #0317 2014/06/05



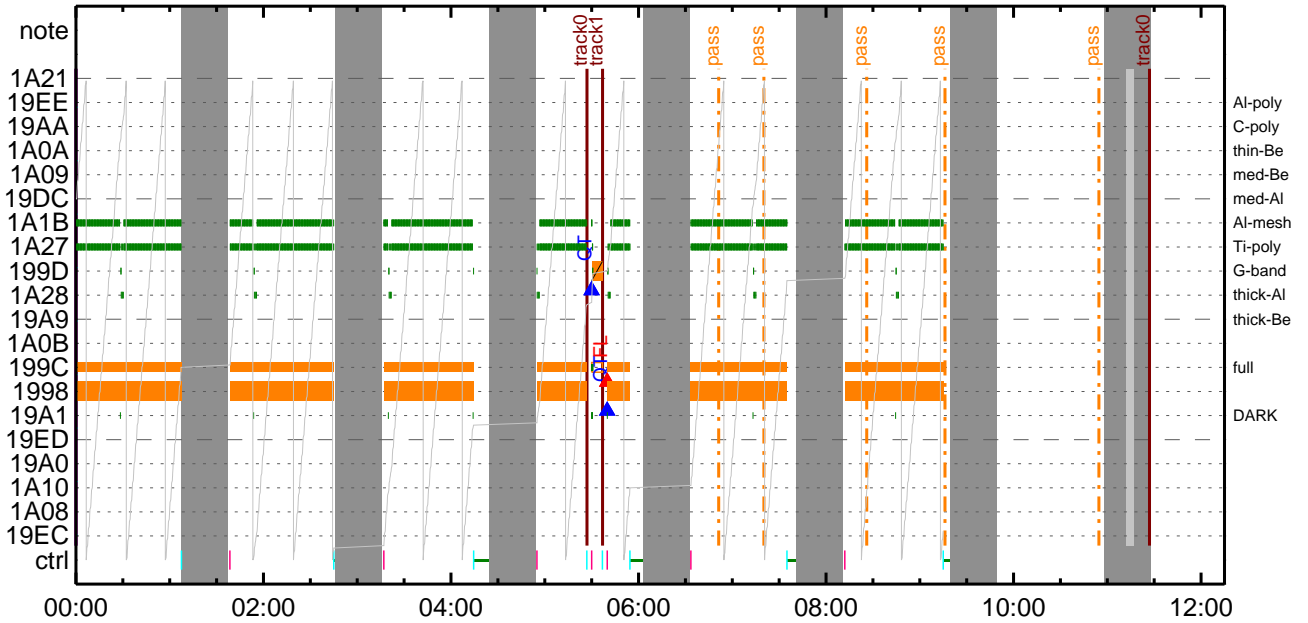
CMDI #0317 2014/06/06



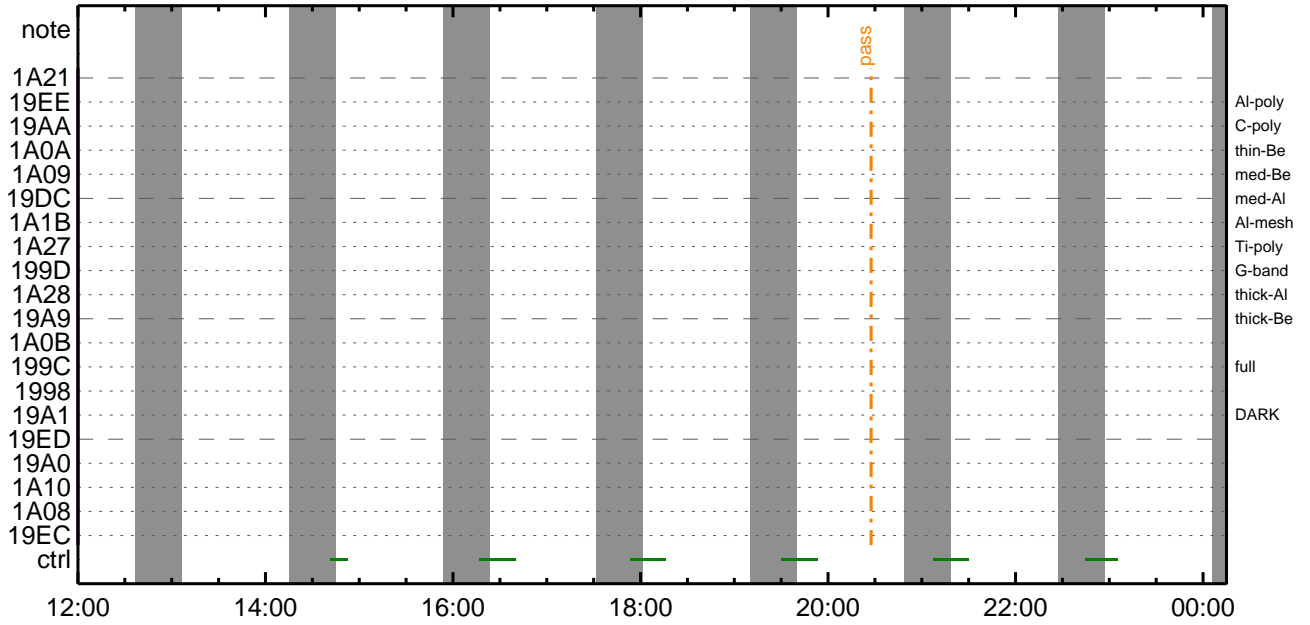
CMDI #0317 2014/06/06



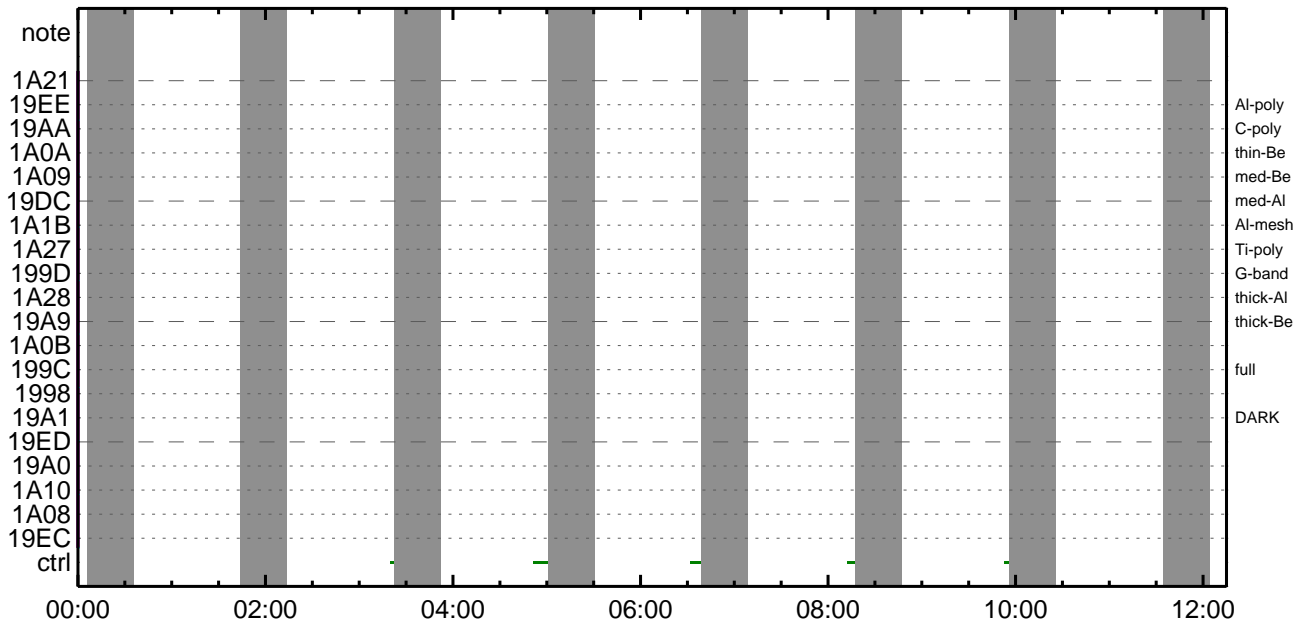
CMDI #0317 2014/06/07



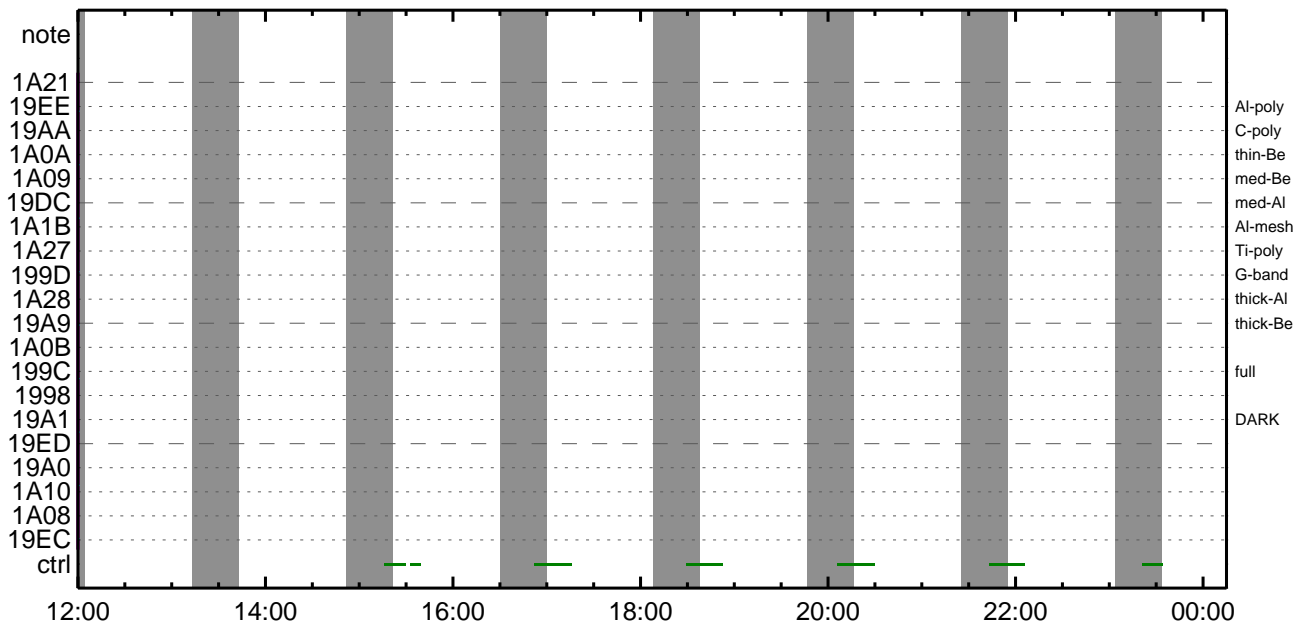
CMDI #0317 2014/06/07



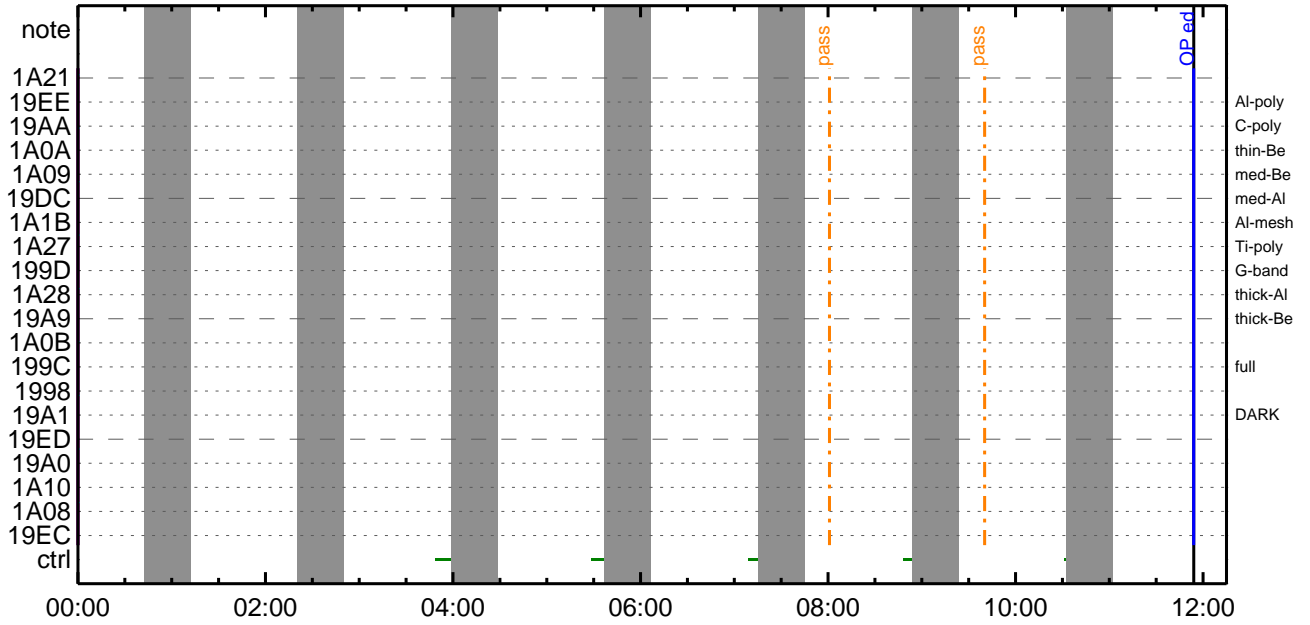
CMDI #0317 2014/06/08



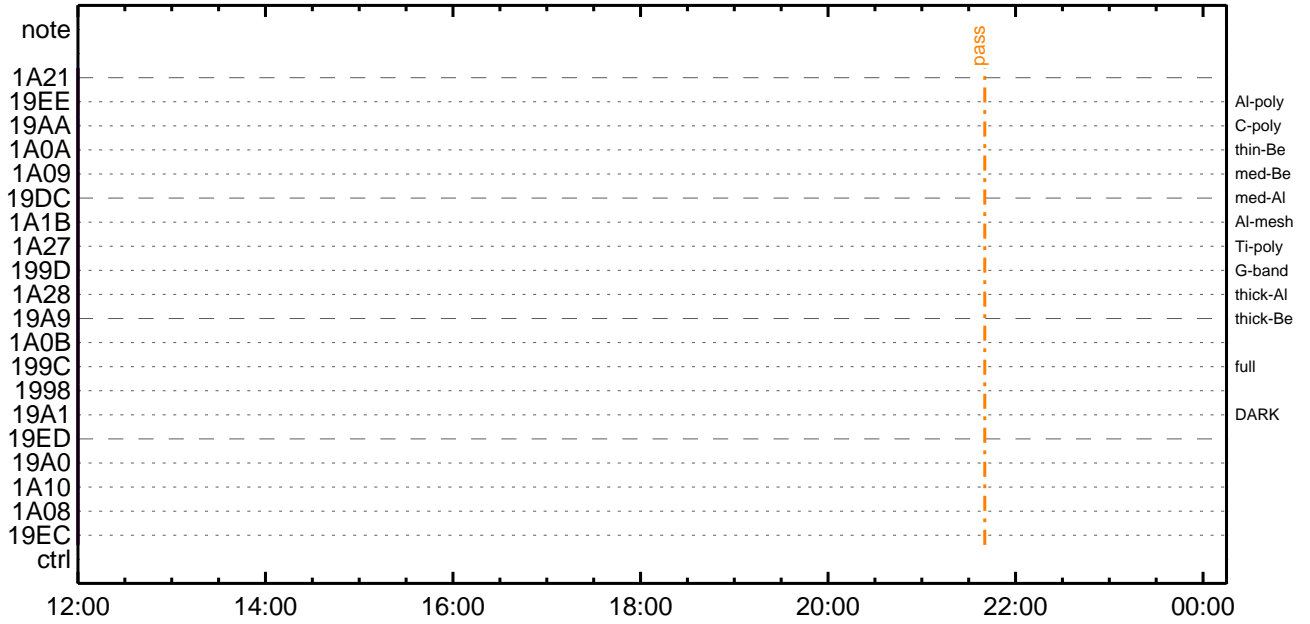
CMDI #0317 2014/06/08



CMDI #0317 2014/06/09



CMDI #0317 2014/06/09




```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAY6Yx
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-420:OP
0104 ( )
0105 S. OG og-420:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPÎÎ°èYAY6Yx;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAY6Yx½ªÎ»ò³ÎÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOGαî½Ë¹Ç·è²ÏOKαò³ÎÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAY6Yx½ªÎ»ò³ÎÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOGαî½Ë¹Ç·è²ÏOKαò³ÎÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAY6Yx½ªÎ»ò³ÎÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OPαî½Ë¹Ç·è²ÏOKαò³ÎÇ§
0165 C.
0166 C. ***** òË²¼αî½Ë¹Ç·è²ÏOKαò³ÎÇ§ *****
0167 C. DHUÿâ;¼YE;Ë½Y½;Y;¼YE;ËòðÎã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOADα-Á÷ç@NGuî½Ë¹Ç·è²ÏOKαò³ÎÇ§ *****
0180 C. αÞαç;çSETαEDUMPαÎË±°iYNY¹αÇ¹Ôα|α³αË;ç
0181 C.
0182 C. TIY³YÞY6YËòðÎã¹ç(UT)
0183 +. TI 2014-05-31 10:46:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2014-05-31 10:46:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2014-05-31 10:46:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```


(a) Spacecraft Operation Procedure (real-commands)

```
main-421 2014-05-31 13:51:43 85 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Ä»Û;ã
0005 C.
0006 C. YÁY$;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãâ•µ°È»Í×ÁÇãÍYçYÁY×Yí;¼YÉ;ÈÈ%µ•íÉ;ÈÈ¼°ÇÔã•ã¿¼í¹çãÍ;çÄ®, ùã¹ãÈãPãÇÁ+¿®ã•ãÈããã³ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCs Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0046 +. DC 07-FC EIS_MODE_CHG_ENA
0047 BC (20)
0048 . C. Verify EIS_MODE_CHG_FLG is ENA
0049 +. DC 07-FC EIS_MODE_MANU
0050 BC (21 02)
0051 . C. Verify EIS in MANUAL mode
0052 . C. Estimated OBSTBL upload time is 1m18s
0053 C. *****
0054 C. EIS START OBSTBL LOAD
0055 C. *****
0056 . S. RAM ram-820:EIS_OBSTBL
0057 ( )
0058 +. DC 07-FC EIS_DUMP_OBSTBL
0059 BC (07 07 07 00 00 70 00)
0060 C.
0061 C. Execute, after the success of OBSTBL upload.
0062 C. Set EIS TI-commands
0063 +. TI 2014-05-31 10:50:50.0
0064 DC 07-FC EIS_MODE_CHG_ENA
0065 BC (20)
0066 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0067 C. *****
0068 C. EIS END OBSTBL LOAD
0069 C. *****
0070 C.
0071 . C. ***** MDP `ûÁîôî»ó¼YãÈÄã¹ãÈDCBC•x²è *****
0072 C. (¼ã°íYÓYÁYÈYpYÈYáYçYÈãÈ¼¼ã¼Ä»Ûã¹ãÈ)
0073 . S. DC-BC dcbc-402:DCBC
0074 (MDP_known_event)
0075 C.
0076 C.
0077 . C. ***** YD¥¹.İ Daily±¿İÑãÈ`Øã¹ãÈDCBC•x²è *****
0078 . S. DC-BC dcbc-153:DCBC
0079 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0080 C.
0081 C.
0082 . C. ;ãLOSÁY$YÁY-¼Ä»Û;ã
0083 C.
0084 . C. ***** LOS *****
0085 C.
```



```

0096 C.
0097 C.
0098 . C. *****
0099 C. SOT table upload
0100 C. *****
0101 . C. < Stop FG table >
0102 +. DC 07-F0 MDP_FG_CTRL_MANU
0103 BC (51)
0104 . C. -----
0105 C. MDP_FG_CTRL_MODE = MANU [ ]
0106 C. -----
0107 C.
0108 . C. <Upload FG Observation Table>
0109 . S. RAM ram-264:MDP_OBS_F
0110 ( )
0111 C.
0112 . C. < Dump RAMID=MDP_OBS_F >
0113 +. DC 07-F0 MDP_DUMP_FGTBL
0114 BC (82 07 00 00 00 38 b8)
0115 C. -----
0116 C. MDP_OBS_F verify = OK/NG [ ]
0117 C. -----
0118 C.
0119 . C. < Upload DPL table >
0120 C.
0121 C. ¥ç¥Ã¥×¥í;¼¥É°âÊSTS_CHKðOFFðËð¹ðë
0122 C.
0123 . S. RAM ram-271:MDP_DPL
0124 ( )
0125 C.
0126 . C. < Dump RAMID=MDP_DPL >
0127 +. DC 07-F0 MDP_DUMP_FGTBL
0128 BC (82 07 00 38 b8 00 40)
0129 C. -----
0130 C. MDP_DPL verify = OK [ ]
0131 C. -----
0132 C.
0133 C. STS_CHKðONðËð¹ðë
0134 C.
0135 . C. < Update MDP DSC PAR1 >
0136 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0137 BC (4c)
0138 C. MDP_CMD_CODE = F04C0700[ ]
0139 C. MDP_CMD_CNT (count-up 1) [ ]
0140 C. -----
0141 C.
0142 . C.
0143 C. *****
0144 C. SOT TI command set
0145 C. *****
0146 C. Execute, after the success of TBL upload.
0147 +. TI 2014-05-31 10:50:18.0
0148 DC 07-F0 MDP_SOT_MODE_OBSV
0149 BC (40)
0150 . C. -----
0151 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0152 C. -----
0153 C.
0154 C.
0155 C. ***** XRT START *****
0156 C.
0157 +. DC 07-F0 MDP_XRT_CTRL_MANU
0158 BC (c1)
0159 + DC 07-F0 MDP_XRT_MODE_STBY
0160 BC (c3)
0161 . C. ----- Success Verify ? OK / NG_____
0162 C.
0163 C. XRT Obs. Table Upload
0164 . S. RAM ram-291:MDP_OBS_X
0165 ( )
0166 C.
0167 +. DC 07-F0 MDP_DUMP_XRTTBL
0168 BC (84 07 00 00 00 3a d4)
0169 . C. ----- Comparison Check ? OK / ERR _____
0170 C.
0171 C.
0172 +. DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 01 b1 b1 04 04)
0174 + DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 02 b1 b1 08 08)
0176 + DC 07-F0 MDP_XRT_ROI_SET
0177 BC (cd 03 b1 b1 08 08)
0178 + DC 07-F0 MDP_XRT_ROI_SET
0179 BC (cd 04 b1 b1 06 06)
0180 + DC 07-F0 MDP_XRT_ROI_SET
0181 BC (cd 05 85 83 06 06)
0182 + DC 07-F0 MDP_XRT_ROI_SET
0183 BC (cd 06 85 83 06 06)
0184 + DC 07-F0 MDP_XRT_ROI_SET
0185 BC (cd 07 80 80 20 20)
0186 + DC 07-F0 MDP_XRT_ROI_SET
0187 BC (cd 08 80 80 20 08)
0188 + DC 07-F0 MDP_XRT_ROI_SET
0189 BC (cd 09 80 80 08 20)
0190 + DC 07-F0 MDP_XRT_ROI_SET
0191 BC (cd 0a c0 c0 10 10)
0192 + DC 07-F0 MDP_XRT_ROI_SET
0193 BC (cd 0b 40 c0 10 10)

```

```

0194 + DC 07-F0 MDP_XRT_ROI_SET
0195 BC (cd 0c 40 40 10 10)
0196 + DC 07-F0 MDP_XRT_ROI_SET
0197 BC (cd 0d c0 40 10 10)
0198 + DC 07-F0 MDP_XRT_ROI_SET
0199 BC (cd 0e 85 83 08 08)
0200 + DC 07-F0 MDP_XRT_ROI_SET
0201 BC (cd 0f 80 80 06 06)
0202 + DC 07-F0 MDP_XRT_ROI_SET
0203 BC (cd 10 80 80 08 08)
0204 + DC 07-F0 MDP_XRT_FLD_ENA
0205 BC (d8)
0206 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0207 BC (c8)
0208 + DC 07-F0 MDP_XRT_AEC_RESET
0209 BC (d0)
0210 + DC 07-F0 MDP_XRT_ARS_DIS
0211 BC (d5)
0212 + DC 07-F0 MDP_XRT_FLD_RESET
0213 BC (da)
0214 + DC 07-F0 MDP_XRT_QT_PROG_SET
0215 BC (c4 13)
0216 + DC 07-F0 MDP_XRT_FL_PROG_SET
0217 BC (c5 01)
0218 . C. ----- Success Verify ? OK / NG ____
0219 C.
0220 C.
0221 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0222 C.
0223 + DC 07-F0 MDP_XRT_MODE_OBSV
0224 BC (c2)
0225 + TI 2014-05-31 10:50:02.0
0226 DC 07-F0 MDP_XRT_MODE_OBSV
0227 BC (c2)
0228 . C. ----- Success Verify ? OK / NG ____
0229 C.
0230 C. ***** XRT END *****
0231 C.
0232 . C. ***** MDP 'úÃîñî»ö¼ýñëãðñ¹ñëDCBC•x²è *****
0233 C. (%á°îÿÓÿÄÿËÿÏÿËÿáÿçÿëñ¼ññ¼Ä»Ûñ¹ñè)
0234 . S. DC-BC dcbc-402:DCBC
0235 (MDP_known_event)
0236 C.
0237 C.
0238 . C. ***** ÿÐÿ¹•ï Daily±¿îññë´øñ¹ñëDCBC•x²è *****
0239 . S. DC-BC dcbc-153:DCBC
0240 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0241 C.
0242 C.
0243 . C. ;ãLOSÿÄÿ§ÿÄÿÿ¼Ä»Û;ä
0244 C.
0245 . C. ***** LOS *****
0246 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```
main-423 2014-05-31 13:51:43 29 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YFYOYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿áÁá•µ°Æ»Í×ÁÇáÍYçYÁY×Yí;¼YÉ;ÈÈèµ•ííÉ;ÈÈÈ¼°ÇÔá•¿¿¼í¹çáÍ;çÀ®, ùá¹áèáááçÁ+¿®á•áÈááá³áÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 C.
0015 . C. ***** MDP `úÁíáí»ò¼YáÈÁáá¹áèDCBC•x²è *****
0016 C. (¼á°íYÓYÁYÈYB¥ÈYáYçYèáÈ¼áá¼Á»Ûá¹áè)
0017 . S. DC-BC dcbc-402:DCBC
0018 (MDP_known_event)
0019 C.
0020 C.
0021 . C. ***** YD¥¹•Í Daily+¿ÍÑáÈ´Øá¹áèDCBC•x²è *****
0022 . S. DC-BC dcbc-153:DCBC
0023 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0024 C.
0025 C.
0026 . C. ;ãLOSÁY$YÁY-¼Á»Û;ã
0027 C.
0028 . C. ***** LOS *****
0029 C.
```

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*** OP Sequence for XRT ***

2014/05/31	11:00:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/05/31	11:00:56.0	XRT_FOCUS_RECALIBRATE_441_OG [0x1b9]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2014/05/31	11:01:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	03 00 00 00 00
2014/05/31	11:04:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/05/31	11:05:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/05/31	11:05:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/05/31	11:05:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/05/31	11:05:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/05/31	11:05:24.0	XRT_FLD_RESET_431_OG [0x1af]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/05/31	11:07:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2014/05/31	11:07:58.0	XRT_FL_PROG_SET_407_OG [0x197]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2014/05/31	11:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/05/31	11:38:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/05/31	11:38:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/05/31	11:38:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/05/31	11:41:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/05/31	12:07:30.0	XRT_Custom_430_OG [0x1ae]			
2014/05/31	12:08:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/05/31	13:16:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/05/31	13:16:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/05/31	13:16:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/05/31	13:19:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/05/31	13:46:00.5	XRT_Custom_430_OG [0x1ae]			
2014/05/31	13:47:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/05/31	14:55:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/05/31	14:55:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/05/31	14:55:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/05/31	14:58:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/05/31	15:33:30.0	XRT_Custom_430_OG [0x1ae]			
2014/05/31	15:34:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/05/31	15:35:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/05/31	15:35:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/05/31	15:35:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/05/31	15:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/05/31	15:42:30.0	XRT_Custom_430_OG [0x1ae]			
2014/05/31	15:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/05/31	16:33:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/05/31	16:33:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/05/31	16:33:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/05/31	16:36:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/05/31	17:19:31.0	XRT_Custom_430_OG [0x1ae]			
2014/05/31	17:20:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/05/31	17:57:24.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/05/31	17:57:26.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/05/31	17:57:30.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2014/05/31	17:57:46.0	XRT_FLD_DIS_434_OG [0x1b2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2014/05/31	18:00:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/05/31	18:00:26.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5

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2014/05/31	18:00:28.0	XRT_QT_PROG_SET_420_OG [0x1a4]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2014/05/31	18:00:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/05/31	18:06:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/05/31	18:06:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2014/05/31	18:06:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/05/31	18:06:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/05/31	18:06:50.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/05/31	18:06:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/05/31	18:06:54.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/05/31	18:07:30.0	AOCS_OrE-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	03	00 00 00 00			
2014/05/31	18:09:26.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2014/05/31	18:09:28.0	XRT_FL_PROG_SET_407_OG [0x197]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2014/05/31	18:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/05/31	18:12:00.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/05/31	18:12:02.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/05/31	18:12:04.5	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/05/31	18:15:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/05/31	18:56:00.0	XRT_Custom_430_OG [0x1ae]							
2014/05/31	18:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/05/31	19:50:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/05/31	19:50:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/05/31	19:50:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/05/31	19:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/05/31	20:33:31.0	XRT_Custom_430_OG [0x1ae]							
2014/05/31	20:34:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/05/31	21:29:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/05/31	21:29:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/05/31	21:29:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/05/31	21:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/05/31	22:09:30.0	XRT_Custom_430_OG [0x1ae]							
2014/05/31	22:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/05/31	23:07:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/05/31	23:07:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/05/31	23:07:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/05/31	23:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/05/31	23:37:00.0	XRT_Custom_430_OG [0x1ae]							
2014/05/31	23:38:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/01	00:45:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/01	00:45:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/01	00:45:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/01	00:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/01	01:15:00.0	XRT_Custom_430_OG [0x1ae]							
2014/06/01	01:16:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/01	02:23:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/01	02:23:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/01	02:23:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/01	02:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/01	02:53:30.0	XRT_Custom_430_OG [0x1ae]							
2014/06/01	02:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/01	03:52:00.0	XRT_CTRL_MANU_400_OG [0x190]							

2014/06/01	03:52:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/01	03:52:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/06/01	03:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/06/01	04:32:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/06/01	04:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/06/01	05:32:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/01	05:32:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/06/01	05:32:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/06/01	05:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/06/01	06:10:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/06/01	06:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/01	06:15:54.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2014/06/01	06:16:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2014/06/01	06:16:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2014/06/01	06:18:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2014/06/01	06:18:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/06/01	06:18:58.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2014/06/01	06:19:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/06/01	06:25:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/01	06:25:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/06/01	06:26:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00		
2014/06/01	06:26:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/06/01	06:26:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/06/01	06:26:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/06/01	06:26:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/06/01	06:26:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/06/01	06:28:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2014/06/01	06:28:58.0	XRT_FL_PROG_SET_407_OG [0x197]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08		
2014/06/01	06:29:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/06/01	07:12:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/01	07:12:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/06/01	07:12:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/06/01	07:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/06/01	07:49:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/06/01	07:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/01	08:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/01	08:52:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/06/01	08:52:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/06/01	08:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/06/01	09:27:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/06/01	09:28:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/01	10:34:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/01	10:34:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/06/01	10:34:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/06/01	10:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/06/01	11:05:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/06/01	11:06:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/06/01	12:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		

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2014/06/01	12:14:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/01	12:14:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/01	12:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/01	12:44:00.0	XRT_Custom_430_OG [0x1ae]								
2014/06/01	12:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/01	13:53:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/01	13:53:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/01	13:53:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/01	13:56:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/01	14:30:00.0	XRT_Custom_430_OG [0x1ae]								
2014/06/01	14:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/01	15:31:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/01	15:31:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/01	15:31:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/01	15:34:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/01	16:18:30.0	XRT_Custom_430_OG [0x1ae]								
2014/06/01	16:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/01	17:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/01	17:10:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/01	17:10:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/01	17:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/01	17:55:00.0	XRT_Custom_430_OG [0x1ae]								
2014/06/01	17:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/01	17:56:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/01	17:56:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/06/01	17:57:00.0	AOCs_Orе-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				
2014/06/01	17:57:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/06/01	17:59:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/06/01	17:59:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/06/01	17:59:58.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c				
2014/06/01	18:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/01	18:06:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/01	18:06:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/06/01	18:07:00.5	AOCs_Orе-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00				
2014/06/01	18:07:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/06/01	18:07:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/06/01	18:07:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/06/01	18:07:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/06/01	18:07:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/01	18:09:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2014/06/01	18:09:58.0	XRT_FL_PROG_SET_407_OG [0x197]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2014/06/01	18:10:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/01	18:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/01	18:48:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/01	18:48:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/01	18:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/01	19:32:00.0	XRT_Custom_430_OG [0x1ae]								
2014/06/01	19:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/01	20:27:00.0	XRT_CTRL_MANU_400_OG [0x190]								

2014/06/01	20:27:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/01	20:27:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/06/01	20:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/06/01	21:09:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/06/01	21:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/06/01	22:05:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/01	22:05:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/06/01	22:05:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/06/01	22:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/06/01	22:44:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/06/01	22:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/01	23:43:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/06/01	23:43:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/01	23:43:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/06/01	23:43:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/06/01	23:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/06/01	23:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/06/02	00:13:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/06/02	00:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/06/02	01:22:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/06/02	01:22:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/02	01:22:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/06/02	01:22:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/06/02	01:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/06/02	01:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/06/02	01:51:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/06/02	01:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/06/02	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/06/02	01:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/02	02:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2014/06/02	02:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2014/06/02	02:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]	AOCU_NM	5	02-76	00 00 00 00 00		
2014/06/02	02:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2014/06/02	02:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2014/06/02	02:02:58.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/06/02	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2014/06/02	02:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/06/02	02:19:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/02	02:20:00.0	AOCS_Ore-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/06/02	02:20:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00		
2014/06/02	02:20:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00		
2014/06/02	02:20:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/06/02	02:20:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/06/02	02:20:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/06/02	02:20:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/06/02	02:22:56.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/06/02	02:22:58.0	XRT_FL_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2014/06/02	02:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08		
2014/06/02	02:59:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/06/02	03:00:00.0	XRT_TCIB_XRT_S_HTR_A_ENA_427_OG [0x1ab]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/02	18:32:00.0	AOCS_Ore-point_Start_2_OG [0x098]	TCIB_XRT_S_HTR_A_ENA	0	04-BC			
2014/06/02	18:42:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00		
2014/06/02	18:42:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00		
2014/06/03	18:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2014/06/03	18:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	00 00 00 00 00		
2014/06/03	18:10:00.0	AOCS_Ore-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	03 00 00 00 00		

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2014/06/04	18:07:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00	00	00	00	00
2014/06/04	18:17:00.0	AOCS_ORe-point_Start_1_OG [0x097] AOCU_NM	5	02-76	03	00	00	00	00
2014/06/05	00:00:00.0	AOCS_ORe-point_Start_3_OG [0x099] AOCU_NM	5	02-76	00	10	7a	af	03
2014/06/05	03:00:00.0	XRT_TCIB_XRT_S_HTR_A_DIS_416_OG [0x1a0] TCIB_XRT_S_HTR_A_DIS	0	04-C0					
2014/06/05	08:59:54.0	XRT_CTRL_MANU_401_OG [0x191] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/05	09:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a] AOCU_NM	5	02-76	00	2e	f9	2e	f9
2014/06/05	09:02:32.0	XRT_FOCUS_POSITION_404_OG [0x194] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/06/05	09:02:52.0	XRT_QT_PROG_SET_409_OG [0x199] MDP_XRT_QT_PROG_SET	2	07-F0	c4	02			
2014/06/05	09:02:54.0	XRT_FLD_DIS_428_OG [0x1ac] MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/06/05	09:02:56.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/06/05	09:02:58.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/06/05	09:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/05	09:09:54.0	XRT_CTRL_MANU_401_OG [0x191] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/05	09:10:00.0	AOCS_ORe-point_Start_5_OG [0x09b] AOCU_NM	5	02-76	00	2e	f9	d1	07
2014/06/05	09:12:32.0	XRT_FOCUS_POSITION_404_OG [0x194] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/06/05	09:12:52.0	XRT_QT_PROG_SET_432_OG [0x1b0] MDP_XRT_QT_PROG_SET	2	07-F0	c4	10			
2014/06/05	09:12:54.0	XRT_FLD_DIS_428_OG [0x1ac] MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/06/05	09:12:56.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/06/05	09:12:58.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/06/05	09:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/05	09:19:54.0	XRT_CTRL_MANU_401_OG [0x191] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/05	09:20:00.0	AOCS_ORe-point_Start_6_OG [0x09c] AOCU_NM	5	02-76	00	d1	07	d1	07
2014/06/05	09:22:32.0	XRT_FOCUS_POSITION_404_OG [0x194] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/06/05	09:22:52.0	XRT_QT_PROG_SET_437_OG [0x1b5] MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2014/06/05	09:22:54.0	XRT_FLD_DIS_428_OG [0x1ac] MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/06/05	09:22:56.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/06/05	09:22:58.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/06/05	09:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/05	09:29:54.0	XRT_CTRL_MANU_401_OG [0x191] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/05	09:30:00.0	AOCS_ORe-point_Start_7_OG [0x09d] AOCU_NM	5	02-76	00	d1	07	2e	f9
2014/06/05	09:32:32.0	XRT_FOCUS_POSITION_404_OG [0x194] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/06/05	09:32:52.0	XRT_QT_PROG_SET_440_OG [0x1b8] MDP_XRT_QT_PROG_SET	2	07-F0	c4	09			
2014/06/05	09:32:54.0	XRT_FLD_DIS_428_OG [0x1ac] MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/06/05	09:32:56.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/06/05	09:32:58.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/06/05	09:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8] MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/05	09:41:00.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/05	09:41:02.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/05	09:41:04.0	XRT_PREFLR_STRT_418_OG [0x1a2] MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/05	09:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/05	10:15:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/05	10:15:56.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/06/05	10:16:00.0	AOCS_ORe-point_Start_2_OG [0x098] AOCU_NM	5	02-76	00	00	00	00	00
2014/06/05	10:16:16.0	XRT_FLD_DIS_434_OG [0x1b2] MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/06/05	10:18:54.0	XRT_FLRCTRL_DIS_405_OG [0x195] MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/06/05	10:18:56.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/06/05	10:18:58.0	XRT_QT_PROG_SET_420_OG [0x1a4]							

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2014/06/05	10:19:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/06/05	10:25:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/05	10:25:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2014/06/05	10:26:00.0	AOCS_OrE-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00	b3 03 00 cc
2014/06/05	10:26:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/06/05	10:26:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/06/05	10:26:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/06/05	10:26:24.0	XRT_ARS_DIS_443_OG [0x1bb]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/06/05	10:28:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/06/05	10:28:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04
2014/06/05	10:28:58.0	XRT_FL_PROG_SET_407_OG [0x197]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2014/06/05	10:29:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/06/05	11:23:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/05	11:23:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/06/05	11:23:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/06/05	11:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/06/05	11:53:30.0	XRT_Custom_430_OG [0x1ae]					
2014/06/05	11:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/06/05	13:02:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/05	13:02:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/06/05	13:02:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/06/05	13:05:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/06/05	13:32:00.0	XRT_Custom_430_OG [0x1ae]					
2014/06/05	13:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/06/05	14:40:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/05	14:40:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/06/05	14:40:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/06/05	14:40:34.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/06/05	14:43:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/06/05	15:19:01.0	XRT_Custom_430_OG [0x1ae]					
2014/06/05	15:20:01.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/06/05	15:25:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/05	15:25:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/06/05	15:25:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/06/05	15:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/06/05	15:28:30.0	XRT_Custom_430_OG [0x1ae]					
2014/06/05	15:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/06/05	16:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/05	16:19:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/06/05	16:19:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/06/05	16:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/06/05	17:05:00.0	XRT_Custom_430_OG [0x1ae]					
2014/06/05	17:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/06/05	17:42:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/05	17:42:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00
2014/06/05	17:43:00.0	AOCS_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00
2014/06/05	17:43:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/06/05	17:45:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/06/05	17:45:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/06/05	17:45:58.0	XRT_QT_PROG_SET_420_OG [0x1a4]					

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2014/06/05	17:46:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/06/05	17:52:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/05	17:52:56.0	XRT_FOCUS_POSITION_449_OG [0x1c1]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2014/06/05	17:53:00.0	AOCS_ORe-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00	ad 59 00 00
2014/06/05	17:53:16.0	XRT_FLD_DIS_446_OG [0x1be]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/06/05	17:53:18.0	XRT_FLRCTRL_DIS_445_OG [0x1bd]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/06/05	17:53:20.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/06/05	17:53:22.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0d
2014/06/05	18:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/06/05	19:52:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/05	19:52:56.0	XRT_FOCUS_POSITION_449_OG [0x1c1]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2014/06/05	19:53:00.0	AOCS_ORe-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00	00 00 54 00
2014/06/05	19:53:16.0	XRT_FLD_DIS_446_OG [0x1be]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/06/05	19:53:18.0	XRT_FLRCTRL_DIS_445_OG [0x1bd]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/06/05	19:53:20.0	XRT_ARS_DIS_414_OG [0x19e]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/06/05	19:53:22.0	XRT_QT_PROG_SET_417_OG [0x1a1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b
2014/06/05	20:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/06/05	21:52:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/05	21:52:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2014/06/05	21:53:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	01	00 00 00 00
2014/06/05	21:53:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/06/05	21:53:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/06/05	21:53:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/06/05	21:53:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/06/05	21:53:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/06/05	21:55:30.0	XRT_Custom_430_OG [0x1ae]					
2014/06/05	21:55:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2014/06/05	21:55:58.0	XRT_FL_PROG_SET_407_OG [0x197]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08
2014/06/05	21:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/06/05	22:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/05	22:52:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/06/05	22:52:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/06/05	22:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/06/05	23:25:30.0	XRT_Custom_430_OG [0x1ae]					
2014/06/05	23:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/06/06	00:31:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/06	00:31:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/06/06	00:31:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/06/06	00:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/06/06	01:01:00.0	XRT_Custom_430_OG [0x1ae]					
2014/06/06	01:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/06/06	02:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/06	02:09:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/06/06	02:09:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/06/06	02:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/06/06	02:39:30.0	XRT_Custom_430_OG [0x1ae]					
2014/06/06	02:40:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/06/06	03:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2014/06/06	03:37:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/06/06	03:37:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/06/06	03:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/06/06	04:17:30.0	XRT_Custom_430_OG [0x1ae]						
2014/06/06	04:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/06/06	05:17:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/06	05:17:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/06/06	05:17:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/06/06	05:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/06/06	05:56:00.0	XRT_Custom_430_OG [0x1ae]						
2014/06/06	05:57:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/06/06	06:01:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/06	06:01:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa	00	
2014/06/06	06:02:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00	00 00	
2014/06/06	06:02:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2014/06/06	06:04:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2014/06/06	06:04:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/06/06	06:04:58.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2014/06/06	06:05:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/06/06	06:12:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00	00 00	
2014/06/06	06:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/06	06:58:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/06/06	06:58:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/06/06	07:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/06/06	07:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/06	07:29:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00	
2014/06/06	07:30:00.0	AOCS_Ore-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	01 00 00 00	00 00	
2014/06/06	07:30:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/06/06	07:30:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/06/06	07:30:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/06/06	07:30:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/06/06	07:30:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/06/06	07:32:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07		
2014/06/06	07:32:58.0	XRT_FL_PROG_SET_407_OG [0x197]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08		
2014/06/06	07:34:30.0	XRT_Custom_430_OG [0x1ae]						
2014/06/06	07:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/06/06	08:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/06	08:37:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/06/06	08:37:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/06/06	08:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/06/06	09:13:00.0	XRT_Custom_430_OG [0x1ae]						
2014/06/06	09:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/06/06	09:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/06/06	09:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97	00	
2014/06/06	10:00:00.0	AOCS_Ore-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00 55 b2	00 cc	
2014/06/06	10:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/06/06	10:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/06/06	10:00:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/06/06	10:00:24.0	XRT_ARS_DIS_443_OG [0x1bb]						

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2014/06/06	10:02:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_ARS_DIS	1	07-F0	d5				
			MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/06	10:02:56.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2014/06/06	10:02:58.0	XRT_FL_PROG_SET_407_OG [0x197]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2014/06/06	10:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/06	10:19:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/06	10:19:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/06	10:19:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/06	10:22:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/06	10:51:30.0	XRT_Custom_430_OG [0x1ae]								
2014/06/06	10:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/06	12:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/06	12:00:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/06	12:00:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/06	12:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/06	12:30:00.0	XRT_Custom_430_OG [0x1ae]								
2014/06/06	12:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/06	13:38:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/06	13:38:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/06	13:38:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/06	13:41:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/06	14:14:30.0	XRT_Custom_430_OG [0x1ae]								
2014/06/06	14:15:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/06	15:17:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/06	15:17:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/06	15:17:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/06	15:20:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/06	16:04:00.0	XRT_Custom_430_OG [0x1ae]								
2014/06/06	16:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/06	16:55:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/06	16:55:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/06	16:55:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/06	16:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/06	17:41:00.5	XRT_Custom_430_OG [0x1ae]								
2014/06/06	17:42:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/06	17:57:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/06	17:57:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/06/06	17:58:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00	00
2014/06/06	17:58:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/06/06	18:00:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/06/06	18:00:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/06/06	18:00:58.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0c			
2014/06/06	18:01:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/06	18:07:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/06	18:07:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/06/06	18:08:00.0	AOCS_Ore-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	01	00	00	00	00
2014/06/06	18:08:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/06/06	18:08:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/06/06	18:08:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/06/06	18:08:22.0	XRT_ARS_DIS_423_OG [0x1a7]								

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2014/06/06	18:08:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/06/06	18:10:56.0	XRT_OT_PROG_SET_429_OG [0x1ad]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/06	18:10:58.0	XRT_FL_PROG_SET_407_OG [0x197]	MDP_XRT_OT_PROG_SET	2	07-F0	c4 07
2014/06/06	18:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2014/06/06	18:34:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/06	18:34:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/06	18:34:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/06	18:37:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/06/06	19:17:31.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/06/06	19:18:31.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2014/06/06	20:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/06	20:12:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/06	20:12:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/06	20:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/06/06	20:55:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/06/06	20:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2014/06/06	21:50:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/06	21:50:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/06	21:50:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/06	21:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/06/06	22:30:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/06/06	22:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2014/06/06	23:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/06	23:29:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/06	23:29:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/06	23:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/06/06	23:59:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/06/07	00:00:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2014/06/07	01:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/07	01:07:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/07	01:07:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/07	01:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/06/07	01:37:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/06/07	01:38:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2014/06/07	02:45:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/07	02:45:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/07	02:45:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/07	02:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/06/07	03:16:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/06/07	03:17:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2014/06/07	04:14:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/07	04:14:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/07	04:14:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/07	04:17:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/06/07	04:54:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/06/07	04:55:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2014/06/07	05:26:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/07	05:26:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/07	05:27:00.0	AOCS_Ore-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/06/07	05:27:16.0	XRT_FLD_DIS_434_OG [0x1b2]	AOCU_NM	5	02-76	00 00 00 00 00

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2014/06/07	05:29:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2014/06/07	05:29:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/06/07	05:29:58.0	XRT_QT_PROG_SET_420_OG [0x1a4]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/06/07	05:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2014/06/07	05:36:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/07	05:36:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/07	05:37:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/06/07	05:37:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00
2014/06/07	05:37:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2014/06/07	05:37:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/06/07	05:37:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2014/06/07	05:37:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/06/07	05:39:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/07	05:39:58.0	XRT_FL_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2014/06/07	05:40:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2014/06/07	05:54:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/07	05:54:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/07	05:54:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/07	05:57:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/06/07	06:32:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/06/07	06:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2014/06/07	07:35:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2014/06/07	07:35:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/07	07:35:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/07	07:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/07	08:11:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/06/07	08:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/06/07	09:15:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2014/06/07	09:15:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/07	09:15:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/07	09:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/07	11:27:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
		AOCU_NM	MDP_XRT_PREFLR_STOP	1	07-F0	e9
			AOCU_NM	5	02-76	00 00 00 00 00