

XRT Timeline to be uploaded on 2014/06/28

Period: 2014/06/28 09:50:00 - 2014/07/03 11:12:00

* * * * *

Normal mode

* * * * *

XOB #19E3: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)

Term	Pointing (x, y)	Comment
06/28 10:07:00 - 06/28 17:56:54	Track (27.9, 52.0) @ 06/28 10:00:00	# OP start + 10min, Track AR 12096.
06/28 18:10:00 - 06/29 05:41:54	Track (101.8, 51.6) @ 06/28 18:07:00	# AR cont
06/29 05:55:00 - 06/29 17:30:54	Track (207.6, 51.5) @ 06/29 05:52:00	# AR cont
06/29 17:44:00 - 06/30 05:59:54	Track (311.2, 52.0) @ 06/29 17:41:00	# AR cont
06/30 06:13:00 - 06/30 18:01:24	Track (416.2, 53.2) @ 06/30 06:10:00	# AR cont
06/30 18:14:30 - 07/01 00:56:00	Fixed (-931.6, -111.4)	# Observe returning AR at limb for EIS.
07/01 06:40:30 - 07/01 09:00:00	Track (602.7, 57.6) @ 07/01 06:37:30	# Return to AR 12096.

PROG= 20 Inf.-time(s)

Subr= 1	1-time(s)	2.0sec										
Seqn= 8	2-time(s)	2.0sec										
Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
Subr= 2	2-time(s)	2.0sec										
Seqn= 24	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
Seqn= 42	4-time(s)	2.0sec										
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Seqn= 62	15-time(s)	120.0sec										
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #1A10: Synoptic Q95 2x2 - Al/mesh(2/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(5/256/1443) + Thin_

Term	Pointing (x, y)	Comment
06/28 18:00:00 - 06/28 18:06:54	Fixed (0.0, 0.0)	synoptic, shifted -3.0 min
06/29 05:45:00 - 06/29 05:51:54	Fixed (0.0, 0.0)	synoptic, shifted -18.0 min
06/29 17:34:00 - 06/29 17:40:54	Fixed (0.0, 0.0)	synoptic, shifted -29.0 min
06/30 06:03:00 - 06/30 06:09:54	Fixed (0.0, 0.0)	synoptic
06/30 18:04:30 - 06/30 18:11:24	Fixed (0.0, 0.0)	synoptic, shifted 1.5 min
07/01 06:30:30 - 07/01 06:37:24	Fixed (0.0, 0.0)	synoptic, shifted 27.5 min

PROG= 14 1-time(s)

Subr= 1	1-time(s)	12.0sec										
Seqn= 35	1-time(s)	4.0sec										
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	707ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 5	1-time(s)	2.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 72	1-time(s)	4.0sec										
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	250ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 67	1-time(s)	2.0sec										
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6	1-time(s)	2.0sec										
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

XOB #19A6: CME watch - 4x4 - AEC 2 - Ti/poly- 60s cad

Term	Pointing (x, y)	Comment
07/01 01:40:30 - 07/01 06:27:24	Track (-27.5, 0.2) @ 07/01 01:00:00	# Disk center for SOT flat-field.

PROG= 03 Inf.-time(s)

Subr= 1	15-time(s)	60.0sec										
Seqn= 44	1-time(s)	4.0sec										
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec

Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

* * * * *

Flare mode

* * * * *

XOB #199C: Flare Standard Obs. with eruptions mode-A (FW1=Open and VLS-Closed)												
Term	Pointing (x, y)		Comment									
06/28 10:07:00 - 06/28 17:56:54	Track (27.9, 52.0)	@ 06/28 10:00:00	# OP start + 10min, Track AR 12096.									
06/28 18:10:00 - 06/29 05:41:54	Track (101.8, 51.6)	@ 06/28 18:07:00	# AR cont									
06/29 05:55:00 - 06/29 17:30:54	Track (207.6, 51.5)	@ 06/29 05:52:00	# AR cont									
06/29 17:44:00 - 06/30 05:59:54	Track (311.2, 52.0)	@ 06/29 17:41:00	# AR cont									
06/30 06:13:00 - 06/30 18:01:24	Track (416.2, 53.2)	@ 06/30 06:10:00	# AR cont									
06/30 18:14:30 - 07/01 00:56:00	Fixed (-931.6, -111.4)		# Observe returning AR at limb for EIS.									
07/01 01:40:30 - 07/01 06:27:24	Track (-27.5, 0.2)	@ 07/01 01:00:00	# Disk center for SOT flat-field.									
07/01 06:40:30 - 07/01 09:00:00	Track (602.7, 57.6)	@ 07/01 06:37:30	# Return to AR 12096.									
PROG= 08 1-time(s)												
Subr= 1 30-time(s) 20.0sec												
Seqn= 55 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 9 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 39 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 55 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 70 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 39 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 55 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 70 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 39 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Subr= 3 30-time(s) 60.0sec												
Seqn= 55 1-time(s) 2.0sec												
Open/thick-Al	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 70 1-time(s) 2.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 4 24-time(s) 600.0sec												
Seqn= 1 1-time(s) 2.0sec												
Open/Al-mesh	Open/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

* * * * *

Active Region Search

* * * * *

NOT USED

* * * * *

Flare Detection

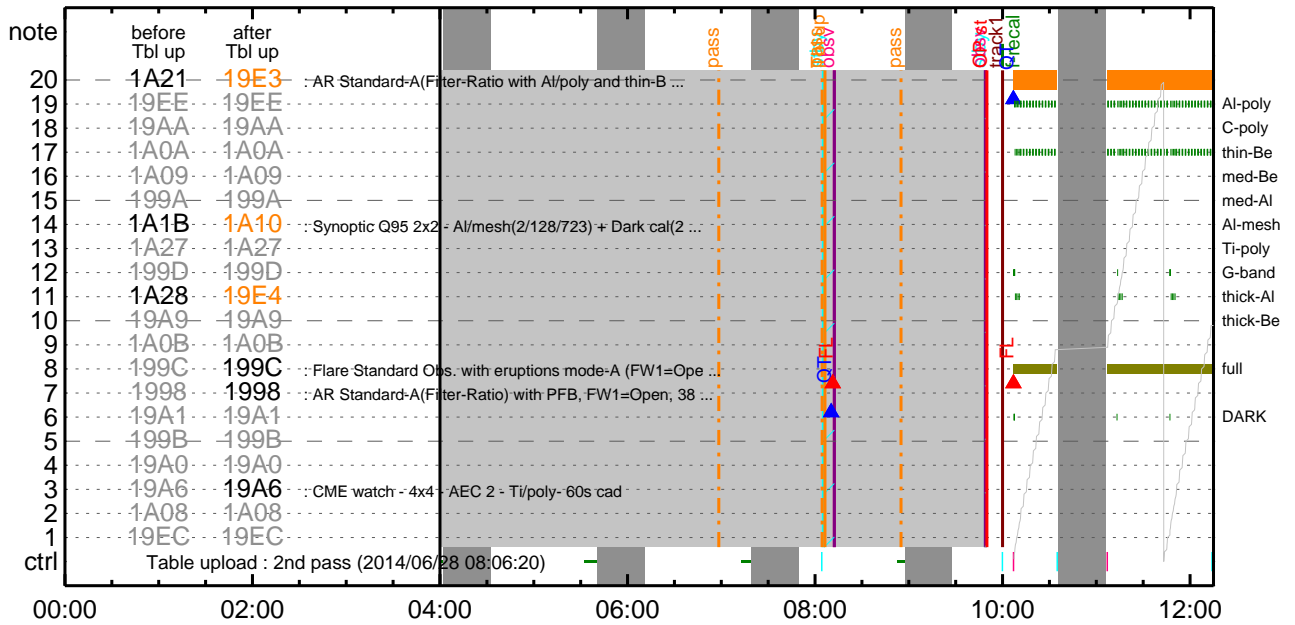
* * * * *

FLD Patrol												
Term	Pointing (x, y)		Comment									
06/28 18:07:16 - 06/29 05:42:16	Track (101.8, 51.6)	@ 06/28 18:07:00	# AR cont									
06/29 05:52:16 - 06/29 17:31:16	Track (207.6, 51.5)	@ 06/29 05:52:00	# AR cont									
06/29 17:41:16 - 06/30 06:00:16	Track (311.2, 52.0)	@ 06/29 17:41:00	# AR cont									

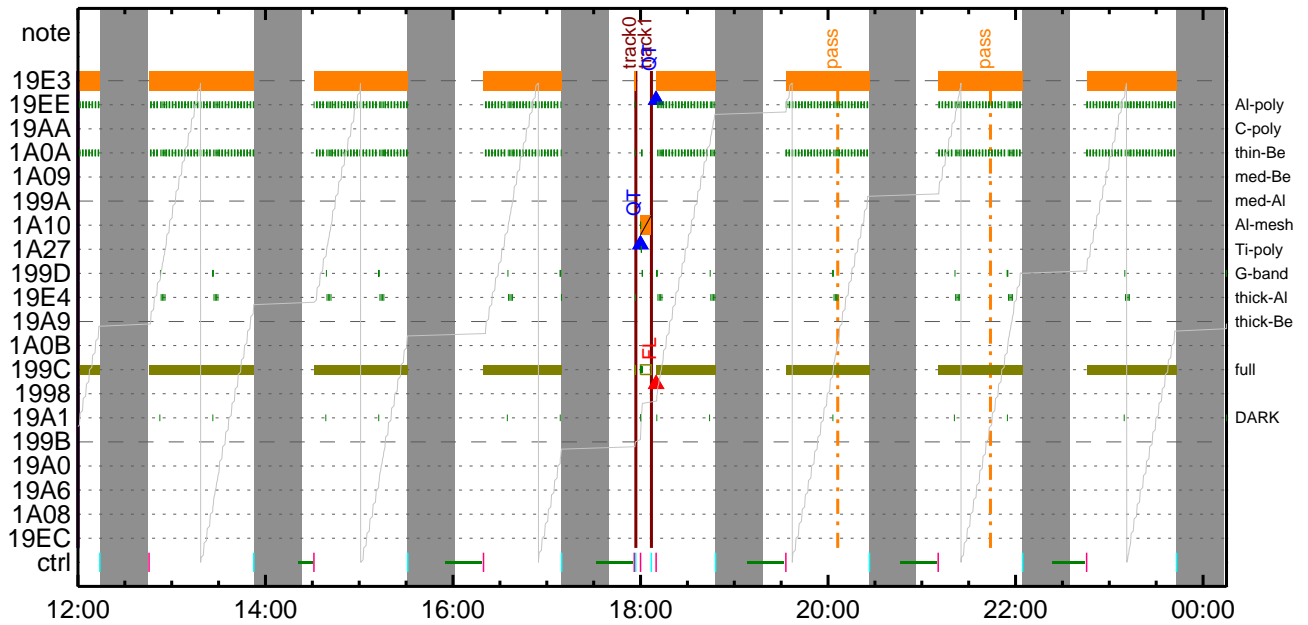
06/30 06:10:16 - 06/30 18:01:46 Track (416.2, 53.2) @ 06/30 06:10:00 # AR cont
 06/30 18:11:46 - 07/01 06:27:46 Fixed (-931.6, -111.4) # Observe returning AR at limb for EIS.
 07/01 06:37:46 - 07/03 11:12:00 Track (602.7, 57.6) @ 07/01 06:37:30 # Return to AR 12096.

Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50	30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

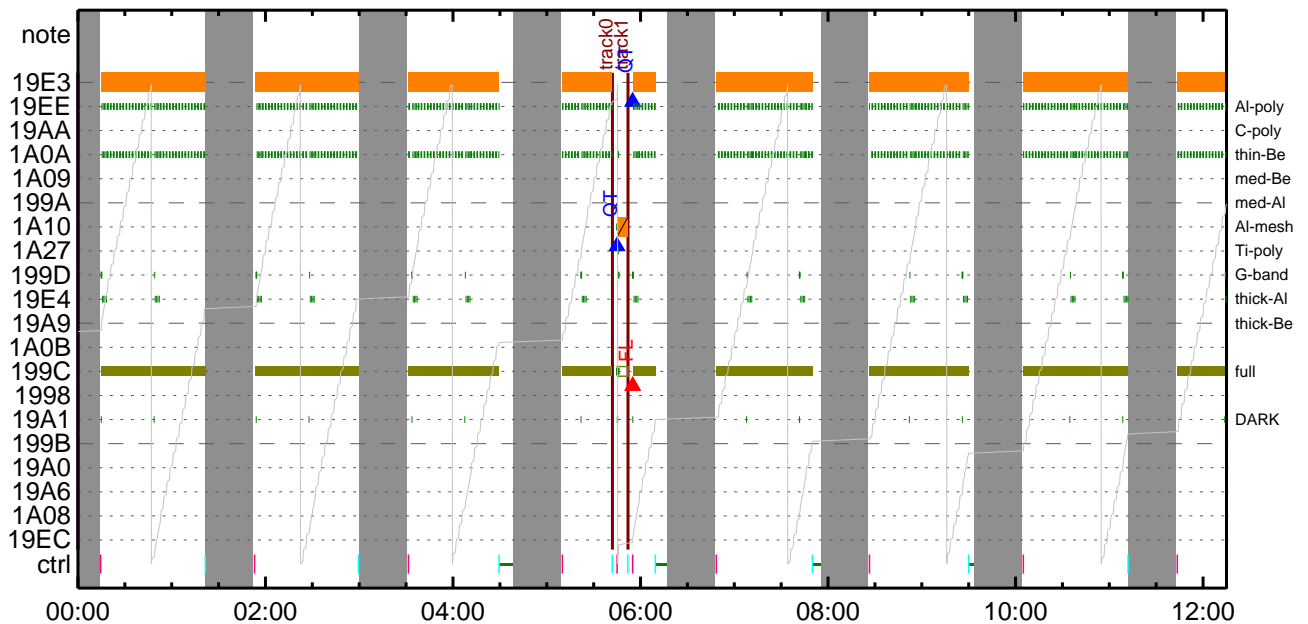
CMDI #0374 2014/06/28



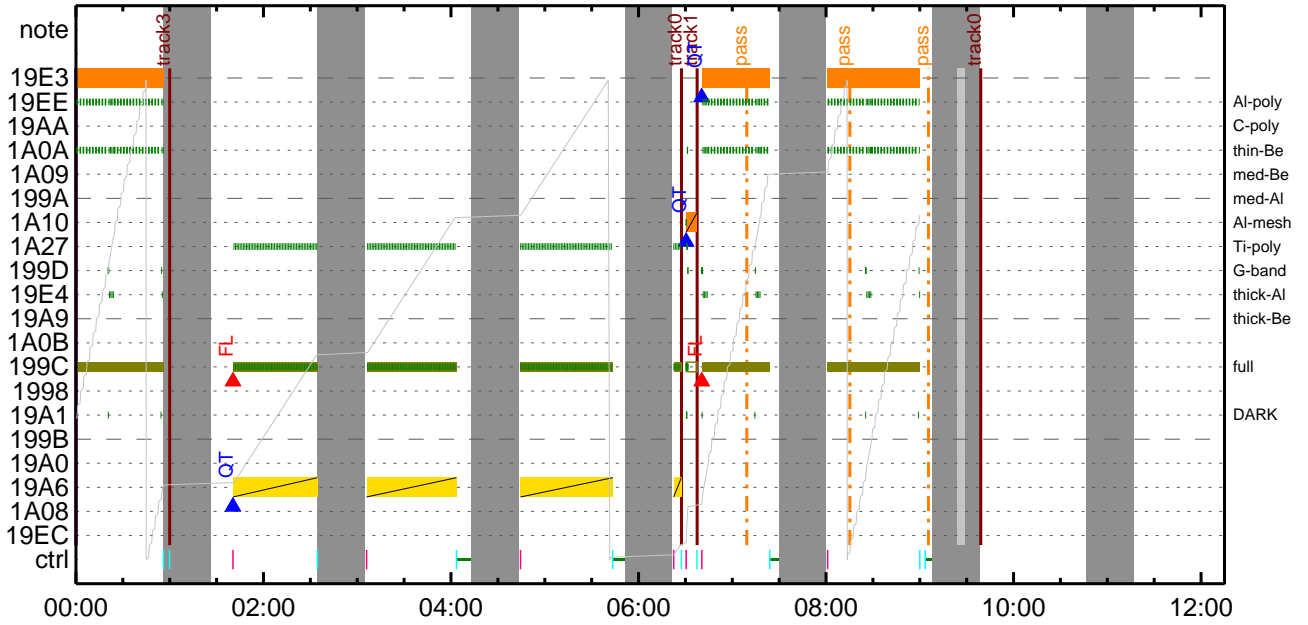
CMDI #0374 2014/06/28



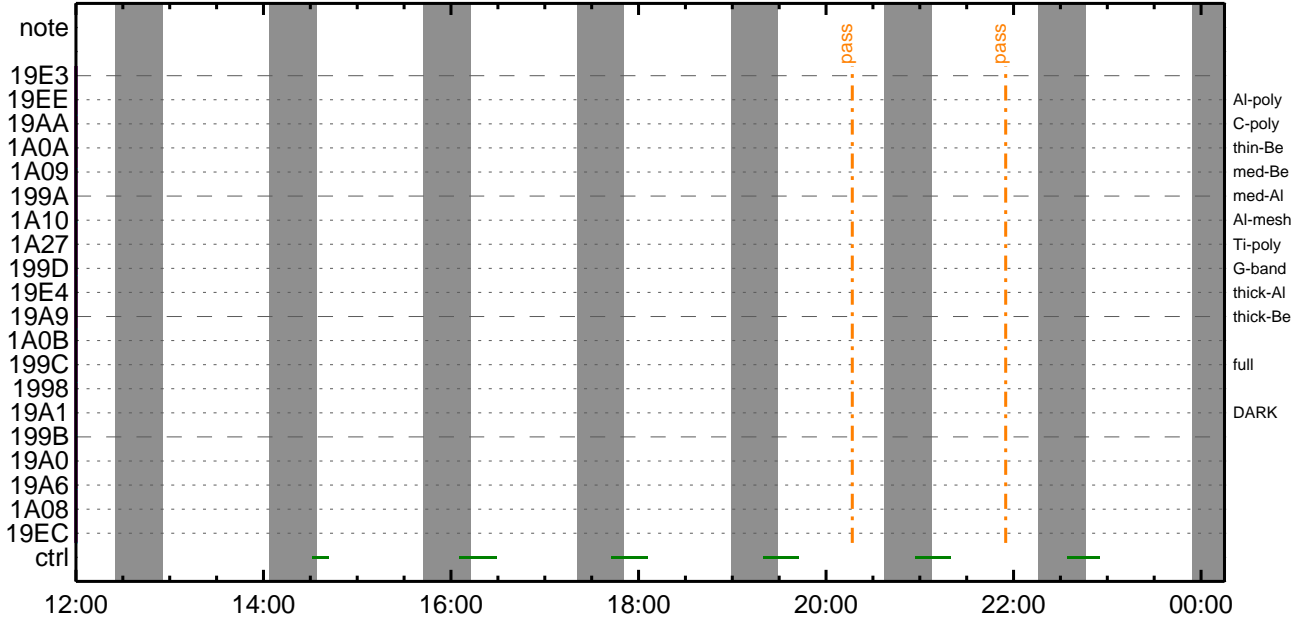
CMDI #0374 2014/06/29



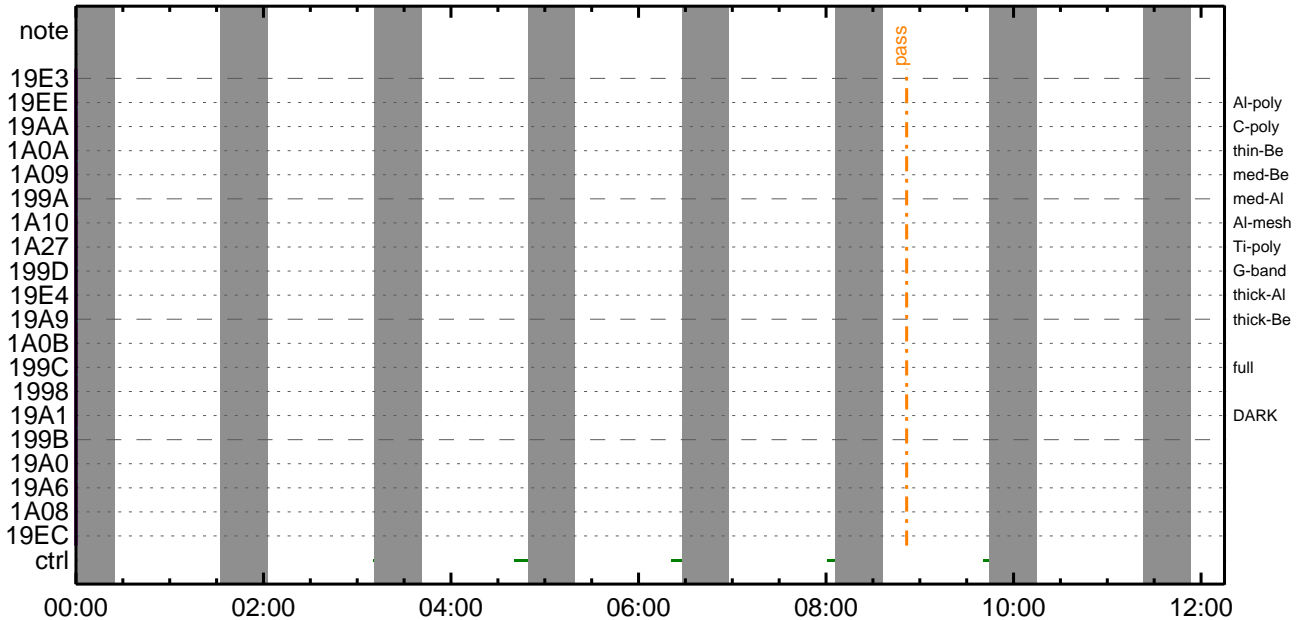
CMDI #0374 2014/07/01



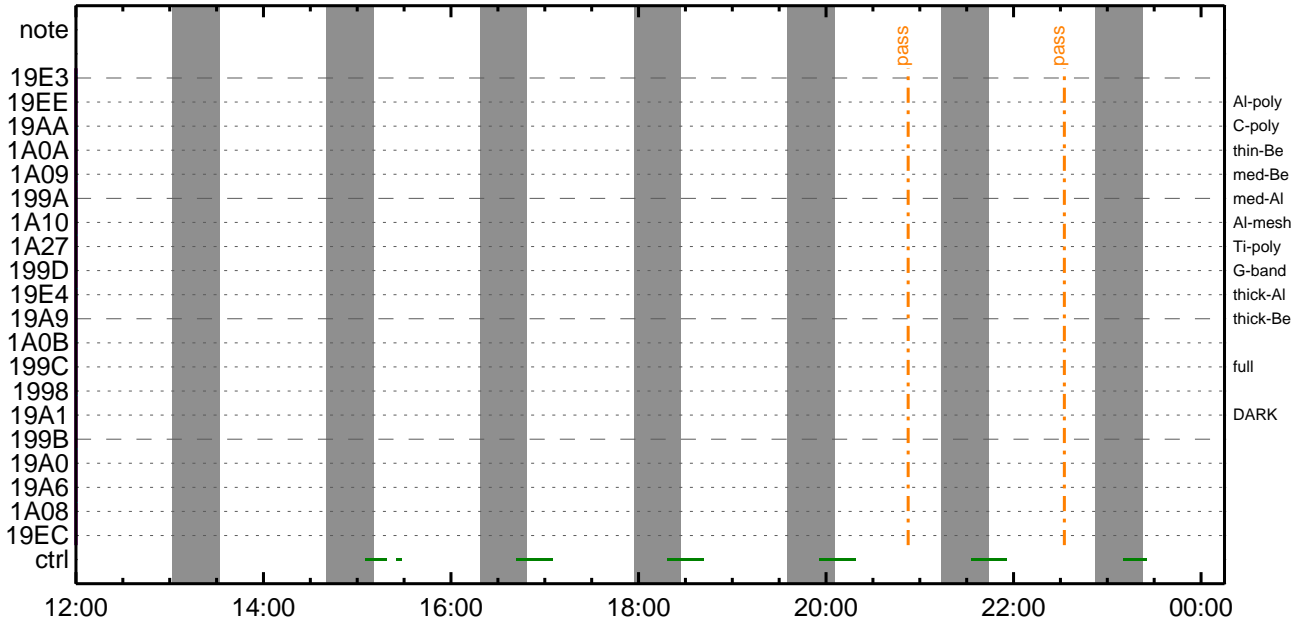
CMDI #0374 2014/07/01



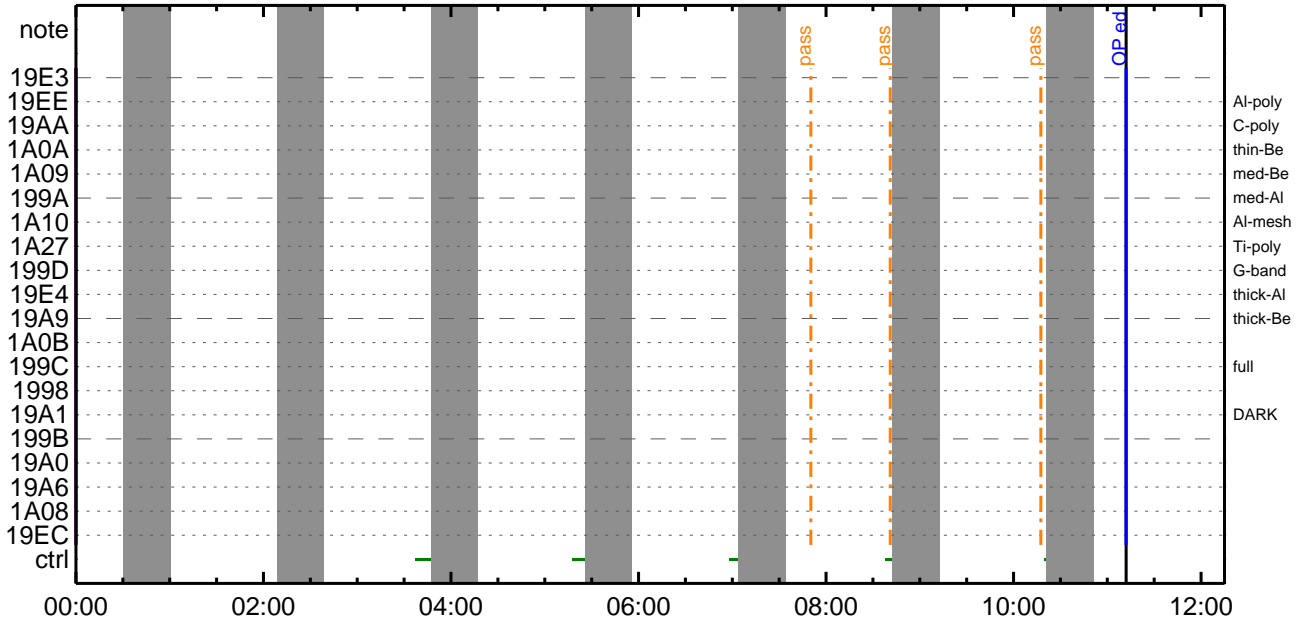
CMDI #0374 2014/07/02



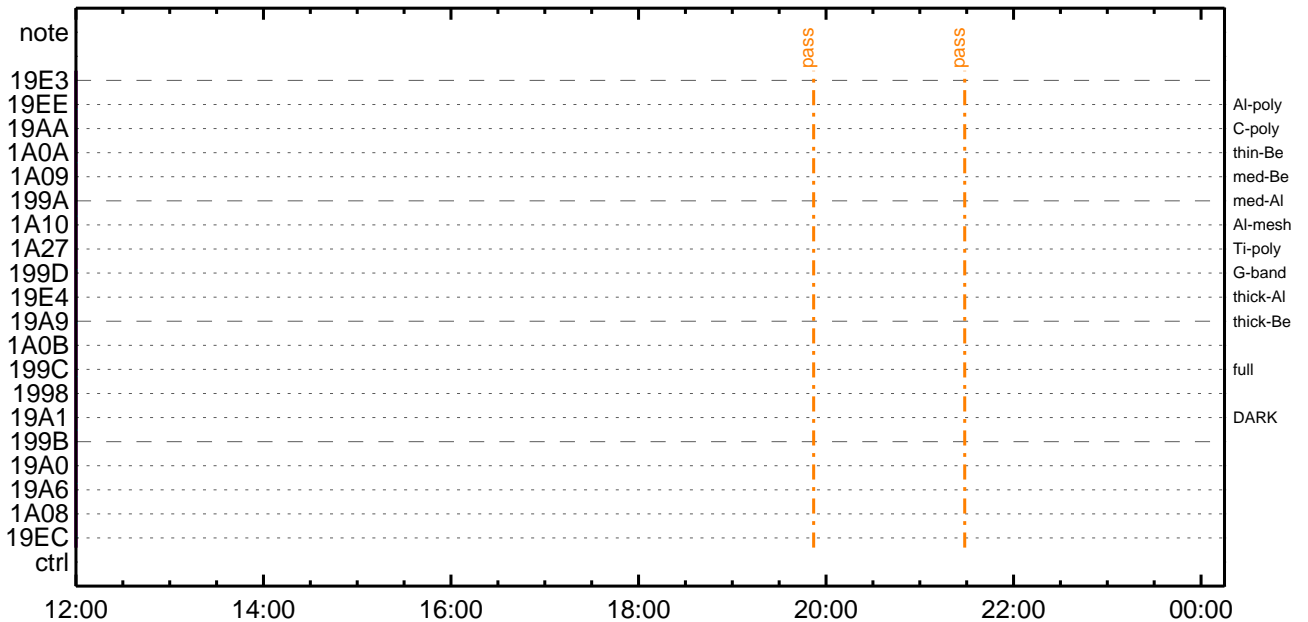
CMDI #0374 2014/07/02



CMDI #0374 2014/07/03



CMDI #0374 2014/07/03




```

0096 C.      0300; SET 0EDUMP 01A±0iYNY¹0Ç¹0a|0³0E; f
0097 C.
0098 . C.   TIY³YF¥ÖYÉ00dÄDİ¿ (UT)
0099 +. TI  2014-06-28 09:45:00.0
0100 DC  01-B3 DHU_OP_STOP
0101 C.      çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0102 C.
0103 +. TI  2014-06-28 09:45:01.0
0104 DC  01-B4 DHU_OP_COPY
0105 C.      çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0106 C.
0107 +. TI  2014-06-28 09:45:01.0
0108 DC  01-B5 DHU_OPOG_COPY
0109 C.      çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0110 C.
0111 +. TI  2014-06-28 09:49:59.5
0112 DC  01-B2 DHU_OP_START
0113 C.      çç[HK1_TI_CMD_NUM]          EQ      1COUNTUP
0114 C.
0115 C.   °Ê²¼0İÄê%îİÑ0İYÁY§YÁY-¹àİÜ
0116 C.      çç[HK1_TI_CMD_ENA/DIS]       EQ      ENA
0117 C.      çç[HK1_TI_CMD_NUM]         EQ      4
0118 C.      çç[HK1_NEXT_EXEC_PIM]       EQ      DHU
0119 C.      çç[HK1_NEXT_EXEC_DC]       EQ      0xB3
0120 C.
0121 . C.   *****
0122 C.   TIİİ°èYÁYÖY×
0123 C.   *****
0124 C.
0125 C.   TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC  01-23 DHU_DMA_DMP_PRM_SET
0127 BC      (03 ab 03 01 02)
0128 C.      çç[HK1_DMP_TOP_ADRS_1]      EQ      07
0129 C.      çç[HK1_DMP_TOP_ADRS_0]      EQ      2B
0130 C.      çç[HK1_DMP_BLOCK_NUM]       EQ      3
0131 C.      çç[HK1_DMP_REPEAT_NUM]      EQ      0
0132 C.      çç[HK1_DMA_DMP_PIM]        EQ      DHU
0133 +. DC  01-22 DHU_MODE_CHNG
0134 BC      (07 0b f8)
0135 C.      çç[HK1_PKT_FORM_NO]         EQ      7
0136 C.      çç[HK1_PKT_GEN_TIME]        EQ      0.25 s
0137 C.      çç[HK1_S_TLM_BIT_RATE]      EQ      32k
0138 C.      çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0139 C.      çç[HK1_DMP_CHK_FLG]        EQ      EXEC
0140 C.
0141 . C.   YÁYÖY×½ªİ»0d³İÇ§
0142 C.      çç[HK1_DMP_CHK_FLG]        EQ      NON
0143 C.
0144 . C.   RAM ID=TI_TBL0İ%È¹Ç•è²İOK0d³İÇ§
0145 C.
0146 . C.   DHU¥â;¼YÉ;Ê¼Y½,¥İ;¼YÈ;Ë0dİá0¹
0147 +. DC  01-22 DHU_MODE_CHNG
0148 BC      (02 0a f8)
0149 C.      çç[HK1_PKT_FORM_NO]         EQ      2
0150 C.      çç[HK1_PKT_GEN_TIME]        EQ      0.5S
0151 C.      çç[HK1_S_TLM_BIT_RATE]      EQ      32K
0152 C.      çç[HK1_X_TLM_BIT_RATE]     EQ      4M
0153 C.
0154 C.   *****
0155 C.   SOT TI command set
0156 C.   *****
0157 C.   Execute, after the success of OP upload.
0158 +. TI  2014-06-28 09:49:16.0
0159 DC  07-F0 MDP_SOT_MODE_STBY
0160 BC      (41)
0161 . C.   -----
0162 C.   HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0163 C.   -----
0164 C.   ***** SOT END *****
0165 C.
0166 C.   ***** XRT START *****
0167 C.   Execute, after the success of OP upload.
0168 +. TI  2014-06-28 09:49:00.0
0169 DC  07-F0 MDP_XRT_MODE_STBY
0170 BC      (c3)
0171 . C.   [ ] [HK1_TI_CMD_NUM]      EQ      1COUNTUP
0172 C.
0173 C.   ***** XRT END *****
0174 . C.   Stop EIS observation and temporarily disable EIS mode changes
0175 C.
0176 C.
0177 C.   ***** Start EIS operation (TI set) *****
0178 C.   Execute, after the success of OP upload.
0179 C.   Set EIS TI-commands
0180 +. TI  2014-06-28 09:49:30.0
0181 DC  07-FC EIS_MODE_MANU
0182 BC      (21 02)
0183 +. TI  2014-06-28 09:49:40.0
0184 DC  07-FC EIS_MODE_CHG_DIS
0185 BC      (22)
0186 . C.   [ ] [HK1_TI_CMD_NUM]      EQ      2 COUNTUP
0187 C.   ***** End EIS operation (TI set) *****
0188 C.
0189 C.
0190 C.
0191 . C.   ***** MDP `ÜÄİ0İ»ö¼Y0ÈÄ0¹0èDCBC•×²è *****
0192 C.   (¼0İYÖYÁYÉY¥YÁYÇYè0È¼00¼Ä»Ü0¹0é)
0193 . S. DC-BC dcbc-402:DCBC

```

```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥D¥!•İ Daily±;İÑøĒ'Øα¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```



```

0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 C.
0130 C. ***** XRT START *****
0131 C.
0132 +. DC 07-F0 MDP_XRT_CTRL_MANU
0133 BC (c1)
0134 + DC 07-F0 MDP_XRT_MODE_STBY
0135 BC (c3)
0136 . C. ----- Success Verify ? OK / NG____
0137 C.
0138 C. XRT Obs. Table Upload
0139 . S. RAM ram-291:MDP_OBS_X
0140 ( )
0141 C.
0142 +. DC 07-F0 MDP_DUMP_XRTTBL
0143 BC (84 07 00 00 00 3a d4)
0144 . C. ----- Comparison Check ? OK / ERR ____
0145 C.
0146 C.
0147 +. DC 07-F0 MDP_XRT_ROI_SET
0148 BC (cd 01 b1 b1 04 04)
0149 + DC 07-F0 MDP_XRT_ROI_SET
0150 BC (cd 02 b1 b1 08 08)
0151 + DC 07-F0 MDP_XRT_ROI_SET
0152 BC (cd 03 b1 b1 08 08)
0153 + DC 07-F0 MDP_XRT_ROI_SET
0154 BC (cd 04 b1 b1 06 06)
0155 + DC 07-F0 MDP_XRT_ROI_SET
0156 BC (cd 05 85 83 06 06)
0157 + DC 07-F0 MDP_XRT_ROI_SET
0158 BC (cd 06 85 83 06 06)
0159 + DC 07-F0 MDP_XRT_ROI_SET
0160 BC (cd 07 85 83 08 08)
0161 + DC 07-F0 MDP_XRT_ROI_SET
0162 BC (cd 08 80 80 20 20)
0163 + DC 07-F0 MDP_XRT_ROI_SET
0164 BC (cd 09 80 80 20 08)
0165 + DC 07-F0 MDP_XRT_ROI_SET
0166 BC (cd 0a 80 80 08 20)
0167 + DC 07-F0 MDP_XRT_ROI_SET
0168 BC (cd 0f 80 80 06 06)
0169 + DC 07-F0 MDP_XRT_ROI_SET
0170 BC (cd 10 80 80 08 08)
0171 + DC 07-F0 MDP_XRT_FLD_ENA
0172 BC (d8)
0173 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0174 BC (c8)
0175 + DC 07-F0 MDP_XRT_AEC_RESET
0176 BC (d0)
0177 + DC 07-F0 MDP_XRT_ARS_DIS
0178 BC (d5)
0179 + DC 07-F0 MDP_XRT_FLD_RESET
0180 BC (da)
0181 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0182 BC (c4 07)
0183 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0184 BC (c5 08)
0185 . C. ----- Success Verify ? OK / NG ____
0186 C.
0187 C.
0188 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0189 C.
0190 +. DC 07-F0 MDP_XRT_MODE_OBSV
0191 BC (c2)
0192 +. TI 2014-06-28 09:49:02.0
0193 DC 07-F0 MDP_XRT_MODE_OBSV

```

```
0194 BC (c2)
0195 . C. ----- Success Verify ? OK / NG ____
0196 C.
0197 C. ***** XRT END *****
0198 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0199 +. DC 07-FC EIS_MODE_CHG_ENA
0200 BC (20)
0201 . C. Verify EIS_MODE_CHG_FLG is ENA
0202 +. DC 07-FC EIS_MODE_MANU
0203 BC (21 02)
0204 . C. Verify EIS in MANUAL mode
0205 . C. Estimated OBSTBL upload time is 28s
0206 C. *****
0207 C. EIS START OBSTBL LOAD
0208 C. *****
0209 . S. RAM ram-820:EIS_OBSTBL
0210 ( )
0211 +. DC 07-FC EIS_DUMP_OBSTBL
0212 BC (07 07 07 00 00 70 00)
0213 C.
0214 C. Execute, after the success of OBSTBL upload.
0215 C. Set EIS TI-commands
0216 +. TI 2014-06-28 09:49:50.0
0217 DC 07-FC EIS_MODE_CHG_ENA
0218 BC (20)
0219 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0220 C. *****
0221 C. EIS END OBSTBL LOAD
0222 C. *****
0223 C.
0224 . C. ***** MDP `úÃîí»ö%ÝðËÂð¹ðËDCBC•x²è *****
0225 C. (%Á°îÝÓÝÃÝÈÝÞÝËÝáÝçÝèðË%¼ð¼Â»Û¹ðè)
0226 . S. DC-BC dcbc-402:DCBC
0227 (MDP_known_event)
0228 C.
0229 C.
0230 . C. ***** ¥ÐÝ¹•İ Daily±¿İÑðË`Ø¹ðËDCBC•x²è *****
0231 . S. DC-BC dcbc-153:DCBC
0232 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0233 C.
0234 C.
0235 . C. ;ãLOS¥Á¥$¥Ã¥`¼Â»Û;ã
0236 C.
0237 . C. ***** LOS *****
0238 C.
```

(a) Spacecraft Operation Procedure (real-commands)

```
main-477 2014-06-28 13:28:49 61 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY~¼Á»Û;ã
0005 C.
0006 C. YÁYB;¼Y³YFÝÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÁø•µ°È»Í×ÁÇóÍYçYÁY×Yí;¼YÉ;ÈÈè¼µ•ííÈ;ÈøÈ¼°ÇÓø•ø¿¼í¹çøÍ;çÁ®, ùø¹øèøøøÇÁ+¿®ø•øÈøøøø³øÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-267:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 C. *****
0036 C. SOT TI command set
0037 C. *****
0038 C. Execute, after the success of TBL upload.
0039 +. TI 2014-06-28 09:49:18.0
0040 DC 07-F0 MDP_SOT_MODE_OBSV
0041 BC (40)
0042 . C. -----
0043 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0044 C. -----
0045 C.
0046 C.
0047 . C. ***** MDP `ûÃÎñî»ó¼YøÈÁø¹øèDCBC•x²è *****
0048 C. (¼á°íYÓYÁYÈYFÝÉYáYçYèøÈ¼øø¼Á»Ûø¹øè)
0049 . S. DC-BC dcbc-402:DCBC
0050 (MDP_known_event)
0051 C.
0052 C.
0053 . C. ***** YDÝ¹.İ Daily±¿ÍÑøÈ´Øø¹øèDCBC•x²è *****
0054 . S. DC-BC dcbc-153:DCBC
0055 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0056 C.
0057 C.
0058 . C. ;ãLOSÁY$YÁY~¼Á»Û;ã
0059 C.
0060 . C. ***** LOS *****
0061 C.
```

Jun 28, 14 13:28

XRT_OGLIST_0374.chk

Page 1/9

*** OP Sequence for XRT ***

2014/06/28	09:59:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/28	09:59:56.0	XRT_FOCUS_RECALIBRATE_441_OG [0x1b9]			
		XRT_FOCUS_RECAL	2	07-F8	78 00
2014/06/28	10:00:00.0	AOCS_OrE-point_Start_1_OG [0x097]			
		AOCU_NM	5	02-76	01 00 00 00 00
2014/06/28	10:03:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]			
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/06/28	10:04:16.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/06/28	10:04:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/06/28	10:04:20.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/06/28	10:04:22.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/06/28	10:04:24.0	XRT_FLD_RESET_431_OG [0x1af]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/28	10:06:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2014/06/28	10:06:58.0	XRT_FL_PROG_SET_407_OG [0x197]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08
2014/06/28	10:07:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/28	10:35:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/28	10:35:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/28	10:35:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/06/28	10:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/06/28	11:06:00.0	XRT_Custom_430_OG [0x1ae]			
2014/06/28	11:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/28	12:14:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/28	12:14:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/28	12:14:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/06/28	12:17:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/06/28	12:44:30.0	XRT_Custom_430_OG [0x1ae]			
2014/06/28	12:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/28	13:52:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/28	13:52:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/28	13:52:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/06/28	13:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/06/28	14:30:00.0	XRT_Custom_430_OG [0x1ae]			
2014/06/28	14:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/28	15:31:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/28	15:31:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/28	15:31:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/06/28	15:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/06/28	16:18:30.0	XRT_Custom_430_OG [0x1ae]			
2014/06/28	16:19:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/28	17:09:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/28	17:09:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/28	17:09:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/06/28	17:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/06/28	17:55:00.0	XRT_Custom_430_OG [0x1ae]			
2014/06/28	17:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/28	17:56:54.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/28	17:56:56.0	XRT_FOCUS_POSITION_403_OG [0x193]			
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/06/28	17:57:00.0	AOCS_OrE-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00
2014/06/28	17:57:16.0	XRT_FLD_DIS_434_OG [0x1b2]			
		MDP_XRT_FLD_DIS	1	07-F0	d9
2014/06/28	17:59:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]			
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/06/28	17:59:56.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5

Jun 28, 14 13:28

XRT_OGLIST_0374.chk

Page 2/9

2014/06/28	17:59:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2014/06/28	18:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/28	18:06:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/28	18:06:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/06/28	18:07:00.0	AOCS_Ore-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01	00	00	00	00
2014/06/28	18:07:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/06/28	18:07:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/06/28	18:07:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/06/28	18:07:22.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/06/28	18:07:24.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/28	18:09:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2014/06/28	18:09:58.0	XRT_FL_PROG_SET_407_OG [0x197]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	08			
2014/06/28	18:10:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/28	18:48:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/28	18:48:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/28	18:48:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/28	18:51:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/28	19:32:00.0	XRT_Custom_430_OG [0x1ae]							
2014/06/28	19:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/28	20:26:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/28	20:26:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/28	20:26:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/28	20:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/28	21:09:30.0	XRT_Custom_430_OG [0x1ae]							
2014/06/28	21:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/28	22:04:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/28	22:04:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/28	22:04:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/28	22:07:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/28	22:44:30.0	XRT_Custom_430_OG [0x1ae]							
2014/06/28	22:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/28	23:43:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/28	23:43:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/28	23:43:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/28	23:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/29	00:13:30.0	XRT_Custom_430_OG [0x1ae]							
2014/06/29	00:14:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/29	01:21:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/29	01:21:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/29	01:21:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/29	01:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/29	01:52:00.0	XRT_Custom_430_OG [0x1ae]							
2014/06/29	01:53:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/29	02:59:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/29	02:59:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/29	02:59:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/29	03:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/29	03:30:30.0	XRT_Custom_430_OG [0x1ae]							
2014/06/29	03:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/29	04:29:30.0	XRT_CTRL_MANU_400_OG [0x190]							

Jun 28, 14 13:28

XRT_OGLIST_0374.chk

Page 3/9

2014/06/29	04:29:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/06/29	04:29:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/06/29	04:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/06/29	05:09:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/06/29	05:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/06/29	05:41:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/06/29	05:41:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2014/06/29	05:42:00.0	AOCs_OrE-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2014/06/29	05:42:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2014/06/29	05:44:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2014/06/29	05:44:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/06/29	05:44:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e			
2014/06/29	05:45:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/06/29	05:51:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/06/29	05:51:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2014/06/29	05:52:00.0	AOCs_OrE-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00			
2014/06/29	05:52:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/06/29	05:52:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/06/29	05:52:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/06/29	05:52:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/06/29	05:52:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/06/29	05:54:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14			
2014/06/29	05:54:58.0	XRT_FL_PROG_SET_407_OG [0x197]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08			
2014/06/29	05:55:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/06/29	06:09:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/06/29	06:09:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/06/29	06:09:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/06/29	06:12:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/06/29	06:47:30.0	XRT_Custom_430_OG [0x1ae]							
2014/06/29	06:48:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/06/29	07:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/06/29	07:50:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/06/29	07:50:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/06/29	07:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/06/29	08:25:30.0	XRT_Custom_430_OG [0x1ae]							
2014/06/29	08:26:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/06/29	09:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/06/29	09:30:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/06/29	09:30:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/06/29	09:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/06/29	10:04:00.0	XRT_Custom_430_OG [0x1ae]							
2014/06/29	10:05:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/06/29	11:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/06/29	11:12:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/06/29	11:12:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/06/29	11:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/06/29	11:42:30.0	XRT_Custom_430_OG [0x1ae]							
2014/06/29	11:43:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/06/29	12:50:30.0	XRT_CTRL_MANU_400_OG [0x190]							

2014/06/29	12:50:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/29	12:50:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/06/29	12:53:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/06/29	13:21:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/06/29	13:22:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/06/29	14:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/29	14:29:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/06/29	14:29:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/06/29	14:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/06/29	15:08:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/06/29	15:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/29	16:07:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/06/29	16:07:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/29	16:07:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/06/29	16:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/06/29	16:54:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/06/29	16:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/06/29	17:30:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/29	17:30:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/29	17:31:00.0	AOCS_OrE-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/06/29	17:31:16.0	XRT_FLD_DIS_434_OG [0x1b2]	AOCU_NM	5	02-76	00 00 00 00 00	
2014/06/29	17:33:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/06/29	17:33:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/06/29	17:33:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/06/29	17:34:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e	
2014/06/29	17:40:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/06/29	17:40:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/29	17:41:00.0	AOCS_OrE-point_Start_1_OG [0x097]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2014/06/29	17:41:16.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	01 00 00 00 00	
2014/06/29	17:41:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/06/29	17:41:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/06/29	17:41:22.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/06/29	17:41:24.0	XRT_FLD_RESET_431_OG [0x1af]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/06/29	17:43:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/06/29	17:43:58.0	XRT_FL_PROG_SET_407_OG [0x197]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14	
2014/06/29	17:44:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 08	
2014/06/29	17:46:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/06/29	17:46:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/29	17:46:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/06/29	17:49:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/06/29	18:31:00.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/06/29	18:32:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/06/29	19:24:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/29	19:24:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/06/29	19:24:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/06/29	19:27:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/06/29	20:08:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/06/29	20:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/06/29	21:02:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	

Jun 28, 14 13:28

XRT_OGLIST_0374.chk

Page 5/9

2014/06/29	21:02:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/29	21:02:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/29	21:05:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/06/29	21:45:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/06/29	21:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/29	22:41:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/29	22:41:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/29	22:41:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/06/29	22:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/06/29	23:17:30.5	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/29	23:18:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/30	00:19:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/30	00:19:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/30	00:19:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/06/30	00:22:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/06/30	00:50:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/30	00:51:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/30	01:58:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/30	01:58:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/30	01:58:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/06/30	02:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/06/30	02:28:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/30	02:29:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/30	03:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/30	03:30:02.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/30	03:30:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/06/30	03:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/06/30	04:07:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/30	04:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/30	05:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/30	05:06:32.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/06/30	05:06:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/06/30	05:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/06/30	05:45:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/30	05:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/30	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/30	05:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/06/30	06:00:00.0	AOCS_Or-e-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00
2014/06/30	06:00:16.0	XRT_FLD_DIS_434_OG [0x1b2]	MDP_XRT_FLD_DIS	1	07-F0	d9
2014/06/30	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/06/30	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/06/30	06:02:58.0	XRT_QT_PROG_SET_444_OG [0x1bc]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e
2014/06/30	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/06/30	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/06/30	06:09:56.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/06/30	06:10:00.0	AOCS_Or-e-point_Start_1_OG [0x097]	AOCU_NM	5	02-76	01 00 00 00 00
2014/06/30	06:10:16.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2014/06/30	06:10:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/06/30	06:10:20.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0

Jun 28, 14 13:28

XRT_OGLIST_0374.chk

Page 6/9

2014/06/30	06:10:22.0	XRT_ARS_DIS_423_OG [0x1a7]		
		MDP_XRT_ARS_DIS	1	07-F0 d5
2014/06/30	06:10:24.0	XRT_FLD_RESET_431_OG [0x1af]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2014/06/30	06:12:56.0	XRT_QT_PROG_SET_443_OG [0x1bb]		
		MDP_XRT_QT_PROG_SET	2	07-F0 c4 14
2014/06/30	06:12:58.0	XRT_FL_PROG_SET_407_OG [0x197]		
		MDP_XRT_FL_PROG_SET	2	07-F0 c5 08
2014/06/30	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2014/06/30	06:47:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2014/06/30	06:47:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2014/06/30	06:47:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2014/06/30	06:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2014/06/30	07:23:30.0	XRT_Custom_430_OG [0x1ae]		
2014/06/30	07:24:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2014/06/30	08:26:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2014/06/30	08:26:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2014/06/30	08:26:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2014/06/30	08:29:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2014/06/30	09:02:00.0	XRT_Custom_430_OG [0x1ae]		
2014/06/30	09:03:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2014/06/30	10:08:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2014/06/30	10:08:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2014/06/30	10:08:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2014/06/30	10:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2014/06/30	10:40:30.0	XRT_Custom_430_OG [0x1ae]		
2014/06/30	10:41:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2014/06/30	11:48:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2014/06/30	11:48:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2014/06/30	11:48:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2014/06/30	11:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2014/06/30	12:19:00.0	XRT_Custom_430_OG [0x1ae]		
2014/06/30	12:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2014/06/30	13:27:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2014/06/30	13:27:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2014/06/30	13:27:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2014/06/30	13:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2014/06/30	13:57:30.0	XRT_Custom_430_OG [0x1ae]		
2014/06/30	13:58:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2014/06/30	13:59:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2014/06/30	13:59:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2014/06/30	13:59:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2014/06/30	14:01:30.0	XRT_Custom_430_OG [0x1ae]		
2014/06/30	14:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2014/06/30	14:02:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2014/06/30	15:05:30.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2014/06/30	15:05:32.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2014/06/30	15:05:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8
2014/06/30	15:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]		
		MDP_XRT_PREFLR_STOP	1	07-F0 e9
2014/06/30	15:53:30.0	XRT_Custom_430_OG [0x1ae]		
2014/06/30	15:54:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]		
		MDP_XRT_CTRL_AUTO	1	07-F0 c0
2014/06/30	16:44:00.0	XRT_CTRL_MANU_400_OG [0x190]		
		MDP_XRT_CTRL_MANU	1	07-F0 c1
2014/06/30	16:44:02.0	XRT_FLD_RESET_415_OG [0x19f]		
		MDP_XRT_FLD_RESET	1	07-F0 da
2014/06/30	16:44:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]		
		MDP_XRT_PREFLR_STRT	1	07-F0 e8

Jun 28, 14 13:28

XRT_OGLIST_0374.chk

Page 7/9

2014/06/30	16:47:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/30	17:30:00.0	XRT_Custom_430_OG [0x1ae]							
2014/06/30	17:31:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/30	18:01:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/30	18:01:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/06/30	18:01:30.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/06/30	18:01:46.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/06/30	18:04:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/06/30	18:04:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/06/30	18:04:28.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2014/06/30	18:04:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/30	18:11:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/30	18:11:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/06/30	18:11:30.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	00 09 e3 52 d0				
2014/06/30	18:11:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/06/30	18:11:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/06/30	18:11:50.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/06/30	18:11:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/06/30	18:11:54.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/30	18:14:26.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2014/06/30	18:14:28.0	XRT_FL_PROG_SET_407_OG [0x197]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2014/06/30	18:14:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/30	18:22:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/30	18:22:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/30	18:22:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/30	18:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/30	19:07:00.0	XRT_Custom_430_OG [0x1ae]							
2014/06/30	19:08:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/30	20:01:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/30	20:01:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/30	20:01:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/30	20:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/30	20:44:00.0	XRT_Custom_430_OG [0x1ae]							
2014/06/30	20:45:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/30	21:39:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/30	21:39:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/30	21:39:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/30	21:42:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/30	22:20:00.0	XRT_Custom_430_OG [0x1ae]							
2014/06/30	22:21:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/06/30	23:17:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/06/30	23:17:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/06/30	23:17:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/06/30	23:20:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/06/30	23:48:00.0	XRT_Custom_430_OG [0x1ae]							
2014/06/30	23:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/01	00:56:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/01	00:56:02.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/01	00:56:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				

Jun 28, 14 13:28

XRT_OGLIST_0374.chk

Page 8/9

2014/07/01	00:59:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/07/01	00:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/01	00:59:56.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/07/01	01:00:00.5	AOCS_Orе-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2014/07/01	01:00:16.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/07/01	01:00:18.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/07/01	01:00:20.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/07/01	01:00:22.0	XRT_ARS_DIS_439_OG [0x1b7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/07/01	01:40:24.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/01	01:40:26.0	XRT_QT_PROG_SET_442_OG [0x1ba]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03				
2014/07/01	01:40:28.0	XRT_FL_PROG_SET_407_OG [0x197]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				
2014/07/01	01:40:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/01	02:34:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/01	02:34:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/01	02:34:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/07/01	02:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/07/01	03:05:00.0	XRT_Custom_430_OG [0x1ae]							
2014/07/01	03:06:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/01	04:03:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/01	04:03:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/01	04:03:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/07/01	04:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/07/01	04:43:30.0	XRT_Custom_430_OG [0x1ae]							
2014/07/01	04:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/01	05:43:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/01	05:43:32.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/01	05:43:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/07/01	05:46:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/07/01	06:21:30.0	XRT_Custom_430_OG [0x1ae]							
2014/07/01	06:22:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/01	06:27:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/01	06:27:26.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/07/01	06:27:30.0	AOCS_Orе-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/07/01	06:27:46.0	XRT_FLD_DIS_434_OG [0x1b2]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/07/01	06:30:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/07/01	06:30:26.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/07/01	06:30:28.0	XRT_QT_PROG_SET_444_OG [0x1bc]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0e				
2014/07/01	06:30:30.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/01	06:37:24.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/01	06:37:26.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/07/01	06:37:30.0	AOCS_Orе-point_Start_1_OG [0x097]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2014/07/01	06:37:46.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/07/01	06:37:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/07/01	06:37:50.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/07/01	06:37:52.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/07/01	06:37:54.0	XRT_FLD_RESET_431_OG [0x1af]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/01	06:40:26.0	XRT_QT_PROG_SET_443_OG [0x1bb]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2014/07/01	06:40:28.0	XRT_FL_PROG_SET_407_OG [0x197]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 08				

Jun 28, 14 13:28

XRT_OGLIST_0374.chk

Page 9/9

2014/07/01	06:40:30.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/01	07:24:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/01	07:24:02.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/07/01	07:24:04.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/07/01	07:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/07/01	08:00:00.0	XRT_Custom_430_OG [0x1ae]			
2014/07/01	08:01:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/01	09:00:00.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/01	09:03:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/01	09:03:32.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/07/01	09:03:34.0	XRT_PREFLR_STRT_418_OG [0x1a2]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/07/01	09:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/07/01	09:39:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00