

# XRT Timeline to be uploaded on 2014/07/24

Period: 2014/07/24 10:51:00 - 2014/07/29 10:36:00

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## Normal mode

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XOB #1A1D: CME watch - 4x4 - AEC 2 - Al-poly - G-band (2x2,8ms) - Leak (33ms) - 80s cad												
Term	Pointing (x, y)		Comment									
07/24 11:04:00 - 07/24 11:18:24	Fixed (	-7.0, -993.0)	# OP start + 10min, HOP79 N-S scan (1/20)									
07/24 11:21:30 - 07/24 11:37:00	Fixed (	-7.0, -893.0)	2/20									
07/24 12:09:30 - 07/24 12:16:24	Fixed (	-7.0, -793.0)	3/20									
07/24 12:19:30 - 07/24 12:26:24	Fixed (	-7.0, -693.0)	4/20									
07/24 12:29:30 - 07/24 12:36:24	Fixed (	-7.0, -593.0)	5/20									
07/24 12:39:30 - 07/24 12:46:24	Fixed (	-7.0, -493.0)	6/20									
07/24 12:49:30 - 07/24 12:56:24	Fixed (	-7.0, -393.0)	7/20									
07/24 12:59:30 - 07/24 13:15:30	Fixed (	-7.0, -293.0)	8/20									
07/24 13:48:00 - 07/24 13:54:54	Fixed (	-7.0, -193.0)	9/20									
07/24 13:58:00 - 07/24 14:04:54	Fixed (	-7.0, -93.0)	10/20									
07/24 14:08:00 - 07/24 14:14:54	Fixed (	-7.0, -3.0)	11/20									
07/24 14:18:00 - 07/24 14:24:54	Fixed (	-7.0, 97.0)	12/20									
07/24 14:28:00 - 07/24 14:34:54	Fixed (	-7.0, 197.0)	13/20									
07/24 14:38:00 - 07/24 14:54:00	Fixed (	-7.0, 297.0)	14/20									
07/24 15:46:00 - 07/24 15:52:54	Fixed (	-7.0, 397.0)	15/20									
07/24 15:56:00 - 07/24 16:02:54	Fixed (	-7.0, 497.0)	16/20									
07/24 16:06:00 - 07/24 16:12:54	Fixed (	-7.0, 597.0)	17/20									
07/24 16:16:00 - 07/24 16:32:30	Fixed (	-7.0, 697.0)	18/20									
07/24 17:23:00 - 07/24 17:29:54	Fixed (	-7.0, 797.0)	19/20									
07/24 17:33:00 - 07/24 17:56:54	Fixed (	-7.0, 897.0)	20/20									

**PROG= 04 Inf.-time(s)**

- Subr= 1 1-time(s) 80.0sec
  - Seqn= 78 1-time(s) 4.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 2 0 2.0sec
  - Subr= 2 1-time(s) 2.0sec
    - Seqn= 6 1-time(s) 2.0sec
      - Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
      - Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #19DA: Synoptic Q95 2x2 - Al/mesh(5/128/723) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/362/1443) + Thin												
Term	Pointing (x, y)		Comment									
07/24 18:00:00 - 07/24 18:06:54	Fixed (	0.0, 0.0)	synoptic, shifted -3.0 min									
07/25 04:33:00 - 07/25 04:39:54	Fixed (	0.0, 0.0)	synoptic, shifted manually									
07/25 17:59:30 - 07/25 18:06:24	Fixed (	0.0, 0.0)	synoptic, shifted -3.5 min									
07/26 04:03:00 - 07/26 04:09:54	Fixed (	0.0, 0.0)	synoptic, shifted manually									

**PROG= 15 1-time(s)**

- Subr= 1 1-time(s) 12.0sec
  - Seqn= 84 1-time(s) 4.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 707ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 5 1-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
  - Seqn= 86 1-time(s) 4.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 95 1-time(s) 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 63ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - thin-Be/Open thin-Be/Open close Safe Norm 2.83s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 6 1-time(s) 2.0sec
    - Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
    - Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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XOB #19E4: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)												
Term	Pointing (x, y)		Comment									
07/24 18:56:30 - 07/25 01:59:54	Track (	-727.5, 11.9) @ 07/24 18:07:00	# Track AR at E-limb									
07/25 06:09:30 - 07/25 17:09:00	Track (	-653.7, 4.2) @ 07/25 06:00:00	# Track AR at E-limb									
07/25 18:09:30 - 07/26 01:21:00	Track (	-569.5, -2.9) @ 07/25 18:06:30	# Cont,									

**PROG= 07 Inf.-time(s)**

- Subr= 1 1-time(s) 2.0sec
  - Seqn= 8 2-time(s) 2.0sec
    - Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec

Subr= 2	2-time(s)	2.0sec										
└─ Seqn= 24	1-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 42	4-time(s)	2.0sec										
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 62	10-time(s)	180.0sec										
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1911: HOP173 prominence Al/mesh, Ti/poly, G-band(45ms)-DPCM 1x1-512x512-FOV -AEC2-5min cadence with G-band Test</b>												
Term	Pointing (x, y)		Comment									
07/25 02:03:00 - 07/25 03:51:30	Fixed ( 672.0, -675.0)		HOP173 prominence at NE limb									
<b>PROG= 06 Inf.-time(s)</b>												
└─ Subr= 3	1-time(s)	2.0sec										
└─ Seqn= 80	1-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	512x512 (1024, 1024)	DPCM	0	0	2.0sec
└─ Subr= 2	1-time(s)	2.0sec										
└─ Seqn= 13	1-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	512x512 (1024, 1024)	Q=95	0	0	2.0sec
└─ Subr= 1	12-time(s)	300.0sec										
└─ Seqn= 32	1-time(s)	2.0sec										
└─ Open/Al-mesh	Open/Ti-poly	close	Safe	Norm	4.00s	Obs	1x1	512x512 (1024, 1024)	DPCM	2	0	2.0sec
└─ Seqn= 73	1-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1024, 1024)	DPCM	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #19E0: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)</b>												
Term	Pointing (x, y)		Comment									
07/25 04:43:00 - 07/25 05:32:00	Track ( -662.4, 5.1) @ 07/25 04:40:00		# HOP173 AR at E-limb									
07/26 02:03:00 - 07/26 03:59:54	Track ( -192.7, -339.6) @ 07/26 02:00:00		# HOP173 AR at SE									
07/26 04:13:00 - 07/26 05:59:54	Track ( -493.5, -8.1) @ 07/26 04:10:00		# HOP173 AR at E-limb									
<b>PROG= 19 Inf.-time(s)</b>												
└─ Subr= 1	1-time(s)	2.0sec										
└─ Seqn= 8	2-time(s)	2.0sec										
└─ Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2	2-time(s)	2.0sec										
└─ Seqn= 24	1-time(s)	2.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 42	4-time(s)	2.0sec										
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─ Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Seqn= 62	36-time(s)	50.0sec										
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─ thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─ Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #19A6: CME watch - 4x4 - AEC 2 - Ti/poly- 60s cad</b>												
Term	Pointing (x, y)		Comment									
07/26 06:03:00 - 07/26 09:20:00	Fixed ( 0.0, 0.0)		SOT flat-field obs									
<b>PROG= 09 Inf.-time(s)</b>												
└─ Subr= 1	15-time(s)	60.0sec										
└─ Seqn= 44	1-time(s)	4.0sec										
└─ Open/Ti-poly	Open/thick-Al	close	Safe	Norm	125ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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**Flare mode**

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<b>XOB #19EC: Flare - high cad multifilter (Be/thin, Be/med, Al/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gband (45ms)</b>												
Term	Pointing (x, y)		Comment									
07/24 11:04:00 - 07/24 11:18:24	Fixed ( -7.0, -993.0)		# OP start + 10min, HOP79 N-S scan (1/20)									
07/24 11:21:30 - 07/24 11:37:00	Fixed ( -7.0, -893.0)		2/20									
07/24 12:09:30 - 07/24 12:16:24	Fixed ( -7.0, -793.0)		3/20									

07/24 12:19:30 - 07/24 12:26:24	Fixed ( -7.0, -693.0)	4/20
07/24 12:29:30 - 07/24 12:36:24	Fixed ( -7.0, -593.0)	5/20
07/24 12:39:30 - 07/24 12:46:24	Fixed ( -7.0, -493.0)	6/20
07/24 12:49:30 - 07/24 12:56:24	Fixed ( -7.0, -393.0)	7/20
07/24 12:59:30 - 07/24 13:15:30	Fixed ( -7.0, -293.0)	8/20
07/24 13:48:00 - 07/24 13:54:54	Fixed ( -7.0, -193.0)	9/20
07/24 13:58:00 - 07/24 14:04:54	Fixed ( -7.0, -93.0)	10/20
07/24 14:08:00 - 07/24 14:14:54	Fixed ( -7.0, -3.0)	11/20
07/24 14:18:00 - 07/24 14:24:54	Fixed ( -7.0, 97.0)	12/20
07/24 14:28:00 - 07/24 14:34:54	Fixed ( -7.0, 197.0)	13/20
07/24 14:38:00 - 07/24 14:54:00	Fixed ( -7.0, 297.0)	14/20
07/24 15:46:00 - 07/24 15:52:54	Fixed ( -7.0, 397.0)	15/20
07/24 15:56:00 - 07/24 16:02:54	Fixed ( -7.0, 497.0)	16/20
07/24 16:06:00 - 07/24 16:12:54	Fixed ( -7.0, 597.0)	17/20
07/24 16:16:00 - 07/24 16:32:30	Fixed ( -7.0, 697.0)	18/20
07/24 17:23:00 - 07/24 17:29:54	Fixed ( -7.0, 797.0)	19/20
07/24 17:33:00 - 07/24 17:56:54	Fixed ( -7.0, 897.0)	20/20
07/24 18:56:30 - 07/25 01:59:54	Track ( -727.5, 11.9) @ 07/24 18:07:00	# Track AR at E-limb
07/25 02:03:00 - 07/25 03:51:30	Fixed ( 672.0, -675.0)	HOP173 prominence at NE limb
07/25 04:43:00 - 07/25 05:32:00	Track ( -662.4, 5.1) @ 07/25 04:40:00	# HOP173 AR at E-limb
07/25 06:09:30 - 07/25 17:09:00	Track ( -653.7, 4.2) @ 07/25 06:00:00	# Track AR at E-limb
07/25 18:09:30 - 07/26 01:21:00	Track ( -569.5, -2.9) @ 07/25 18:06:30	# Cont,
07/26 02:03:00 - 07/26 03:59:54	Track ( -192.7, -339.6) @ 07/26 02:00:00	# HOP173 AR at SE
07/26 04:13:00 - 07/26 05:59:54	Track ( -493.5, -8.1) @ 07/26 04:10:00	# HOP173 AR at E-limb

**PROG= 01 30-time(s)**

<b>Subr= 1 30-time(s) 2.0sec</b>
└─ <b>Seqn= 26 1-time(s) 4.0sec</b>
└─ thin-Be/Open med-Be/Open close Safe Norm 8ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ med-Be/Open Open/thick-Al close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ Open/thick-Al Open/thick-Be close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>
└─ <b>Seqn= 10 1-time(s) 2.0sec</b>
└─ med-Al/Open med-Al/thick-Al close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ <b>Seqn= 11 1-time(s) 2.0sec</b>
└─ Al-poly/Open Al-poly/thick-Al close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
└─ <b>Seqn= 15 1-time(s) 2.0sec</b>
└─ Open/G-band Open/G-band open Safe Norm 44ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

**XOB #199C: Flare Standard Obs. with eruptions mode-A (FW1=Open and VLS-Closed)**

Term	Pointing (x, y)	Comment
07/26 06:03:00 - 07/26 09:20:00	Fixed ( 0.0, 0.0)	SOT flat-field obs

**PROG= 17 1-time(s)**

<b>Subr= 1 30-time(s) 20.0sec</b>
└─ <b>Seqn= 55 1-time(s) 2.0sec</b>
└─ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ <b>Seqn= 9 1-time(s) 2.0sec</b>
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>
└─ <b>Seqn= 39 1-time(s) 2.0sec</b>
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 384x384 (1024, 1024) DPCM 0 0 2.0sec
└─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
<b>Subr= 3 30-time(s) 60.0sec</b>
└─ <b>Seqn= 55 1-time(s) 2.0sec</b>
└─ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ <b>Seqn= 70 1-time(s) 2.0sec</b>
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>
└─ <b>Seqn= 39 1-time(s) 2.0sec</b>
└─ Open/G-band Open/G-band open Safe Norm 63ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 384x384 (1024, 1024) DPCM 0 0 2.0sec
└─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
└─ Open/thick-Al Open/thick-Al close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
<b>Subr= 3 30-time(s) 60.0sec</b>
└─ <b>Seqn= 55 1-time(s) 2.0sec</b>
└─ Open/thick-Al Open/thick-Al close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ Open/thick-Be Open/thick-Be close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
└─ <b>Seqn= 70 1-time(s) 2.0sec</b>
└─ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
└─ Open/Ti-poly Open/Ti-poly close Safe Norm 1.00s Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec

<b>Subr= 2</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
<b>Seqn= 39</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	384x384 (1024, 1024)	DPCM	0	0	2.0sec
Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-AI	Open/thick-AI	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
<b>Subr= 3</b>		<b>30-time(s)</b>		<b>60.0sec</b>								
<b>Seqn= 55</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
Open/thick-AI	Open/thick-AI	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 70</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
Open/AI-mesh	Open/AI-mesh	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Subr= 4</b>		<b>24-time(s)</b>		<b>600.0sec</b>								
<b>Seqn= 1</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
Open/AI-mesh	Open/thick-AI	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/Ti-poly	Open/thick-Be	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	1	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	63ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/AI-mesh	Open/AI-mesh	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \*

### Active Region Search

\* \* \* \* \*

NOT USED

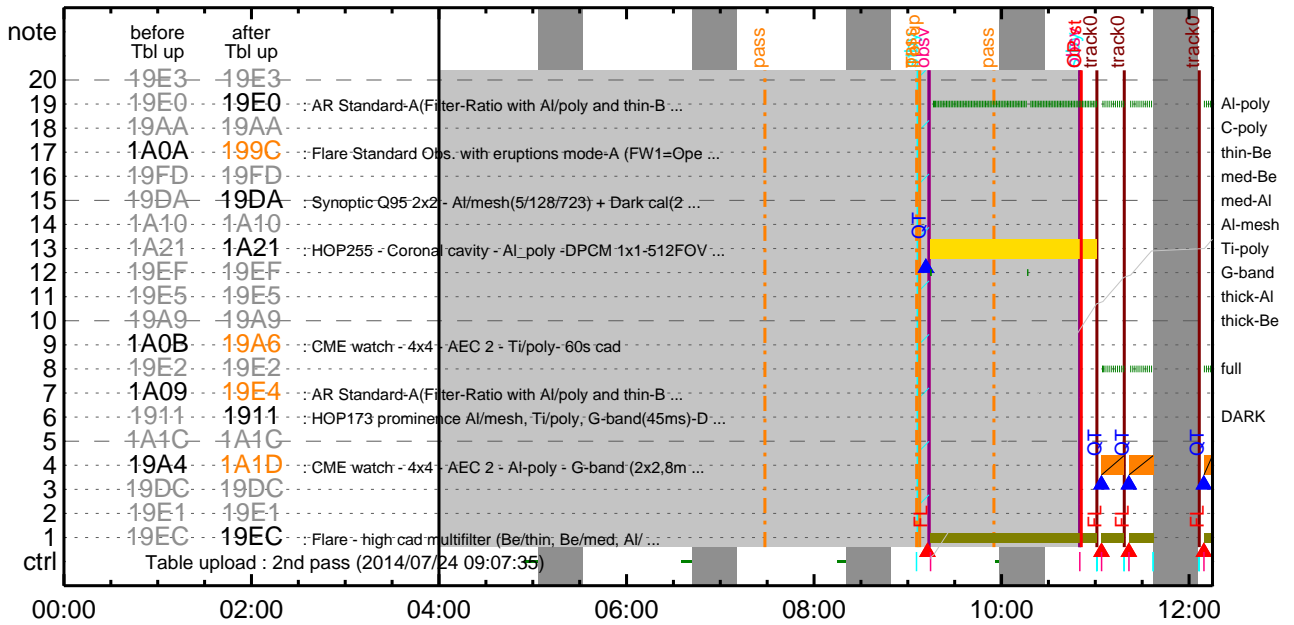
\* \* \* \* \*

### Flare Detection

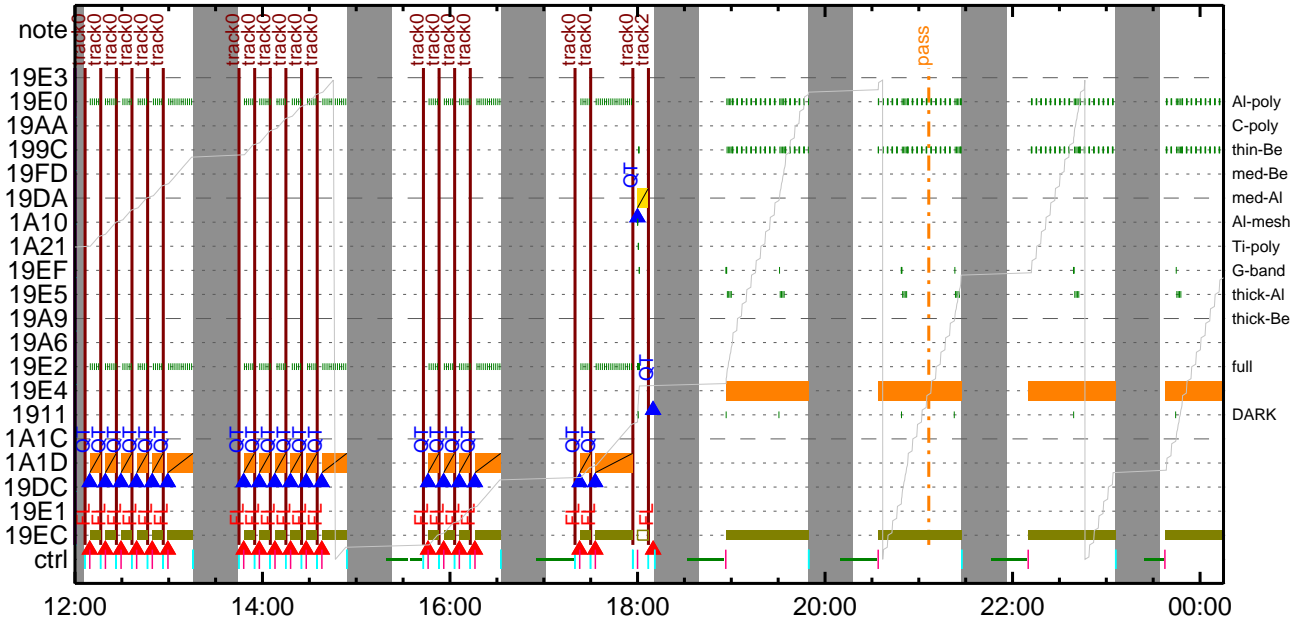
\* \* \* \* \*

<b>FLD Patrol</b>												
Term		Pointing (x, y)				Comment						
07/24 18:07:18 - 07/25 04:30:18	Track (-727.5, 11.9) @ 07/24 18:07:00	# Track AR at E-limb										
07/25 04:40:18 - 07/25 17:56:48	Track (-662.4, 5.1) @ 07/25 04:40:00	# HOP173 AR at E-limb										
07/25 18:06:48 - 07/26 04:00:18	Track (-569.5, -2.9) @ 07/25 18:06:30	# Cont,										
Open/Ti-poly	Open/thick-AI	close	Safe	Norm	8ms	Obs	8x8		Q=50		30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

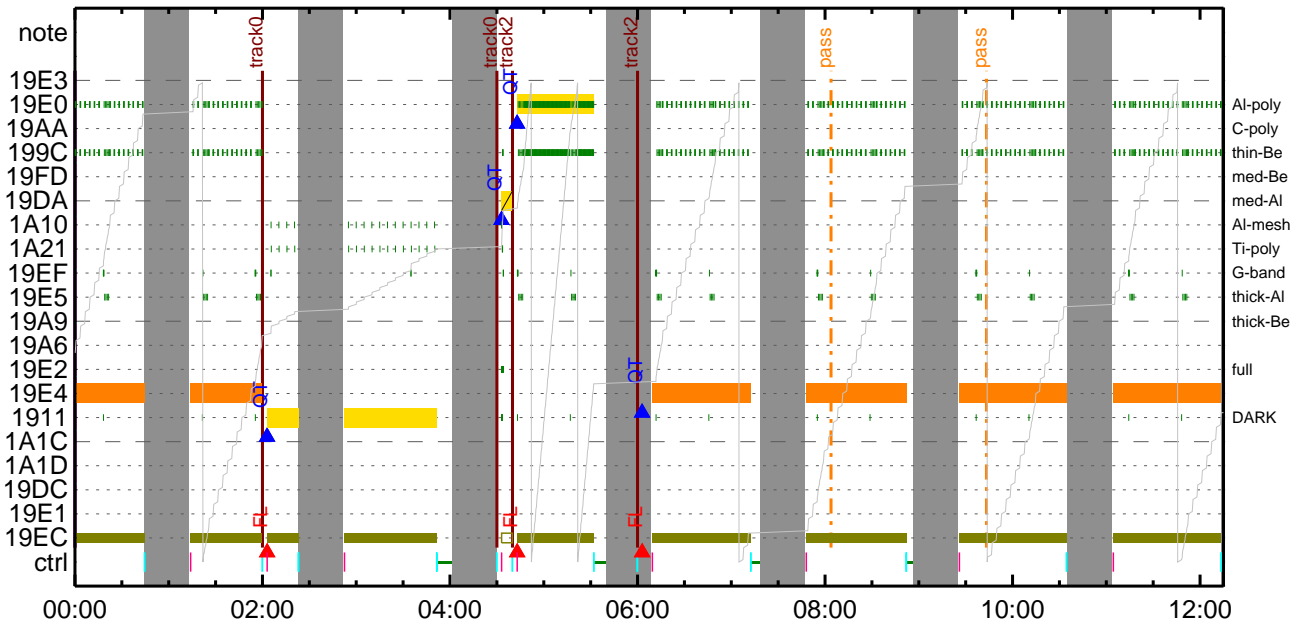
### CMDI #0428 2014/07/24



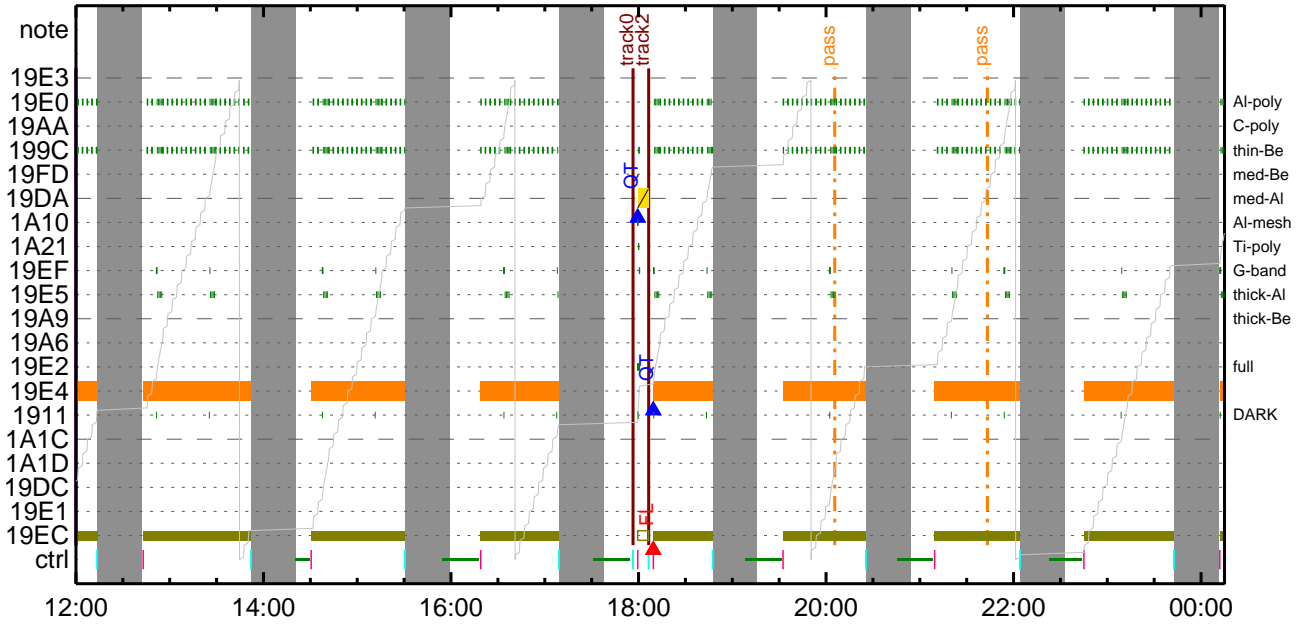
### CMDI #0428 2014/07/24



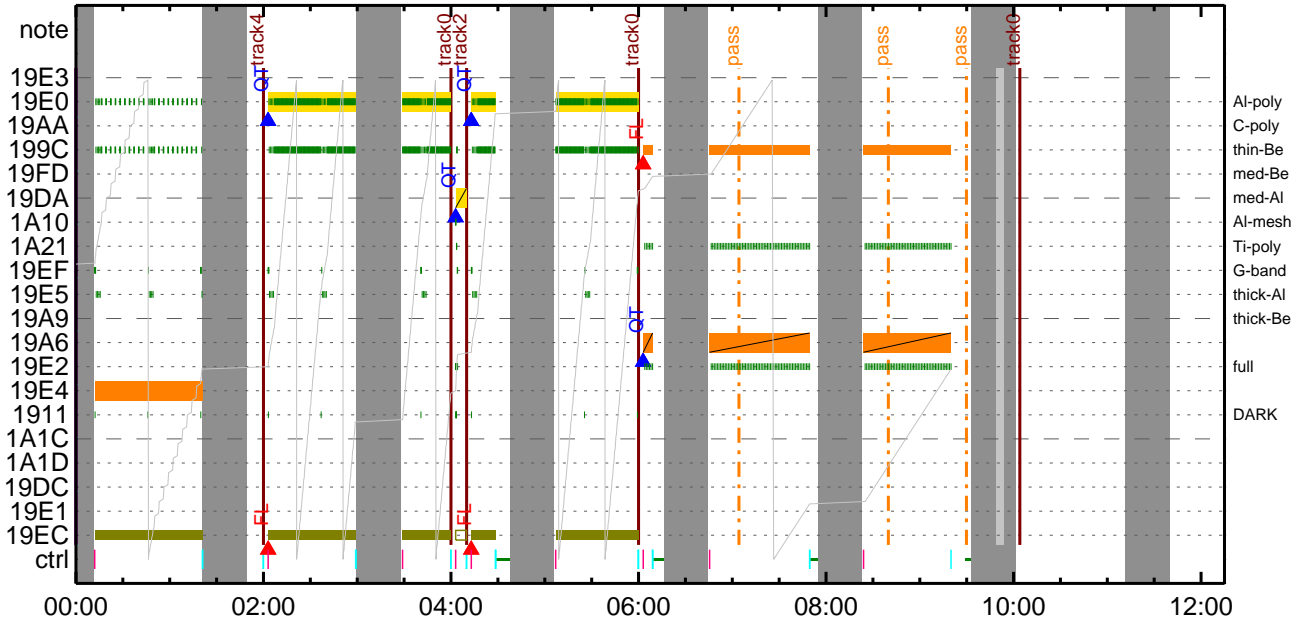
### CMDI #0428 2014/07/25



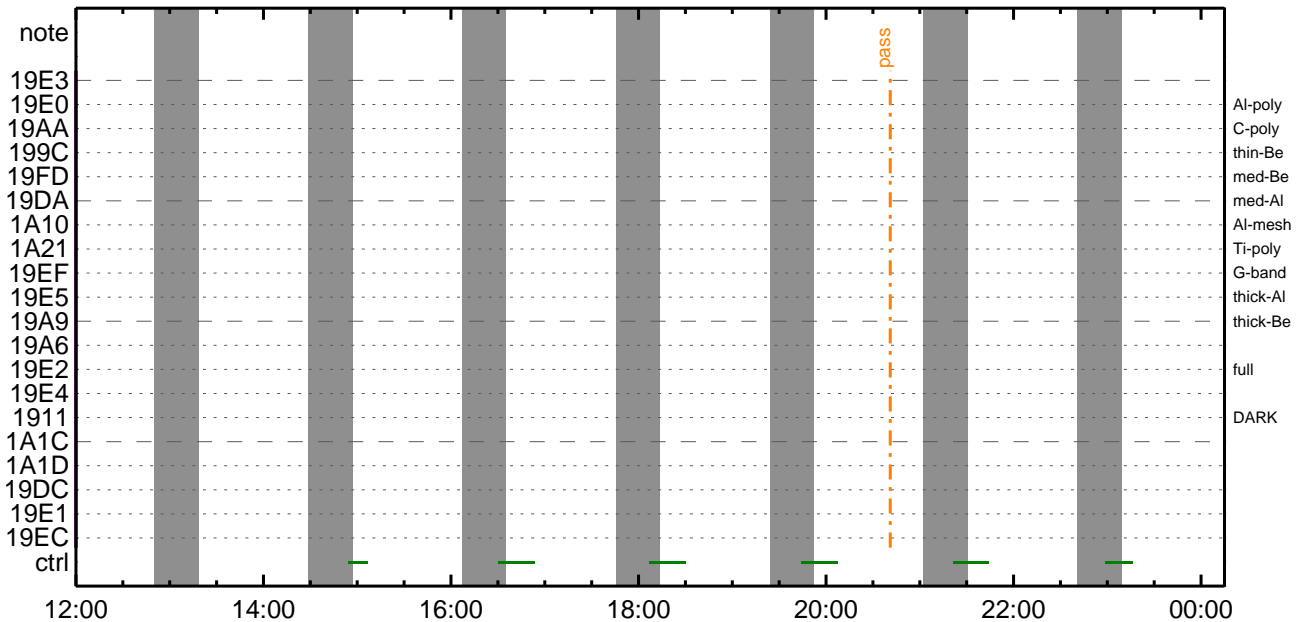
CMDI #0428 2014/07/25



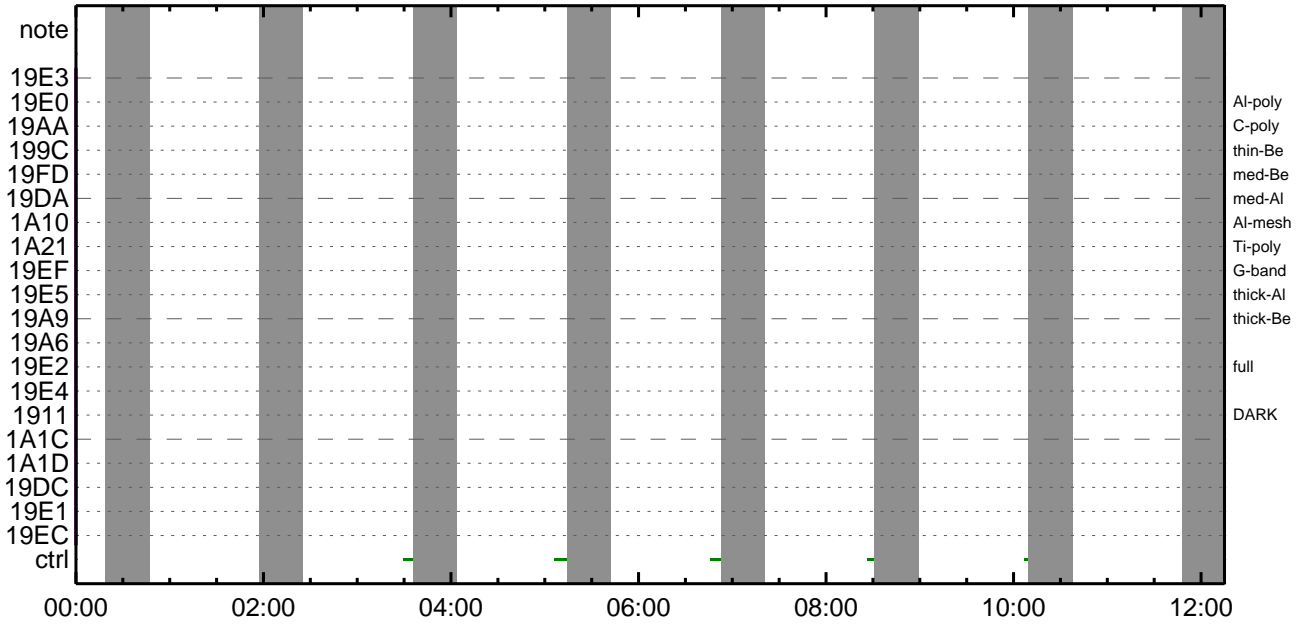
CMDI #0428 2014/07/26



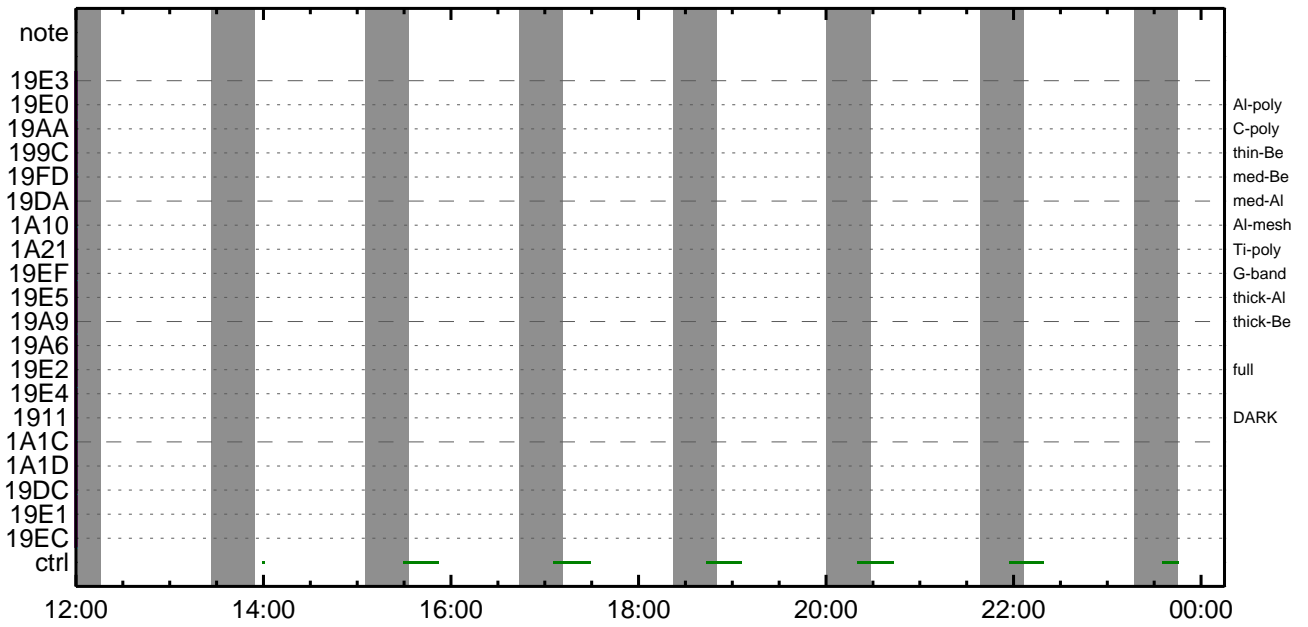
CMDI #0428 2014/07/26



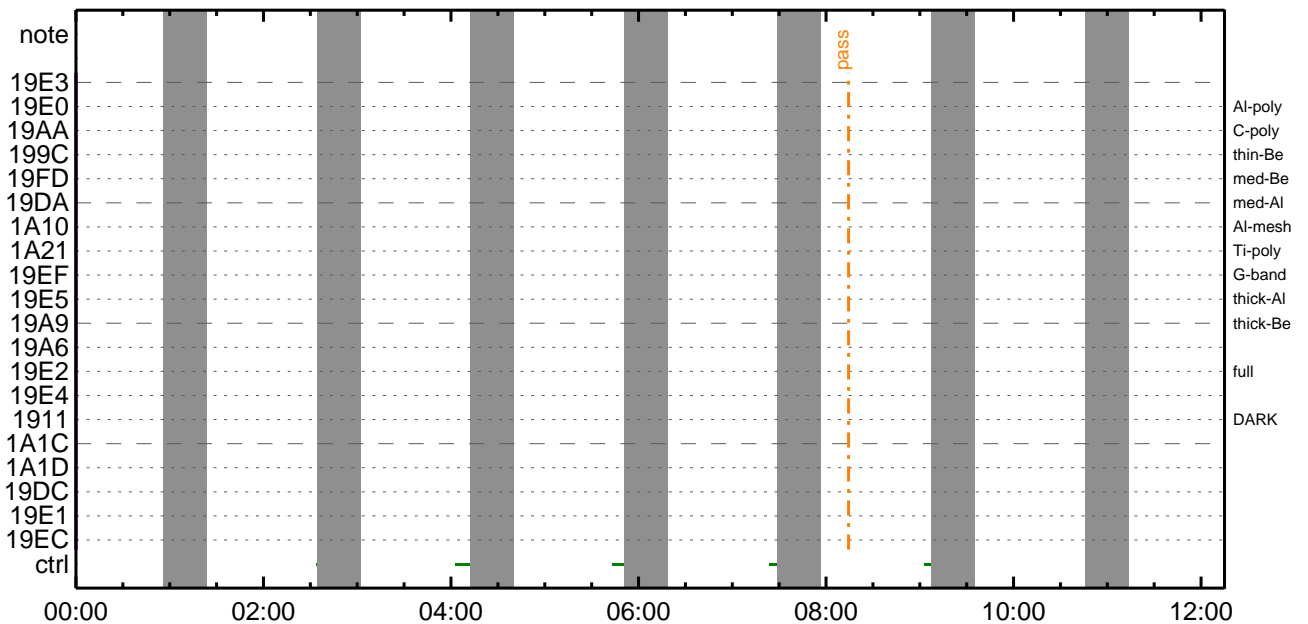
CMDI #0428 2014/07/27



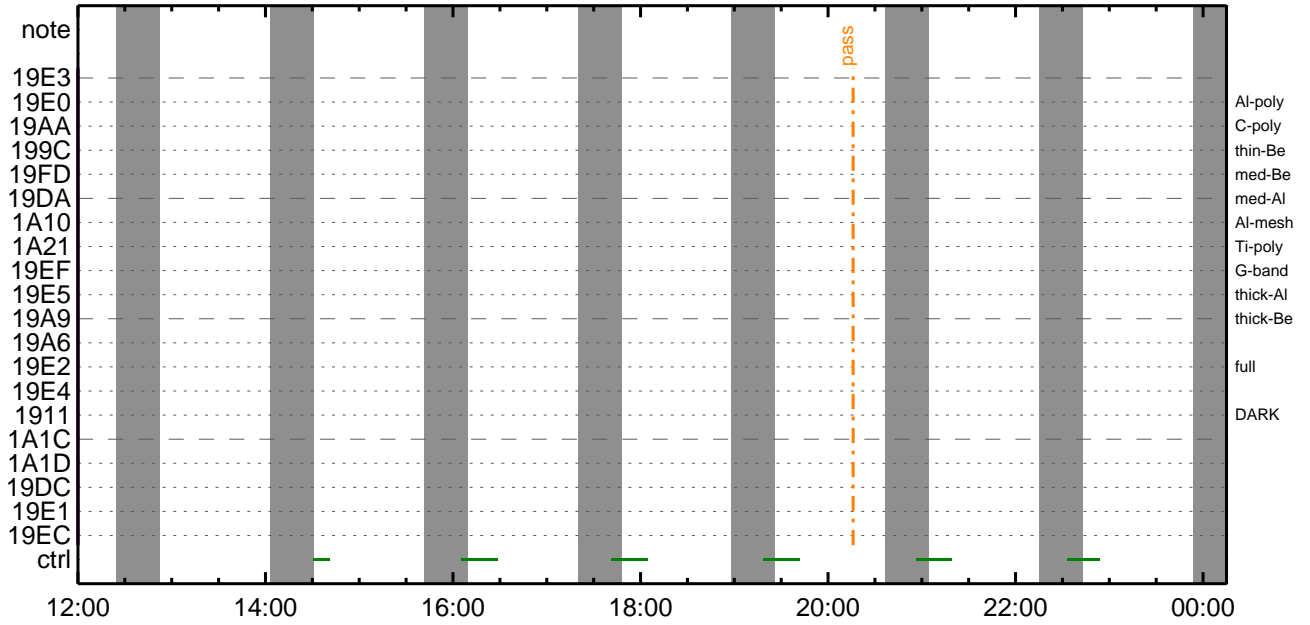
CMDI #0428 2014/07/27



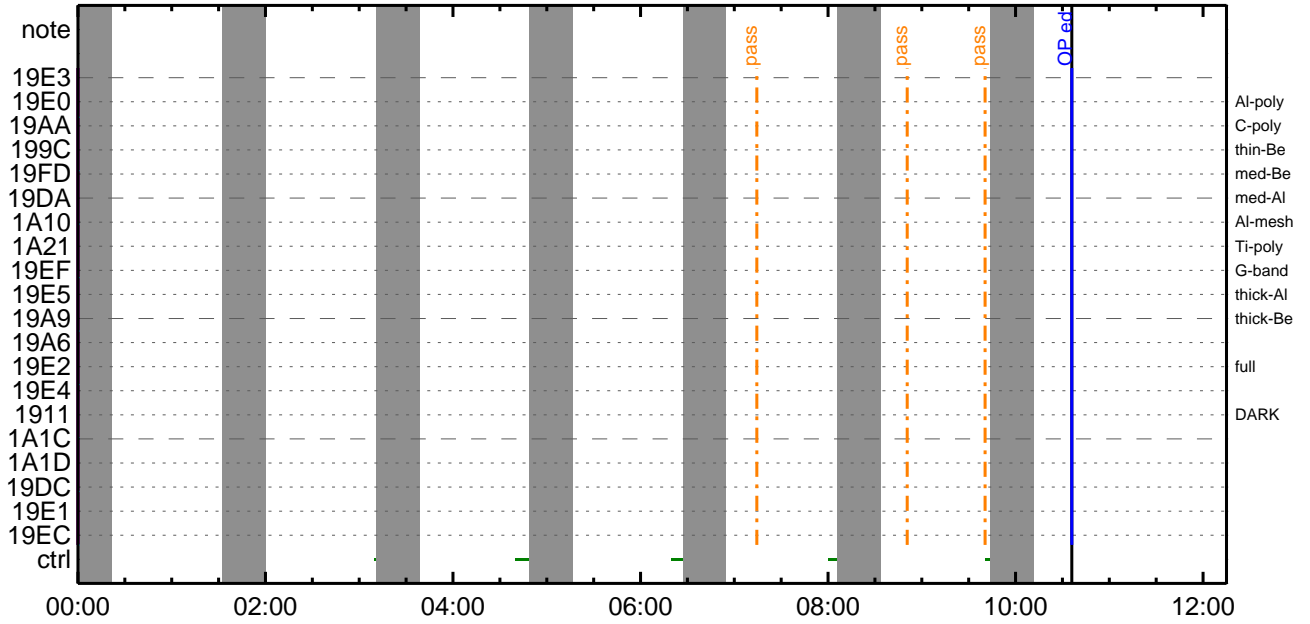
CMDI #0428 2014/07/28



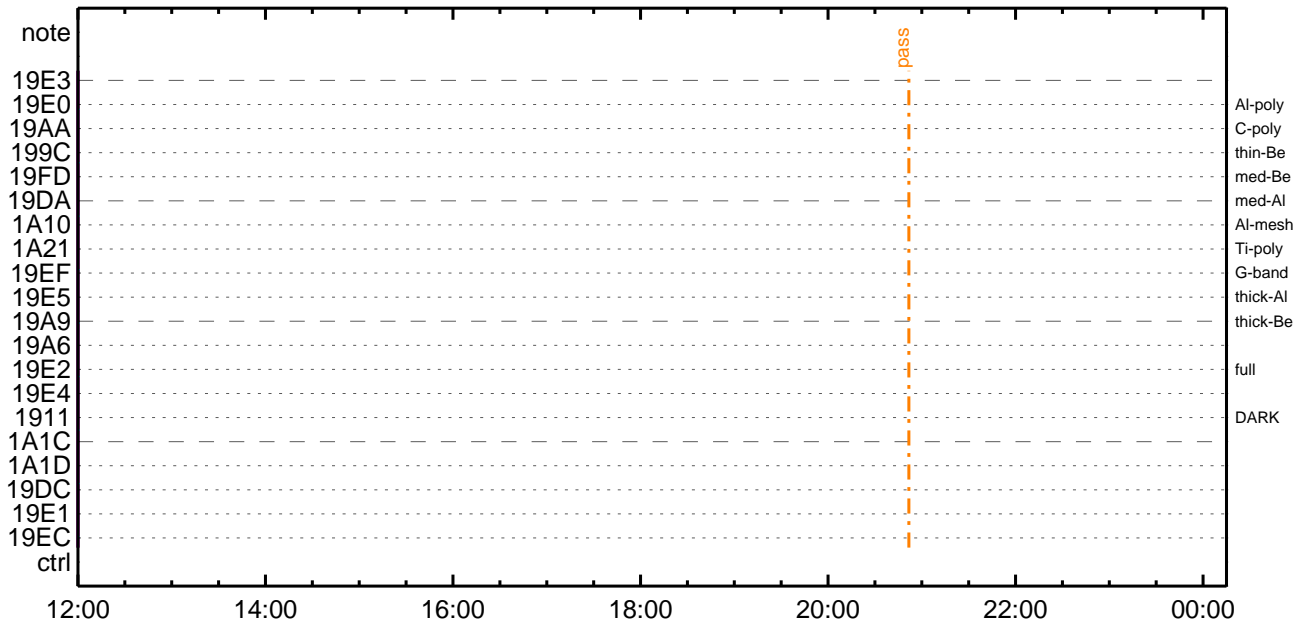
### CMDI #0428 2014/07/28



### CMDI #0428 2014/07/29



### CMDI #0428 2014/07/29







0096 C.  
0097 C.  
0098 C. \*\*\*\*\*  
0099 C. OP/OGY1;Y4E;YAYOYx  
0100 C. \*\*\*\*\*  
0101 C.  
0102 C. ;aOP/OGY1;Y4E;a  
0103 S. OP op-527:OP  
0104 (  
0105 S. OG og-527:OG  
0106 (  
0107 C.  
0108 C. ;aNMOG&OPf^°eYAYOYx;a  
0109 C. NMOG(0x200000-0x207FFF;s 32 kbyte)  
0110 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0111 BC (20 00 7f 01 02)  
0112 C. c{[HK1\_DMP\_TOP\_ADRS\_1] EQ 40  
0113 C. c{[HK1\_DMP\_TOP\_ADRS\_0] EQ 0  
0114 C. c{[HK1\_DMP\_BLOCK\_NUM] EQ 127  
0115 C. c{[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0116 C. c{[HK1\_DMA\_DMP\_PIM] EQ DHU  
0117 +. DC 01-22 DHU\_MODE\_CHNG  
0118 BC (07 0b f8)  
0119 C. c{[HK1\_PKT\_FORM\_NO] EQ 7  
0120 C. c{[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0121 C. c{[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0122 C. c{[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0123 C. c{[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0124 C. YAYOYxY2^a^I»oD^IcS  
0125 C. c{[HK1\_DMP\_CHK\_FLG] EQ NON  
0126 C. RAM ID=NMOG^I%E^1c.ê^2^IOK^oD^3^IcS  
0127 C.  
0128 C. NMOG(0x208000-0x20FFFF;s 32 kbyte)  
0129 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0130 BC (20 80 7f 01 02)  
0131 C. c{[HK1\_DMP\_TOP\_ADRS\_1] EQ 41  
0132 C. c{[HK1\_DMP\_TOP\_ADRS\_0] EQ 0  
0133 C. c{[HK1\_DMP\_BLOCK\_NUM] EQ 127  
0134 C. c{[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0135 C. c{[HK1\_DMA\_DMP\_PIM] EQ DHU  
0136 +. DC 01-22 DHU\_MODE\_CHNG  
0137 BC (07 0b f8)  
0138 C. c{[HK1\_PKT\_FORM\_NO] EQ 7  
0139 C. c{[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0140 C. c{[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0141 C. c{[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0142 C. c{[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0143 C. YAYOYxY2^a^I»oD^IcS  
0144 C. c{[HK1\_DMP\_CHK\_FLG] EQ NON  
0145 C. RAM ID=NMOG^I%E^1c.ê^2^IOK^oD^3^IcS  
0146 C.  
0147 C. NMOG(0x210000-0x2100FF;s 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)  
0148 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0149 BC (21 00 41 01 02)  
0150 C. c{[HK1\_DMP\_TOP\_ADRS\_1] EQ 42  
0151 C. c{[HK1\_DMP\_TOP\_ADRS\_0] EQ 0  
0152 C. c{[HK1\_DMP\_BLOCK\_NUM] EQ 65  
0153 C. c{[HK1\_DMP\_REPEAT\_NUM] EQ 0  
0154 C. c{[HK1\_DMA\_DMP\_PIM] EQ DHU  
0155 +. DC 01-22 DHU\_MODE\_CHNG  
0156 BC (07 0b f8)  
0157 C. c{[HK1\_PKT\_FORM\_NO] EQ 7  
0158 C. c{[HK1\_PKT\_GEN\_TIME] EQ 0.25 s  
0159 C. c{[HK1\_S\_TLM\_BIT\_RATE] EQ 32k  
0160 C. c{[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0161 C. c{[HK1\_DMP\_CHK\_FLG] EQ EXEC  
0162 C. YAYOYxY2^a^I»oD^IcS  
0163 C. c{[HK1\_DMP\_CHK\_FLG] EQ NON  
0164 C. RAM ID=NMOG,RAM ID=OP^I%E^1c.ê^2^IOK^oD^3^IcS  
0165 C.  
0166 C. \*\*\*\*\* oE^2^oI%Ã^¶A^oEE^-oA^÷;L@ (%ãµ-YAYOYxY4E^½c^oD^A^O^A^æ^oC^½^a^o^oE%i^1c^oC^oâ) \*\*\*\*\*  
0167 C. DHUYâ;Y4E;E^½Y^½;Y1;Y4E;E^oD^I^a^1  
0168 +. DC 01-22 DHU\_MODE\_CHNG  
0169 BC (02 0a f8)  
0170 C. c{[HK1\_PKT\_FORM\_NO] EQ 2  
0171 C. c{[HK1\_PKT\_GEN\_TIME] EQ 0.5S  
0172 C. c{[HK1\_S\_TLM\_BIT\_RATE] EQ 32K  
0173 C. c{[HK1\_X\_TLM\_BIT\_RATE] EQ 4M  
0174 C.  
0175 C. \*\*\*\*\*  
0176 C. TI-CMD SET (OPOG STOP/COPY/START)  
0177 C. \*\*\*\*\*  
0178 C.  
0179 C. NOTICE ;s OPOG UPLOAD^oA^÷;L@NG^u^I%1^1c;ç^oE^2^oI^TI-CMD^A^÷;L@^oI%1^1O^o^oE^oD^3^oE;f  
0180 C. ^p^o;ç^SET^oE^DUMP^oI^E^±^oI^N^Y^1^oC^1^O^o|^o^3^oE;f  
0181 C.  
0182 C. TIY^3^Yp^Y6^Y6^oD^A^D^I^L^ (UT)  
0183 +. TI 2014-07-24 10:46:00.0  
0184 DC 01-B3 DHU\_OP\_STOP  
0185 C. c{[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP  
0186 C.  
0187 +. TI 2014-07-24 10:46:01.0  
0188 DC 01-B4 DHU\_OP\_COPY  
0189 C. c{[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP  
0190 C.  
0191 +. TI 2014-07-24 10:46:01.0  
0192 DC 01-B5 DHU\_OPOG\_COPY  
0193 C. c{[HK1\_TI\_CMD\_NUM] EQ 1COUNTUP

0194 C.  
0195 +. TI 2014-07-24 10:50:59.5  
0196 DC 01-B2 DHU\_OP\_START  
0197 C.            çç[HK1\_TI\_CMD\_NUM]   EQ           1COUNTUP  
0198 C.  
0199 C.   °È²¼αïÄë%îíñαî¥Ä¥§¥Ä¥-¹âiû  
0200 C.            çç[HK1\_TI\_CMD\_ENA/DIS]                                       EQ           ENA  
0201 C.            çç[HK1\_TI\_CMD\_NUM]   EQ           4  
0202 C.            çç[HK1\_NEXT\_EXEC\_PIM]                                       EQ           DHU  
0203 C.            çç[HK1\_NEXT\_EXEC\_DC]   EQ           0xB3  
0204 C.  
0205 . C.   \*\*\*\*\*  
0206 C.   TIîî°è¥Ä¥Ö¥x  
0207 C.   \*\*\*\*\*  
0208 C.  
0209 C.   TI\_TBL(0x03AB00-0x03AEFF;§ 1024byte)  
0210 +. DC 01-23 DHU\_DMA\_DMP\_PRM\_SET  
0211 BC           (03 ab 03 01 02)  
0212 C.            çç[HK1\_DMP\_TOP\_ADRS\_1]                                       EQ           07  
0213 C.            çç[HK1\_DMP\_TOP\_ADRS\_0]                                       EQ           2B  
0214 C.            çç[HK1\_DMP\_BLOCK\_NUM]                                       EQ           3  
0215 C.            çç[HK1\_DMP\_REPEAT\_NUM]                                       EQ           0  
0216 C.            çç[HK1\_DMA\_DMP\_PIM]   EQ           DHU  
0217 +. DC 01-22 DHU\_MODE\_CHNG  
0218 BC           (07 0b f8)  
0219 C.            çç[HK1\_PKT\_FORM\_NO]   EQ           7  
0220 C.            çç[HK1\_PKT\_GEN\_TIME]   EQ           0.25 s  
0221 C.            çç[HK1\_S\_TLM\_BIT\_RATE]                                       EQ           32k  
0222 C.            çç[HK1\_X\_TLM\_BIT\_RATE]                                       EQ           4M  
0223 C.            çç[HK1\_DMP\_CHK\_FLG]   EQ           EXEC  
0224 C.  
0225 . C.   ¥Ä¥Ö¥x½ªî»αð³îÇ§  
0226 C.            çç[HK1\_DMP\_CHK\_FLG]   EQ           NON  
0227 C.  
0228 . C.   RAM ID=TI\_TBLαîî¼È¹ç.è²îOKαð³îÇ§  
0229 C.  
0230 . C.   DHU¥â;¼¥È;È¼¥¼.¥î;¼¥È;Èαðîãα¹  
0231 +. DC 01-22 DHU\_MODE\_CHNG  
0232 BC           (02 0a f8)  
0233 C.            çç[HK1\_PKT\_FORM\_NO]   EQ           2  
0234 C.            çç[HK1\_PKT\_GEN\_TIME]   EQ           0.5S  
0235 C.            çç[HK1\_S\_TLM\_BIT\_RATE]                                       EQ           32K  
0236 C.            çç[HK1\_X\_TLM\_BIT\_RATE]                                       EQ           4M  
0237 C.  
0238 C.   \*\*\*\*\*  
0239 C.   SOT TI command set  
0240 C.   \*\*\*\*\*  
0241 C.   Execute, after the success of OP upload.  
0242 +. TI 2014-07-24 10:50:16.0  
0243 DC 07-F0 MDP\_SOT\_MODE\_STBY  
0244 BC           (41)  
0245 . C.   -----  
0246 C.   HK1\_TI\_CMD\_NUM                         = 1 CNTUP [ ]  
0247 C.   -----  
0248 C.   \*\*\*\*\* SOT END \*\*\*\*\*  
0249 . C.   Stop EIS observation and temporarily disable EIS mode changes  
0250 C.  
0251 C.  
0252 C.   \*\*\*\*\* Start EIS operation (TI set) \*\*\*\*\*  
0253 C.   Execute, after the success of OP upload.  
0254 C.   Set EIS TI-commands  
0255 +. TI 2014-07-24 10:50:30.0  
0256 DC 07-FC EIS\_MODE\_MANU  
0257 BC           (21 02)  
0258 +. TI 2014-07-24 10:50:40.0  
0259 DC 07-FC EIS\_MODE\_CHG\_DIS  
0260 BC           (22)  
0261 . C.   [    ] [HK1\_TI\_CMD\_NUM]           EQ           2 COUNTUP  
0262 C.   \*\*\*\*\* End EIS operation (TI set) \*\*\*\*\*  
0263 C.  
0264 C.  
0265 C.  
0266 C.   \*\*\*\*\* XRT START \*\*\*\*\*  
0267 C.   Execute, after the success of OP upload.  
0268 +. TI 2014-07-24 10:50:00.0  
0269 DC 07-F0 MDP\_XRT\_MODE\_STBY  
0270 BC           (c3)  
0271 . C.   [    ] [HK1\_TI\_CMD\_NUM]           EQ           1COUNTUP  
0272 C.  
0273 C.   \*\*\*\*\* XRT END \*\*\*\*\*  
0274 C.  
0275 . C.   \*\*\*\*\* MDP `úÄîαî»ö¼ÝαÈÄα¹αèDCBC.x²è \*\*\*\*\*  
0276 C.   (%â°î¥Ö¥Ä¥È¥¥¥È¥á¥ç¥èè%¼αα¼Ä»Ûα¹αè)  
0277 . S. DC-BC dcbc-402:DCBC  
0278           (MDP\_known\_event)  
0279 C.  
0280 C.  
0281 . C.   \*\*\*\*\* ¥Ð¥¹.î Daily±çîñαè'Øα¹αèDCBC.x²è \*\*\*\*\*  
0282 . S. DC-BC dcbc-153:DCBC  
0283           (SPECIAL-CMD\_DAILY\_OPERATIN\_DCB)  
0284 C.  
0285 C.  
0286 . C.   ;ãLOS¥Ä¥§¥Ä¥-¼Ä»Û;ã  
0287 C.  
0288 . C.   \*\*\*\*\* LOS \*\*\*\*\*  
0289 C.



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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0130 +. DC 07-FC EIS_MODE_CHG_ENA
0131 BC (20)
0132 . C. Verify EIS_MODE_CHG_FLG is ENA
0133 +. DC 07-FC EIS_MODE_MANU
0134 BC (21 02)
0135 . C. Verify EIS in MANUAL mode
0136 . C. Estimated OBSTBL upload time is 1ml0s
0137 C. *****
0138 C. EIS START OBSTBL LOAD
0139 C. *****
0140 . S. RAM ram-820:EIS_OBSTBL
0141 ( )
0142 +. DC 07-FC EIS_DUMP_OBSTBL
0143 BC (07 07 07 00 00 70 00)
0144 C.
0145 C. Execute, after the success of OBSTBL upload.
0146 C. Set EIS TI-commands
0147 +. TI 2014-07-24 10:50:50.0
0148 DC 07-FC EIS_MODE_CHG_ENA
0149 BC (20)
0150 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0151 C. *****
0152 C. EIS END OBSTBL LOAD
0153 C. *****
0154 C.
0155 C. ***** XRT START *****
0156 C.
0157 +. DC 07-F0 MDP_XRT_CTRL_MANU
0158 BC (c1)
0159 + DC 07-F0 MDP_XRT_MODE_STBY
0160 BC (c3)
0161 . C. ----- Success Verify ? OK / NG____
0162 C.
0163 C. XRT Obs. Table Upload
0164 . S. RAM ram-291:MDP_OBS_X
0165 ( )
0166 C.
0167 +. DC 07-F0 MDP_DUMP_XRTTBL
0168 BC (84 07 00 00 00 3a d4)
0169 . C. ----- Comparison Check ? OK / ERR ____
0170 C.
0171 C.
0172 +. DC 07-F0 MDP_XRT_ROI_SET
0173 BC (cd 01 b1 b1 04 04)
0174 + DC 07-F0 MDP_XRT_ROI_SET
0175 BC (cd 02 b1 b1 08 08)
0176 + DC 07-F0 MDP_XRT_ROI_SET
0177 BC (cd 03 b1 b1 08 08)
0178 + DC 07-F0 MDP_XRT_ROI_SET
0179 BC (cd 04 b1 b1 06 06)
0180 + DC 07-F0 MDP_XRT_ROI_SET
0181 BC (cd 05 85 83 06 06)
0182 + DC 07-F0 MDP_XRT_ROI_SET
0183 BC (cd 06 80 80 08 08)
0184 + DC 07-F0 MDP_XRT_ROI_SET
0185 BC (cd 07 80 80 20 20)
0186 + DC 07-F0 MDP_XRT_ROI_SET
0187 BC (cd 08 80 80 20 08)
0188 + DC 07-F0 MDP_XRT_ROI_SET
0189 BC (cd 09 80 80 08 20)
0190 + DC 07-F0 MDP_XRT_ROI_SET
0191 BC (cd 0a 85 83 06 06)
0192 + DC 07-F0 MDP_XRT_ROI_SET
0193 BC (cd 0b 85 83 08 08)

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0194 + DC 07-F0 MDP_XRT_ROI_SET
0195 BC (cd 0f 80 80 06 06)
0196 + DC 07-F0 MDP_XRT_ROI_SET
0197 BC (cd 10 80 80 08 08)
0198 + DC 07-F0 MDP_XRT_FLD_ENA
0199 BC (d8)
0200 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0201 BC (c8)
0202 + DC 07-F0 MDP_XRT_ARS_DIS
0203 BC (d5)
0204 + DC 07-F0 MDP_XRT_QT_PROG_SET
0205 BC (c4 0d)
0206 + DC 07-F0 MDP_XRT_FL_PROG_SET
0207 BC (c5 01)
0208 . C. ----- Success Verify ? OK / NG ____
0209 C.
0210 C.
0211 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0212 C.
0213 + DC 07-F0 MDP_XRT_MODE_OBSV
0214 BC (c2)
0215 + DC 07-F0 MDP_XRT_CTRL_AUTO
0216 BC (c0)
0217 + TI 2014-07-24 10:50:02.0
0218 DC 07-F0 MDP_XRT_MODE_OBSV
0219 BC (c2)
0220 + TI 2014-07-24 10:50:04.0
0221 DC 07-F0 MDP_XRT_CTRL_AUTO
0222 BC (c0)
0223 . C. ----- Success Verify ? OK / NG ____
0224 C.
0225 C. ***** XRT END *****
0226 C.
0227 . C. ***** MDP `uãîñï»ö%ýðëâðñ¹ðëDCBC•x²è *****
0228 C. (%á°îÿÓÿÃÿÈÿPÿËÿáÿçÿëðÈ%¼ð¼Â»Ûñ¹ðè)
0229 . S. DC-BC dcbc-402:DCBC
0230 (MDP_known_event)
0231 C.
0232 C.
0233 . C. ***** ÿDÿ¹•Ï Daily±¿îñðË´Øñ¹ðëDCBC•x²è *****
0234 . S. DC-BC dcbc-153:DCBC
0235 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0236 C.
0237 C.
0238 . C. ;ãLOSÿÁÿ$ÿÃÿ~¼Â»Û;ã
0239 C.
0240 . C. ***** LOS *****
0241 C.
```



0096 C. FPP\_SP\_CCDHTR\_DC = 25 [ ]  
0097 C. FPP\_SP\_CCDHTR\_DB\_HI =-45 [ ]  
0098 C. FPP\_SP\_CCDHTR\_DB\_LO =-50 [ ]  
0099 C. -----  
0100 C.  
0101 . C. < Enable SP Heater >  
0102 +. DC 07-F2 FPP\_PWR\_ON\_ENA  
0103 BC (12 02)  
0104 +. DC 07-F2 FPP\_SP\_OPHTER\_ENA  
0105 BC (48 02)  
0106 . C. -----  
0107 C. FPP\_SPHTR\_STAT > 01 [ ]  
0108 C. -----  
0109 C.  
0110 . C. Resume XRT obs by XRT\_CTRL\_AUTO  
0111 C.  
0112 C.  
0113 . C. \*\*\*\*\* MDP 'úÃîâî»ö¼ÝðÈÂð¹âèDCBC•x²è \*\*\*\*\*  
0114 C. (¼â°îÿÓÿÄÿÈÿÞÿËÿâÿçÿèðÈ¼¼¼¼»Û¹âè)  
0115 . S. DC-BC dcbc-402:DCBC  
0116 (MDP\_known\_event)  
0117 C.  
0118 C.  
0119 . C. \*\*\*\*\* ÿDÿ¹•î Daily±¿îÑâÈ´Ø¹âèDCBC•x²è \*\*\*\*\*  
0120 . S. DC-BC dcbc-153:DCBC  
0121 (SPECIAL-CMD\_DAILY\_OPERATIN\_DCB)  
0122 C.  
0123 C.  
0124 . C. ;ãLOSÿÁÿ\$ÿÄÿÿ¼Ä»Û;ä  
0125 C.  
0126 . C. \*\*\*\*\* LOS \*\*\*\*\*  
0127 C.



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\*\*\* OP Sequence for XRT \*\*\*

2014/07/24	11:00:54.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/24	11:00:56.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/24	11:00:58.0	XRT_FOCUS_POSITION_403_OG [0x193]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/07/24	11:01:00.0	AOCS_Ore-point_Start_1_OG [0x097]				
		AOCU_NM	5	02-76	00 58 41 00 9b	
2014/07/24	11:01:18.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/07/24	11:01:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/07/24	11:01:22.0	XRT_AEC_RESET_413_OG [0x19d]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/07/24	11:01:24.0	XRT_ARS_DIS_423_OG [0x1a7]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/07/24	11:01:26.0	XRT_FLD_RESET_407_OG [0x197]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/24	11:03:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2014/07/24	11:03:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01	
2014/07/24	11:04:00.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/24	11:18:24.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/24	11:18:26.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/24	11:18:28.0	XRT_FOCUS_POSITION_403_OG [0x193]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/07/24	11:18:30.0	AOCS_Ore-point_Start_2_OG [0x098]				
		AOCU_NM	5	02-76	00 4f 64 00 9b	
2014/07/24	11:18:48.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/07/24	11:18:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/07/24	11:18:52.0	XRT_AEC_RESET_413_OG [0x19d]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/07/24	11:18:54.0	XRT_ARS_DIS_423_OG [0x1a7]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/07/24	11:18:56.0	XRT_FLD_RESET_407_OG [0x197]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/24	11:21:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2014/07/24	11:21:28.0	XRT_FL_PROG_SET_438_OG [0x1b6]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01	
2014/07/24	11:21:30.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/24	11:37:00.0	XRT_CTRL_MANU_400_OG [0x190]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/24	11:37:02.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/24	11:37:04.0	XRT_FLD_RESET_415_OG [0x19f]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/24	11:37:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]				
		MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/07/24	11:40:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]				
		MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/07/24	12:06:24.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/24	12:06:26.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/24	12:06:28.0	XRT_FOCUS_POSITION_403_OG [0x193]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/07/24	12:06:30.0	AOCS_Ore-point_Start_3_OG [0x099]				
		AOCU_NM	5	02-76	00 46 7e 00 9b	
2014/07/24	12:06:48.0	XRT_FLD_ENA_411_OG [0x19b]				
		MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/07/24	12:06:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]				
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/07/24	12:06:52.0	XRT_AEC_RESET_413_OG [0x19d]				
		MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/07/24	12:06:54.0	XRT_ARS_DIS_423_OG [0x1a7]				
		MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/07/24	12:06:56.0	XRT_FLD_RESET_407_OG [0x197]				
		MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/24	12:09:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]				
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04	
2014/07/24	12:09:28.0	XRT_FL_PROG_SET_438_OG [0x1b6]				
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01	
2014/07/24	12:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]				
		MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/24	12:16:24.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/24	12:16:26.0	XRT_CTRL_MANU_402_OG [0x192]				
		MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/24	12:16:28.0	XRT_FOCUS_POSITION_403_OG [0x193]				
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/07/24	12:16:30.0	AOCS_Ore-point_Start_4_OG [0x09a]				
		AOCU_NM	5	02-76	00 3d 99 00 9b	
2014/07/24	12:16:48.0	XRT_FLD_ENA_411_OG [0x19b]				

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2014/07/24	12:16:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8		
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/07/24	12:16:52.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/07/24	12:16:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/07/24	12:16:56.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/07/24	12:19:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04	
2014/07/24	12:19:28.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	01	
2014/07/24	12:19:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/07/24	12:26:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/24	12:26:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/24	12:26:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa 00
2014/07/24	12:26:30.0	AOCS_OrE-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00	34	b4 00 9b
2014/07/24	12:26:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/07/24	12:26:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/07/24	12:26:52.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/07/24	12:26:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/07/24	12:26:56.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/07/24	12:29:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04	
2014/07/24	12:29:28.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	01	
2014/07/24	12:29:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/07/24	12:36:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/24	12:36:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/24	12:36:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa 00
2014/07/24	12:36:30.0	AOCS_OrE-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00	2b	ce 00 9b
2014/07/24	12:36:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/07/24	12:36:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/07/24	12:36:52.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/07/24	12:36:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/07/24	12:36:56.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/07/24	12:39:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04	
2014/07/24	12:39:28.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	01	
2014/07/24	12:39:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/07/24	12:46:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/24	12:46:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/24	12:46:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa 00
2014/07/24	12:46:30.0	AOCS_OrE-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00	22	f1 00 9b
2014/07/24	12:46:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/07/24	12:46:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/07/24	12:46:52.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/07/24	12:46:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/07/24	12:46:56.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/07/24	12:49:26.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04	
2014/07/24	12:49:28.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	01	
2014/07/24	12:49:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/07/24	12:56:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/24	12:56:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/24	12:56:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa 00

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2014/07/24	12:56:30.0	AOCS_ORe-point_Start_8_OG [0x09e] AOCU_NM	5	02-76	00	1a	0c	00	9b
2014/07/24	12:56:48.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0		d8			
2014/07/24	12:56:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2014/07/24	12:56:52.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0		d0			
2014/07/24	12:56:54.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5			
2014/07/24	12:56:56.0	XRT_FLD_RESET_407_OG [0x197] MDP_XRT_FLD_RESET	1	07-F0		da			
2014/07/24	12:59:26.0	XRT_QT_PROG_SET_435_OG [0x1b3] MDP_XRT_QT_PROG_SET	2	07-F0		c4	04		
2014/07/24	12:59:28.0	XRT_FL_PROG_SET_438_OG [0x1b6] MDP_XRT_FL_PROG_SET	2	07-F0		c5	01		
2014/07/24	12:59:30.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2014/07/24	13:15:30.0	XRT_CTRL_MANU_400_OG [0x190] MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/07/24	13:15:32.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/07/24	13:15:34.0	XRT_FLD_RESET_415_OG [0x19f] MDP_XRT_FLD_RESET	1	07-F0		da			
2014/07/24	13:15:36.0	XRT_PREFLR_STRT_439_OG [0x1b7] MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2014/07/24	13:18:44.0	XRT_PREFLR_STOP_419_OG [0x1a3] MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2014/07/24	13:44:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/07/24	13:44:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/07/24	13:44:58.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/07/24	13:45:00.0	AOCS_ORe-point_Start_9_OG [0x09f] AOCU_NM	5	02-76	00	11	26	00	9b
2014/07/24	13:45:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0		d8			
2014/07/24	13:45:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2014/07/24	13:45:22.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0		d0			
2014/07/24	13:45:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5			
2014/07/24	13:45:26.0	XRT_FLD_RESET_407_OG [0x197] MDP_XRT_FLD_RESET	1	07-F0		da			
2014/07/24	13:47:56.0	XRT_QT_PROG_SET_435_OG [0x1b3] MDP_XRT_QT_PROG_SET	2	07-F0		c4	04		
2014/07/24	13:47:58.0	XRT_FL_PROG_SET_438_OG [0x1b6] MDP_XRT_FL_PROG_SET	2	07-F0		c5	01		
2014/07/24	13:48:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2014/07/24	13:54:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/07/24	13:54:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/07/24	13:54:58.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/07/24	13:55:00.0	AOCS_ORe-point_Start_10_OG [0x0a0] AOCU_NM	5	02-76	00	08	41	00	9b
2014/07/24	13:55:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0		d8			
2014/07/24	13:55:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2014/07/24	13:55:22.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0		d0			
2014/07/24	13:55:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5			
2014/07/24	13:55:26.0	XRT_FLD_RESET_407_OG [0x197] MDP_XRT_FLD_RESET	1	07-F0		da			
2014/07/24	13:57:56.0	XRT_QT_PROG_SET_435_OG [0x1b3] MDP_XRT_QT_PROG_SET	2	07-F0		c4	04		
2014/07/24	13:57:58.0	XRT_FL_PROG_SET_438_OG [0x1b6] MDP_XRT_FL_PROG_SET	2	07-F0		c5	01		
2014/07/24	13:58:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2014/07/24	14:04:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/07/24	14:04:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/07/24	14:04:58.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/07/24	14:05:00.0	AOCS_ORe-point_Start_11_OG [0x0a1] AOCU_NM	5	02-76	00	00	41	00	9b
2014/07/24	14:05:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0		d8			
2014/07/24	14:05:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2014/07/24	14:05:22.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0		d0			
2014/07/24	14:05:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0		d5			
2014/07/24	14:05:26.0	XRT_FLD_RESET_407_OG [0x197]							

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2014/07/24	14:07:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2014/07/24	14:07:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/07/24	14:08:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/24	14:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/24	14:14:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/24	14:14:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/07/24	14:15:00.0	AOCS_Or-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00 f7 65 00 9b
2014/07/24	14:15:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2014/07/24	14:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/07/24	14:15:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2014/07/24	14:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/07/24	14:15:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/07/24	14:17:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2014/07/24	14:17:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/07/24	14:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/24	14:24:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/24	14:24:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/24	14:24:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/07/24	14:25:00.0	AOCS_Or-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00 ee 7f 00 9b
2014/07/24	14:25:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2014/07/24	14:25:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/07/24	14:25:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2014/07/24	14:25:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/07/24	14:25:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/07/24	14:27:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2014/07/24	14:27:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/07/24	14:28:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/24	14:34:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/24	14:34:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/24	14:34:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/07/24	14:35:00.0	AOCS_Or-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00 e5 9a 00 9b
2014/07/24	14:35:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8
2014/07/24	14:35:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/07/24	14:35:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0
2014/07/24	14:35:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/07/24	14:35:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/07/24	14:37:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2014/07/24	14:37:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/07/24	14:38:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/24	14:54:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/24	14:54:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/24	14:54:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/07/24	14:54:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/07/24	14:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/07/24	15:42:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/24	15:42:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1

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2014/07/24	15:42:58.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/07/24	15:43:00.0	AOCs_OrE-point_Start_15_OG [0x0a5] AOCU_NM	5	02-76	00 dc b5 00 9b
2014/07/24	15:43:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2014/07/24	15:43:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/07/24	15:43:22.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0	d0
2014/07/24	15:43:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2014/07/24	15:43:26.0	XRT_FLD_RESET_407_OG [0x197] MDP_XRT_FLD_RESET	1	07-F0	da
2014/07/24	15:45:56.0	XRT_QT_PROG_SET_435_OG [0x1b3] MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2014/07/24	15:45:58.0	XRT_FL_PROG_SET_438_OG [0x1b6] MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/07/24	15:46:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/24	15:52:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/24	15:52:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/24	15:52:58.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/07/24	15:53:00.0	AOCs_OrE-point_Start_16_OG [0x0a6] AOCU_NM	5	02-76	00 d3 cf 00 9b
2014/07/24	15:53:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2014/07/24	15:53:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/07/24	15:53:22.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0	d0
2014/07/24	15:53:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2014/07/24	15:53:26.0	XRT_FLD_RESET_407_OG [0x197] MDP_XRT_FLD_RESET	1	07-F0	da
2014/07/24	15:55:56.0	XRT_QT_PROG_SET_435_OG [0x1b3] MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2014/07/24	15:55:58.0	XRT_FL_PROG_SET_438_OG [0x1b6] MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/07/24	15:56:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/24	16:02:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/24	16:02:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/24	16:02:58.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/07/24	16:03:00.0	AOCs_OrE-point_Start_17_OG [0x0a7] AOCU_NM	5	02-76	00 ca f2 00 9b
2014/07/24	16:03:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2014/07/24	16:03:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/07/24	16:03:22.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0	d0
2014/07/24	16:03:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2014/07/24	16:03:26.0	XRT_FLD_RESET_407_OG [0x197] MDP_XRT_FLD_RESET	1	07-F0	da
2014/07/24	16:05:56.0	XRT_QT_PROG_SET_435_OG [0x1b3] MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2014/07/24	16:05:58.0	XRT_FL_PROG_SET_438_OG [0x1b6] MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/07/24	16:06:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/24	16:12:54.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/24	16:12:56.0	XRT_CTRL_MANU_402_OG [0x192] MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/24	16:12:58.0	XRT_FOCUS_POSITION_403_OG [0x193] XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/07/24	16:13:00.0	AOCs_OrE-point_Start_18_OG [0x0a8] AOCU_NM	5	02-76	00 c2 0d 00 9b
2014/07/24	16:13:18.0	XRT_FLD_ENA_411_OG [0x19b] MDP_XRT_FLD_ENA	1	07-F0	d8
2014/07/24	16:13:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c] MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/07/24	16:13:22.0	XRT_AEC_RESET_413_OG [0x19d] MDP_XRT_AEC_RESET	1	07-F0	d0
2014/07/24	16:13:24.0	XRT_ARS_DIS_423_OG [0x1a7] MDP_XRT_ARS_DIS	1	07-F0	d5
2014/07/24	16:13:26.0	XRT_FLD_RESET_407_OG [0x197] MDP_XRT_FLD_RESET	1	07-F0	da
2014/07/24	16:15:56.0	XRT_QT_PROG_SET_435_OG [0x1b3] MDP_XRT_QT_PROG_SET	2	07-F0	c4 04
2014/07/24	16:15:58.0	XRT_FL_PROG_SET_438_OG [0x1b6] MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/07/24	16:16:00.0	XRT_CTRL_AUTO_408_OG [0x198] MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/24	16:32:30.0	XRT_CTRL_MANU_400_OG [0x190]			

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2014/07/24	16:32:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/07/24	16:32:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/07/24	16:32:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/07/24	16:35:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/07/24	17:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/07/24	17:19:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/07/24	17:19:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/07/24	17:20:00.0	AOCS_OrE-point_Start_19_OG [0x0a9]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2014/07/24	17:20:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 b9 27 00 9b			
2014/07/24	17:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/07/24	17:20:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/07/24	17:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/07/24	17:20:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/07/24	17:22:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/07/24	17:22:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04			
2014/07/24	17:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01			
2014/07/24	17:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/07/24	17:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/07/24	17:29:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/07/24	17:30:00.0	AOCS_OrE-point_Start_20_OG [0x0aa]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2014/07/24	17:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 b0 42 00 9b			
2014/07/24	17:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/07/24	17:30:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/07/24	17:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/07/24	17:30:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/07/24	17:32:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/07/24	17:32:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04			
2014/07/24	17:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01			
2014/07/24	17:56:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/07/24	17:56:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/07/24	17:56:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/07/24	17:57:00.0	AOCS_OrE-point_Start_21_OG [0x0ab]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2014/07/24	17:57:18.0	XRT_FLD_DIS_422_OG [0x1a6]	AOCU_NM	5	02-76	00 00 00 00 00			
2014/07/24	17:59:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2014/07/24	17:59:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2014/07/24	17:59:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/07/24	18:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f			
2014/07/24	18:06:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/07/24	18:06:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/07/24	18:06:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/07/24	18:07:00.0	AOCS_OrE-point_Start_22_OG [0x0ac]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2014/07/24	18:07:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00			
2014/07/24	18:07:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/07/24	18:07:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/07/24	18:07:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/07/24	18:07:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/07/24			MDP_XRT_FLD_RESET	1	07-F0	da			

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2014/07/24	18:09:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	07			
2014/07/24	18:09:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2014/07/24	18:11:00.5	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/24	18:11:02.5	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/24	18:11:04.5	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/24	18:11:06.5	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/07/24	18:14:14.5	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/07/24	18:55:30.0	XRT_Custom_430_OG [0x1ae]							
2014/07/24	18:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/24	19:49:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/24	19:49:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/24	19:49:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/24	19:49:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/07/24	19:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/07/24	20:33:00.0	XRT_Custom_430_OG [0x1ae]							
2014/07/24	20:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/24	21:27:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/24	21:27:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/24	21:27:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/24	21:27:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/07/24	21:30:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/07/24	22:09:00.0	XRT_Custom_430_OG [0x1ae]							
2014/07/24	22:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/24	23:06:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/24	23:06:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/24	23:06:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/24	23:06:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/07/24	23:09:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/07/24	23:36:30.0	XRT_Custom_430_OG [0x1ae]							
2014/07/24	23:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/25	00:44:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/25	00:44:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/25	00:44:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/25	00:44:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/07/25	00:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/07/25	01:13:00.0	XRT_Custom_430_OG [0x1ae]							
2014/07/25	01:14:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/25	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/25	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/25	01:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00			
2014/07/25	02:00:00.0	AOCS_Ore-point_Start_23_OG [0x0ad]							
		AOCU_NM	5	02-76	00	3c 00 c4 42			
2014/07/25	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/07/25	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/07/25	02:00:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/07/25	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/07/25	02:00:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/25	02:02:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	06			
2014/07/25	02:02:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2014/07/25	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				

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2014/07/25	02:23:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/25	02:23:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/25	02:23:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/07/25	02:23:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/07/25	02:26:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/07/25	02:51:30.0	XRT_Custom_430_OG [0x1ae]						
2014/07/25	02:52:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/07/25	03:51:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/25	03:51:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/25	03:51:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/07/25	03:51:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/07/25	03:54:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/07/25	04:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/25	04:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/25	04:29:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2014/07/25	04:30:00.0	AOCS_Ore-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00 00 00 00 00		
2014/07/25	04:30:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2014/07/25	04:32:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2014/07/25	04:32:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/07/25	04:32:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f		
2014/07/25	04:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/07/25	04:39:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/25	04:39:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/25	04:39:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/07/25	04:40:00.0	AOCS_Ore-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	02 00 00 00 00		
2014/07/25	04:40:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/07/25	04:40:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/07/25	04:40:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/07/25	04:40:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/07/25	04:40:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/07/25	04:42:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 13		
2014/07/25	04:42:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01		
2014/07/25	04:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/07/25	05:32:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/25	05:32:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/25	05:32:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/07/25	05:32:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/07/25	05:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/07/25	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/25	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/07/25	05:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/07/25	06:00:00.0	AOCS_Ore-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	02 00 00 00 00		
2014/07/25	06:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/07/25	06:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/07/25	06:00:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/07/25	06:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/07/25	06:00:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		



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2014/07/25	06:02:56.0	XRT_QT_PROG_SET_429_OG [0x1ad]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 07
2014/07/25	06:02:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/07/25	06:08:30.5	XRT_Custom_430_OG [0x1ae]			
2014/07/25	06:09:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/25	07:12:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/25	07:12:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/25	07:12:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/07/25	07:12:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/07/25	07:15:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/07/25	07:47:00.0	XRT_Custom_430_OG [0x1ae]			
2014/07/25	07:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/25	08:52:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/25	08:52:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/25	08:52:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/07/25	08:52:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/07/25	08:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/07/25	09:25:00.0	XRT_Custom_430_OG [0x1ae]			
2014/07/25	09:26:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/25	10:34:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/25	10:34:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/25	10:34:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/07/25	10:34:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/07/25	10:37:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/07/25	11:03:30.0	XRT_Custom_430_OG [0x1ae]			
2014/07/25	11:04:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/25	12:13:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/25	12:13:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/25	12:13:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/07/25	12:13:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/07/25	12:16:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/07/25	12:42:00.0	XRT_Custom_430_OG [0x1ae]			
2014/07/25	12:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/25	13:52:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/25	13:52:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/25	13:52:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/07/25	13:52:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/07/25	13:55:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/07/25	14:29:30.0	XRT_Custom_430_OG [0x1ae]			
2014/07/25	14:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/25	15:30:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/25	15:30:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/25	15:30:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/07/25	15:30:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/07/25	15:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/07/25	16:18:00.0	XRT_Custom_430_OG [0x1ae]			
2014/07/25	16:19:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/07/25	17:09:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/25	17:09:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/07/25	17:09:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/07/25	17:09:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8

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2014/07/25	17:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/07/25	17:56:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/25	17:56:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/25	17:56:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/07/25	17:56:30.0	AOCS_Ore-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00 00 00 00 00	
2014/07/25	17:56:48.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/07/25	17:59:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/07/25	17:59:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/07/25	17:59:28.0	XRT_QT_PROG_SET_421_OG [0x1a5]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f	
2014/07/25	17:59:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/25	18:06:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/25	18:06:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/25	18:06:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2014/07/25	18:06:30.5	AOCS_Ore-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	02 00 00 00 00	
2014/07/25	18:06:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/07/25	18:06:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/07/25	18:06:52.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/07/25	18:06:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/07/25	18:06:56.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/25	18:09:26.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 07	
2014/07/25	18:09:28.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01	
2014/07/25	18:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/25	18:47:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/25	18:47:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/25	18:47:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/25	18:47:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/07/25	18:50:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/07/25	19:31:30.0	XRT_Custom_430_OG [0x1ae]					
2014/07/25	19:32:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/25	20:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/25	20:25:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/25	20:25:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/25	20:25:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/07/25	20:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/07/25	21:08:30.0	XRT_Custom_430_OG [0x1ae]					
2014/07/25	21:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/25	22:04:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/25	22:04:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/25	22:04:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/25	22:04:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/07/25	22:07:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/07/25	22:44:00.0	XRT_Custom_430_OG [0x1ae]					
2014/07/25	22:45:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/25	23:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/25	23:42:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/25	23:42:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/25	23:42:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/07/25	23:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	

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2014/07/26	00:11:00.0	XRT_Custom_430_OG [0x1ae]							
2014/07/26	00:12:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/26	01:21:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/26	01:21:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/26	01:21:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/26	01:21:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/07/26	01:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/07/26	01:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/26	01:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/26	01:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/07/26	02:00:00.0	AOCS_ORe-point_Start_24_OG [0x0ae]							
		AOCU_NM	5	02-76	04 00 00 00 00				
2014/07/26	02:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/07/26	02:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/07/26	02:00:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/07/26	02:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/07/26	02:00:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/26	02:02:56.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2014/07/26	02:02:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01				
2014/07/26	02:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/26	02:59:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/26	02:59:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/26	02:59:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/26	02:59:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/07/26	03:02:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/07/26	03:28:00.0	XRT_Custom_430_OG [0x1ae]							
2014/07/26	03:29:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/26	03:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/26	03:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/26	03:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/07/26	04:00:00.0	AOCS_ORe-point_Start_21_OG [0x0ab]							
		AOCU_NM	5	02-76	00 00 00 00 00				
2014/07/26	04:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/07/26	04:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/07/26	04:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/07/26	04:02:58.0	XRT_QT_PROG_SET_421_OG [0x1a5]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2014/07/26	04:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/26	04:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/26	04:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/07/26	04:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/07/26	04:10:00.0	AOCS_ORe-point_Start_22_OG [0x0ac]							
		AOCU_NM	5	02-76	02 00 00 00 00				
2014/07/26	04:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/07/26	04:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/07/26	04:10:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/07/26	04:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/07/26	04:10:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/07/26	04:12:56.0	XRT_QT_PROG_SET_406_OG [0x196]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 13				
2014/07/26	04:12:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01				
2014/07/26	04:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/07/26	04:28:30.0	XRT_CTRL_MANU_400_OG [0x190]							

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2014/07/26	04:28:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/26	04:28:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/26	04:28:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/07/26	04:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/07/26	05:06:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/07/26	05:07:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]				
2014/07/26	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2014/07/26	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/26	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/26	06:00:00.0	AOCS_OrE-point_Start_21_OG [0x0ab]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/07/26	06:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 00 00 00 00	
2014/07/26	06:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	XRT_FLD_ENA_411_OG [0x19b]				
2014/07/26	06:00:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/07/26	06:00:24.0	XRT_ARS_DIS_427_OG [0x1ab]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/07/26	06:02:54.0	XRT_FLD_RESET_415_OG [0x19f]	XRT_ARS_DIS_427_OG [0x1ab]				
2014/07/26	06:02:56.0	XRT_QT_PROG_SET_440_OG [0x1b8]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/07/26	06:02:58.0	XRT_FL_PROG_SET_401_OG [0x191]	XRT_FLD_RESET_415_OG [0x19f]				
2014/07/26	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09	
2014/07/26	06:09:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 11	
2014/07/26	06:09:02.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_408_OG [0x198]				
2014/07/26	06:09:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/26	06:09:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/26	06:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	XRT_FLD_RESET_415_OG [0x19f]				
2014/07/26	06:44:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	da	
2014/07/26	06:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e8	
2014/07/26	07:49:30.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_Custom_430_OG [0x1ae]				
2014/07/26	07:49:32.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2014/07/26	07:49:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/07/26	07:49:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/07/26	07:52:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	XRT_FLD_RESET_415_OG [0x19f]				
2014/07/26	08:23:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	da	
2014/07/26	08:24:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e8	
2014/07/26	09:20:00.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_CTRL_AUTO_424_OG [0x1a8]				
2014/07/26	10:04:00.0	AOCS_OrE-point_Start_21_OG [0x0ab]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
		AOCU_NM	MDP_XRT_CTRL_MANU	1	07-F0	c1	
			AOCU_NM	5	02-76	00 00 00 00 00	