

# XRT Timeline to be uploaded on 2014/09/09

Period: 2014/09/09 10:56:00 - 2014/09/13 10:06:00

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Normal mode

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XOB #1A1B: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 33ms											
Term	Pointing (x, y)		Comment								
09/09 11:09:00 - 09/09 18:39:54	Fixed ( -24.0, 885.0)		# OP start + 10min. HOP 206 at North Pole.								
09/10 11:03:00 - 09/10 18:08:00	Fixed ( -24.0, -915.0)		# HOP 81 - South Pole								
09/10 23:18:00 - 09/11 00:29:54	Fixed ( -24.0, 920.0)		# North Pole NUSTAR observations								
<b>PROG= 06 Inf.-time(s)</b>											
└─ Subr= 1 1-time(s) 2.0sec											
└─ Seqn= 8 2-time(s) 2.0sec											
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0 0 2.0sec
└─ Subr= 2 1-time(s) 2.0sec											
└─ Seqn= 48 1-time(s) 30.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=90	0 0 2.0sec
└─ Subr= 3 30-time(s) 2.0sec											
└─ Seqn= 57 1-time(s) 60.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	4.00s	Obs	1x1	384x384 (1064, 1048)	Q=90	0 0 2.0sec
	Al-poly/Open	Al-poly/Open	close	Safe	Norm	5.66s	Obs	1x1	384x384 (1064, 1048)	Q=90	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

XOB #1A3D: Synoptic Q95 2x2 - Al/mesh(5/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/362/2048) + Thi											
Term	Pointing (x, y)		Comment								
09/09 18:43:00 - 09/09 18:49:54	Fixed ( 0.0, 0.0)		synoptic, shifted about 40 mins.								
09/10 05:45:30 - 09/10 05:52:24	Fixed ( 0.0, 0.0)		synoptic, shifted -17.5 min								
09/11 06:03:00 - 09/11 06:09:54	Fixed ( 0.0, 0.0)		synoptic								
<b>PROG= 03 1-time(s)</b>											
└─ Subr= 1 1-time(s) 12.0sec											
└─ Seqn= 2 1-time(s) 4.0sec											
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	5ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
└─ Seqn= 5 1-time(s) 2.0sec											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0 0 2.0sec
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0 0 2.0sec
└─ Seqn= 4 1-time(s) 4.0sec											
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
└─ Seqn= 96 1-time(s) 2.0sec											
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
└─ Seqn= 6 1-time(s) 2.0sec											
	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0 0 2.0sec
	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0 0 2.0sec
	Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer Interval

XOB #1A02: AR standard-B (thin-Be) with PFB (384FOV), 512x512 at 1064 1048, thick Al and Al/Poly context, With G-band (33ms/45ms leak), 5min cad											
Term	Pointing (x, y)		Comment								
09/09 18:53:00 - 09/10 05:42:24	Track ( -175.8, -384.0) <sup>Ⓢ 09/09 18:50:00</sup>		# AR 12157								
09/10 18:33:00 - 09/10 21:09:54	Track ( 30.6, -386.2) <sup>Ⓢ 09/10 18:30:00</sup>		# AR cont.								
09/11 00:44:30 - 09/11 05:59:54	Track ( 83.1, -385.7) <sup>Ⓢ 09/11 00:30:00</sup>		# Return to AR 12157								
<b>PROG= 18 Inf.-time(s)</b>											
└─ Subr= 1 1-time(s) 2.0sec											
└─ Seqn= 20 2-time(s) 2.0sec											
	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	512x512 (1064, 1048)	DPCM	0 0 2.0sec
└─ Seqn= 75 1-time(s) 2.0sec											
	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=98	0 0 2.0sec
	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	512x512 (1064, 1048)	Q=98	0 0 2.0sec
└─ Subr= 2 2-time(s) 2.0sec											
└─ Seqn= 66 2-time(s) 2.0sec											
	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	512x512 (1064, 1048)	Q=95	3 0 2.0sec
	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	512x512 (1064, 1048)	Q=95	3 0 2.0sec
└─ Seqn= 85 7-time(s) 300.0sec											
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	512x512 (1064, 1048)	Q=95	3 0 17.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 1 17.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 1 17.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 2 17.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 2 17.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 3 17.0sec
	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3 3 17.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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**XOB #19E3: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)**

Term	Pointing (x, y)	Comment
09/10 05:55:30 - 09/10 10:59:54	Track ( -80.0, -385.8) @ 09/10 05:52:30	# AR cont. and HOP 257 officially 8-11UT.
09/11 06:13:00 - 09/11 09:12:25	Track ( -1.5, 106.9) @ 09/11 06:10:00	# AR 12158 for HOP 257 officially 8-11UT.

**PROG= 12 Inf.-time(s)**

<b>Subr= 1</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
└─	<b>Seqn= 8</b>	<b>2-time(s)</b>	<b>2.0sec</b>										
└─└─	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
<b>Subr= 2</b>	<b>2-time(s)</b>	<b>2.0sec</b>											
└─	<b>Seqn= 24</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
└─└─	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─└─	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─	<b>Seqn= 42</b>	<b>4-time(s)</b>	<b>2.0sec</b>										
└─└─	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─└─	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─└─	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─	<b>Seqn= 62</b>	<b>15-time(s)</b>	<b>120.0sec</b>										
└─└─	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─└─	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─└─	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─└─	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	1	2.0sec
└─└─	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec
└─└─	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	2	2.0sec

**XOB #1A3E: Synoptic Q95 1x1 - Al/mesh(24/512/2897) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(88/1443/5795) + Ti-poly(88/1443/5795) + Ti-poly(88/1443/5795)**

Term	Pointing (x, y)	Comment
09/10 21:13:00 - 09/10 21:19:54	Fixed ( 0.0, 0.0)	synoptic, shifted a few hours.

**PROG= 09 1-time(s)**

<b>Subr= 1</b>	<b>1-time(s)</b>	<b>12.0sec</b>											
└─	<b>Seqn= 59</b>	<b>1-time(s)</b>	<b>4.0sec</b>										
└─└─	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	24ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─└─	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	500ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─└─	Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2.83s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─	<b>Seqn= 5</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
└─└─	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─└─	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─└─	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─└─	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
└─└─	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
└─	<b>Seqn= 3</b>	<b>1-time(s)</b>	<b>4.0sec</b>										
└─└─	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	86ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─└─	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─└─	Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	5.66s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─	<b>Seqn= 17</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
└─└─	thin-Be/Open	thin-Be/Open	close	Safe	Norm	250ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─└─	thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.00s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─└─	thin-Be/Open	thin-Be/Open	close	Safe	Norm	11.3s	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─	<b>Seqn= 46</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
└─└─	Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─	<b>Seqn= 38</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
└─└─	med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
└─	<b>Seqn= 6</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
└─└─	Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
└─└─	Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec

**XOB #19FD: AR standard-B (thin-Be) with PFB, 384 FOV at 1064x1048, thin-Be, thick-Al, and Al/Poly context, With G-band (33ms/45ms leak), 10s cad, low level**

Term	Pointing (x, y)	Comment
09/10 21:46:00 - 09/10 23:00:00	Track ( -80.3, 107.3) @ 09/10 21:20:00	# AR 12158 for NUSTAR observations

**PROG= 16 Inf.-time(s)**

<b>Subr= 1</b>	<b>1-time(s)</b>	<b>2.0sec</b>											
└─	<b>Seqn= 8</b>	<b>2-time(s)</b>	<b>2.0sec</b>										
└─└─	Open/G-band	Open/G-band	close	Safe	Norm	44ms	Obs	1x1	384x384 (1064, 1048)	DPCM	0	0	2.0sec
<b>Subr= 2</b>	<b>2-time(s)</b>	<b>2.0sec</b>											
└─	<b>Seqn= 24</b>	<b>1-time(s)</b>	<b>2.0sec</b>										
└─└─	Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─└─	Open/G-band	Open/G-band	open	Safe	Norm	32ms	Obs	1x1	384x384 (1064, 1048)	Q=98	0	0	2.0sec
└─	<b>Seqn= 42</b>	<b>2-time(s)</b>	<b>2.0sec</b>										
└─└─	Al-poly/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─└─	thin-Be/Open	med-Be/Open	close	Safe	Norm	5.66s	Obs	1x1	512x512 (1064, 1048)	Q=95	3	0	2.0sec
└─└─	Open/thick-Al	Open/thick-Al	close	Safe	Norm	16.0s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	2.0sec
└─	<b>Seqn= 87</b>	<b>75-time(s)</b>	<b>2.0sec</b>										
└─└─	thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	1x1	384x384 (1064, 1048)	Q=95	3	0	10.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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### Flare mode

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#### XOB #19EC: Flare - high cad multifilter (Be/thin, Be/med, Al/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gband (4

Term	Pointing (x, y)	Comment
09/09 11:09:00 - 09/09 18:39:54	Fixed ( -24.0, 885.0)	# OP start + 10min. HOP 206 at North Pole.
09/09 18:53:00 - 09/10 05:42:24	Track ( -175.8, -384.0) @ 09/09 18:50:00	# AR 12157
09/10 05:55:30 - 09/10 10:59:54	Track ( -80.0, -385.8) @ 09/10 05:52:30	# AR cont. and HOP 257 officially 8-11UT.
09/10 11:03:00 - 09/10 18:08:00	Fixed ( -24.0, -915.0)	# HOP 81 - South Pole
09/10 18:33:00 - 09/10 21:09:54	Track ( 30.6, -386.2) @ 09/10 18:30:00	# AR cont.
09/10 21:46:00 - 09/10 23:00:00	Track ( -80.3, 107.3) @ 09/10 21:20:00	# AR 12158 for NUSTAR observations
09/10 23:18:00 - 09/11 00:29:54	Fixed ( -24.0, 920.0)	# North Pole NUSTAR observations
09/11 00:44:30 - 09/11 05:59:54	Track ( 83.1, -385.7) @ 09/11 00:30:00	# Return to AR 12157
09/11 06:13:00 - 09/11 09:12:25	Track ( -1.5, 106.9) @ 09/11 06:10:00	# AR 12158 for HOP 257 officially 8-11UT.

#### PROG= 01 30-time(s)

Subr=	1-time(s)	30-time(s)	2.0sec										
Subr= 1	30-time(s)	2.0sec											
└─ Seqn= 26	1-time(s)	4.0sec											
└─ thin-Be/Open	med-Be/Open	close	Safe Norm 8ms	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec				
└─ med-Be/Open	Open/thick-Al	close	Safe Norm 250ms	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec				
└─ Open/thick-Be	Open/thick-Be	close	Safe Norm 1.00s	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec				
└─ Subr= 2	1-time(s)	2.0sec											
└─ Seqn= 10	1-time(s)	2.0sec											
└─ med-Al/Open	med-Al/thick-Al	close	Safe Norm 500ms	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec				
└─ Open/thick-Be	Open/thick-Be	close	Safe Norm 2.00s	Obs 1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec				
└─ Seqn= 11	1-time(s)	2.0sec											
└─ Al-poly/Open	Al-poly/thick-Al	close	Safe Norm 125ms	Obs 2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec				
└─ Seqn= 15	1-time(s)	2.0sec											
└─ Open/G-band	Open/G-band	open	Safe Norm 44ms	Obs 1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec				
└─ Open/thick-Al	Open/thick-Al	close	Safe Dark 1.00s	Obs 1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec				
└─ Open/thick-Al	Open/thick-Al	close	Safe Dark 1.00s	Obs 2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec				
Default Filter	Thicker Filter	VLS	mode image Exp.	CCD Bin	ROI: size (center)	Comp.	AEC Buffer	Interval					

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### Active Region Search

\* \* \* \* \*

NOT USED

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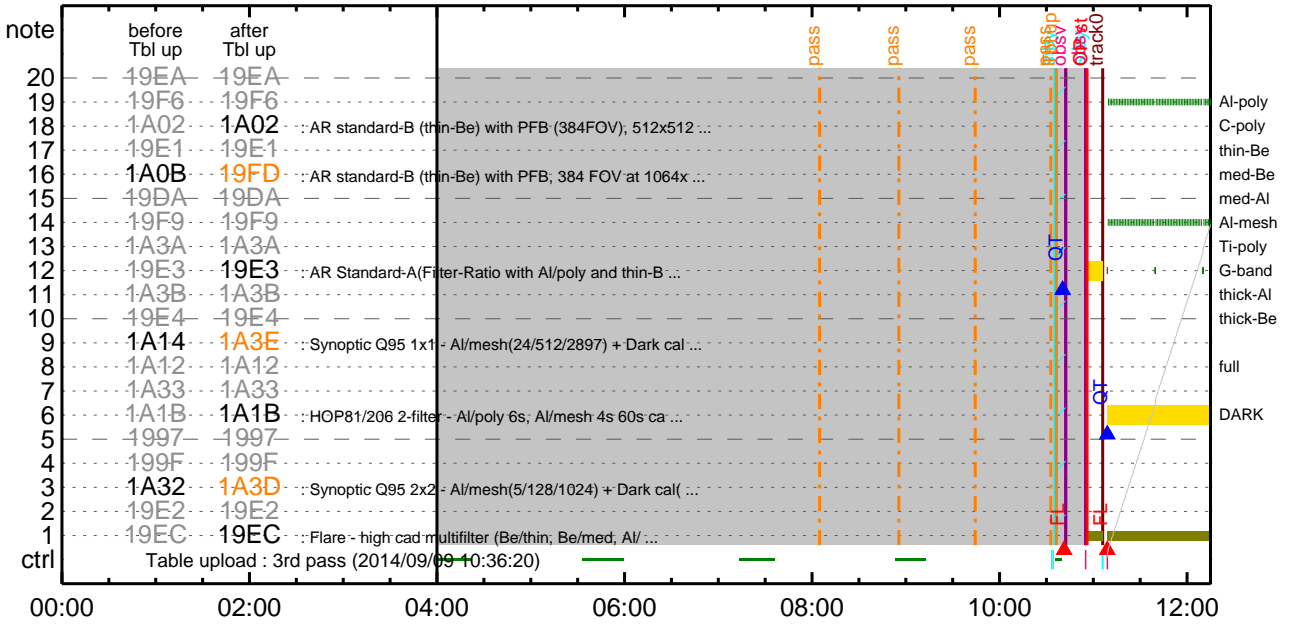
### Flare Detection

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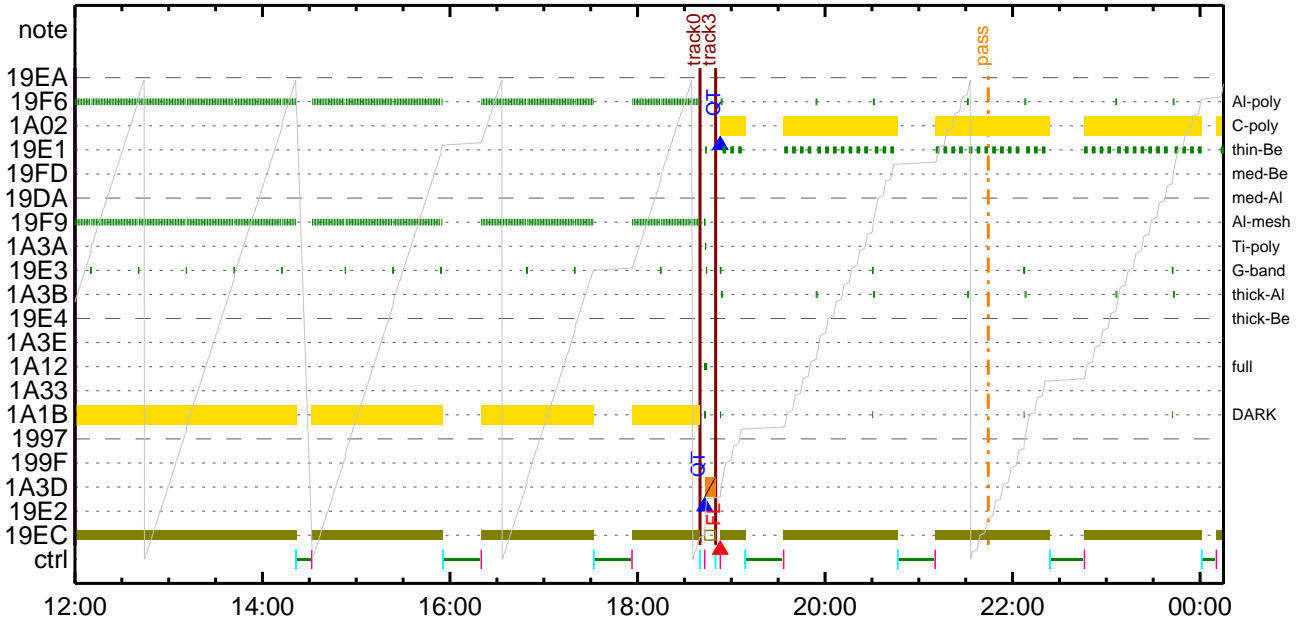
#### FLD Patrol

Term	Pointing (x, y)	Comment
09/09 18:50:18 - 09/10 05:42:48	Track ( -175.8, -384.0) @ 09/09 18:50:00	# AR 12157
09/10 05:52:48 - 09/10 21:10:18	Track ( -80.0, -385.8) @ 09/10 05:52:30	# AR cont. and HOP 257 officially 8-11UT.
09/10 21:20:18 - 09/11 06:00:18	Track ( -80.3, 107.3) @ 09/10 21:20:00	# AR 12158 for NUSTAR observations
09/11 06:10:18 - 09/13 10:06:00	Track ( -1.5, 106.9) @ 09/11 06:10:00	# AR 12158 for HOP 257 officially 8-11UT.
Open/Ti-poly	Open/thick-Al	close Safe Norm 8ms Obs 8x8 Q=50 80sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

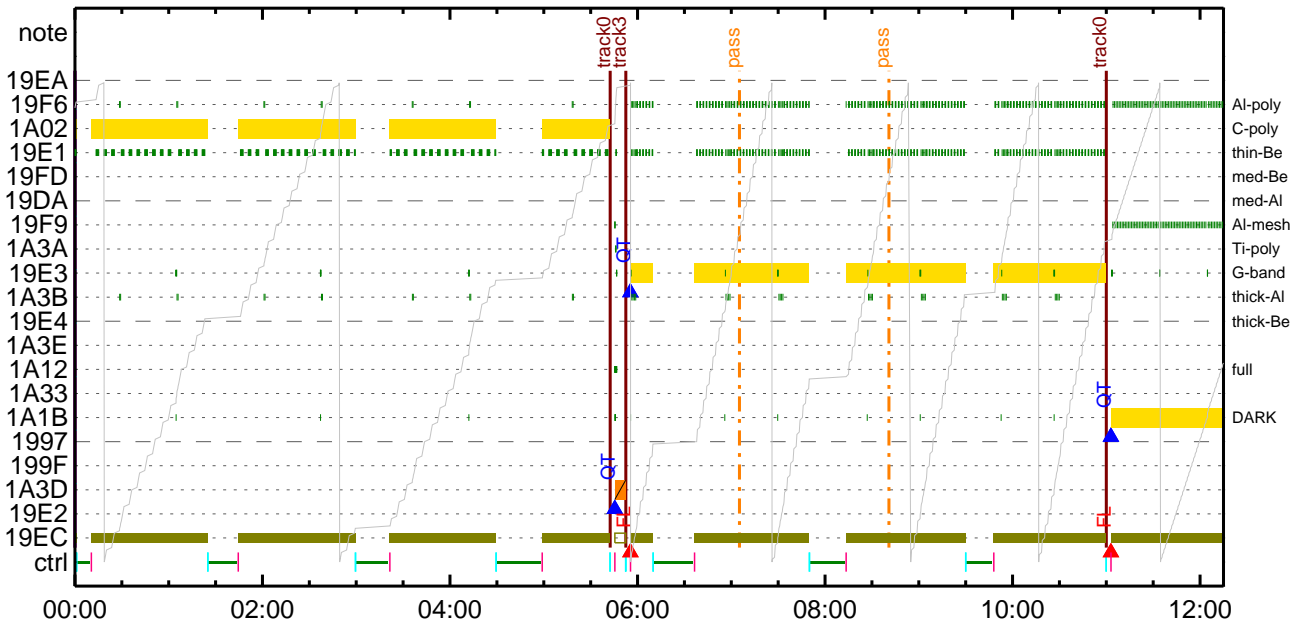
### CMDI #0560 2014/09/09



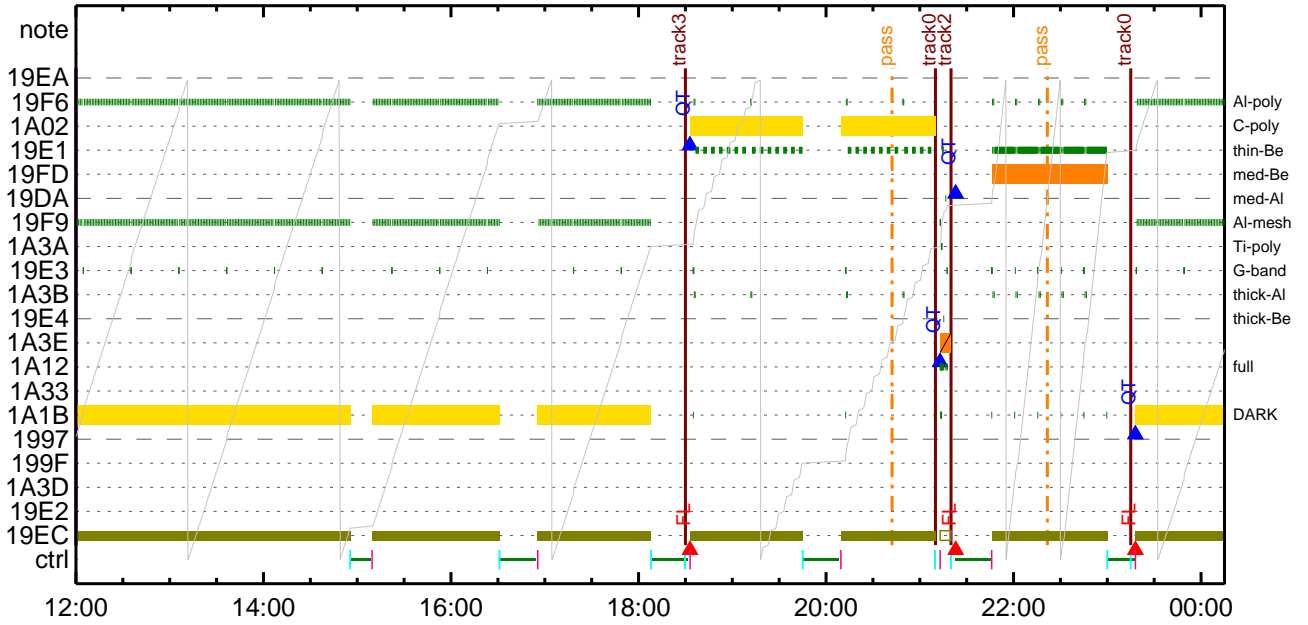
### CMDI #0560 2014/09/09



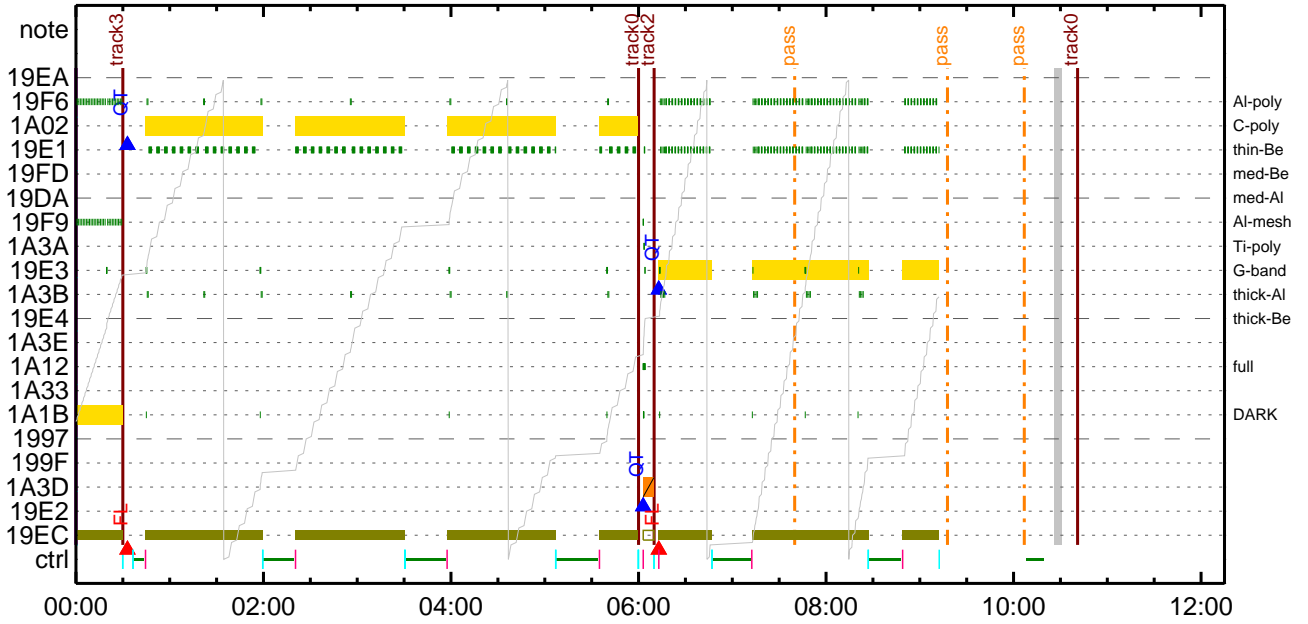
### CMDI #0560 2014/09/10



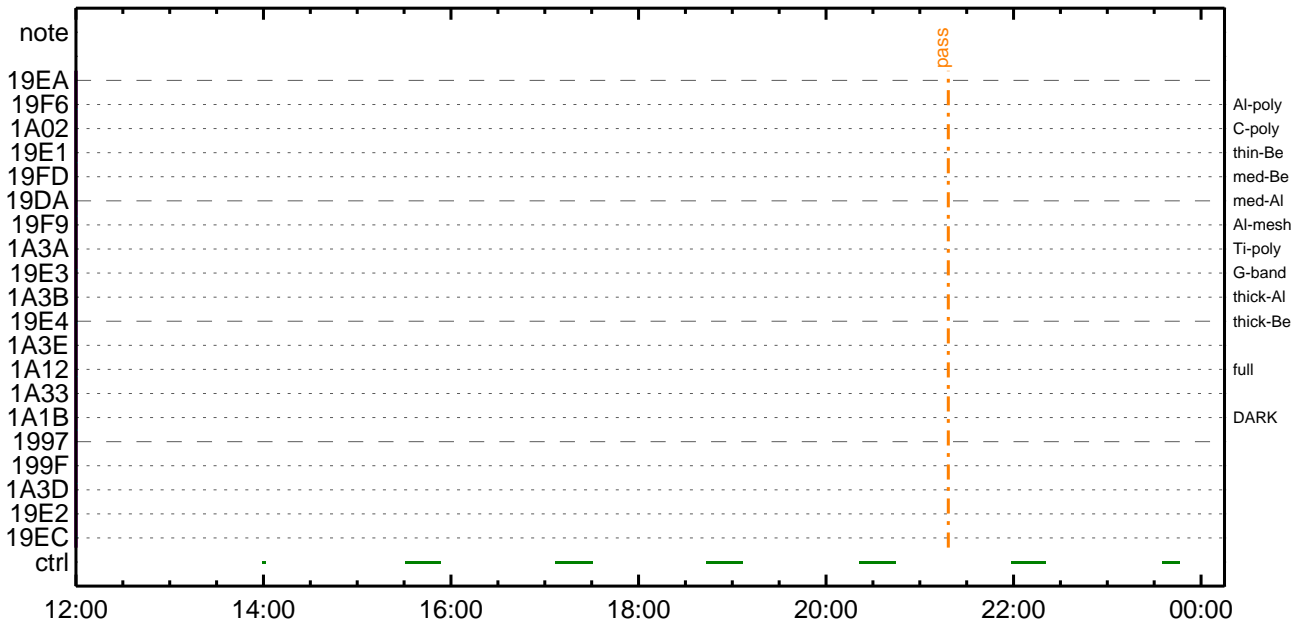
CMDI #0560 2014/09/10



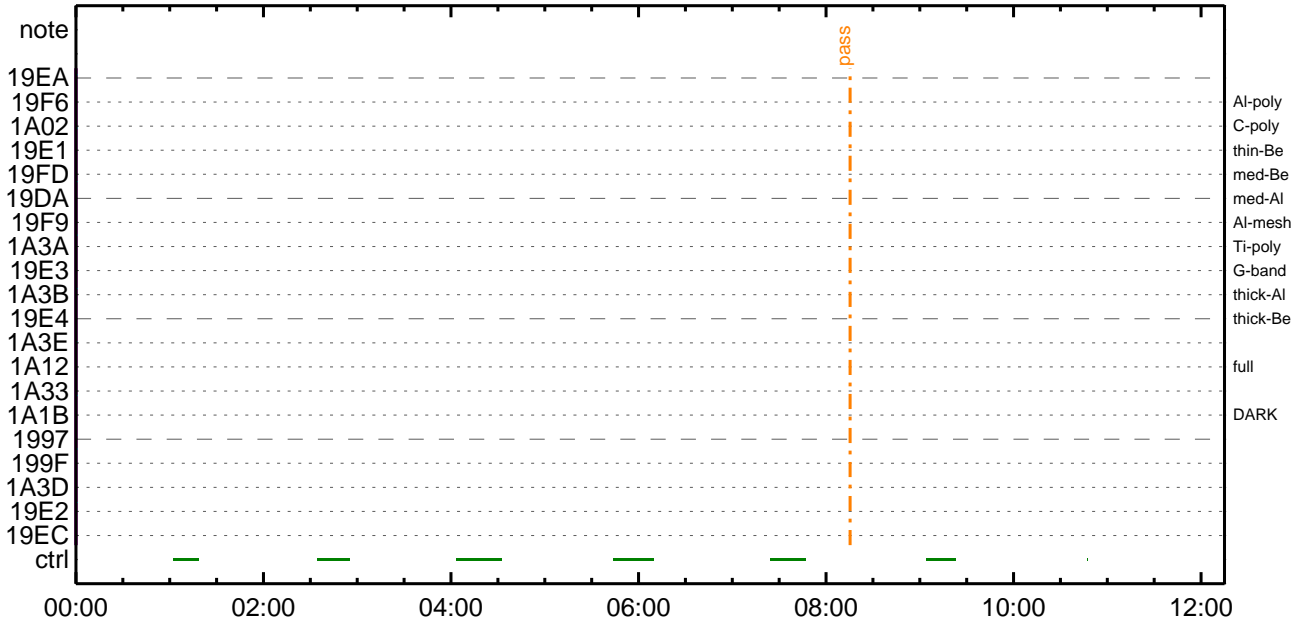
CMDI #0560 2014/09/11



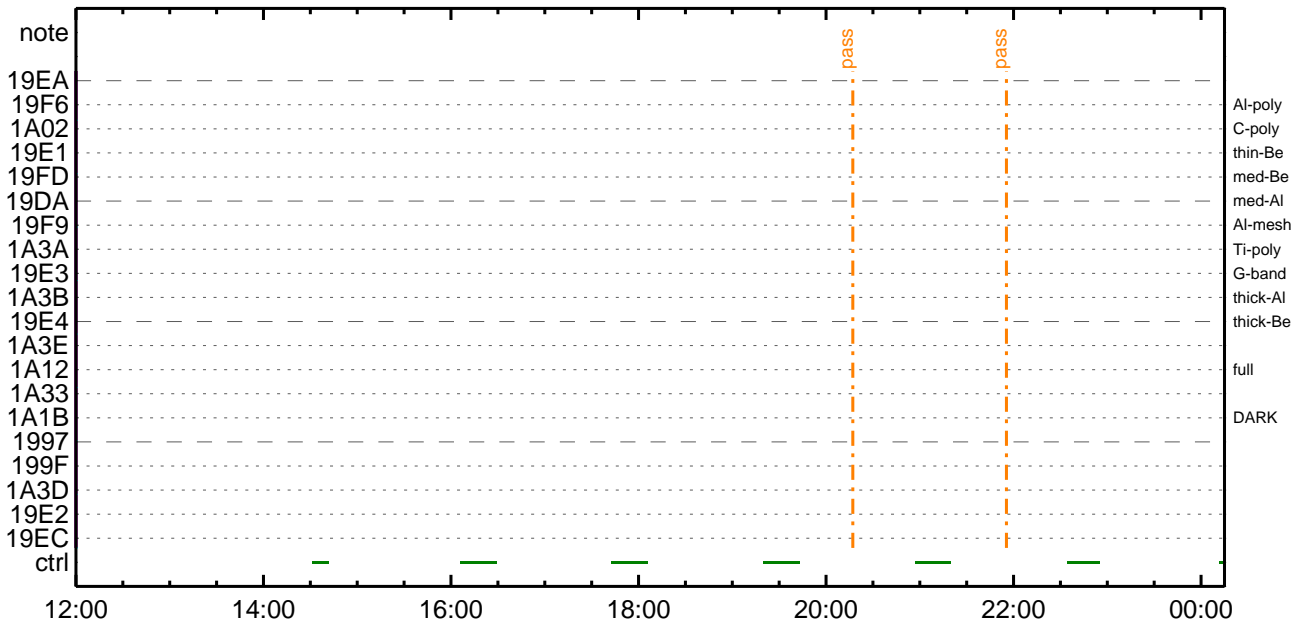
CMDI #0560 2014/09/11



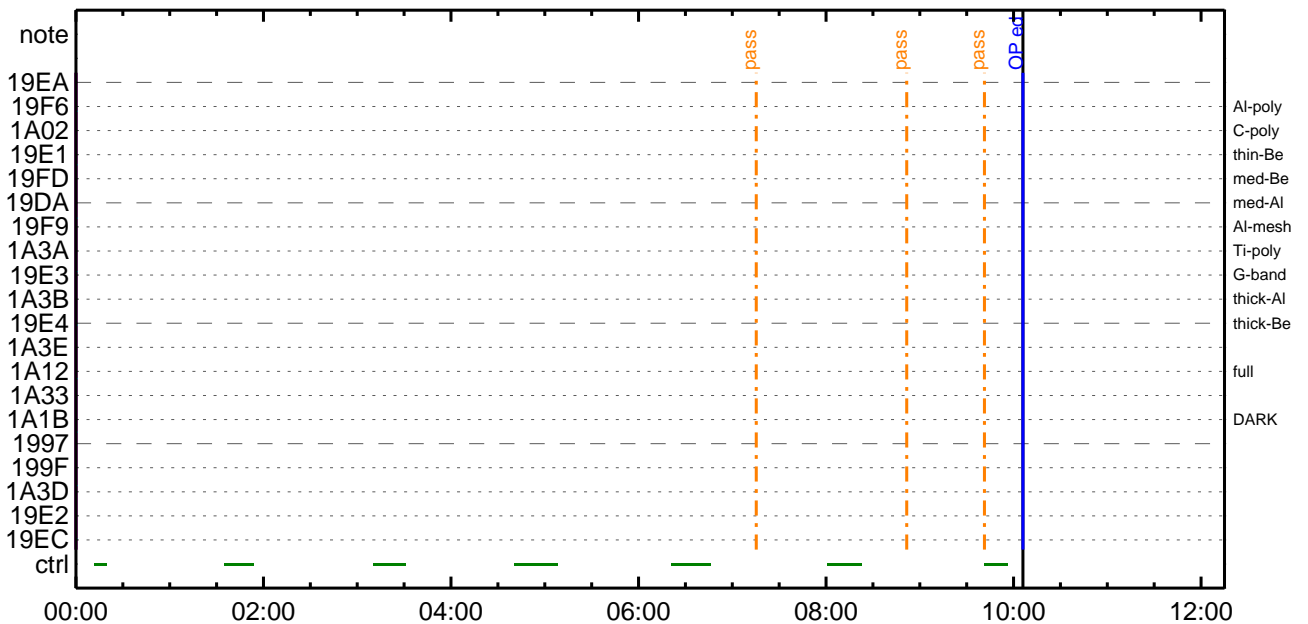
CMDI #0560 2014/09/12



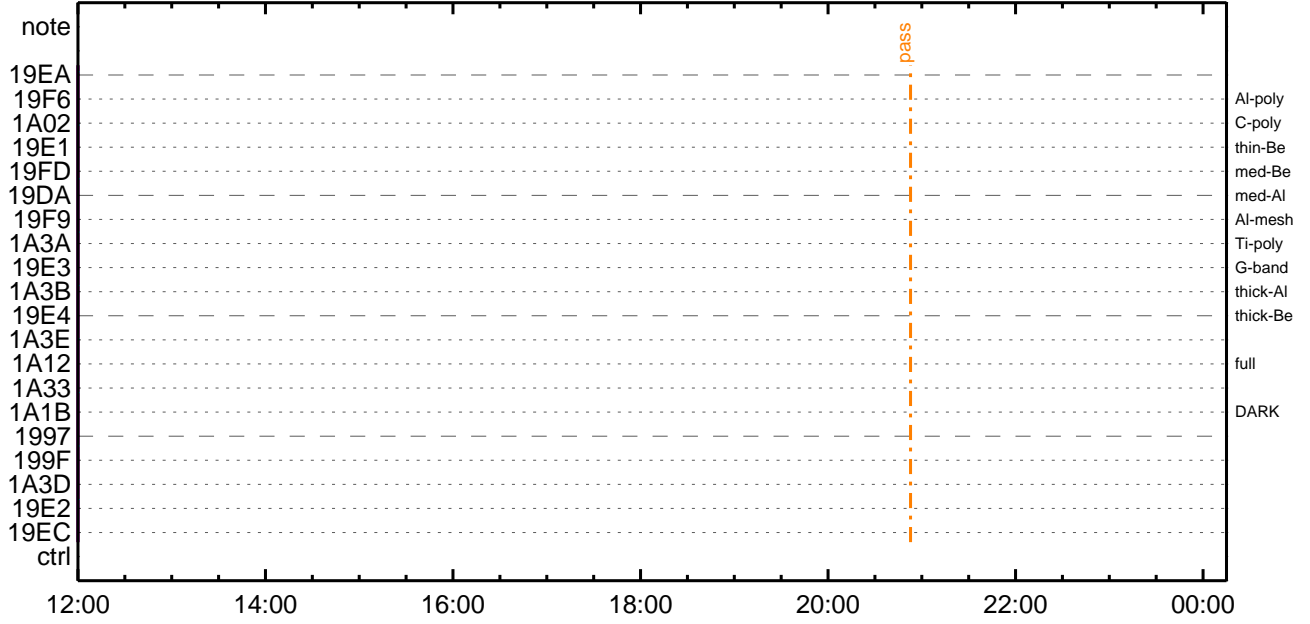
CMDI #0560 2014/09/12



CMDI #0560 2014/09/13



CMDI #0560 2014/09/13







```

0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-624:OP
0104 ( )
0105 S. OG og-624:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ª î»ò³ îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è² îOKò³ îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ª î»ò³ îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è² îOKò³ îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ª î»ò³ îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è² îOKò³ îÇ§
0165 C.
0166 C. ***** °E²¼ò î½Ã´ ¶Á°òEÉ¬ò°Á÷¿@ (½âµ-YAYOXx½ê½çòðÁÔÃæç½ª°²òè½î¹çòçòâ) *****
0167 C. DHUYâ;4YE;E½Y½;Yi;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼ò îTI-CMDÁ÷¿@²î½î¹ç°²òè²î¹çòçòâ;f
0180 C. çç[HK1_PKT_FORM_NO] EQ 2
0181 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0182 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0183 +. TI 2014-09-09 10:51:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2014-09-09 10:51:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2014-09-09 10:51:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

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```

0194 C.
0195 +. TI 2014-09-09 10:55:59.5
0196 DC 01-B2 DHU_OP_START
0197 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0198 C.
0199 C. °Ê²¼αîÄê%îíñαîîŷÄŷ§ŷÄŷ-¹àîü
0200 C.          çç[HK1_TI_CMD_ENA/DIS]       EQ          ENA
0201 C.          çç[HK1_TI_CMD_NUM]          EQ          4
0202 C.          çç[HK1_NEXT_EXEC_PIM]       EQ          DHU
0203 C.          çç[HK1_NEXT_EXEC_DC]       EQ          0xB3
0204 C.
0205 C. *****
0206 C. TIîî°èŷÄŷÖŷ×
0207 C. *****
0208 C.
0209 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0210 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0211 BC          (03 ab 03 01 02)
0212 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ          07
0213 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ          2B
0214 C.          çç[HK1_DMP_BLOCK_NUM]      EQ          3
0215 C.          çç[HK1_DMP_REPEAT_NUM]     EQ          0
0216 C.          çç[HK1_DMA_DMP_PIM]       EQ          DHU
0217 +. DC 01-22 DHU_MODE_CHNG
0218 BC          (07 0b f8)
0219 C.          çç[HK1_PKT_FORM_NO]        EQ          7
0220 C.          çç[HK1_PKT_GEN_TIME]       EQ          0.25 s
0221 C.          çç[HK1_S_TLM_BIT_RATE]    EQ          32k
0222 C.          çç[HK1_X_TLM_BIT_RATE]    EQ          4M
0223 C.          çç[HK1_DMP_CHK_FLG]       EQ          EXEC
0224 C.
0225 C. ŷÄŷÖŷ×½ªî»αò³îç§
0226 C.          çç[HK1_DMP_CHK_FLG]       EQ          NON
0227 C.
0228 C. RAM ID=TI_TBLαîî¾È¹ç•è²îOKαò³îç§
0229 C.
0230 C. DHUŷâ;¼ŷÉ;È¼ŷ¼. ŷî;¼ŷÈ;Èαòîãα¹
0231 +. DC 01-22 DHU_MODE_CHNG
0232 BC          (02 0a f8)
0233 C.          çç[HK1_PKT_FORM_NO]        EQ          2
0234 C.          çç[HK1_PKT_GEN_TIME]       EQ          0.5S
0235 C.          çç[HK1_S_TLM_BIT_RATE]    EQ          32K
0236 C.          çç[HK1_X_TLM_BIT_RATE]    EQ          4M
0237 C.
0238 C. *****
0239 C. SOT TI command set
0240 C. *****
0241 C. Execute, after the success of OP upload.
0242 +. TI 2014-09-09 10:55:16.0
0243 DC 07-F0 MDP_SOT_MODE_STBY
0244 BC          (41)
0245 C. -----
0246 C. HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0247 C. -----
0248 C. ***** SOT END *****
0249 C. Stop EIS observation and temporarily disable EIS mode changes
0250 C.
0251 C.
0252 C. ***** Start EIS operation (TI set) *****
0253 C. Execute, after the success of OP upload.
0254 C. Set EIS TI-commands
0255 +. TI 2014-09-09 10:55:30.0
0256 DC 07-FC EIS_MODE_MANU
0257 BC          (21 02)
0258 +. TI 2014-09-09 10:55:40.0
0259 DC 07-FC EIS_MODE_CHG_DIS
0260 BC          (22)
0261 C.          [ ] [HK1_TI_CMD_NUM]       EQ          2 COUNTUP
0262 C. ***** End EIS operation (TI set) *****
0263 C.
0264 C.
0265 C.
0266 C. ***** XRT START *****
0267 C. Execute, after the success of OP upload.
0268 +. TI 2014-09-09 10:55:00.0
0269 DC 07-F0 MDP_XRT_MODE_STBY
0270 BC          (c3)
0271 C.          [ ] [HK1_TI_CMD_NUM]       EQ          1COUNTUP
0272 C.
0273 C. ***** XRT END *****
0274 C.
0275 C. ***** MDP ´ûÃîαî»ö¼ŷαÈÄα¹αèDCBC•x²è *****
0276 C. (¼ª°îŷÖŷÄŷÈŷŷŷÈŷáŷçŷèèÈ¼αα¼Ã»Ûα¹αè)
0277 S. DC-BC dcbc-402:DCBC
0278 (MDP_known_event)
0279 C.
0280 C.
0281 C. ***** ŷÐŷ¹•î Daily±çîñαè´Øα¹αèDCBC•x²è *****
0282 S. DC-BC dcbc-153:DCBC
0283 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0284 C.
0285 C.
0286 C. ;ãLOSŷÄŷ§ŷÄŷ-¼Ã»Û;ã
0287 C.
0288 C. ***** LOS *****
0289 C.

```

(a) Spacecraft Operation Procedure (real-commands)

```

main-625 2014-09-09 12:28:21 94 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY~¼Ä»Û;ä
0005 C.
0006 C. YÀYB;¼Y³YFYOYÉÄ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;Èá¿âÁð•µ°Æ»Í×ÁÇáÍ¥çYÁY×Yí;¼YÉj;ÈÈè%µ•íÉj;ÈÈÈ¼°ÇÔâ•â¿¼i¹çáI;çÄ®.ùá¹áèâáçÄ+¿®â•âÈâáá³âÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0015 C. Upload the Orbit Element and the Target Attitude
0016 C. RAM-ID:TARGET_ATT
0017 . S. RAM ram-150:TARGET_ATT
0018 ( )
0019 C.
0020 C.
0021 C. Set the dump memory area of TARGET_ATT
0022 +. DC 02-48 AOCU_DUMP_SET
0023 BC (07 00 00 00 18 00)
0024 C.
0025 C. <A_STS1>[MEMORY OPERATE SATUS] ADRS = 070000 [ ]
0026 C.
0027 C.
0028 C. Change the TLMFormatNo for the AOCs Dump Format
0029 +. DC 01-22 DHU_MODE_CHNG
0030 BC (04 0b f8)
0031 C.
0032 C. Wait for AOCSDUMP to end
0033 C.
0034 . C. Check the dump memory
0035 C.
0036 C. Result = OK [ ]
0037 C.
0038 +. DC 01-22 DHU_MODE_CHNG
0039 BC (02 0a f8)
0040 C.
0041 C. <A_***>[TLM STS] FMT = 2 [ ]
0042 C.
0043 +. DC 02-8E AOCU_ORB_UPD
0044 . C.
0045 . C. ***** AOCs Commands (Orbital Element Update) *****
0046 C. Update the orbital element
0047 +. DC 02-50 AOCU_ORB_PRPGT_START
0048 BC (16)
0049 + DC 02-8E AOCU_ORB_UPD
0050 C.
0051 C. <A_ORB>[ORBIT] EPC = 7652815.3 +- 1.0 (s) [ ]
0052 C.
0053 . C.
0054 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0055 +. DC 07-FC EIS_MODE_CHG_ENA
0056 BC (20)
0057 . C. Verify EIS_MODE_CHG_FLG is ENA
0058 +. DC 07-FC EIS_MODE_MANU
0059 BC (21 02)
0060 . C. Verify EIS in MANUAL mode
0061 . C. Estimated OBSTBL upload time is 21s
0062 C. *****
0063 C. EIS START OBSTBL LOAD
0064 C. *****
0065 . S. RAM ram-820:EIS_OBSTBL
0066 ( )
0067 +. DC 07-FC EIS_DUMP_OBSTBL
0068 BC (07 07 07 00 00 70 00)
0069 C.
0070 C. Execute, after the success of OBSTBL upload.
0071 C. Set EIS TI-commands
0072 +. TI 2014-09-09 10:55:50.0
0073 DC 07-FC EIS_MODE_CHG_ENA
0074 BC (20)
0075 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0076 C. *****
0077 C. EIS END OBSTBL LOAD
0078 C. *****
0079 C.
0080 . C. ***** MDP `ûÁîáí»ö¼YáÉÁðá¹âDCBC•×²è *****
0081 C. (¼á°íYÓYÁYÉYßYÉYáYçYèâE¼¼â¼¼Ä»Ûá¹âè)
0082 . S. DC-BC dcbc-402:DCBC
0083 (MDP_known_event)
0084 C.
0085 C.
0086 . C. ***** YDÿ¹.İ Daily±¿ÎÑâÉ´Øá¹âèDCBC•×²è *****
0087 . S. DC-BC dcbc-153:DCBC
0088 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0089 C.
0090 C.
0091 C. ;ãLOSÁYŞYÁY~¼Ä»Û;ä
0092 C.
0093 . C. ***** LOS *****
0094 C.

```



```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 04 b1 b1 06 06)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 05 85 83 06 06)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 06 85 83 06 06)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 07 85 83 08 08)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 08 80 80 20 20)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 09 80 80 20 08)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 0a 80 80 08 20)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 0f 80 80 06 06)
0112 + DC 07-F0 MDP_XRT_ROI_SET
0113 BC (cd 10 80 80 08 08)
0114 + DC 07-F0 MDP_XRT_FLD_ENA
0115 BC (d8)
0116 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0117 BC (c8)
0118 + DC 07-F0 MDP_XRT_AEC_RESET
0119 BC (d0)
0120 + DC 07-F0 MDP_XRT_ARS_DIS
0121 BC (d5)
0122 + DC 07-F0 MDP_XRT_FLD_RESET
0123 BC (da)
0124 + DC 07-F0 MDP_XRT_QT_PROG_SET
0125 BC (c4 0c)
0126 + DC 07-F0 MDP_XRT_FL_PROG_SET
0127 BC (c5 01)
0128 . C. ----- Success Verify ? OK / NG ____
0129 C.
0130 C.
0131 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0132 C.
0133 + DC 07-F0 MDP_XRT_MODE_OBSV
0134 BC (c2)
0135 + TI 2014-09-09 10:55:02.0
0136 DC 07-F0 MDP_XRT_MODE_OBSV
0137 BC (c2)
0138 + TI 2014-09-09 10:55:04.0
0139 DC 07-F0 MDP_XRT_CTRL_AUTO
0140 BC (c0)
0141 . C. ----- Success Verify ? OK / NG ____
0142 C.
0143 C. ***** XRT END *****
0144 C.
0145 . C. ***** MDP `uAÎnî»ô%ÿnÊÂð¹nèDCBC•x²è *****
0146 C. (%â°îÿÓÿÄÿÈÿPÿËÿâÿçÿènÈ%¼n¼Ä»Û¹nè)
0147 . S. DC-BC dcbc-402:DCBC
0148 (MDP_known_event)
0149 C.
0150 C.
0151 . C. ***** ÿDÿ¹.Ï Daily±;îÑnÈ´Ø¹nèDCBC•x²è *****
0152 . S. DC-BC dcbc-153:DCBC
0153 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0154 C.
0155 C.
0156 . C. ;ãLOSÿÁÿSÿÿÿ¼Ä»Û;ã
0157 C.
0158 . C. ***** LOS *****
0159 C.
```

Sep 09, 14 12:28

XRT\_OGLIST\_0560.chk

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\*\*\* OP Sequence for XRT \*\*\*

2014/09/09	11:05:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/09	11:05:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/09	11:05:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/09/09	11:06:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	00 b1 59 02 24		
2014/09/09	11:06:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/09/09	11:06:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/09/09	11:06:22.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/09/09	11:06:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/09/09	11:06:26.0	XRT_FLD_RESET_407_OG [0x197]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/09/09	11:08:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06		
2014/09/09	11:08:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01		
2014/09/09	11:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/09/09	14:21:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/09	14:21:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/09	14:21:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/09/09	14:21:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/09/09	14:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/09/09	14:30:30.0	XRT_Custom_430_OG [0x1ae]					
2014/09/09	14:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/09/09	15:55:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/09	15:55:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/09	15:55:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/09/09	15:55:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/09/09	15:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/09/09	16:19:00.0	XRT_Custom_430_OG [0x1ae]					
2014/09/09	16:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/09/09	17:32:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/09	17:32:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/09	17:32:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/09/09	17:32:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/09/09	17:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/09/09	17:55:30.0	XRT_Custom_430_OG [0x1ae]					
2014/09/09	17:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/09/09	18:39:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/09	18:39:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/09	18:39:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
		XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2014/09/09	18:40:00.0	AOCS_Ore-point_Start_2_OG [0x098]					
		AOCU_NM	5	02-76	00 00 00 00 00		
2014/09/09	18:40:18.0	XRT_FLD_DIS_422_OG [0x1a6]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2014/09/09	18:42:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2014/09/09	18:42:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/09/09	18:42:58.0	XRT_QT_PROG_SET_428_OG [0x1ac]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2014/09/09	18:43:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/09/09	18:49:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/09	18:49:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/09	18:49:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/09/09	18:50:00.0	AOCS_Ore-point_Start_3_OG [0x099]					
		AOCU_NM	5	02-76	03 00 00 00 00		
2014/09/09	18:50:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		

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2014/09/09	18:50:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/09/09	18:50:22.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/09/09	18:50:24.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/09/09	18:50:26.0	XRT_FLD_RESET_407_OG [0x197]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/09/09	18:52:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12
2014/09/09	18:52:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/09/09	18:53:00.0	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/09/09	19:09:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/09	19:09:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/09	19:09:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/09/09	19:09:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/09/09	19:12:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/09/09	19:32:30.0	XRT_Custom_430_OG [0x1ae]			
2014/09/09	19:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/09/09	20:46:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/09	20:46:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/09	20:46:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/09/09	20:46:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/09/09	20:49:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/09/09	21:09:30.0	XRT_Custom_430_OG [0x1ae]			
2014/09/09	21:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/09/09	22:24:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/09	22:24:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/09	22:24:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/09/09	22:24:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/09/09	22:27:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/09/09	22:45:00.0	XRT_Custom_430_OG [0x1ae]			
2014/09/09	22:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/09/10	00:01:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/10	00:01:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/10	00:01:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/09/10	00:01:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/09/10	00:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/09/10	00:09:30.0	XRT_Custom_430_OG [0x1ae]			
2014/09/10	00:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/09/10	01:25:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/10	01:25:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/10	01:25:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/09/10	01:25:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/09/10	01:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/09/10	01:43:30.0	XRT_Custom_430_OG [0x1ae]			
2014/09/10	01:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/09/10	02:59:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/10	02:59:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/10	02:59:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/09/10	02:59:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/09/10	03:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/09/10	03:20:30.5	XRT_Custom_430_OG [0x1ae]			
2014/09/10	03:21:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/09/10	04:29:30.0	XRT_CTRL_MANU_400_OG [0x190]			

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2014/09/10	04:29:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/10	04:29:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/10	04:29:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/09/10	04:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/09/10	04:58:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/09/10	04:59:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/09/10	05:42:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/10	05:42:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/10	05:42:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00		
2014/09/10	05:42:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00		
2014/09/10	05:42:48.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2014/09/10	05:45:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2014/09/10	05:45:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/09/10	05:45:28.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03		
2014/09/10	05:45:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/09/10	05:52:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/10	05:52:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/10	05:52:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/09/10	05:52:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	03 00 00 00 00		
2014/09/10	05:52:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/09/10	05:52:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/09/10	05:52:52.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/09/10	05:52:54.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/09/10	05:52:56.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/09/10	05:55:26.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c		
2014/09/10	05:55:28.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01		
2014/09/10	05:55:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/09/10	06:10:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/10	06:10:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/10	06:10:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/09/10	06:10:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/09/10	06:13:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/09/10	06:35:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/09/10	06:36:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/10	07:50:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/10	07:50:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/10	07:50:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/09/10	07:50:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/09/10	07:53:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/09/10	08:12:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/09/10	08:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/10	09:30:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/10	09:30:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/10	09:30:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/09/10	09:30:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/09/10	09:33:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/09/10	09:47:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/09/10	09:48:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		



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2014/09/10	10:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/10	10:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/10	10:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/09/10	11:00:00.0	AOCS_Ore-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	00 51 58 02 24				
2014/09/10	11:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/09/10	11:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/09/10	11:00:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/09/10	11:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/09/10	11:00:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/09/10	11:02:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 06				
2014/09/10	11:02:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01				
2014/09/10	11:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/09/10	14:55:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/10	14:55:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/10	14:55:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/09/10	14:55:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/09/10	14:58:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/09/10	15:08:30.0	XRT_Custom_430_OG [0x1ae]							
2014/09/10	15:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/09/10	16:31:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/10	16:31:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/10	16:31:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/09/10	16:31:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/09/10	16:34:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/09/10	16:54:30.0	XRT_Custom_430_OG [0x1ae]							
2014/09/10	16:55:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/09/10	18:08:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/10	18:08:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/10	18:08:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/09/10	18:08:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/09/10	18:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/09/10	18:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/10	18:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/10	18:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/09/10	18:30:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	03 00 00 00 00				
2014/09/10	18:30:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/09/10	18:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/09/10	18:30:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/09/10	18:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/09/10	18:30:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/09/10	18:32:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 12				
2014/09/10	18:32:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01				
2014/09/10	18:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/09/10	19:45:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/10	19:45:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/10	19:45:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/09/10	19:45:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/09/10	19:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							

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2014/09/10	20:08:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/09/10	20:09:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2014/09/10	21:09:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/09/10	21:09:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/10	21:09:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/10	21:10:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00
2014/09/10	21:10:18.0	XRT_FLD_DIS_422_OG [0x1a6]	AOCU_NM	5	02-76	00 00 00 00 00
2014/09/10	21:12:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2014/09/10	21:12:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/09/10	21:12:58.0	XRT_QT_PROG_SET_401_OG [0x191]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/09/10	21:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 09
2014/09/10	21:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/09/10	21:19:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/10	21:19:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/10	21:20:00.5	AOCS_ORe-point_Start_5_OG [0x09b]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/09/10	21:20:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02 00 00 00 00
2014/09/10	21:20:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2014/09/10	21:20:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/09/10	21:20:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2014/09/10	21:20:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/09/10	21:22:56.0	XRT_QT_PROG_SET_406_OG [0x196]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/09/10	21:22:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 10
2014/09/10	21:45:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/09/10	21:46:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]				
2014/09/10	23:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/09/10	23:00:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/10	23:00:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/10	23:00:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/09/10	23:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/09/10	23:14:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/09/10	23:14:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/10	23:14:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/10	23:15:00.0	AOCS_ORe-point_Start_6_OG [0x09c]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/09/10	23:15:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	00 ae 36 02 24
2014/09/10	23:15:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
2014/09/10	23:15:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/09/10	23:15:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2014/09/10	23:15:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/09/10	23:17:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/09/10	23:17:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 06
2014/09/10	23:18:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/09/11	00:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/09/11	00:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/11	00:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/11	00:30:00.0	AOCS_ORe-point_Start_3_OG [0x099]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/09/11	00:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	03 00 00 00 00
2014/09/11	00:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8

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2014/09/11	00:30:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/09/11	00:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/09/11	00:30:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/09/11	00:32:56.0	XRT_QT_PROG_SET_434_OG [0x1b2]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	12			
2014/09/11	00:32:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2014/09/11	00:36:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/11	00:36:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/11	00:36:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/09/11	00:36:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/09/11	00:39:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/09/11	00:43:30.0	XRT_Custom_430_OG [0x1ae]							
2014/09/11	00:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/09/11	01:59:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/11	01:59:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/11	01:59:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/09/11	01:59:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/09/11	02:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/09/11	02:19:30.0	XRT_Custom_430_OG [0x1ae]							
2014/09/11	02:20:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/09/11	03:30:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/11	03:30:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/11	03:30:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/09/11	03:30:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/09/11	03:33:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/09/11	03:56:30.5	XRT_Custom_430_OG [0x1ae]							
2014/09/11	03:57:30.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/09/11	05:07:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/11	05:07:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/11	05:07:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/09/11	05:07:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/09/11	05:10:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/09/11	05:34:00.5	XRT_Custom_430_OG [0x1ae]							
2014/09/11	05:35:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/09/11	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/11	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/11	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/09/11	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2014/09/11	06:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/09/11	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/09/11	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/09/11	06:02:58.0	XRT_QT_PROG_SET_428_OG [0x1ac]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	03			
2014/09/11	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/09/11	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/11	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/09/11	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/09/11	06:10:00.0	AOCS_Ore-point_Start_5_OG [0x09b]							
		AOCU_NM	5	02-76	02	00	00	00	00
2014/09/11	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/09/11	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/09/11	06:10:22.0	XRT_AEC_RESET_413_OG [0x19d]							

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2014/09/11	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0
2014/09/11	06:10:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/09/11	06:12:56.0	XRT_QT_PROG_SET_425_OG [0x1a9]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/09/11	06:12:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0c
2014/09/11	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01
2014/09/11	06:47:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/09/11	06:47:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/11	06:47:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/11	06:47:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/09/11	06:50:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/09/11	07:11:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/09/11	07:12:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	XRT_Custom_430_OG [0x1ae]			
2014/09/11	08:27:00.0	XRT_CTRL_MANU_400_OG [0x190]	XRT_CTRL_AUTO_424_OG [0x1a8]			
2014/09/11	08:27:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/09/11	08:27:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/11	08:27:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/09/11	08:30:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/09/11	08:48:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/09/11	08:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/09/11	09:12:25.0	XRT_CTRL_MANU_402_OG [0x192]	XRT_Custom_430_OG [0x1ae]			
2014/09/11	10:41:00.0	AOCS_ORe-point_Start_2_OG [0x098]	XRT_CTRL_AUTO_424_OG [0x1a8]			
		AOCU_NM	MDP_XRT_CTRL_AUTO	1	07-F0	c0
			MDP_XRT_CTRL_MANU	1	07-F0	c1
				5	02-76	00 00 00 00 00