

# XRT Timeline to be uploaded on 2014/09/30

Period: 2014/09/30 10:33:00 - 2014/10/04 09:42:00

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Normal mode

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## XOB #1A45: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context (2 loops), with G-b

Term	Pointing (x, y)	Comment
09/30 10:46:00 - 09/30 17:40:28	Track ( 693.8, -271.1) @ 09/30 10:43:00	# HOP 257 @AR12172
09/30 18:03:36 - 09/30 20:22:30	Track ( 693.8, -271.1) @ 09/30 10:43:00	# HOP 257 @AR12172
09/30 21:01:00 - 10/01 06:21:54	Track ( -326.7, 81.7) @ 09/30 20:58:00	# track AR12177
10/01 06:35:00 - 10/01 06:59:54	Track ( -243.8, 78.9) @ 10/01 06:32:00	# track AR12177
10/01 11:33:00 - 10/01 17:44:30	Track ( -199.9, 77.9) @ 10/01 11:30:00	# track AR12177
10/01 18:21:00 - 10/01 22:07:54	Track ( -139.1, 76.8) @ 10/01 18:18:00	# track AR12177
10/02 02:30:00 - 10/02 05:56:24	Track ( -65.3, 76.1) @ 10/02 02:27:00	# track AR12177
10/02 06:09:30 - 10/02 07:59:54	Track ( -32.1, 76.1) @ 10/02 06:06:30	# track AR12177

**PROG= 17 Inf.-time(s)**

- Subr= 1 1-time(s) 2.0sec
  - Seqn= 8 2-time(s) 2.0sec
    - Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
- Subr= 2 2-time(s) 2.0sec
  - Seqn= 24 1-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
    - Open/G-band Open/G-band open Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
  - Seqn= 42 2-time(s) 2.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec
    - Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
  - Seqn= 62 8-time(s) 270.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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## XOB #1A46: AR Standard-B(Morphology), Be-thin w/ 8s cad, no PFB, multifilter context w/ 20 min cad, G-band test w/ 60 min cad, 384x384, 1064x1048

Term	Pointing (x, y)	Comment
09/30 17:41:00 - 09/30 18:03:00	Track ( 693.8, -271.1) @ 09/30 10:43:00	# HOP 257 @AR12172

**PROG= 16 Inf.-time(s)**

- Subr= 1 1-time(s) 2.0sec
  - Seqn= 8 1-time(s) 2.0sec
    - Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
  - Seqn= 24 1-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
    - Open/G-band Open/G-band open Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
- Subr= 2 3-time(s) 2.0sec
  - Seqn= 68 3-time(s) 2.0sec
    - Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - Al-poly/Open thin-Be/Open close Safe Norm 177ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - med-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
    - med-Al/Open Open/thick-Al close Safe Norm 2.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
  - Seqn= 25 150-time(s) 8.0sec
    - thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec

Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval
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## XOB #1A3D: Synoptic Q95 2x2 - Al/mesh(5/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(24/362/2048) + Thi

Term	Pointing (x, y)	Comment
09/30 20:51:00 - 09/30 20:57:54	Fixed ( 0.0, 0.0)	synoptic, shifted 3.5 min
10/01 06:25:00 - 10/01 06:31:54	Fixed ( 0.0, 0.0)	synoptic, shifted 22.0 min
10/01 18:11:00 - 10/01 18:17:54	Fixed ( 0.0, 0.0)	synoptic, shifted 8.0 min
10/02 05:59:30 - 10/02 06:06:24	Fixed ( 0.0, 0.0)	synoptic, shifted -5.5 min

**PROG= 03 1-time(s)**

- Subr= 1 1-time(s) 12.0sec
  - Seqn= 2 1-time(s) 4.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 5ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
    - Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
  - Seqn= 5 1-time(s) 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
    - Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
  - Seqn= 4 1-time(s) 4.0sec
    - Open/Ti-poly Open/Ti-poly close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec

Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 96</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 6</b>		<b>1-time(s)</b>		<b>2.0sec</b>								
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

**XOB #1930: AR Standard-B(Morphology) with PFB, thin-Be + multifilter context, 384x384 at 1064 1048, 120s-cad -w/ G-band VLS Closed Test**

Term	Pointing (x, y)	Comment
10/01 07:03:00 - 10/01 11:29:54	Track ( -495.5, -134.3) @ 10/01 07:00:00	# HOP 257 @AR12178
10/02 08:25:30 - 10/02 09:40:00	Track ( -283.5, -144.7) @ 10/02 08:00:00	# HOP 257 @AR12178
<b>PROG= 14 Inf-time(s)</b>		
<b>Subr= 3</b>		<b>1-time(s)</b> <b>2.0sec</b>
<b>Seqn= 90</b>		<b>1-time(s)</b> <b>2.0sec</b>
Open/G-band	Open/G-band	close Safe Norm 63ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec
<b>Subr= 1</b>		<b>1-time(s)</b> <b>2.0sec</b>
<b>Seqn= 51</b>		<b>1-time(s)</b> <b>4.0sec</b>
Open/Ti-poly	Open/thick-Al	close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
Open/G-band	Open/G-band	open Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec
<b>Seqn= 30</b>		<b>3-time(s)</b> <b>14.0sec</b>
Open/Ti-poly	Open/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
Al-poly/Open	Al-poly/thick-Be	close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
C-poly/Open	C-poly/thick-Al	close Safe Norm 250ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
thin-Be/Open	med-Be/Open	close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
med-Be/Open	med-Be/Open	close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
med-Al/Open	med-Al/thick-Al	close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec
<b>Subr= 2</b>		<b>1-time(s)</b> <b>2.0sec</b>
<b>Seqn= 74</b>		<b>40-time(s)</b> <b>120.0sec</b>
thin-Be/Open	Open/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 20.0sec
thin-Be/Open	Open/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 20.0sec
thin-Be/Open	Open/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 20.0sec
thin-Be/Open	Open/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 3 20.0sec
thin-Be/Open	Open/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 20.0sec
thin-Be/Open	Open/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 20.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Flare mode**

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**XOB #19EC: Flare - high cad multifilter (Be/thin, Be/med, Al/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gband (4**

Term	Pointing (x, y)	Comment
09/30 10:46:00 - 09/30 17:40:28	Track ( 693.8, -271.1) @ 09/30 10:43:00	# HOP 257 @AR12172
09/30 18:03:36 - 09/30 20:22:30	Track ( 693.8, -271.1) @ 09/30 10:43:00	# HOP 257 @AR12172
09/30 21:01:00 - 10/01 06:21:54	Track ( -326.7, 81.7) @ 09/30 20:58:00	# track AR12177
10/01 06:35:00 - 10/01 06:59:54	Track ( -243.8, 78.9) @ 10/01 06:32:00	# track AR12177
10/01 07:03:00 - 10/01 11:29:54	Track ( -495.5, -134.3) @ 10/01 07:00:00	# HOP 257 @AR12178
10/01 11:33:00 - 10/01 17:44:30	Track ( -199.9, 77.9) @ 10/01 11:30:00	# track AR12177
10/01 18:21:00 - 10/01 22:07:54	Track ( -139.1, 76.8) @ 10/01 18:18:00	# track AR12177
10/02 02:30:00 - 10/02 05:56:24	Track ( -65.3, 76.1) @ 10/02 02:27:00	# track AR12177
10/02 06:09:30 - 10/02 07:59:54	Track ( -32.1, 76.1) @ 10/02 06:06:30	# track AR12177
10/02 08:25:30 - 10/02 09:40:00	Track ( -283.5, -144.7) @ 10/02 08:00:00	# HOP 257 @AR12178
<b>PROG= 01 30-time(s)</b>		
<b>Subr= 1</b>		<b>30-time(s)</b> <b>2.0sec</b>
<b>Seqn= 26</b>		<b>1-time(s)</b> <b>4.0sec</b>
thin-Be/Open	med-Be/Open	close Safe Norm 8ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
med-Be/Open	Open/thick-Al	close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be	close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
<b>Subr= 2</b>		<b>1-time(s)</b> <b>2.0sec</b>
<b>Seqn= 10</b>		<b>1-time(s)</b> <b>2.0sec</b>
med-Al/Open	med-Al/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be	close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
<b>Seqn= 11</b>		<b>1-time(s)</b> <b>2.0sec</b>
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
<b>Seqn= 15</b>		<b>1-time(s)</b> <b>2.0sec</b>
Open/G-band	Open/G-band	open Safe Norm 44ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

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**Active Region Search**

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NOT USED

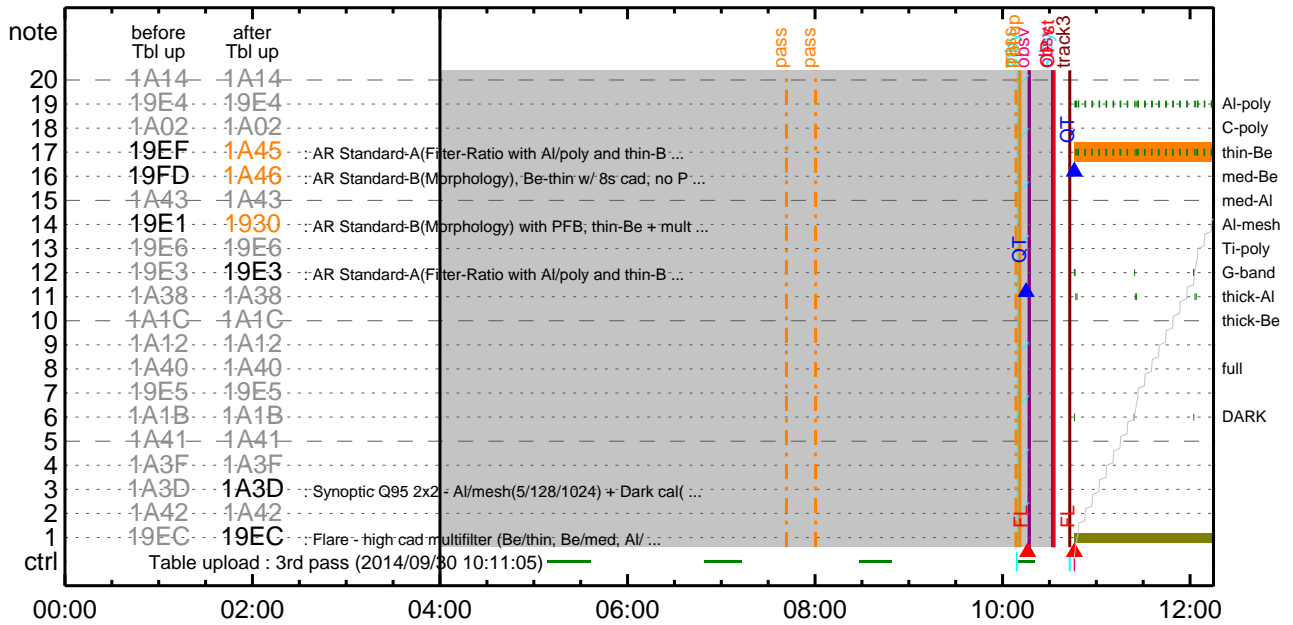
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## Flare Detection

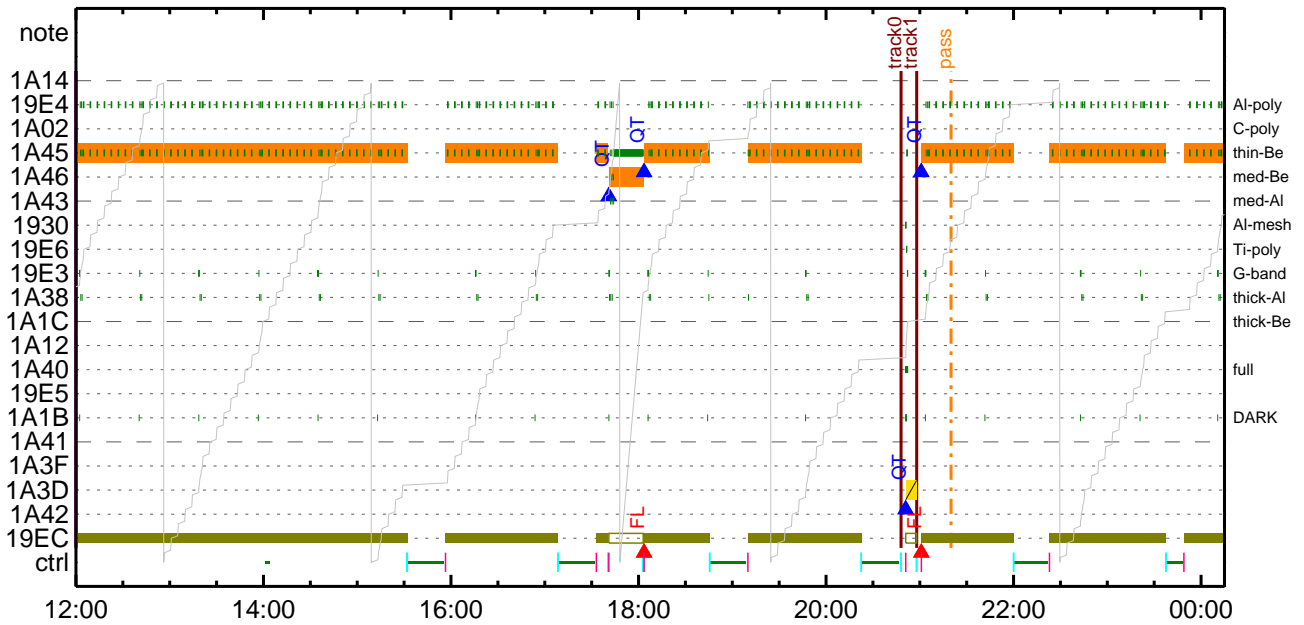
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FLD Patrol											
Term		Pointing (x, y)						Comment			
09/30 18:03:24 - 09/30 20:48:18	Track ( 693.8, -271.1)	④ 09/30 10:43:00				# HOP 257 @AR12172					
09/30 20:58:18 - 10/01 06:22:18	Track ( -326.7, 81.7)	④ 09/30 20:58:00				# track AR12177					
10/01 06:32:18 - 10/01 18:08:18	Track ( -243.8, 78.9)	④ 10/01 06:32:00				# track AR12177					
10/01 18:18:18 - 10/02 05:56:48	Track ( -139.1, 76.8)	④ 10/01 18:18:00				# track AR12177					
10/02 06:06:48 - 10/04 09:42:00	Track ( -32.1, 76.1)	④ 10/02 06:06:30				# track AR12177					
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50	80sec		
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval

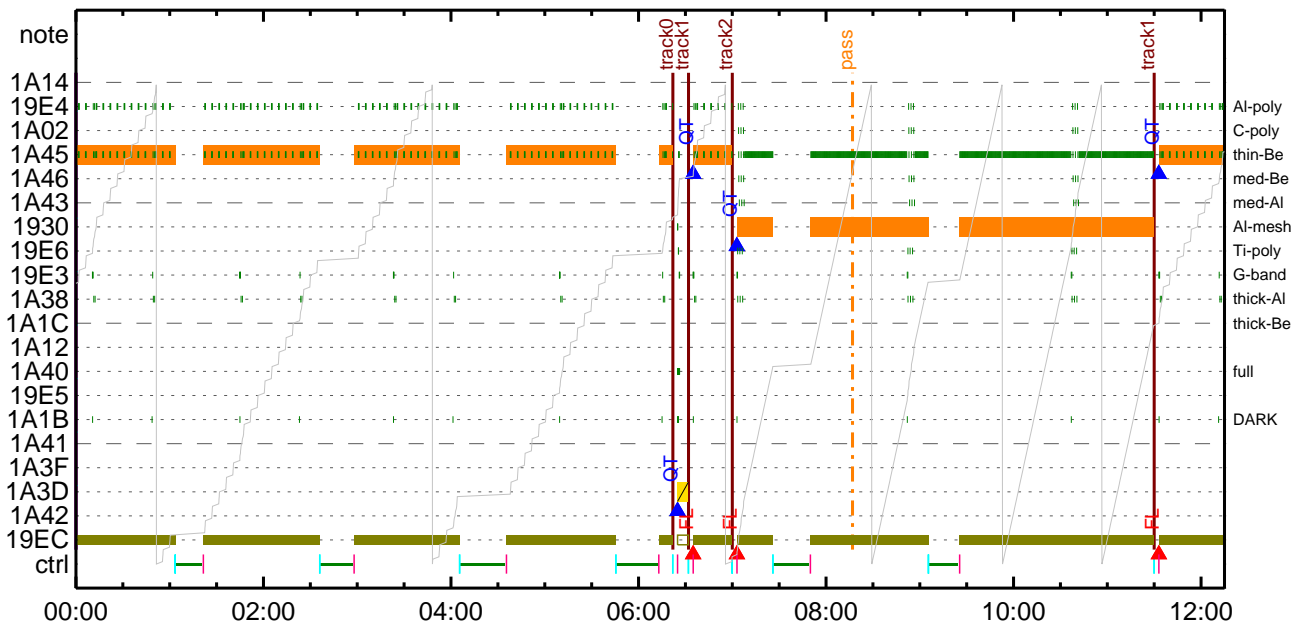
### CMDI #0605 2014/09/30



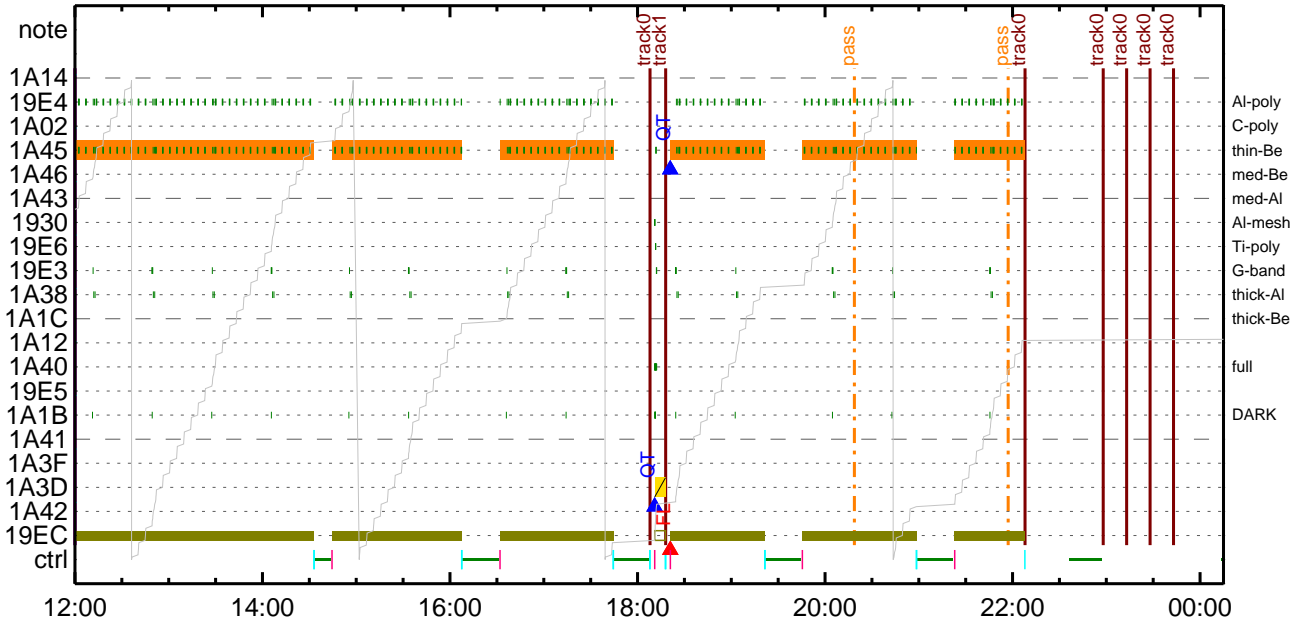
### CMDI #0605 2014/09/30



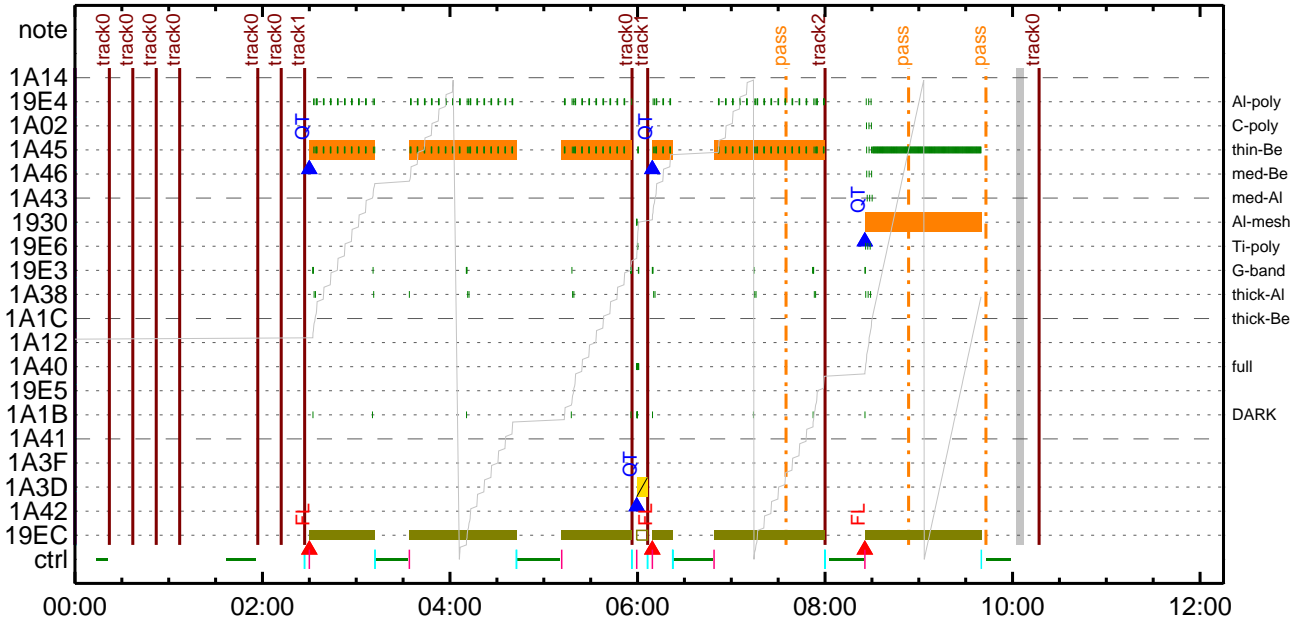
### CMDI #0605 2014/10/01



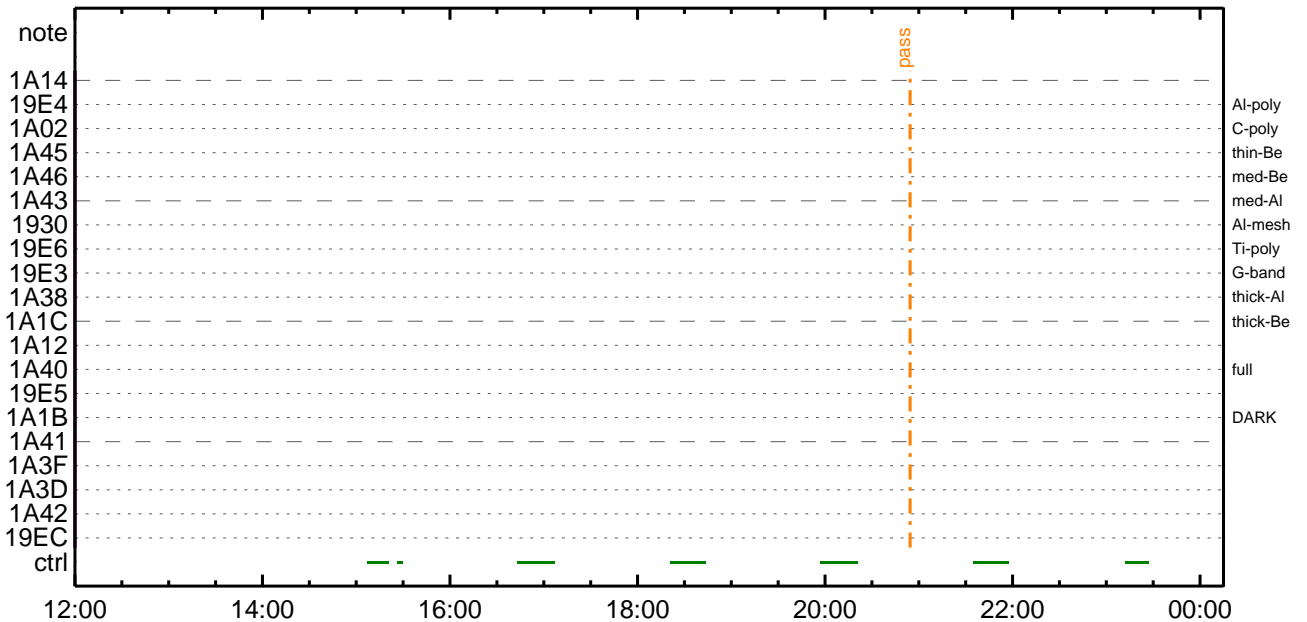
### CMDI #0605 2014/10/01



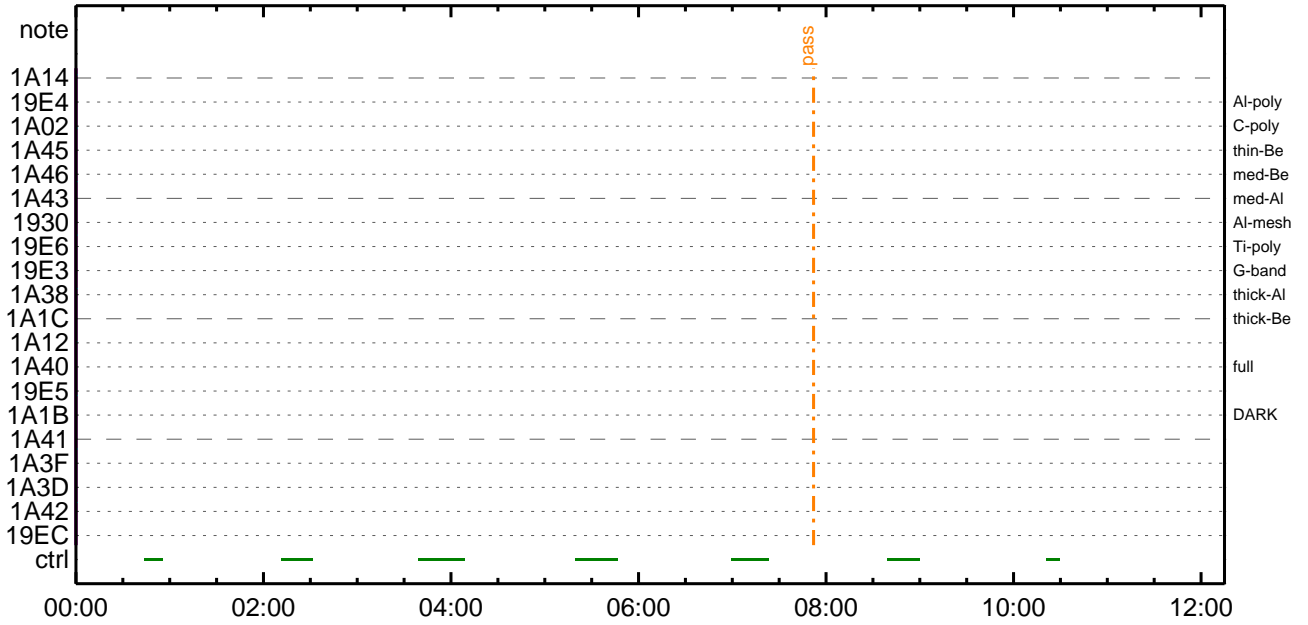
### CMDI #0605 2014/10/02



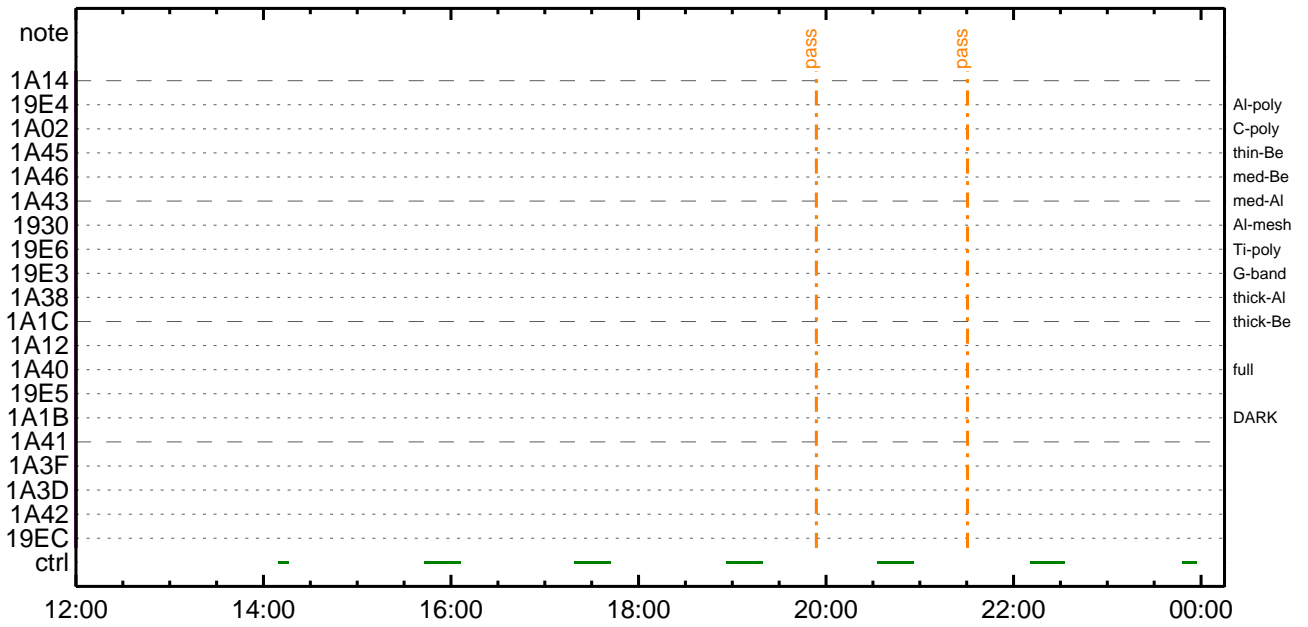
### CMDI #0605 2014/10/02



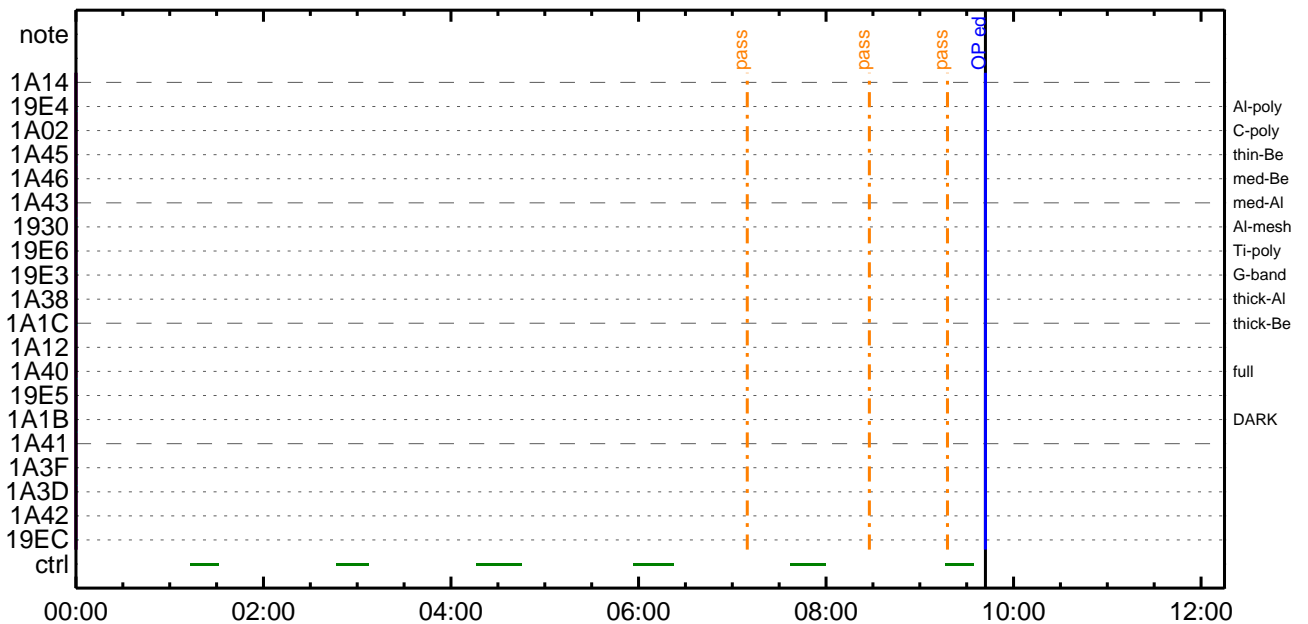
CMDI #0605 2014/10/03



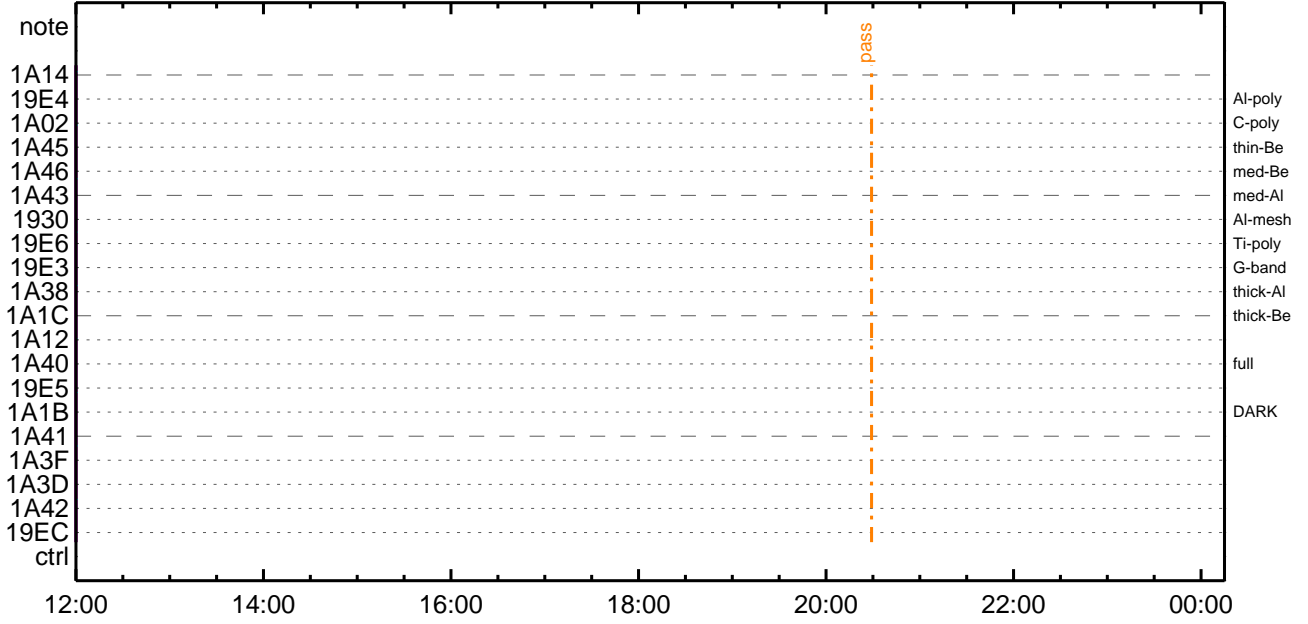
CMDI #0605 2014/10/03



CMDI #0605 2014/10/04



CMDI #0605 2014/10/04







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0096 C.
0097 C.
0098 C. *****
0099 C. OP/OGY1;4YE;|YAYOX
0100 C. *****
0101 C.
0102 C. ;ãOP/OGY1;4YE;ã
0103 S. OP op-664:OP
0104 ( )
0105 S. OG og-664:OG
0106 ( )
0107 C.
0108 C. ;ãNMOG&OPfî°èYAYOX;ã
0109 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0110 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0111 BC (20 00 7f 01 02)
0112 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0113 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0114 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0115 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0116 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0117 +. DC 01-22 DHU_MODE_CHNG
0118 BC (07 0b f8)
0119 C. çç[HK1_PKT_FORM_NO] EQ 7
0120 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0121 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0122 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0123 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0124 C. YAYOXx½ªî»ò³îÇ§
0125 C. çç[HK1_DMP_CHK_FLG] EQ NON
0126 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0127 C.
0128 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0129 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0130 BC (20 80 7f 01 02)
0131 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0132 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0133 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0134 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0135 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0136 +. DC 01-22 DHU_MODE_CHNG
0137 BC (07 0b f8)
0138 C. çç[HK1_PKT_FORM_NO] EQ 7
0139 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0140 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0141 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0142 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0143 C. YAYOXx½ªî»ò³îÇ§
0144 C. çç[HK1_DMP_CHK_FLG] EQ NON
0145 C. RAM ID=NMOG²î½E¹ç•è²îOKò³îÇ§
0146 C.
0147 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0148 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0149 BC (21 00 41 01 02)
0150 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0151 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0152 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0153 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0154 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0155 +. DC 01-22 DHU_MODE_CHNG
0156 BC (07 0b f8)
0157 C. çç[HK1_PKT_FORM_NO] EQ 7
0158 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0159 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0160 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0161 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0162 C. YAYOXx½ªî»ò³îÇ§
0163 C. çç[HK1_DMP_CHK_FLG] EQ NON
0164 C. RAM ID=NMOG, RAM ID=OP²î½E¹ç•è²îOKò³îÇ§
0165 C.
0166 C. ***** °E²¼òî½Ã´¶Á°òEÉ¬ò°Á÷¿@ (¼âµ-YAYOXx½ê½çòðÁÓÆòÇ¼ª°¬òE¼î¹çòÇòâ) *****
0167 C. DHUYâ;4YE;E½Y½, Yî;4YE;Eòðîã¹
0168 +. DC 01-22 DHU_MODE_CHNG
0169 BC (02 0a f8)
0170 C. çç[HK1_PKT_FORM_NO] EQ 2
0171 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0172 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0173 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0174 C.
0175 C. *****
0176 C. TI-CMD SET (OPOG STOP/COPY/START)
0177 C. *****
0178 C.
0179 C. NOTICE ;§ OPOG UPLOAD²-Á÷¿@NG²î½î¹ç;ç°E²¼òîTI-CMDÁ÷¿@²î½Á¹Ôª°¬E²ò³òE;f
0180 C. ²ò³òE;çSET²EEDUMP²î½±°îYÑY¹ç¹Ôª|²³òE;f
0181 C.
0182 C. TIY³Y½Y½E²òðÁDî¿(UT)
0183 +. TI 2014-09-30 10:28:00.0
0184 DC 01-B3 DHU_OP_STOP
0185 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0186 C.
0187 +. TI 2014-09-30 10:28:01.0
0188 DC 01-B4 DHU_OP_COPY
0189 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP
0190 C.
0191 +. TI 2014-09-30 10:28:01.0
0192 DC 01-B5 DHU_OPOG_COPY
0193 C. çç[HK1_TI_CMD_NUM] EQ 1COUNTUP

```





(a) Spacecraft Operation Procedure (real-commands)

```
main-666 2014-09-30 14:36:34 154 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁY$YÁY-¼Á»Û;ã
0005 C.
0006 C. YÀYB;¼Y³YF¥ÓYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È¿¿ãÁã•µ°È»Í×ÁÇãÍYçYÁY×Yí;¼YÉ;ÈÈè¼µ•ííÈ;ÈãÈ¼°ÇÔã•ã¿¼í¹çãÍ;çÁ®, ùã¹ãÈãÈãÇÁ+¿®ã•ãÈããã³ãÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-265:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Upload DPL table >
0036 C.
0037 C. YçYÁY×Yí;¼YÉãîÁ°ãÈSTS_CHKãðOFFãÈã¹ãè
0038 C.
0039 . S. RAM ram-271:MDP_DPL
0040 ( )
0041 C.
0042 . C. < Dump RAMID=MDP_DPL >
0043 +. DC 07-F0 MDP_DUMP_FGTBL
0044 BC (82 07 00 38 b8 00 40)
0045 C. -----
0046 C. MDP_DPL verify = OK [ ]
0047 C. -----
0048 C.
0049 C. STS_CHKãðONãÈã¹ãè
0050 C.
0051 . C. < Update MDP DSC PAR1 >
0052 +. DC 07-F0 MDP_DSC_PAR1_UPDATE
0053 BC (4c)
0054 C. MDP_CMD_CODE = F04C0700[ ]
0055 C. MDP_CMD_CNT (count-up 1) [ ]
0056 C. -----
0057 C.
0058 C.
0059 C. *****
0060 C. SOT TI command set
0061 C. *****
0062 C. Execute, after the success of TBL upload.
0063 +. TI 2014-09-30 10:32:18.0
0064 DC 07-F0 MDP_SOT_MODE_OBSV
0065 BC (40)
0066 . C. -----
0067 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0068 C. -----
0069 C.
0070 C.
0071 C. ***** XRT START *****
0072 C.
0073 +. DC 07-F0 MDP_XRT_CTRL_MANU
0074 BC (c1)
0075 +. DC 07-F0 MDP_XRT_MODE_STBY
0076 BC (c3)
0077 . C. ----- Success Verify ? OK / NG_____
0078 C.
0079 C. XRT Obs. Table Upload
0080 . S. RAM ram-291:MDP_OBS_X
0081 ( )
0082 C.
0083 +. DC 07-F0 MDP_DUMP_XRTTBL
0084 BC (84 07 00 00 00 3a d4)
0085 . C. ----- Comparison Check ? OK / ERR _____
0086 C.
0087 C.
0088 +. DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 01 b1 b1 04 04)
0090 +. DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 02 b1 b1 08 08)
0092 +. DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 03 b1 b1 08 08)
0094 +. DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 04 b1 b1 06 06)
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0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 05 85 83 06 06)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 06 85 83 06 06)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 07 85 83 08 08)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 08 80 80 20 20)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 09 80 80 20 08)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0a 80 80 08 20)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 0f 80 80 06 06)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 10 80 80 08 08)
0112 + DC 07-F0 MDP_XRT_FLD_ENA
0113 BC (d8)
0114 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0115 BC (c8)
0116 + DC 07-F0 MDP_XRT_AEC_RESET
0117 BC (d0)
0118 + DC 07-F0 MDP_XRT_ARS_DIS
0119 BC (d5)
0120 + DC 07-F0 MDP_XRT_FLD_RESET
0121 BC (da)
0122 + DC 07-F0 MDP_XRT_QT_PROG_SET
0123 BC (c4 0c)
0124 + DC 07-F0 MDP_XRT_FL_PROG_SET
0125 BC (c5 01)
0126 . C. ----- Success Verify ? OK / NG ____
0127 C.
0128 C.
0129 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0130 C.
0131 + DC 07-F0 MDP_XRT_MODE_OBSV
0132 BC (c2)
0133 + TI 2014-09-30 10:32:02.0
0134 DC 07-F0 MDP_XRT_MODE_OBSV
0135 BC (c2)
0136 . C. ----- Success Verify ? OK / NG ____
0137 C.
0138 C. ***** XRT END *****
0139 C.
0140 . C. ***** MDP 'uÃîqî»ö¼ÝqÊÄð¹nèDCBC•x²è *****
0141 C. (%á°îÿÓÿÄÿËÿÏÿËÿäÿçÿèq¼q¼Ä»Û¹qè)
0142 . S. DC-BC dcbc-402:DCBC
0143 (MDP_known_event)
0144 C.
0145 C.
0146 . C. ***** ÝÐÿ¹•î Daily±¿îÑqÊ´Ø¹qèDCBC•x²è *****
0147 . S. DC-BC dcbc-153:DCBC
0148 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0149 C.
0150 C.
0151 . C. ;ãLOSÿÁÿÏÿÄÿÿ-¼Ä»Û;ä
0152 C.
0153 . C. ***** LOS *****
0154 C.

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\*\*\* OP Sequence for XRT \*\*\*

2014/09/30	10:42:54.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/30	10:42:56.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/30	10:42:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/09/30	10:43:00.0	AOCS_Ore-point_Start_1_OG [0x097]					
		AOCU_NM	5	02-76	03 00 00 00 00		
2014/09/30	10:43:18.0	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/09/30	10:43:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/09/30	10:43:22.0	XRT_AEC_RESET_413_OG [0x19d]					
		MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/09/30	10:43:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/09/30	10:43:26.0	XRT_FLD_RESET_407_OG [0x197]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/09/30	10:45:56.0	XRT_QT_PROG_SET_432_OG [0x1b0]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2014/09/30	10:45:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01		
2014/09/30	10:46:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/09/30	15:32:00.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/30	15:32:02.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/30	15:32:04.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/09/30	15:32:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/09/30	15:35:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/09/30	15:55:30.0	XRT_Custom_430_OG [0x1ae]					
2014/09/30	15:56:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/09/30	17:08:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/30	17:08:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/30	17:08:34.0	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/09/30	17:08:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
		MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/09/30	17:11:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
		MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/09/30	17:32:00.0	XRT_Custom_430_OG [0x1ae]					
2014/09/30	17:33:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/09/30	17:40:28.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/30	17:40:30.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/30	17:40:32.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/09/30	17:40:52.0	XRT_FLD_DIS_431_OG [0x1af]					
		MDP_XRT_FLD_DIS	1	07-F0	d9		
2014/09/30	17:40:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2014/09/30	17:40:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/09/30	17:40:58.0	XRT_QT_PROG_SET_406_OG [0x196]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 10		
2014/09/30	17:41:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/09/30	18:03:00.5	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/30	18:03:02.5	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/30	18:03:04.5	XRT_FOCUS_POSITION_410_OG [0x19a]					
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00		
2014/09/30	18:03:24.5	XRT_FLD_ENA_411_OG [0x19b]					
		MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/09/30	18:03:26.5	XRT_FLRCTRL_ENA_412_OG [0x19c]					
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/09/30	18:03:28.5	XRT_ARS_DIS_423_OG [0x1a7]					
		MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/09/30	18:03:30.5	XRT_FLD_RESET_415_OG [0x19f]					
		MDP_XRT_FLD_RESET	1	07-F0	da		
2014/09/30	18:03:32.5	XRT_QT_PROG_SET_432_OG [0x1b0]					
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 11		
2014/09/30	18:03:34.5	XRT_FL_PROG_SET_438_OG [0x1b6]					
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 01		
2014/09/30	18:03:36.5	XRT_CTRL_AUTO_408_OG [0x198]					
		MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/09/30	18:45:30.0	XRT_CTRL_MANU_400_OG [0x190]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/30	18:45:32.0	XRT_CTRL_MANU_402_OG [0x192]					
		MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/09/30	18:45:34.0	XRT_FLD_RESET_415_OG [0x19f]					

Sep 30, 14 14:36

## XRT\_OGLIST\_0605.chk

Page 2/7

2014/09/30	18:45:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/09/30	18:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/09/30	19:09:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/09/30	19:10:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/09/30	20:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/09/30	20:22:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/09/30	20:22:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/09/30	20:22:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/09/30	20:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/09/30	20:47:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/09/30	20:47:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/09/30	20:47:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/09/30	20:48:00.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00	
2014/09/30	20:48:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/09/30	20:50:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/09/30	20:50:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/09/30	20:50:58.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2014/09/30	20:51:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/09/30	20:57:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/09/30	20:57:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/09/30	20:57:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2014/09/30	20:58:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 00 00 00 00	
2014/09/30	20:58:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/09/30	20:58:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/09/30	20:58:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/09/30	20:58:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/09/30	20:58:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/09/30	21:00:56.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2014/09/30	21:00:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01	
2014/09/30	21:01:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/09/30	22:00:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/09/30	22:00:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/09/30	22:00:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/09/30	22:00:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/09/30	22:03:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/09/30	22:22:00.0	XRT_Custom_430_OG [0x1ae]					
2014/09/30	22:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/09/30	23:37:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/09/30	23:37:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/09/30	23:37:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/09/30	23:37:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/09/30	23:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/09/30	23:48:00.0	XRT_Custom_430_OG [0x1ae]					
2014/09/30	23:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/10/01	01:03:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/01	01:03:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/01	01:03:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/10/01	01:03:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					

2014/10/01	01:06:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/10/01	01:20:30.0	XRT_Custom_430_OG [0x1ae]					
2014/10/01	01:21:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/10/01	02:36:00.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/01	02:36:02.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/01	02:36:04.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/10/01	02:36:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/10/01	02:39:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/10/01	02:57:00.0	XRT_Custom_430_OG [0x1ae]					
2014/10/01	02:58:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/10/01	04:05:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/01	04:05:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/01	04:05:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/10/01	04:05:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/10/01	04:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/10/01	04:34:30.0	XRT_Custom_430_OG [0x1ae]					
2014/10/01	04:35:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/10/01	05:45:30.0	XRT_CTRL_MANU_400_OG [0x190]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/01	05:45:32.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/01	05:45:34.0	XRT_FLD_RESET_415_OG [0x19f]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/10/01	05:45:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]					
			MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/10/01	05:48:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]					
			MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/10/01	06:12:00.0	XRT_Custom_430_OG [0x1ae]					
2014/10/01	06:13:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/10/01	06:21:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/01	06:21:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/01	06:21:58.0	XRT_FOCUS_POSITION_403_OG [0x193]					
			XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00	
2014/10/01	06:22:00.5	AOCS_Ore-point_Start_2_OG [0x098]					
			AOCU_NM	5	02-76	00 00 00 00 00	
2014/10/01	06:22:18.0	XRT_FLD_DIS_422_OG [0x1a6]					
			MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/10/01	06:24:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]					
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/10/01	06:24:56.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/10/01	06:24:58.0	XRT_QT_PROG_SET_428_OG [0x1ac]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 03	
2014/10/01	06:25:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/10/01	06:31:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/01	06:31:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/01	06:31:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	
2014/10/01	06:32:00.0	AOCS_Ore-point_Start_3_OG [0x099]					
			AOCU_NM	5	02-76	01 00 00 00 00	
2014/10/01	06:32:18.0	XRT_FLD_ENA_411_OG [0x19b]					
			MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/10/01	06:32:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]					
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/10/01	06:32:22.0	XRT_AEC_RESET_413_OG [0x19d]					
			MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/10/01	06:32:24.0	XRT_ARS_DIS_423_OG [0x1a7]					
			MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/10/01	06:32:26.0	XRT_FLD_RESET_407_OG [0x197]					
			MDP_XRT_FLD_RESET	1	07-F0	da	
2014/10/01	06:34:56.0	XRT_QT_PROG_SET_432_OG [0x1b0]					
			MDP_XRT_QT_PROG_SET	2	07-F0	c4 11	
2014/10/01	06:34:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]					
			MDP_XRT_FL_PROG_SET	2	07-F0	c5 01	
2014/10/01	06:35:00.0	XRT_CTRL_AUTO_408_OG [0x198]					
			MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/10/01	06:59:54.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/01	06:59:56.0	XRT_CTRL_MANU_402_OG [0x192]					
			MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/01	06:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]					
			XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00	



Sep 30, 14 14:36

## XRT\_OGLIST\_0605.chk

Page 4/7

2014/10/01	07:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]							
		AOCU_NM	5	02-76	02	00	00	00	00
2014/10/01	07:00:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/10/01	07:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/10/01	07:00:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/10/01	07:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/10/01	07:00:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/01	07:02:56.0	XRT_QT_PROG_SET_427_OG [0x1ab]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e			
2014/10/01	07:02:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2014/10/01	07:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/01	07:26:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/01	07:26:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/01	07:26:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/01	07:26:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/10/01	07:29:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/10/01	07:49:00.0	XRT_Custom_430_OG [0x1ae]							
2014/10/01	07:50:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/01	09:05:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/01	09:05:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/01	09:05:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/01	09:05:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/10/01	09:08:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/10/01	09:24:30.0	XRT_Custom_430_OG [0x1ae]							
2014/10/01	09:25:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/01	11:29:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/01	11:29:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/01	11:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/10/01	11:30:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01	00	00	00	00
2014/10/01	11:30:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/10/01	11:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/10/01	11:30:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/10/01	11:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/10/01	11:30:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/01	11:32:56.0	XRT_QT_PROG_SET_432_OG [0x1b0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	11			
2014/10/01	11:32:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	01			
2014/10/01	11:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/01	14:33:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/01	14:33:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/01	14:33:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/01	14:33:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/10/01	14:36:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/10/01	14:43:30.0	XRT_Custom_430_OG [0x1ae]							
2014/10/01	14:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/10/01	16:07:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/01	16:07:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/10/01	16:07:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/10/01	16:07:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/10/01	16:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/10/01	16:31:00.0	XRT_Custom_430_OG [0x1ae]							
2014/10/01	16:32:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							

Sep 30, 14 14:36

## XRT\_OGLIST\_0605.chk

Page 5/7

2014/10/01	17:44:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
			MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/10/01	17:44:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/10/01	17:44:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/10/01	17:44:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/10/01	17:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/10/01	18:07:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/10/01	18:07:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/10/01	18:07:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2014/10/01	18:08:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00			
2014/10/01	18:08:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2014/10/01	18:10:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2014/10/01	18:10:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/10/01	18:10:58.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 03			
2014/10/01	18:11:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/10/01	18:17:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/10/01	18:17:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/10/01	18:17:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00			
2014/10/01	18:18:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 00 00 00 00			
2014/10/01	18:18:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8			
2014/10/01	18:18:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8			
2014/10/01	18:18:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0			
2014/10/01	18:18:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/10/01	18:18:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/10/01	18:20:56.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 11			
2014/10/01	18:20:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 01			
2014/10/01	18:21:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/10/01	19:21:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/10/01	19:21:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/10/01	19:21:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/10/01	19:21:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/10/01	19:24:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/10/01	19:44:30.0	XRT_Custom_430_OG [0x1ae]							
2014/10/01	19:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/10/01	20:58:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/10/01	20:58:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/10/01	20:58:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/10/01	20:58:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/10/01	21:01:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/10/01	21:22:00.0	XRT_Custom_430_OG [0x1ae]							
2014/10/01	21:23:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/10/01	22:07:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/10/01	22:08:00.0	AOCS_Ore-point_Start_5_OG [0x09b]	AOCU_NM	5	02-76	00 ec 5b 53 74			
2014/10/01	22:58:00.0	AOCS_Ore-point_Start_6_OG [0x09c]	AOCU_NM	5	02-76	00 f1 27 44 cc			
2014/10/01	23:13:00.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 f3 03 34 fd			
2014/10/01	23:28:00.0	AOCS_Ore-point_Start_8_OG [0x09e]	AOCU_NM	5	02-76	00 f4 32 24 9b			
2014/10/01	23:43:00.0	AOCS_Ore-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00 f4 e6 13 ce			
2014/10/02	00:22:00.0	AOCS_Ore-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00 f5 0f 02 7e			

Sep 30, 14 14:36

## XRT\_OGLIST\_0605.chk

Page 6/7

2014/10/02	00:37:00.0	AOCS_ORe-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	00	f4	e6	f0	73
2014/10/02	00:52:00.0	AOCS_ORe-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00	f4	32	df	a6
2014/10/02	01:07:00.0	AOCS_ORe-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00	f3	03	cf	4c
2014/10/02	01:57:00.0	AOCS_ORe-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00	f1	27	bf	75
2014/10/02	02:12:00.0	AOCS_ORe-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00	ec	5b	b0	cd
2014/10/02	02:26:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/10/02	02:26:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/10/02	02:26:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/10/02	02:27:00.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01	00	00	00	00
2014/10/02	02:27:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0		d8			
2014/10/02	02:27:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2014/10/02	02:27:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0		d0			
2014/10/02	02:27:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0		d5			
2014/10/02	02:27:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0		da			
2014/10/02	02:29:56.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_QT_PROG_SET	2	07-F0		c4	11		
2014/10/02	02:29:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_FL_PROG_SET	2	07-F0		c5	01		
2014/10/02	02:30:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2014/10/02	03:12:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/10/02	03:12:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/10/02	03:12:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0		da			
2014/10/02	03:12:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2014/10/02	03:15:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2014/10/02	03:33:00.0	XRT_Custom_430_OG [0x1ae]								
2014/10/02	03:34:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2014/10/02	04:42:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/10/02	04:42:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/10/02	04:42:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0		da			
2014/10/02	04:42:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0		e8			
2014/10/02	04:45:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0		e9			
2014/10/02	05:10:30.0	XRT_Custom_430_OG [0x1ae]								
2014/10/02	05:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2014/10/02	05:56:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/10/02	05:56:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/10/02	05:56:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/10/02	05:56:30.0	AOCS_ORe-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00	00	00	00
2014/10/02	05:56:48.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0		d9			
2014/10/02	05:59:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0		c9			
2014/10/02	05:59:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0		d5			
2014/10/02	05:59:28.0	XRT_QT_PROG_SET_428_OG [0x1ac]	MDP_XRT_QT_PROG_SET	2	07-F0		c4	03		
2014/10/02	05:59:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0		c0			
2014/10/02	06:06:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/10/02	06:06:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0		c1			
2014/10/02	06:06:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/10/02	06:06:30.0	AOCS_ORe-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01	00	00	00	00
2014/10/02	06:06:48.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0		d8			
2014/10/02	06:06:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0		c8			
2014/10/02	06:06:52.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0		d0			
2014/10/02	06:06:54.0	XRT_ARS_DIS_423_OG [0x1a7]								

Sep 30, 14 14:36

## XRT\_OGLIST\_0605.chk

Page 7/7

2014/10/02	06:06:56.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/10/02	06:09:26.0	XRT_QT_PROG_SET_432_OG [0x1b0]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/10/02	06:09:28.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	11
2014/10/02	06:09:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	01
2014/10/02	06:22:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/10/02	06:22:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/02	06:22:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/02	06:22:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/10/02	06:25:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/10/02	06:48:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/10/02	06:49:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CUSTOM_430_OG	1	07-F0	c0	
2014/10/02	07:59:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/10/02	07:59:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/02	07:59:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/02	08:00:00.0	AOCS_ORe-point_Start_4_OG [0x09a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00
2014/10/02	08:00:18.0	XRT_FLD_ENA_411_OG [0x19b]	AOCU_NM	5	02-76	02	00 00 00 00
2014/10/02	08:00:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLD_ENA	1	07-F0	d8	
2014/10/02	08:00:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8	
2014/10/02	08:00:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_AEC_RESET	1	07-F0	d0	
2014/10/02	08:00:26.0	XRT_FLD_RESET_426_OG [0x1aa]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/10/02	08:25:26.0	XRT_QT_PROG_SET_427_OG [0x1ab]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/10/02	08:25:28.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	0e
2014/10/02	08:25:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	01
2014/10/02	09:40:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/10/02	10:17:00.0	AOCS_ORe-point_Start_2_OG [0x098]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
		AOCU_NM		5	02-76	00	00 00 00 00