

# XRT Timeline to be uploaded on 2014/10/07

Period: 2014/10/07 09:19:00 - 2014/10/11 11:10:00

\* \* \* \* \*

Normal mode

\* \* \* \* \*

## XOB #1A08: CCD Monitor During Bakeout - G-Band 33ms - 1kx1k - Q90 - 1st Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment
10/08 12:23:00 - 10/08 12:29:54	Fixed ( -528.4, -528.4)	# Post bakeout quadrant #1
<b>PROG= 10 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 12.0sec</b>		
└─ <b>Seqn= 88 1-time(s) 12.0sec</b>		
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 1536) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (1536, 1536) Q=98 0 0 2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 23 2-time(s) 2.0sec</b>		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ <b>Subr= 3 2-time(s) 2.0sec</b>		
└─ <b>Seqn= 12 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ <b>Seqn= 14 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #1A09: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 2nd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms-2

Term	Pointing (x, y)	Comment
10/08 12:33:00 - 10/08 12:39:54	Fixed ( 528.4, -528.4)	# Post bakeout quadrant #2
<b>PROG= 19 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 12.0sec</b>		
└─ <b>Seqn= 31 1-time(s) 12.0sec</b>		
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 1536) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 1536) Q=98 0 0 2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 23 2-time(s) 2.0sec</b>		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ <b>Subr= 3 2-time(s) 2.0sec</b>		
└─ <b>Seqn= 12 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ <b>Seqn= 14 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #1A0A: CCD Monitor During Bakeout - G-Band 45ms - 1kx1k - Q90 - 3rd Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms-2

Term	Pointing (x, y)	Comment
10/08 12:43:00 - 10/08 12:49:54	Fixed ( 528.4, 528.4)	# Post bakeout quadrant #3
<b>PROG= 11 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 12.0sec</b>		
└─ <b>Seqn= 81 1-time(s) 12.0sec</b>		
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (512, 512) Q=90 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
└─ Open/thick-Be	Open/thick-Be close	Safe Dark 32ms Obs 1x1 1024x1024 (512, 512) Q=98 0 0 2.0sec
└─ <b>Subr= 2 1-time(s) 2.0sec</b>		
└─ <b>Seqn= 23 2-time(s) 2.0sec</b>		
└─ Open/Al-mesh	Open/Ti-poly close	Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ Open/Ti-poly	Open/thick-Al close	Safe Norm 1.41s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
└─ <b>Subr= 3 2-time(s) 2.0sec</b>		
└─ <b>Seqn= 12 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band close	Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) DPCM 0 0 2.0sec
└─ <b>Seqn= 14 1-time(s) 2.0sec</b>		
└─ Open/G-band	Open/G-band open	Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

## XOB #1A0B: CCD Monitor During Bakeout - G-Band 33ms - 1kx1k - Q90 - 4th Quadrant - Al/mesh (512ms), Ti/Poly (1443ms) - w leak image-33 ms

Term	Pointing (x, y)	Comment
10/08 12:53:00 - 10/08 12:59:54	Fixed ( -528.4, 528.4)	# Post bakeout quadrant #4
<b>PROG= 05 1-time(s)</b>		
└─ <b>Subr= 1 1-time(s) 12.0sec</b>		
└─ <b>Seqn= 28 1-time(s) 12.0sec</b>		
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec
└─ Open/G-band	Open/G-band open	Safe Norm 32ms Obs 1x1 1024x1024 (1536, 512) Q=90 0 0 2.0sec



Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
<b>Seqn= 86 1-time(s) 4.0sec</b>												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	24ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 95 1-time(s) 2.0sec</b>												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	2.83s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Seqn= 6 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
<b>Subr= 2 2-time(s) 12.0sec</b>												
<b>Seqn= 56 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	63ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Open	Al-poly/Open	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 22 1-time(s) 2.0sec</b>												
C-poly/Open	C-poly/Open	close	Safe	Norm	177ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
C-poly/Open	C-poly/Open	close	Safe	Norm	1.41s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 43 1-time(s) 2.0sec</b>												
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 38 1-time(s) 4.0sec</b>												
med-Al/Open	med-Al/Open	close	Safe	Norm	22.6s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
<b>Seqn= 46 1-time(s) 4.0sec</b>												
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

<b>XOB #1A4A: Coronal Cavity (2 filters) - HOP264 with ARs - DPCM 2x2 - 768FOV - AEC0 with light leak</b>												
Term	Pointing (x, y)		Comment									
10/09 06:30:30 - 10/09 08:44:44	Fixed ( 770.0, -524.8)		# HOP 264									
<b>PROG= 02 Inf.-time(s)</b>												
<b>Subr= 1 1-time(s) 2.0sec</b>												
<b>Seqn= 47 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	16ms	Obs	2x2	1024x1024 (1536, 1536)	Q=90	0	0	2.0sec
<b>Seqn= 13 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	1024x1024 (1536, 1536)	DPCM	0	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 45 120-time(s) 30.0sec</b>												
Al-poly/Open	Al-poly/Open	close	Safe	Norm	4.00s	Obs	2x2	1024x1024 (1536, 1536)	DPCM	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	8.00s	Obs	2x2	1024x1024 (1536, 1536)	DPCM	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

\* \* \* \* \* **Flare mode** \* \* \* \* \*

<b>XOB #19EC: Flare - high cad multifilter (Be/thin, Be/med, Al/thick), AEC 3, 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512x512 2x2) + Gband (4</b>												
Term	Pointing (x, y)		Comment									
10/08 13:03:00 - 10/08 17:59:54	Track ( 896.2, -79.6) @ 10/08 13:00:00		# AR 12178									
10/08 18:13:00 - 10/08 18:41:00	Fixed ( 894.0, -76.7)		# AR 12178 (fixed pointing)									
<b>PROG= 01 30-time(s)</b>												
<b>Subr= 1 30-time(s) 2.0sec</b>												
<b>Seqn= 26 1-time(s) 4.0sec</b>												
thin-Be/Open	med-Be/Open	close	Safe	Norm	8ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	250ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Al	Open/thick-Be	close	Safe	Norm	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Subr= 2 1-time(s) 2.0sec</b>												
<b>Seqn= 10 1-time(s) 2.0sec</b>												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
<b>Seqn= 11 1-time(s) 2.0sec</b>												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
<b>Seqn= 15 1-time(s) 2.0sec</b>												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

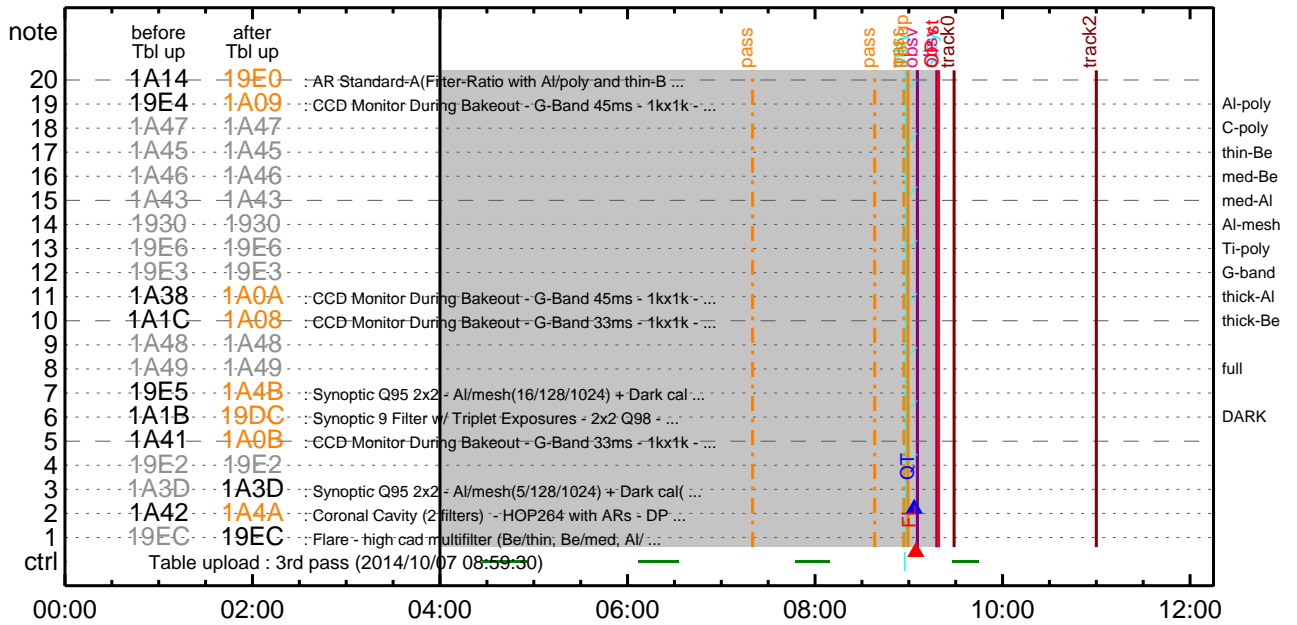
\* \* \* \* \* **Active Region Search** \* \* \* \* \*

NOT USED

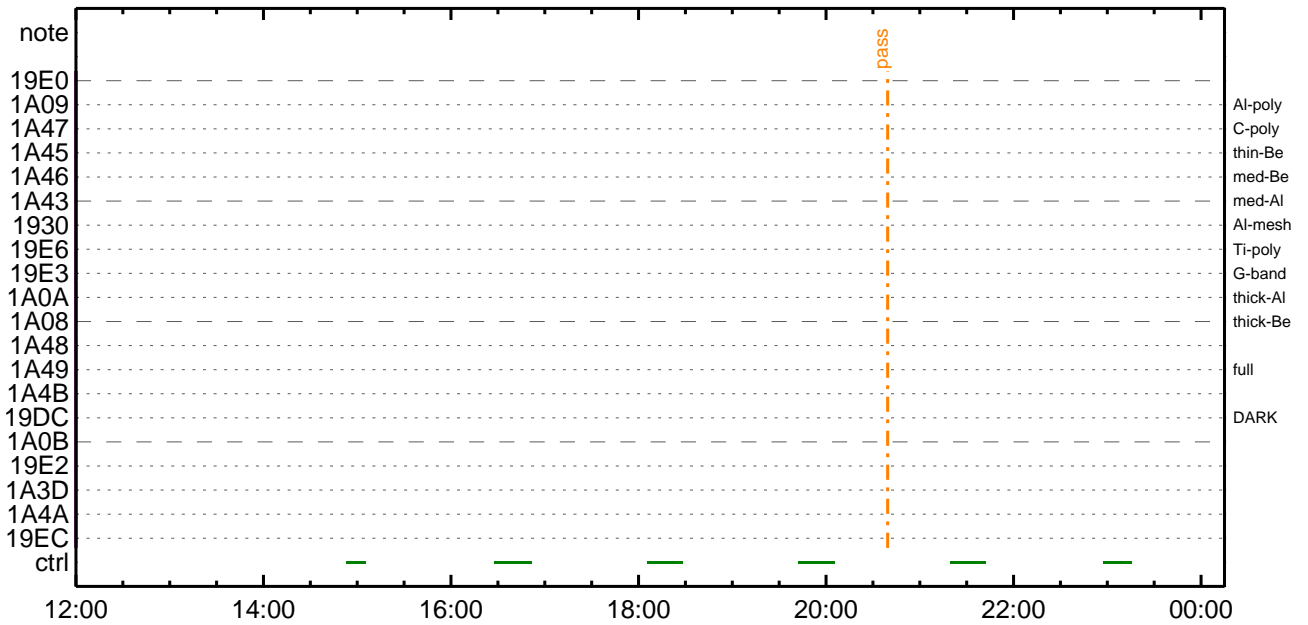
\* \* \* \* \* **Flare Detection** \* \* \* \* \*

Term	Pointing (x, y)		Comment									
10/08 13:00:23 - 10/08 18:00:18	Track ( 896.2, -79.6) @ 10/08 13:00:00		# AR 12178									
10/08 18:10:18 - 10/08 19:42:48	Fixed ( 894.0, -76.7)		# AR 12178 (fixed pointing)									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8	Q=50			30sec	
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)			Comp. AEC Buffer Interval	

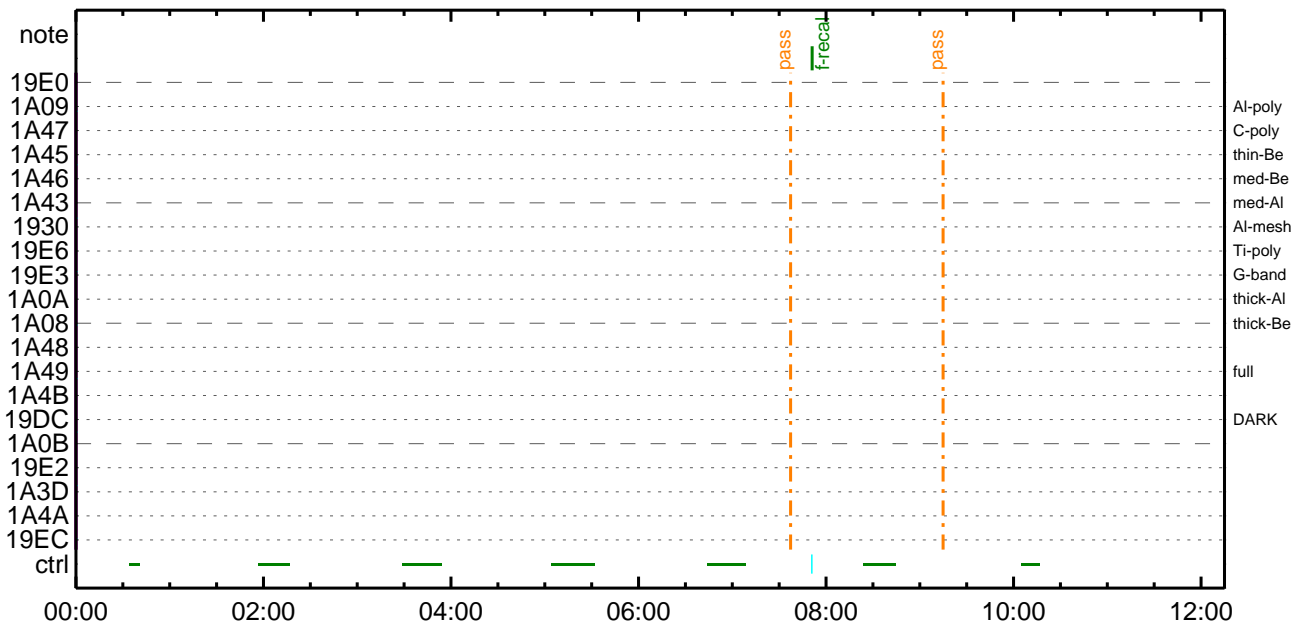
### CMDI #0618 2014/10/07



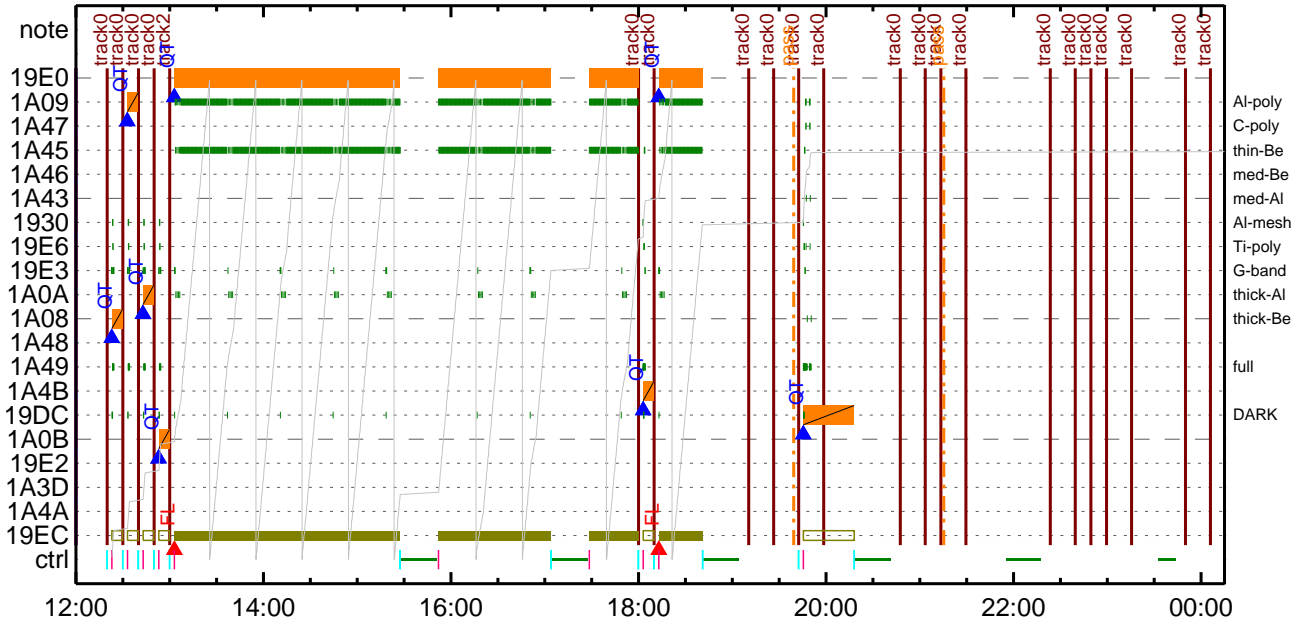
### CMDI #0618 2014/10/07



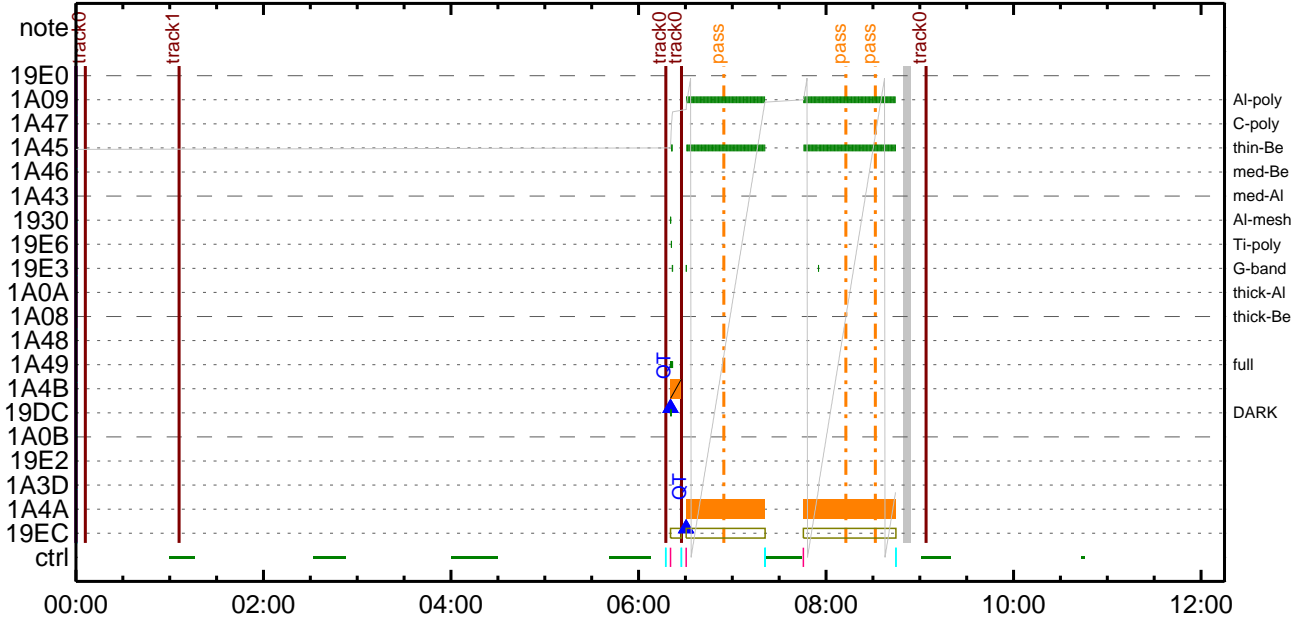
### CMDI #0618 2014/10/08



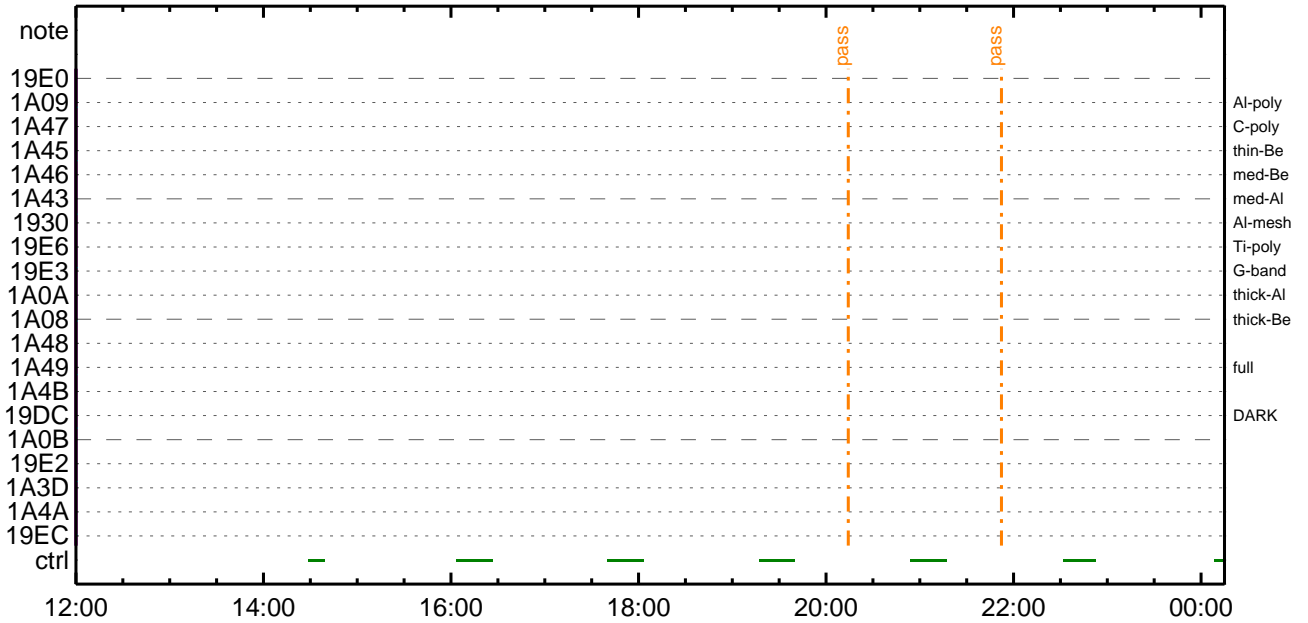
### CMDI #0618 2014/10/08



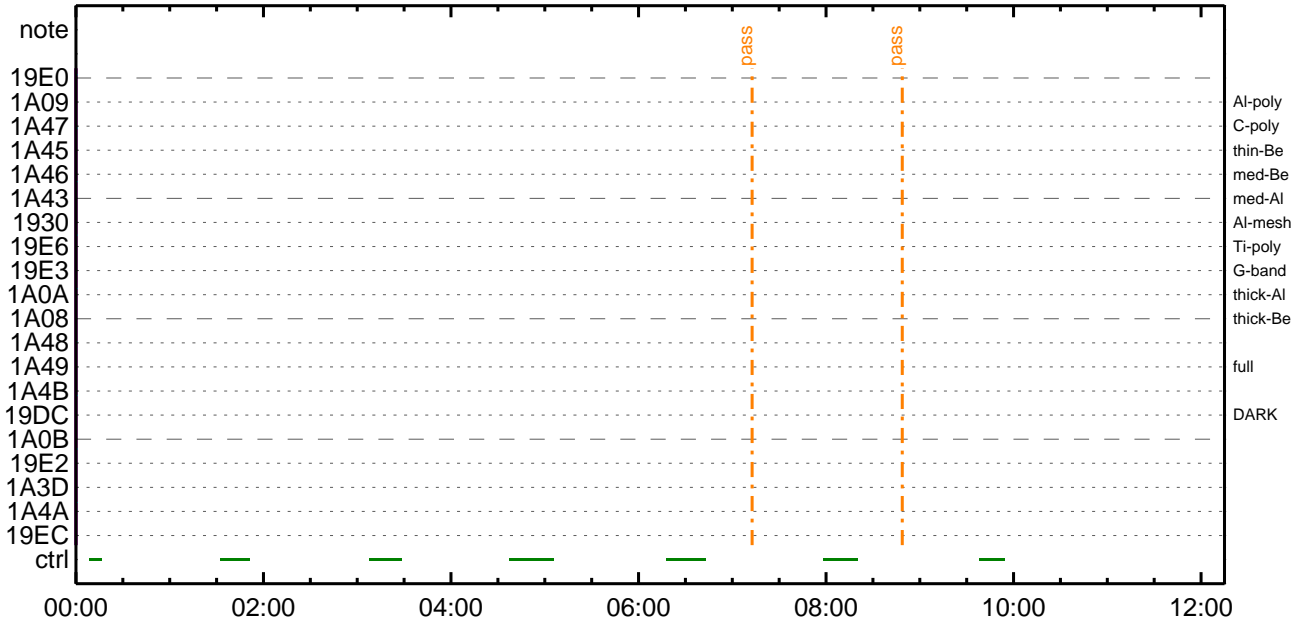
### CMDI #0618 2014/10/09



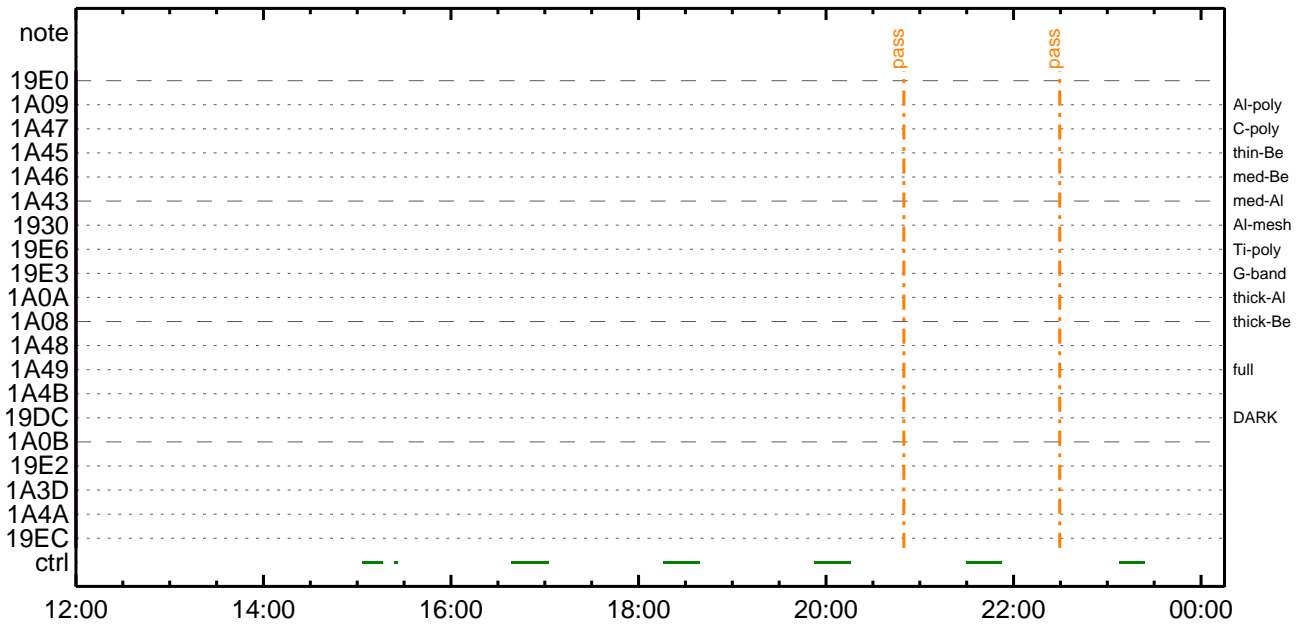
### CMDI #0618 2014/10/09



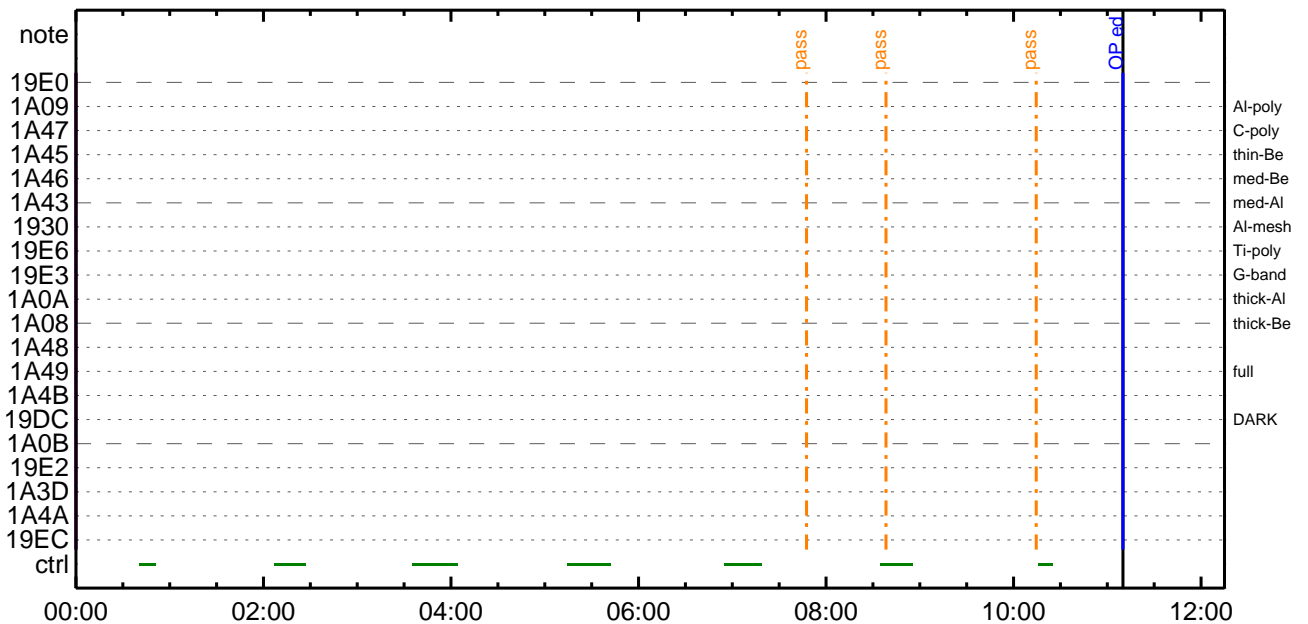
### CMDI #0618 2014/10/10



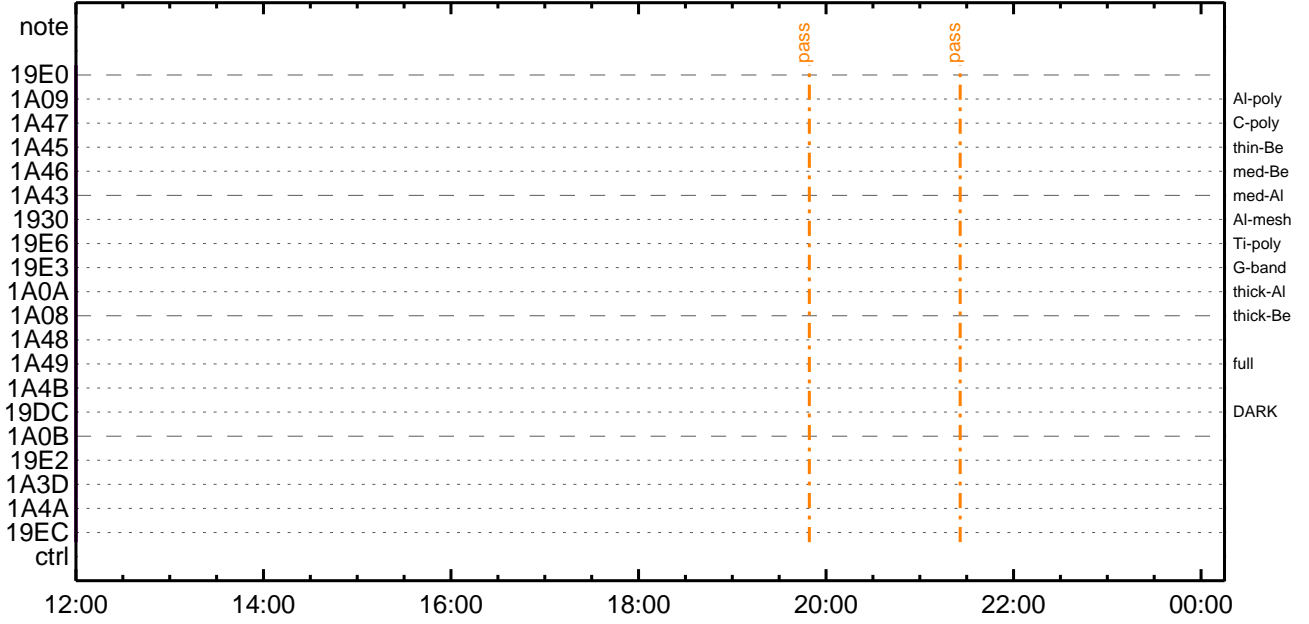
### CMDI #0618 2014/10/10



### CMDI #0618 2014/10/11



CMDI #0618 2014/10/11









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0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥D¥!•İ Daily±;İÑøĒ'Øα¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ä
0203 C.
0204 . C. ***** LOS *****
0205 C.
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0096 C.
0097 C.
0098 . C. ***** AOCs Commands (Tracking Curve Upload) *****
0099 C. Upload the Orbit Element and the Target Attitude
0100 C. RAM-ID:TARGET_ATT
0101 . S. RAM ram-150:TARGET_ATT
0102 ( )
0103 C.
0104 C.
0105 C. Set the dump memory area of TARGET_ATT
0106 +. DC 02-48 AOCU_DUMP_SET
0107 BC (07 00 00 00 18 00)
0108 C.
0109 C. <A_STS1>[MEMORY OPERATE STATUS] ADRS = 070000 [ ]
0110 C.
0111 C.
0112 C. Change the TLMFormatNo for the AOCs Dump Format
0113 +. DC 01-22 DHU_MODE_CHNG
0114 BC (04 0b f8)
0115 C.
0116 C. Wait for AOCSDUMP to end
0117 C.
0118 . C. Check the dump memory
0119 C.
0120 C. Result = OK [ ]
0121 C.
0122 +. DC 01-22 DHU_MODE_CHNG
0123 BC (02 0a f8)
0124 C.
0125 C. <A_***>[TLM STS] FMT = 2 [ ]
0126 C.
0127 +. DC 02-8E AOCU_ORB_UPD
0128 . C.
0129 . C. ***** AOCs Commands (Orbital Element Update) *****
0130 C. Update the orbital element
0131 +. DC 02-50 AOCU_ORB_PRPGT_START
0132 BC (16)
0133 +. DC 02-8E AOCU_ORB_UPD
0134 C.
0135 C. <A_ORB>[ORBIT] EPC = 1687024.7 +- 1.0 (s) [ ]
0136 C.
0137 . C.
0138 . C. Load OBSTBL, dump OBSTBL, enable EIS mode changes
0139 +. DC 07-FC EIS_MODE_CHG_ENA
0140 BC (20)
0141 . C. Verify EIS_MODE_CHG_FLG is ENA
0142 +. DC 07-FC EIS_MODE_MANU
0143 BC (21 02)
0144 . C. Verify EIS in MANUAL mode
0145 . C. Estimated OBSTBL upload time is 49s
0146 C. *****
0147 C. EIS START OBSTBL LOAD
0148 C. *****
0149 . S. RAM ram-820:EIS_OBSTBL
0150 ( )
0151 +. DC 07-FC EIS_DUMP_OBSTBL
0152 BC (07 07 07 00 00 70 00)
0153 C.
0154 C. Execute, after the success of OBSTBL upload.
0155 C. Set EIS TI-commands
0156 +. TI 2014-10-07 09:18:50.0
0157 DC 07-FC EIS_MODE_CHG_ENA
0158 BC (20)
0159 . C. [ ] [HK1_TI_CMD_NUM] EQ 1 COUNTUP
0160 C. *****
0161 C. EIS END OBSTBL LOAD
0162 C. *****
0163 C.
0164 . C. ***** MDP `úÃîî»ö¼ÝðËÄð¹ñèDCBC•x²è *****
0165 C. (¼á°îÝÓÝÄÝËÝÞÝËÝ¼ÝèñË¼ð¼Ä»Ü¹ñè)
0166 . S. DC-BC dcbc-402:DCBC
0167 (MDP_known_event)
0168 C.
0169 C.
0170 . C. ***** ÝÐÝ¹•ï Daily±¿îÑñË´Ø¹ñèDCBC•x²è *****
0171 . S. DC-BC dcbc-153:DCBC
0172 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0173 C.
0174 C.
0175 . C. ;ãLOSÝÁÝSÝÄÝ¹¼Ä»Ü;ã
0176 C.
0177 . C. ***** LOS *****
0178 C.

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0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 05 85 83 06 06)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 06 80 80 20 20)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 07 80 80 20 08)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 08 80 80 08 20)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 09 c0 c0 10 10)
0106 + DC 07-F0 MDP_XRT_ROI_SET
0107 BC (cd 0a 40 c0 10 10)
0108 + DC 07-F0 MDP_XRT_ROI_SET
0109 BC (cd 0b 40 40 10 10)
0110 + DC 07-F0 MDP_XRT_ROI_SET
0111 BC (cd 0c c0 40 10 10)
0112 + DC 07-F0 MDP_XRT_ROI_SET
0113 BC (cd 0d 85 83 06 06)
0114 + DC 07-F0 MDP_XRT_ROI_SET
0115 BC (cd 0e 85 83 08 08)
0116 + DC 07-F0 MDP_XRT_ROI_SET
0117 BC (cd 0f 80 80 06 06)
0118 + DC 07-F0 MDP_XRT_ROI_SET
0119 BC (cd 10 80 80 08 08)
0120 + DC 07-F0 MDP_XRT_FLD_DIS
0121 BC (d9)
0122 + DC 07-F0 MDP_XRT_FLRCTRL_DIS
0123 BC (c9)
0124 + DC 07-F0 MDP_XRT_ARS_DIS
0125 BC (d5)
0126 + DC 07-F0 MDP_XRT_QT_PROG_SET
0127 BC (c4 03)
0128 + DC 07-F0 MDP_XRT_FL_PROG_SET
0129 BC (c5 01)
0130 . C. ----- Success Verify ? OK / NG ____
0131 C.
0132 C.
0133 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0134 C.
0135 + DC 07-F0 MDP_XRT_MODE_OBSV
0136 BC (c2)
0137 + TI 2014-10-07 09:18:02.0
0138 DC 07-F0 MDP_XRT_MODE_OBSV
0139 BC (c2)
0140 . C. ----- Success Verify ? OK / NG ____
0141 C.
0142 C. ***** XRT END *****
0143 C.
0144 . C. ***** MDP `úÃîñí»ö%ÝñĒĀðñ¹ñēDCBC•x²è *****
0145 C. (%á°îÿÓÿĀÿĒÿPÿĤÿĀÿçÿēñĒ%¼ññ%Ā»Ūñ¹ñē)
0146 . S. DC-BC dcbc-402:DCBC
0147 (MDP_known_event)
0148 C.
0149 C.
0150 . C. ***** ÿDÿ¹•Ī Daily±¿ĪññĒ´Øñ¹ñēDCBC•x²è *****
0151 . S. DC-BC dcbc-153:DCBC
0152 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0153 C.
0154 C.
0155 . C. ;ãLOSÿĀÿ$ÿĀÿ-¼Ā»Ū;ä
0156 C.
0157 . C. ***** LOS *****
0158 C.
```

\*\*\* OP Sequence for XRT \*\*\*

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2014/10/07 09:29:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                    5 02-76 00 d1 07 3e 35
2014/10/07 11:00:00.5 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM                    5 02-76 02 00 00 00 00
2014/10/08 06:20:00.0 XRT_TCIB_XRT_S_HTR_A_DIS_437_OG [0x1b5]
                        TCIB_XRT_S_HTR_A_DIS 0 04-C0
2014/10/08 07:50:55.0 XRT_CTRL_MANU_441_OG [0x1b9]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2014/10/08 07:51:05.0 XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]
                        XRT_FOCUS_RECAL          2 07-F8 78 00
2014/10/08 07:55:05.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2014/10/08 12:19:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2014/10/08 12:19:56.0 XRT_CTRL_MANU_444_OG [0x1bc]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2014/10/08 12:20:00.0 AOCs_OrE-point_Start_3_OG [0x099]
                        AOCU_NM                    5 02-76 00 2e f9 2e f9
2014/10/08 12:22:32.0 XRT_FOCUS_POSITION_447_OG [0x1bf]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2014/10/08 12:22:52.0 XRT_QT_PROG_SET_436_OG [0x1b4]
                        MDP_XRT_QT_PROG_SET      2 07-F0 c4 0a
2014/10/08 12:22:54.0 XRT_FLD_DIS_431_OG [0x1af]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2014/10/08 12:22:56.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2014/10/08 12:22:58.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2014/10/08 12:23:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO        1 07-F0 c0
2014/10/08 12:29:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2014/10/08 12:29:56.0 XRT_CTRL_MANU_444_OG [0x1bc]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2014/10/08 12:30:00.0 AOCs_OrE-point_Start_4_OG [0x09a]
                        AOCU_NM                    5 02-76 00 2e f9 d1 07
2014/10/08 12:32:32.0 XRT_FOCUS_POSITION_447_OG [0x1bf]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2014/10/08 12:32:52.0 XRT_QT_PROG_SET_417_OG [0x1a1]
                        MDP_XRT_QT_PROG_SET      2 07-F0 c4 13
2014/10/08 12:32:54.0 XRT_FLD_DIS_431_OG [0x1af]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2014/10/08 12:32:56.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2014/10/08 12:32:58.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2014/10/08 12:33:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO        1 07-F0 c0
2014/10/08 12:39:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2014/10/08 12:39:56.0 XRT_CTRL_MANU_444_OG [0x1bc]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2014/10/08 12:40:00.0 AOCs_OrE-point_Start_5_OG [0x09b]
                        AOCU_NM                    5 02-76 00 d1 07 d1 07
2014/10/08 12:42:32.0 XRT_FOCUS_POSITION_447_OG [0x1bf]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2014/10/08 12:42:52.0 XRT_QT_PROG_SET_433_OG [0x1b1]
                        MDP_XRT_QT_PROG_SET      2 07-F0 c4 0b
2014/10/08 12:42:54.0 XRT_FLD_DIS_431_OG [0x1af]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2014/10/08 12:42:56.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2014/10/08 12:42:58.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2014/10/08 12:43:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO        1 07-F0 c0
2014/10/08 12:49:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2014/10/08 12:49:56.0 XRT_CTRL_MANU_444_OG [0x1bc]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2014/10/08 12:50:00.0 AOCs_OrE-point_Start_6_OG [0x09c]
                        AOCU_NM                    5 02-76 00 d1 07 2e f9
2014/10/08 12:52:32.0 XRT_FOCUS_POSITION_447_OG [0x1bf]
                        XRT_FOCUS_POSITION        4 07-F8 22 ff aa 00
2014/10/08 12:52:52.0 XRT_QT_PROG_SET_409_OG [0x199]
                        MDP_XRT_QT_PROG_SET      2 07-F0 c4 05
2014/10/08 12:52:54.0 XRT_FLD_DIS_431_OG [0x1af]
                        MDP_XRT_FLD_DIS          1 07-F0 d9
2014/10/08 12:52:56.0 XRT_FLRCTRL_DIS_405_OG [0x195]
                        MDP_XRT_FLRCTRL_DIS      1 07-F0 c9
2014/10/08 12:52:58.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS          1 07-F0 d5
2014/10/08 12:53:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO        1 07-F0 c0
2014/10/08 12:59:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2014/10/08 12:59:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU        1 07-F0 c1
2014/10/08 12:59:58.0 XRT_ROI_A_449_OG [0x1c1]
                        MDP_XRT_ROI_SET          6 07-F0 cd 05 85 83 06 06
                        MDP_XRT_ROI_SET          6 07-F0 cd 06 80 80 20 20

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			MDP_XRT_ROI_SET	6	07-F0	cd	07	80	80	20	08
			MDP_XRT_ROI_SET	6	07-F0	cd	08	80	80	08	20
			MDP_XRT_ROI_SET	6	07-F0	cd	09	85	83	0c	0c
			MDP_XRT_ROI_SET	6	07-F0	cd	0d	85	83	06	06
			MDP_XRT_ROI_SET	6	07-F0	cd	0e	85	83	08	08
			MDP_XRT_ROI_SET	6	07-F0	cd	0f	80	80	06	06
2014/10/08	12:59:58.5	XRT_ROI_B_420_OG	[0x1a4]								
			MDP_XRT_ROI_SET	6	07-F0	cd	0f	80	80	06	06
			MDP_XRT_ROI_SET	6	07-F0	cd	10	80	80	08	08
2014/10/08	13:00:00.0	AOCS_ORe-point_Start_2_OG	[0x098]								
			AOCU_NM	5	02-76	02	00	00	00	00	00
2014/10/08	13:00:03.5	XRT_FOCUS_POSITION_410_OG	[0x19a]								
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00		
2014/10/08	13:00:23.5	XRT_FLD_ENA_411_OG	[0x19b]								
			MDP_XRT_FLD_ENA	1	07-F0	d8					
2014/10/08	13:00:25.5	XRT_FLRCTRL_ENA_412_OG	[0x19c]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2014/10/08	13:00:27.5	XRT_AEC_RESET_413_OG	[0x19d]								
			MDP_XRT_AEC_RESET	1	07-F0	d0					
2014/10/08	13:00:29.5	XRT_ARS_DIS_423_OG	[0x1a7]								
			MDP_XRT_ARS_DIS	1	07-F0	d5					
2014/10/08	13:00:31.5	XRT_FLD_RESET_446_OG	[0x1be]								
			MDP_XRT_FLD_RESET	1	07-F0	da					
2014/10/08	13:02:56.5	XRT_QT_PROG_SET_435_OG	[0x1b3]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	14				
2014/10/08	13:02:58.5	XRT_FL_PROG_SET_438_OG	[0x1b6]								
			MDP_XRT_FL_PROG_SET	2	07-F0	c5	01				
2014/10/08	13:03:00.5	XRT_CTRL_AUTO_408_OG	[0x198]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2014/10/08	15:27:30.0	XRT_CTRL_MANU_400_OG	[0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/10/08	15:27:32.0	XRT_CTRL_MANU_402_OG	[0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/10/08	15:27:34.0	XRT_FLD_RESET_415_OG	[0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da					
2014/10/08	15:27:36.0	XRT_PREFLR_STRT_439_OG	[0x1b7]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2014/10/08	15:30:44.0	XRT_PREFLR_STOP_419_OG	[0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2014/10/08	15:51:00.0	XRT_Custom_430_OG	[0x1ae]								
2014/10/08	15:52:00.0	XRT_CTRL_AUTO_424_OG	[0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2014/10/08	17:04:00.0	XRT_CTRL_MANU_400_OG	[0x190]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/10/08	17:04:02.0	XRT_CTRL_MANU_402_OG	[0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/10/08	17:04:04.0	XRT_FLD_RESET_415_OG	[0x19f]								
			MDP_XRT_FLD_RESET	1	07-F0	da					
2014/10/08	17:04:06.0	XRT_PREFLR_STRT_439_OG	[0x1b7]								
			MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2014/10/08	17:07:14.0	XRT_PREFLR_STOP_419_OG	[0x1a3]								
			MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2014/10/08	17:27:30.0	XRT_Custom_430_OG	[0x1ae]								
2014/10/08	17:28:30.0	XRT_CTRL_AUTO_424_OG	[0x1a8]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2014/10/08	17:59:54.0	XRT_CTRL_MANU_402_OG	[0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/10/08	17:59:56.0	XRT_CTRL_MANU_402_OG	[0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/10/08	17:59:58.0	XRT_FOCUS_POSITION_403_OG	[0x193]								
			XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00		
2014/10/08	18:00:00.0	AOCS_ORe-point_Start_7_OG	[0x09d]								
			AOCU_NM	5	02-76	00	00	00	00	00	00
2014/10/08	18:00:18.0	XRT_FLD_DIS_422_OG	[0x1a6]								
			MDP_XRT_FLD_DIS	1	07-F0	d9					
2014/10/08	18:02:54.0	XRT_FLRCTRL_DIS_405_OG	[0x195]								
			MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2014/10/08	18:02:56.0	XRT_ARS_DIS_423_OG	[0x1a7]								
			MDP_XRT_ARS_DIS	1	07-F0	d5					
2014/10/08	18:02:58.0	XRT_QT_PROG_SET_429_OG	[0x1ad]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	07				
2014/10/08	18:03:00.0	XRT_CTRL_AUTO_408_OG	[0x198]								
			MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2014/10/08	18:09:54.0	XRT_CTRL_MANU_402_OG	[0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/10/08	18:09:56.0	XRT_CTRL_MANU_402_OG	[0x192]								
			MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/10/08	18:09:58.0	XRT_FOCUS_POSITION_410_OG	[0x19a]								
			XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00		
2014/10/08	18:10:00.0	AOCS_ORe-point_Start_8_OG	[0x09e]								
			AOCU_NM	5	02-76	00	06	d0	b0	8c	
2014/10/08	18:10:18.0	XRT_FLD_ENA_411_OG	[0x19b]								
			MDP_XRT_FLD_ENA	1	07-F0	d8					
2014/10/08	18:10:20.0	XRT_FLRCTRL_ENA_412_OG	[0x19c]								
			MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2014/10/08	18:10:22.0	XRT_AEC_RESET_413_OG	[0x19d]								
			MDP_XRT_AEC_RESET	1	07-F0	d0					
2014/10/08	18:10:24.0	XRT_ARS_DIS_423_OG	[0x1a7]								
			MDP_XRT_ARS_DIS	1	07-F0	d5					
2014/10/08	18:10:26.0	XRT_FLD_RESET_407_OG	[0x197]								
			MDP_XRT_FLD_RESET	1	07-F0	da					
2014/10/08	18:12:56.0	XRT_QT_PROG_SET_435_OG	[0x1b3]								
			MDP_XRT_QT_PROG_SET	2	07-F0	c4	14				

2014/10/08	18:12:58.0	XRT_FL_PROG_SET_438_OG [0x1b6]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	01
2014/10/08	18:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/10/08	18:41:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/08	18:41:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/08	18:41:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/10/08	18:41:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/10/08	18:44:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/10/08	19:10:30.0	AOCS_Ore-point_Start_9_OG [0x09f]	AOCU_NM	5	02-76	00 00 00	ac cd
2014/10/08	19:26:30.0	AOCS_Ore-point_Start_10_OG [0x0a0]	AOCU_NM	5	02-76	00 00 00	d6 67
2014/10/08	19:42:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/08	19:42:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/08	19:42:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff	aa 00
2014/10/08	19:42:30.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 00 00	00 00
2014/10/08	19:42:48.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/10/08	19:45:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/10/08	19:45:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/10/08	19:45:28.0	XRT_QT_PROG_SET_442_OG [0x1ba]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	06
2014/10/08	19:45:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/10/08	19:58:30.0	AOCS_Ore-point_Start_11_OG [0x0a1]	AOCU_NM	5	02-76	00 00 00	29 99
2014/10/08	20:18:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/08	20:18:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/08	20:18:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da	
2014/10/08	20:18:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8	
2014/10/08	20:21:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9	
2014/10/08	20:47:30.0	AOCS_Ore-point_Start_12_OG [0x0a2]	AOCU_NM	5	02-76	00 00 00	53 33
2014/10/08	21:03:30.0	AOCS_Ore-point_Start_13_OG [0x0a3]	AOCU_NM	5	02-76	00 d6 36	b7 8e
2014/10/08	21:13:30.0	AOCS_Ore-point_Start_14_OG [0x0a4]	AOCU_NM	5	02-76	00 b4 b5	db 75
2014/10/08	21:29:30.0	AOCS_Ore-point_Start_15_OG [0x0a5]	AOCU_NM	5	02-76	00 ac 5b	00 00
2014/10/08	22:23:30.5	AOCS_Ore-point_Start_16_OG [0x0a6]	AOCU_NM	5	02-76	00 b4 b5	24 8b
2014/10/08	22:39:30.0	AOCS_Ore-point_Start_17_OG [0x0a7]	AOCU_NM	5	02-76	00 d6 36	48 72
2014/10/08	22:49:30.0	AOCS_Ore-point_Start_18_OG [0x0a8]	AOCU_NM	5	02-76	00 29 ca	b7 8e
2014/10/08	22:59:30.0	AOCS_Ore-point_Start_19_OG [0x0a9]	AOCU_NM	5	02-76	00 4b 4b	db 75
2014/10/08	23:15:30.0	AOCS_Ore-point_Start_20_OG [0x0aa]	AOCU_NM	5	02-76	00 53 a5	00 00
2014/10/08	23:50:00.0	AOCS_Ore-point_Start_21_OG [0x0ab]	AOCU_NM	5	02-76	00 4b 4b	24 8b
2014/10/09	00:06:00.0	AOCS_Ore-point_Start_22_OG [0x0ac]	AOCU_NM	5	02-76	00 29 db	48 72
2014/10/09	01:06:00.0	AOCS_Ore-point_Start_23_OG [0x0ad]	AOCU_NM	5	02-76	01 00 00	00 00
2014/10/09	06:17:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/09	06:17:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/09	06:17:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff	aa 00
2014/10/09	06:17:30.0	AOCS_Ore-point_Start_7_OG [0x09d]	AOCU_NM	5	02-76	00 00 00	00 00
2014/10/09	06:17:48.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9	
2014/10/09	06:20:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9	
2014/10/09	06:20:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5	
2014/10/09	06:20:28.0	XRT_QT_PROG_SET_429_OG [0x1ad]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	07
2014/10/09	06:20:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0	
2014/10/09	06:27:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	
2014/10/09	06:27:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1	

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2014/10/09	06:27:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/10/09	06:27:30.0	AOCS_ORe-point_Start_24_OG [0x0ae]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00
2014/10/09	06:27:48.0	XRT_FLD_DIS_422_OG [0x1a6]	AOCU_NM	5	02-76	00 2e a7 bb 8e
2014/10/09	06:30:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLD_DIS	1	07-F0	d9
2014/10/09	06:30:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9
2014/10/09	06:30:28.0	XRT_QT_PROG_SET_418_OG [0x1a2]	MDP_XRT_ARS_DIS	1	07-F0	d5
2014/10/09	06:30:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 02
2014/10/09	07:21:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/10/09	07:21:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/10/09	07:21:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/10/09	07:21:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da
2014/10/09	07:24:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/10/09	07:44:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/10/09	07:45:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/10/09	08:44:44.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/10/09	08:44:46.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/10/09	09:04:00.0	AOCS_ORe-point_Start_7_OG [0x09d]	MDP_XRT_CTRL_MANU	1	07-F0	c1
		AOCU_NM		5	02-76	00 00 00 00 00