

XRT Timeline to be uploaded on 2014/11/13

Period: 2014/11/13 10:35:00 - 2014/11/18 11:10:00

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Normal mode

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XOB #19F3: CME watch - 4x4 - AEC 2 - Be-thin (Be-Med) - 240s cad												
Term	Pointing (x, y)	Comment										
11/13 10:39:30 - 11/13 10:44:54	Fixed (,)											
PROG= 03 Inf.-time(s)												
└─ Subr= 1 15-time(s) 240.0sec												
└─ Seqn= 29 1-time(s) 4.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	1.41s	Obs	4x4	2048x2048 (1024, 1024)	Q=98	2	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A5B: HOP244 (East Limb)- 120s cadence (thin-Be only) 384x384 at 936 1048 with PFB												
Term	Pointing (x, y)	Comment										
11/13 10:48:00 - 11/13 16:59:42	Fixed (-933.7, -256.4)	# OP start + 10min, previously AR12192 obs (HOP244 12-15UT)										
11/13 22:44:00 - 11/13 23:59:30	Track (-927.9, -259.9) ^{Ⓜ 11/13 18:06:00}	# previously AR12192 (HOP275 0:08-2:08UT)										
11/14 02:10:18 - 11/14 07:19:54	Track (-927.9, -259.9) ^{Ⓜ 11/13 18:06:00}	# previously AR12192 (HOP275 0:08-2:08UT)										
11/14 07:33:00 - 11/14 11:59:42	Track (-905.3, -266.1) ^{Ⓜ 11/14 07:30:00}	# Adjust track curve 1 (HOP244 12-15UT)										
11/14 15:07:30 - 11/14 17:42:54	Track (-905.3, -266.1) ^{Ⓜ 11/14 07:30:00}	# Adjust track curve 1 (HOP244 12-15UT)										
11/14 17:56:00 - 11/14 22:58:00	Track (-877.5, -270.5) ^{Ⓜ 11/14 17:53:00}	# Previously AR12192 (HOP275 23:20-01:20)										
11/15 01:20:18 - 11/15 05:59:54	Track (-877.5, -270.5) ^{Ⓜ 11/14 17:53:00}	# Previously AR12192 (HOP275 23:20-01:20)										
11/15 06:13:00 - 11/15 10:03:30	Track (-833.4, -275.4) ^{Ⓜ 11/15 06:10:00}	# Track previously AR12192										
PROG= 20 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 98 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (936, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (936, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 34 1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (936, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 7 120-time(s) 120.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (936, 1048)	Q=95	1	0	15.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (936, 1048)	Q=95	1	1	45.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (936, 1048)	Q=95	1	2	15.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (936, 1048)	Q=95	1	3	45.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A5A: HOP244 (East Limb)- 30s cadence (thin-Be only) 384x384 at 936 1048 with PFB												
Term	Pointing (x, y)	Comment										
11/13 17:00:00 - 11/13 17:30:30	Fixed (-933.7, -256.4)	# OP start + 10min, previously AR12192 obs (HOP244 12-15UT)										
11/13 18:09:00 - 11/13 22:22:00	Track (-927.9, -259.9) ^{Ⓜ 11/13 18:06:00}	# previously AR12192 (HOP275 0:08-2:08UT)										
11/14 00:09:00 - 11/14 02:10:00	Track (-927.9, -259.9) ^{Ⓜ 11/13 18:06:00}	# previously AR12192 (HOP275 0:08-2:08UT)										
11/14 12:00:00 - 11/14 14:54:00	Track (-905.3, -266.1) ^{Ⓜ 11/14 07:30:00}	# Adjust track curve 1 (HOP244 12-15UT)										
11/14 23:17:30 - 11/15 01:20:00	Track (-877.5, -270.5) ^{Ⓜ 11/14 17:53:00}	# Previously AR12192 (HOP275 23:20-01:20)										
PROG= 15 Inf.-time(s)												
└─ Subr= 1 1-time(s) 2.0sec												
└─ Seqn= 98 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	16.0s	Obs	1x1	384x384 (936, 1048)	Q=98	0	0	2.0sec
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (936, 1048)	Q=98	0	0	2.0sec
└─ Seqn= 34 1-time(s) 2.0sec												
Open/G-band	Open/G-band	close	Safe	Norm	63ms	Obs	1x1	384x384 (936, 1048)	DPCM	0	0	2.0sec
└─ Subr= 2 1-time(s) 2.0sec												
└─ Seqn= 94 240-time(s) 30.0sec												
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (936, 1048)	Q=95	1	0	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (936, 1048)	Q=95	1	1	10.0sec
thin-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (936, 1048)	Q=95	1	2	2.0sec
med-Be/Open	Open/thick-Al	close	Safe	Norm	1.00s	Obs	1x1	384x384 (936, 1048)	Q=95	1	3	10.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A59: Synoptic Q95 2x2 - Al/mesh(2/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(8/362/2048) + Thin												
Term	Pointing (x, y)	Comment										
11/13 17:59:00 - 11/13 18:05:54	Fixed (0.0, 0.0)	synoptic, shifted -4.0 min										
11/14 07:23:00 - 11/14 07:29:54	Fixed (0.0, 0.0)	synoptic, shifted -19.5 min										
11/14 17:46:00 - 11/14 17:52:54	Fixed (0.0, 0.0)	synoptic, shifted -17.0 min										
11/15 06:03:00 - 11/15 06:09:54	Fixed (0.0, 0.0)	synoptic										
PROG= 04 1-time(s)												
└─ Subr= 1 1-time(s) 12.0sec												
└─ Seqn= 71 1-time(s) 4.0sec												
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	2ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	125ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Al-mesh	Open/Al-mesh	close	Safe	Norm	1.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
└─ Seqn= 5 1-time(s) 2.0sec												
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	4x4	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec

Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	8x8	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	2048x512 (1024, 1024)	DPCM	0	0	2.0sec
Open/Ti-poly	Open/thick-Al	close	Safe	Dark	500ms	Obs	1x1	512x2048 (1024, 1024)	DPCM	0	0	2.0sec
Seqn= 27 1-time(s) 4.0sec												
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	354ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Open/Ti-poly	Open/Ti-poly	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 93 1-time(s) 2.0sec												
thin-Be/Open	thin-Be/Open	close	Safe	Norm	16ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
thin-Be/Open	thin-Be/Open	close	Safe	Norm	4.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Seqn= 6 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	8ms	Obs	2x2	2048x2048 (1024, 1024)	Q=90	0	0	2.0sec
Open/G-band	Open/G-band	close	Safe	Norm	32ms	Obs	1x1	2048x2048 (1024, 1024)	Q=95	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Flare mode

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XOB #1A14: Flare - Med-Be w/ 6s cad - Al-poly w/ 1min cad - Med-Al, Thick-Be, G-band, Darks w/ 5.5min cad												
Term	Pointing (x, y)		Comment									
11/13 10:39:30 - 11/13 10:44:54	Fixed (,)											
11/13 10:48:00 - 11/13 16:59:42	Fixed (-933.7, -256.4)		# OP start + 10min, previously AR12192 obs (HOP244 12-15UT)									
11/13 17:00:00 - 11/13 17:30:30	Fixed (-933.7, -256.4)		# OP start + 10min, previously AR12192 obs (HOP244 12-15UT)									
11/13 18:09:00 - 11/13 22:22:00	Track (-927.9, -259.9) @ 11/13 18:06:00		# previously AR12192 (HOP275 0:08-2:08UT)									
11/13 22:44:00 - 11/13 23:59:30	Track (-927.9, -259.9) @ 11/13 18:06:00		# previously AR12192 (HOP275 0:08-2:08UT)									
11/14 00:09:00 - 11/14 02:10:00	Track (-927.9, -259.9) @ 11/13 18:06:00		# previously AR12192 (HOP275 0:08-2:08UT)									
11/14 02:10:18 - 11/14 07:19:54	Track (-927.9, -259.9) @ 11/13 18:06:00		# previously AR12192 (HOP275 0:08-2:08UT)									
11/14 07:33:00 - 11/14 11:59:42	Track (-905.3, -266.1) @ 11/14 07:30:00		# Adjust track curve 1 (HOP244 12-15UT)									
11/14 12:00:00 - 11/14 14:54:00	Track (-905.3, -266.1) @ 11/14 07:30:00		# Adjust track curve 1 (HOP244 12-15UT)									
11/14 15:07:30 - 11/14 17:42:54	Track (-905.3, -266.1) @ 11/14 07:30:00		# Adjust track curve 1 (HOP244 12-15UT)									
11/14 17:56:00 - 11/14 22:58:00	Track (-877.5, -270.5) @ 11/14 17:53:00		# Previously AR12192 (HOP275 23:20-01:20)									
11/14 23:17:30 - 11/15 01:20:00	Track (-877.5, -270.5) @ 11/14 17:53:00		# Previously AR12192 (HOP275 23:20-01:20)									
11/15 01:20:18 - 11/15 05:59:54	Track (-877.5, -270.5) @ 11/14 17:53:00		# Previously AR12192 (HOP275 23:20-01:20)									
11/15 06:13:00 - 11/15 10:03:30	Track (-833.4, -275.4) @ 11/15 06:10:00		# Track previously AR12192									
PROG= 18 15-time(s)												
Subr= 1 5-time(s) 2.0sec												
Seqn= 53 10-time(s) 2.0sec												
med-Be/Open	med-Be/Open	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	6.0sec
Seqn= 11 1-time(s) 2.0sec												
Al-poly/Open	Al-poly/thick-Al	close	Safe	Norm	125ms	Obs	2x2	512x512 (1024, 1024)	Q=95	2	0	2.0sec
Subr= 2 1-time(s) 2.0sec												
Seqn= 10 1-time(s) 2.0sec												
med-Al/Open	med-Al/thick-Al	close	Safe	Norm	500ms	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Open/thick-Be	Open/thick-Be	close	Safe	Norm	2.00s	Obs	1x1	384x384 (1024, 1024)	Q=95	3	0	2.0sec
Seqn= 15 1-time(s) 2.0sec												
Open/G-band	Open/G-band	open	Safe	Norm	44ms	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	1x1	384x384 (1024, 1024)	Q=98	0	0	2.0sec
Open/thick-Al	Open/thick-Al	close	Safe	Dark	1.00s	Obs	2x2	512x512 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

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Active Region Search

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NOT USED

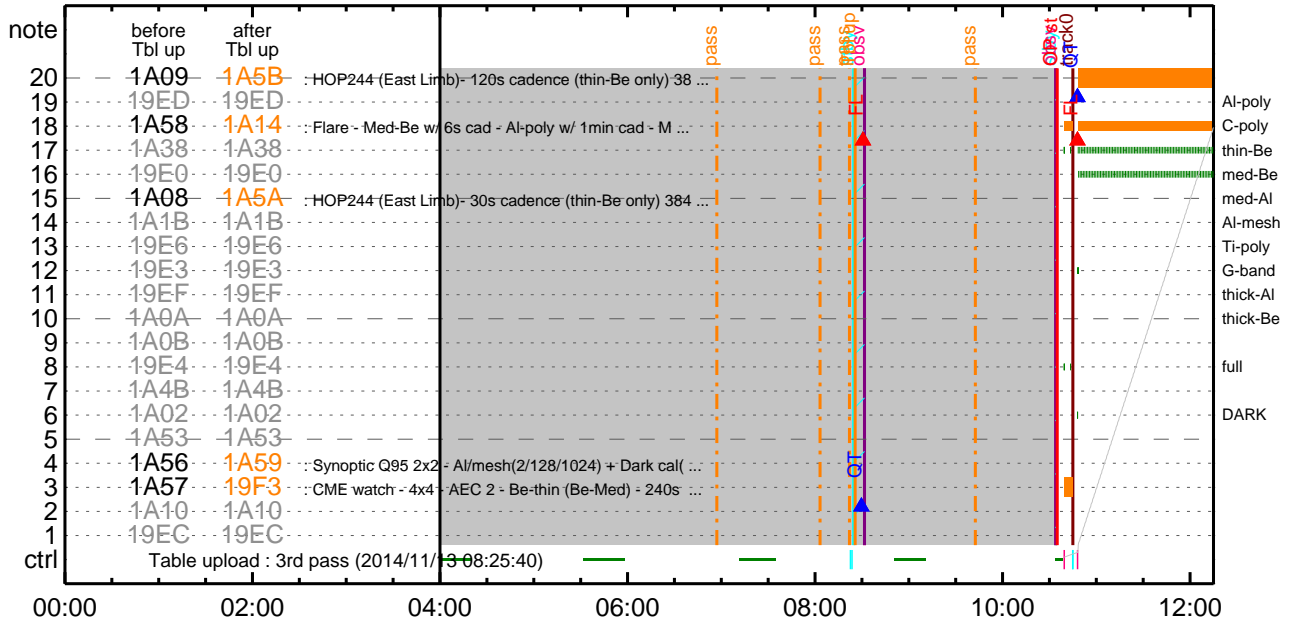
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Flare Detection

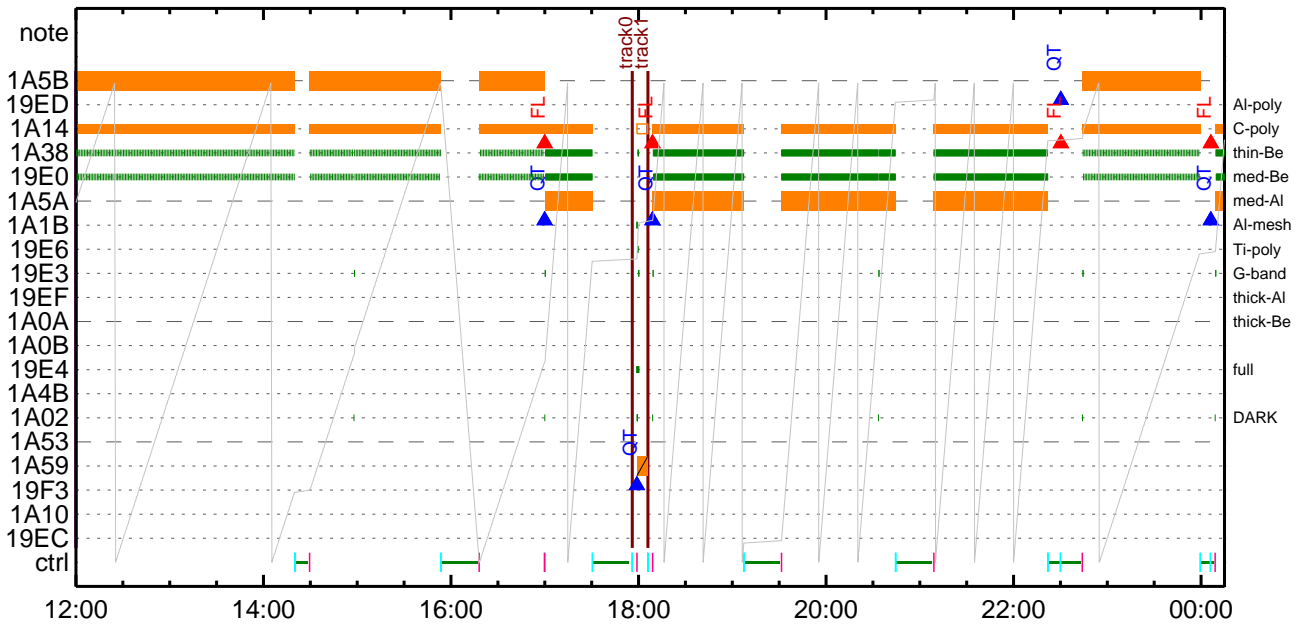
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FLD Patrol												
Term	Pointing (x, y)		Comment									
11/13 18:06:18 - 11/14 07:20:18	Track (-927.9, -259.9) @ 11/13 18:06:00		# previously AR12192 (HOP275 0:08-2:08UT)									
11/14 07:30:18 - 11/14 17:43:18	Track (-905.3, -266.1) @ 11/14 07:30:00		# Adjust track curve 1 (HOP244 12-15UT)									
11/14 17:53:18 - 11/15 06:00:18	Track (-877.5, -270.5) @ 11/14 17:53:00		# Previously AR12192 (HOP275 23:20-01:20)									
11/15 06:10:18 - 11/18 11:10:00	Track (-833.4, -275.4) @ 11/15 06:10:00		# Track previously AR12192									
Open/Ti-poly	Open/thick-Al	close	Safe	Norm	8ms	Obs	8x8		Q=50			30sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

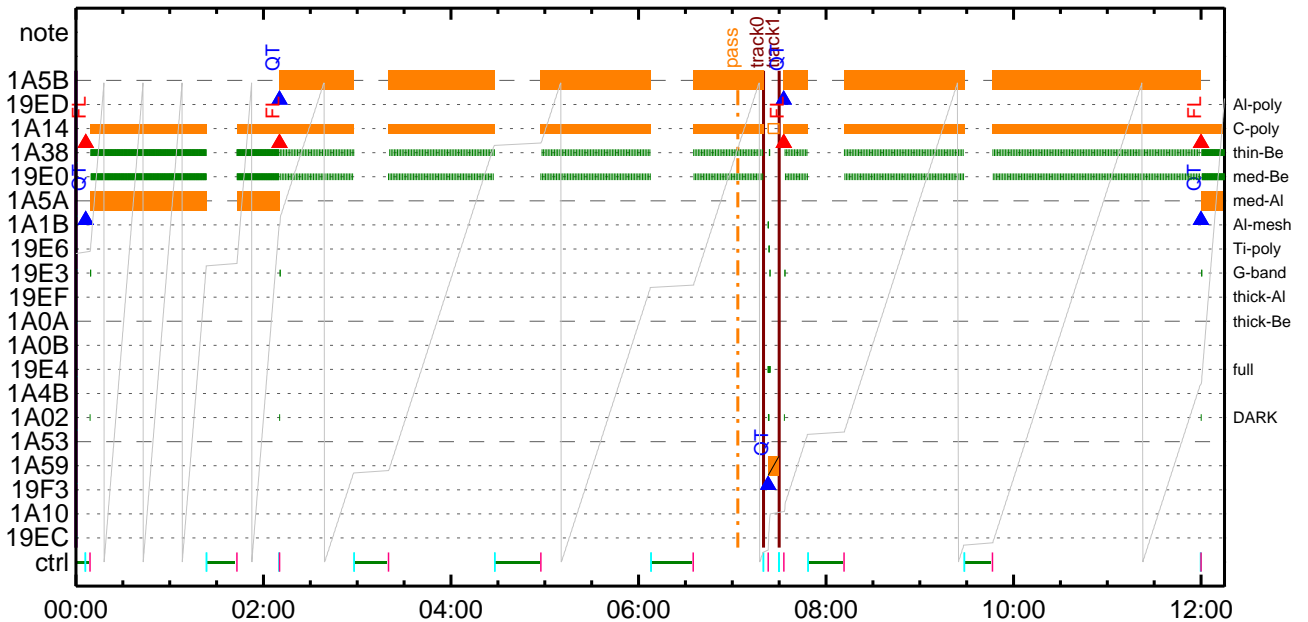
CMDI #0719 2014/11/13



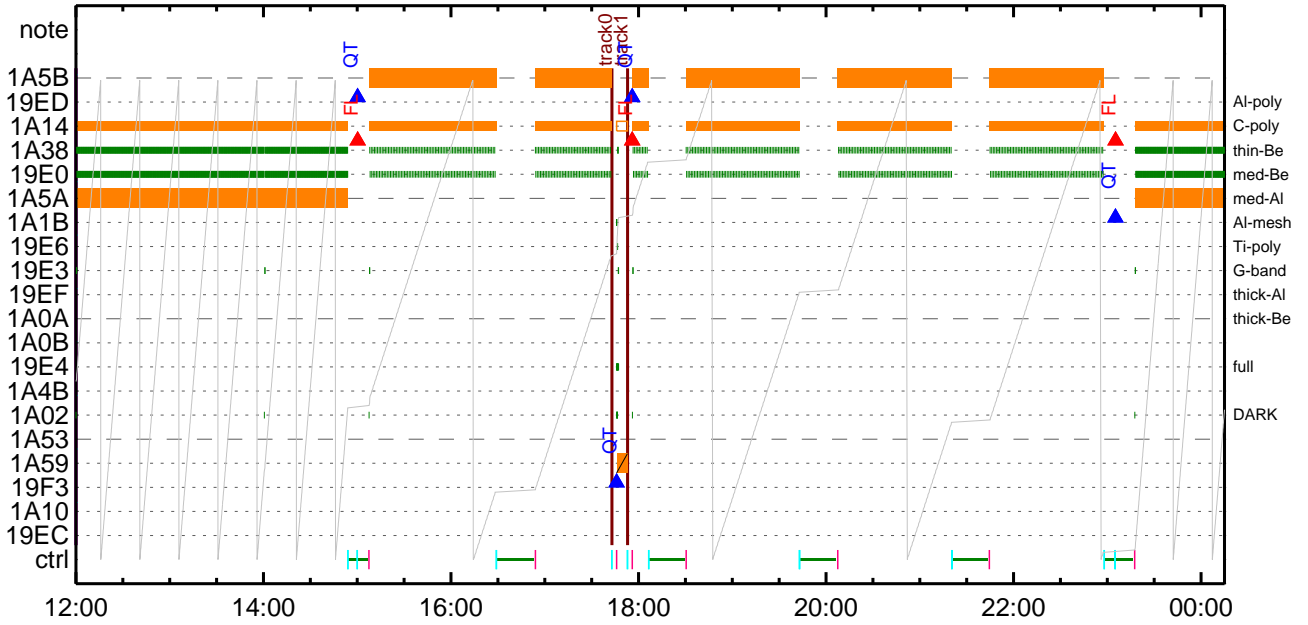
CMDI #0719 2014/11/13



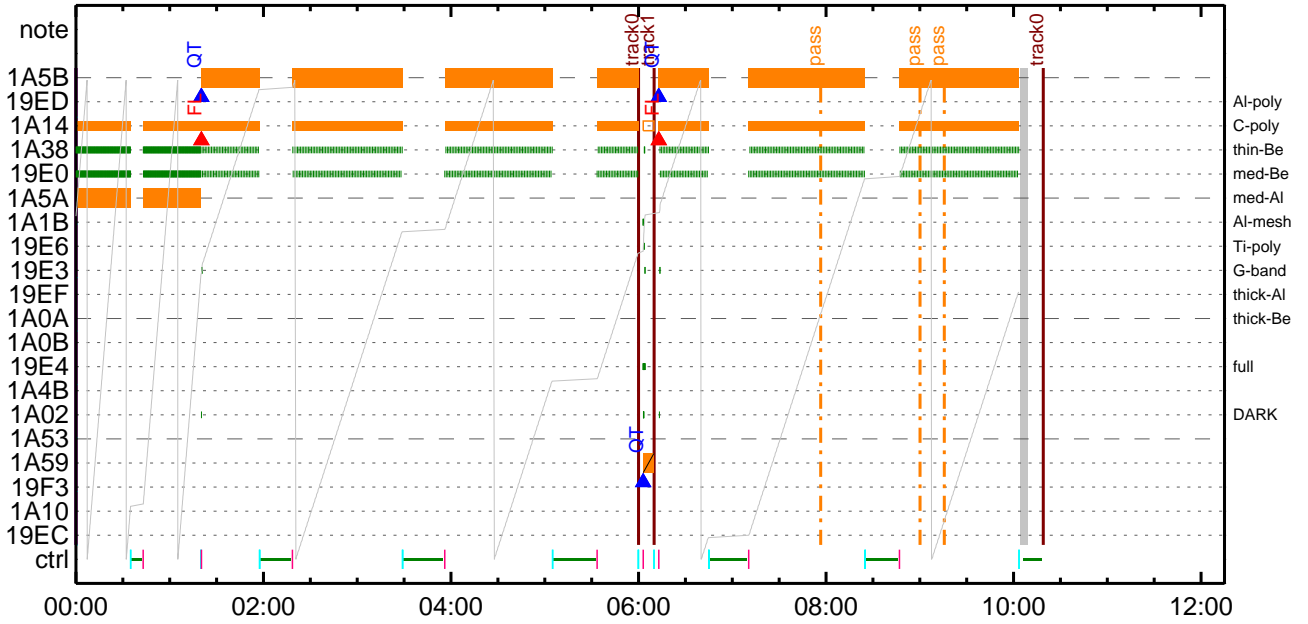
CMDI #0719 2014/11/14



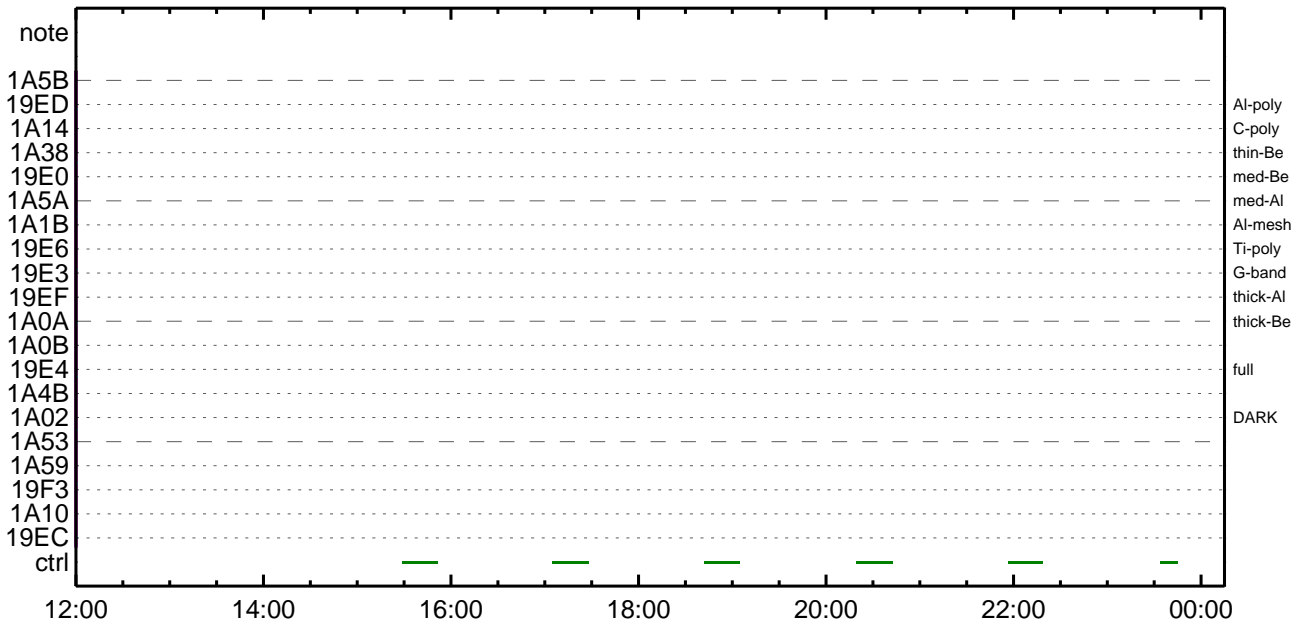
CMDI #0719 2014/11/14



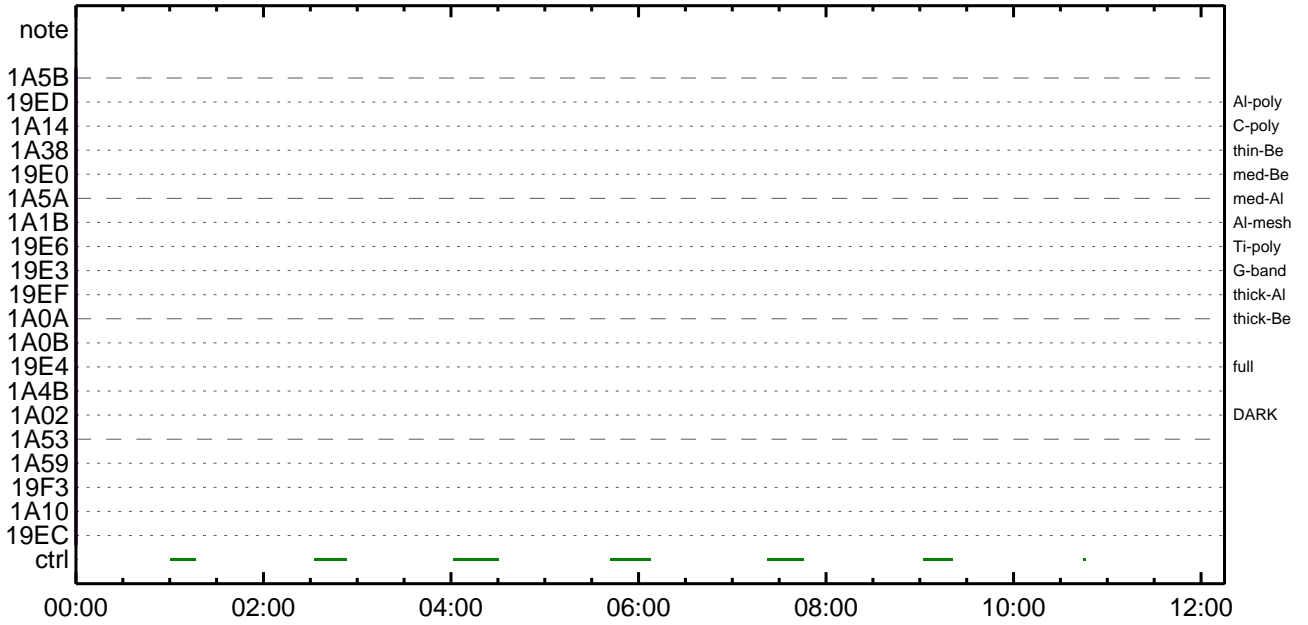
CMDI #0719 2014/11/15



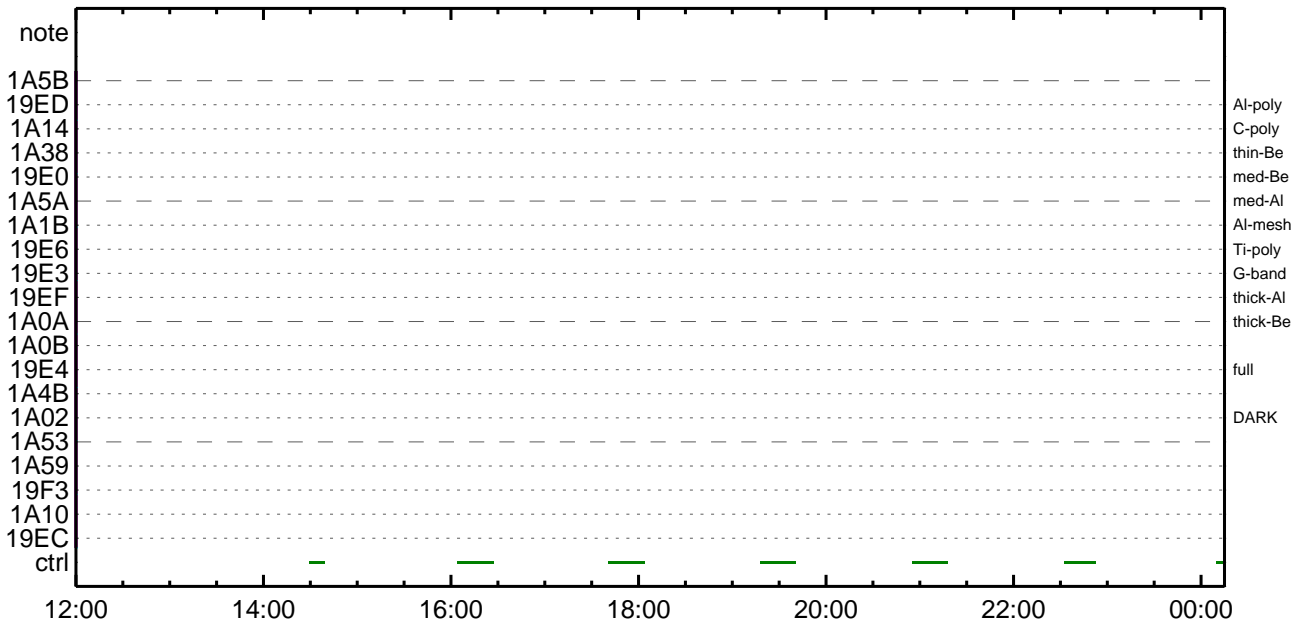
CMDI #0719 2014/11/15



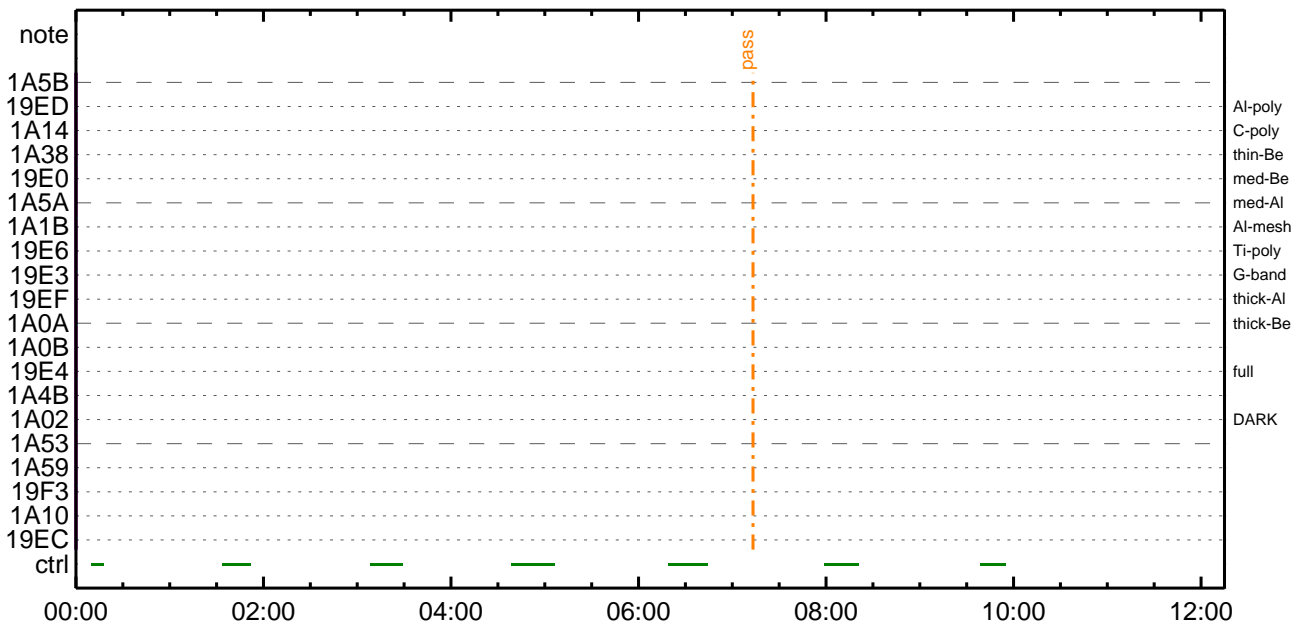
CMDI #0719 2014/11/16



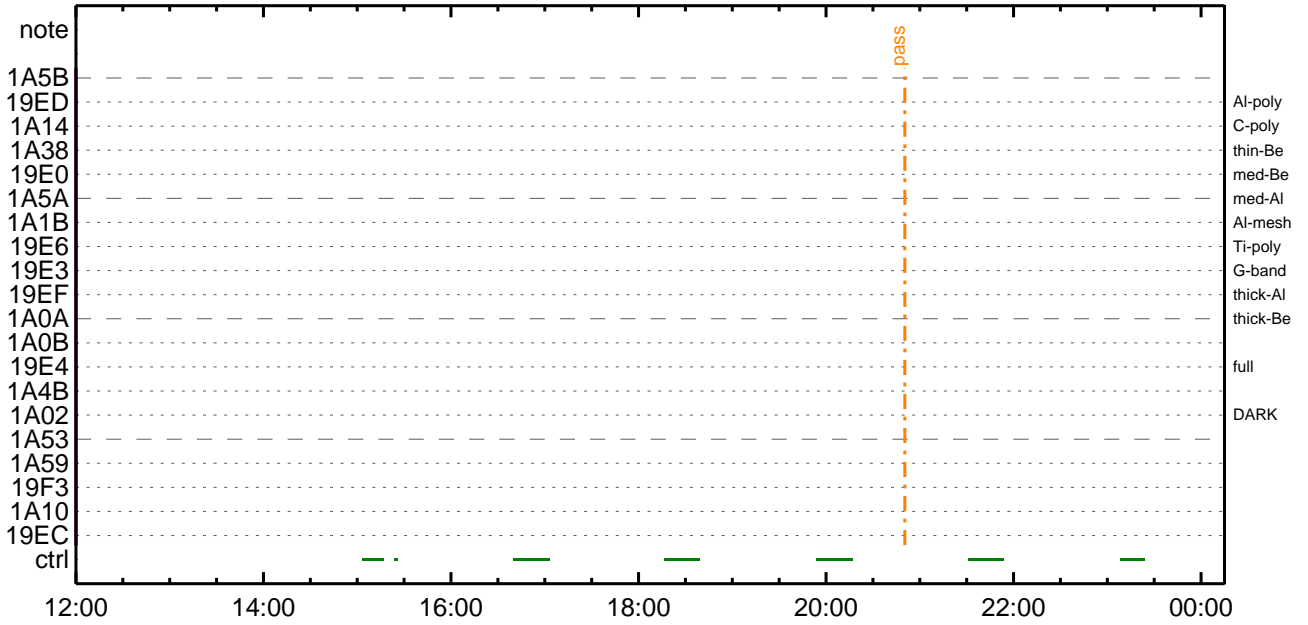
CMDI #0719 2014/11/16



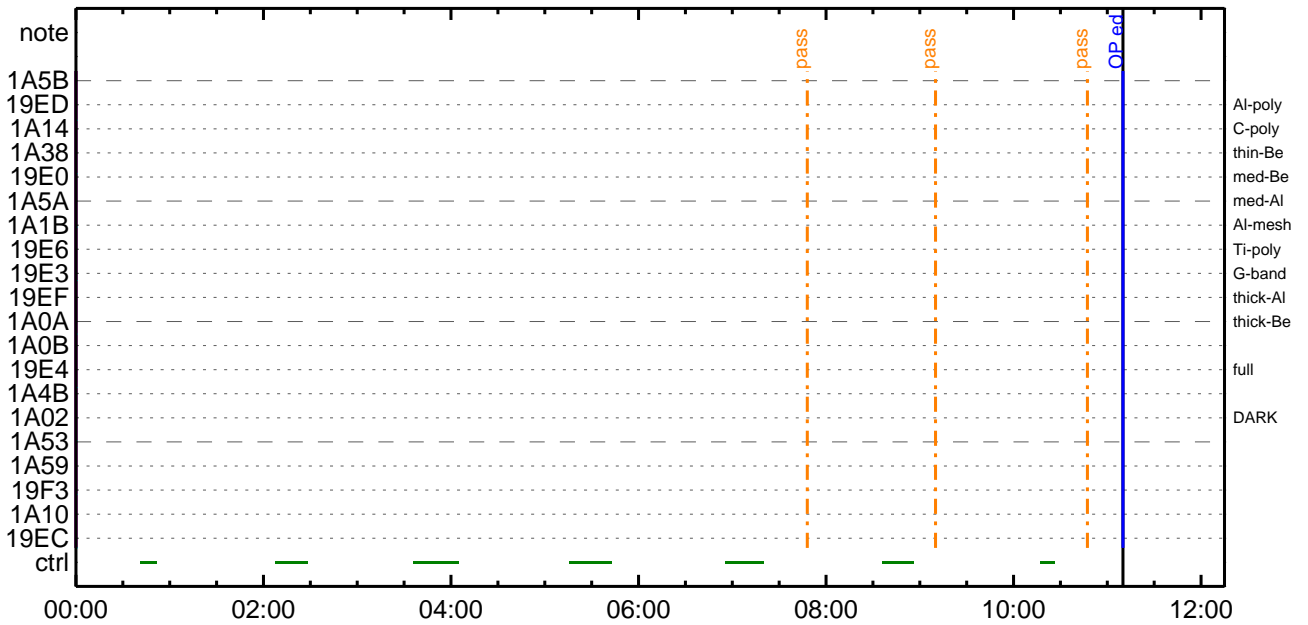
CMDI #0719 2014/11/17



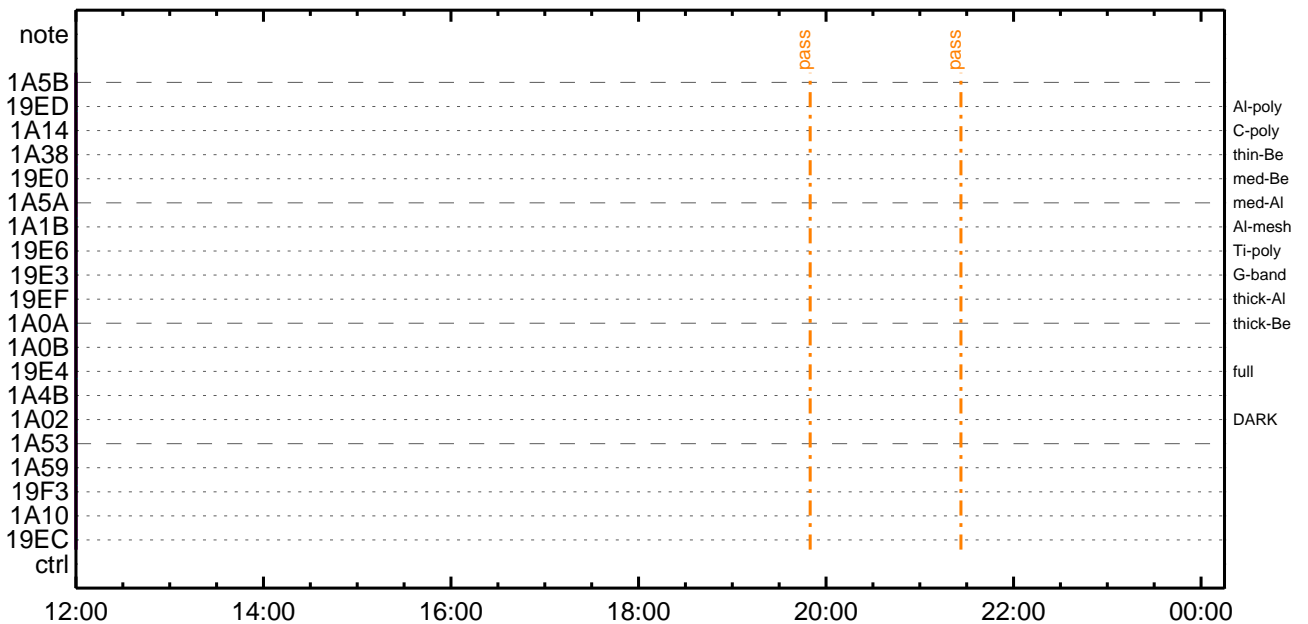
CMDI #0719 2014/11/17



CMDI #0719 2014/11/18



CMDI #0719 2014/11/18



(a) Spacecraft Operation Procedure (real-commands)

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main-754 2014-11-13 14:30:05 205 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÄY~¼Ä»Û;ã
0005 C.
0006 C. YÄYŞ;¼Y³YÞYÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCs : Reload orbital element (send every contact) *****
0010 C. Áí;È±¿±Ä±·µ°Æ»Ï×ÁÇ±íYçYÄY×Yí;¼YÉ;ÈÈ±µ·íÉ;È±È¼°ÇÖ±·±¿¼í¹ç±í;çÄ®, ù±¹±è±±±çÄ+¿®±·±È±±±±±è;É;É
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. OP/OGYí;¼YÉ;|YÄYÖY×
0016 C. *****
0017 C.
0018 . C. ;ãOP/OGYí;¼YÉ;ã
0019 . S. OP op-754:OP
0020 ( )
0021 . S. OG og-754:OG
0022 ( )
0023 C.
0024 . C. ;ãNMOG&OPí°èYÄYÖY×;ã
0025 C. NMOG(0x200000-0x207FFF;§ 32 kbyte)
0026 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0027 BC (20 00 7f 01 02)
0028 C. çç[HK1_DMP_TOP_ADRS_1] EQ 40
0029 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0030 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0031 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0032 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0033 +. DC 01-22 DHU_MODE_CHNG
0034 BC (07 0b f8)
0035 C. çç[HK1_PKT_FORM_NO] EQ 7
0036 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0037 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0038 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0039 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0040 . C. YÄYÖY×¼³¹î»±±³¹Ç§
0041 C. çç[HK1_DMP_CHK_FLG] EQ NON
0042 . C. RAM ID=NMOG±î¼È¹ç·è²¹OK±±³¹Ç§
0043 C.
0044 C. NMOG(0x208000-0x20FFFF;§ 32 kbyte)
0045 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0046 BC (20 80 7f 01 02)
0047 C. çç[HK1_DMP_TOP_ADRS_1] EQ 41
0048 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0049 C. çç[HK1_DMP_BLOCK_NUM] EQ 127
0050 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0051 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0052 +. DC 01-22 DHU_MODE_CHNG
0053 BC (07 0b f8)
0054 C. çç[HK1_PKT_FORM_NO] EQ 7
0055 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0056 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0057 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0058 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0059 . C. YÄYÖY×¼³¹î»±±³¹Ç§
0060 C. çç[HK1_DMP_CHK_FLG] EQ NON
0061 . C. RAM ID=NMOG±î¼È¹ç·è²¹OK±±³¹Ç§
0062 C.
0063 C. NMOG(0x210000-0x2100FF;§ 256byte)+OP(0x210100-0x2141FF: 16.25kbyte)
0064 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0065 BC (21 00 41 01 02)
0066 C. çç[HK1_DMP_TOP_ADRS_1] EQ 42
0067 C. çç[HK1_DMP_TOP_ADRS_0] EQ 0
0068 C. çç[HK1_DMP_BLOCK_NUM] EQ 65
0069 C. çç[HK1_DMP_REPEAT_NUM] EQ 0
0070 C. çç[HK1_DMA_DMP_PIM] EQ DHU
0071 +. DC 01-22 DHU_MODE_CHNG
0072 BC (07 0b f8)
0073 C. çç[HK1_PKT_FORM_NO] EQ 7
0074 C. çç[HK1_PKT_GEN_TIME] EQ 0.25 s
0075 C. çç[HK1_S_TLM_BIT_RATE] EQ 32k
0076 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0077 C. çç[HK1_DMP_CHK_FLG] EQ EXEC
0078 . C. YÄYÖY×¼³¹î»±±³¹Ç§
0079 C. çç[HK1_DMP_CHK_FLG] EQ NON
0080 . C. RAM ID=NMOG,RAM ID=OP±î¼È¹ç·è²¹OK±±³¹Ç§
0081 C.
0082 . C. ***** °È²¼±±¼Ä´¶¹±±èÈ-±±Ä+¿® (¼±µ-YÄYÖY×¼¼½ç±±Ä±±ç¼±±±±±è) *****
0083 C. DHUYá;¼YÉ;È¼Y¼;Yí;¼YÉ;È±±¼±¹
0084 +. DC 01-22 DHU_MODE_CHNG
0085 BC (02 0a f8)
0086 C. çç[HK1_PKT_FORM_NO] EQ 2
0087 C. çç[HK1_PKT_GEN_TIME] EQ 0.5S
0088 C. çç[HK1_S_TLM_BIT_RATE] EQ 32K
0089 C. çç[HK1_X_TLM_BIT_RATE] EQ 4M
0090 C.
0091 . C. *****
0092 C. TI-CMD SET (OPOG STOP/COPY/START)
0093 C. *****
0094 C.
0095 . C. NOTICE ;§ OPOG UPLOAD±-Á+¿¿®NG±¼í¹¹ç;ç°È²¼±±¼TI-CMDÁ+¿¿®¼Ä¹Ö±±±±±è;É;É
```

```

0096 C.          0000; SET EDUMP I±°iYNY¹aÇ¹Ôa|a³aE;E
0097 C.
0098 C. TIY³YBYÖYÉaðdÄDİ¿(UT)
0099 +. TI 2014-11-13 10:30:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0102 C.
0103 +. TI 2014-11-13 10:30:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0106 C.
0107 +. TI 2014-11-13 10:30:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0110 C.
0111 +. TI 2014-11-13 10:34:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.          çç[HK1_TI_CMD_NUM]          EQ          1COUNTUP
0114 C.
0115 C.  °E²¼aİÄê%îİÑaİYÁYŞYÄY-¹àİÜ
0116 C.          çç[HK1_TI_CMD_ENA/DIS]      EQ          ENA
0117 C.          çç[HK1_TI_CMD_NUM]        EQ          4
0118 C.          çç[HK1_NEXT_EXEC_PIM]     EQ          DHU
0119 C.          çç[HK1_NEXT_EXEC_DC]      EQ          0xB3
0120 C.
0121 C. *****
0122 C. TIİİ°èYÄYÖY×
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF;§ 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC          (03 ab 03 01 02)
0128 C.          çç[HK1_DMP_TOP_ADRS_1]     EQ          07
0129 C.          çç[HK1_DMP_TOP_ADRS_0]     EQ          2B
0130 C.          çç[HK1_DMP_BLOCK_NUM]      EQ          3
0131 C.          çç[HK1_DMP_REPEAT_NUM]     EQ          0
0132 C.          çç[HK1_DMA_DMP_PIM]       EQ          DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC          (07 0b f8)
0135 C.          çç[HK1_PKT_FORM_NO]        EQ          7
0136 C.          çç[HK1_PKT_GEN_TIME]       EQ          0.25 s
0137 C.          çç[HK1_S_TLM_BIT_RATE]     EQ          32k
0138 C.          çç[HK1_X_TLM_BIT_RATE]    EQ          4M
0139 C.          çç[HK1_DMP_CHK_FLG]       EQ          EXEC
0140 C.
0141 C.  YÄYÖY×¼ª¹İ»að³İÇŞ
0142 C.          çç[HK1_DMP_CHK_FLG]       EQ          NON
0143 C.
0144 C. RAM ID=TI_TBLaİ%È¹Ç•è²İOKað³İÇŞ
0145 C.
0146 C. DHUYâ;¼YÉ;È¼Y¼, Yİ;¼YÈ;Èaðİáa¹
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC          (02 0a f8)
0149 C.          çç[HK1_PKT_FORM_NO]        EQ          2
0150 C.          çç[HK1_PKT_GEN_TIME]       EQ          0.5S
0151 C.          çç[HK1_S_TLM_BIT_RATE]     EQ          32K
0152 C.          çç[HK1_X_TLM_BIT_RATE]    EQ          4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2014-11-13 10:34:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC          (41)
0161 C. -----
0162 C.          HK1_TI_CMD_NUM          = 1 CNTUP [ ]
0163 C. -----
0164 C. ***** SOT END *****
0165 C. Stop EIS observation and temporarily disable EIS mode changes
0166 C.
0167 C.
0168 C. ***** Start EIS operation (TI set) *****
0169 C. Execute, after the success of OP upload.
0170 C. Set EIS TI-commands
0171 +. TI 2014-11-13 10:34:30.0
0172 DC 07-FC EIS_MODE_MANU
0173 BC          (21 02)
0174 +. TI 2014-11-13 10:34:40.0
0175 DC 07-FC EIS_MODE_CHG_DIS
0176 BC          (22)
0177 C.          [ ] [HK1_TI_CMD_NUM]      EQ          2 COUNTUP
0178 C. ***** End EIS operation (TI set) *****
0179 C.
0180 C.
0181 C.
0182 C. ***** XRT START *****
0183 C. Execute, after the success of OP upload.
0184 +. TI 2014-11-13 10:34:00.0
0185 DC 07-F0 MDP_XRT_MODE_STBY
0186 BC          (c3)
0187 C.          [ ] [HK1_TI_CMD_NUM]      EQ          1COUNTUP
0188 C.
0189 C. ***** XRT END *****
0190 C.
0191 C. ***** MDP `üÄİaİ»ö¼YöÈÄa¹aèDCBC•×²è *****
0192 C. (¼a°İYÖYÄYÈYBYPYÈYáYçYèaÈ%¼a¼Ä»Üa¹aè)
0193 C. DC-BC dcbc-402:DCBC

```



```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```


(a) Spacecraft Operation Procedure (real-commands)

```
main-756 2014-11-13 14:30:05 147 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;äAOSÿÁÿ$ÿÄÿ-¼Ä»Û;ä
0005 C.
0006 C. ÿÀÿB;¼ÿ³ÿFÿóÿÉÄ+ç®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Āí;ÈçðĀá•µ°È»Í×ÁÇŏÍŷçÿÄÿ×ÿÍ;¼ÿÉ;ÈÈ%µ•ííÈ;ÈÈÈ¼°ÇÔñ•ñç¼í¹çñĪ;çĀ®, ùñ¹ñÈñÈñçĀ+ç®ñ•ñÈñññ³ñÈ;ñ
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-264:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Resume FG table (auto mode) >
0036 +. DC 07-F0 MDP_FG_CTRL_AUTO
0037 BC (50)
0038 . C. -----
0039 C. MDP_FG_CTRL_MODE = AUTO [ ]
0040 C. -----
0041 C.
0042 C. *****
0043 C. SOT TI command set
0044 C. *****
0045 C. Execute, after the success of TBL upload.
0046 +. TI 2014-11-13 10:34:18.0
0047 DC 07-F0 MDP_SOT_MODE_OBSV
0048 BC (40)
0049 . C. -----
0050 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0051 C. -----
0052 C.
0053 C. Only when FG_CTRL_AUTO is used in RT.
0054 +. TI 2014-11-13 10:34:20.0
0055 DC 07-F0 MDP_FG_CTRL_AUTO
0056 BC (50)
0057 . C. -----
0058 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0059 C. -----
0060 C. ***** SOT END *****
0061 C.
0062 C. ***** XRT START *****
0063 C.
0064 +. DC 07-F0 MDP_XRT_CTRL_MANU
0065 BC (c1)
0066 +. DC 07-F0 MDP_XRT_CTRL_MANU
0067 BC (c1)
0068 +. DC 07-F8 XRT_FOCUS_POSITION
0069 BC (22 ff aa 00)
0070 +. DC 07-F0 MDP_XRT_MODE_STBY
0071 BC (c3)
0072 . C. ----- Success Verify ? OK / NG_____
0073 C.
0074 C. XRT Obs. Table Upload
0075 . S. RAM ram-291:MDP_OBS_X
0076 ( )
0077 C.
0078 +. DC 07-F0 MDP_DUMP_XRTTBL
0079 BC (84 07 00 00 00 3a d4)
0080 . C. ----- Comparison Check ? OK / ERR _____
0081 C.
0082 C.
0083 +. DC 07-F0 MDP_XRT_ROI_SET
0084 BC (cd 01 b1 b1 04 04)
0085 +. DC 07-F0 MDP_XRT_ROI_SET
0086 BC (cd 02 b1 b1 08 08)
0087 +. DC 07-F0 MDP_XRT_ROI_SET
0088 BC (cd 03 b1 b1 08 08)
0089 +. DC 07-F0 MDP_XRT_ROI_SET
0090 BC (cd 04 b1 b1 06 06)
0091 +. DC 07-F0 MDP_XRT_ROI_SET
0092 BC (cd 05 75 83 06 06)
0093 +. DC 07-F0 MDP_XRT_ROI_SET
0094 BC (cd 06 80 80 20 20)
0095 +. DC 07-F0 MDP_XRT_ROI_SET
```

```

0096 BC (cd 07 75 83 06 06)
0097 + DC 07-F0 MDP_XRT_ROI_SET
0098 BC (cd 08 80 80 20 08)
0099 + DC 07-F0 MDP_XRT_ROI_SET
0100 BC (cd 09 80 80 08 20)
0101 + DC 07-F0 MDP_XRT_ROI_SET
0102 BC (cd 0f 80 80 06 06)
0103 + DC 07-F0 MDP_XRT_ROI_SET
0104 BC (cd 10 80 80 08 08)
0105 + DC 07-F0 MDP_XRT_FLD_ENA
0106 BC (d8)
0107 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0108 BC (c8)
0109 + DC 07-F0 MDP_XRT_AEC_RESET
0110 BC (d0)
0111 + DC 07-F0 MDP_XRT_ARS_DIS
0112 BC (d5)
0113 + DC 07-F0 MDP_XRT_FLD_RESET
0114 BC (da)
0115 +. DC 07-F0 MDP_XRT_QT_PROG_SET
0116 BC (c4 03)
0117 +. DC 07-F0 MDP_XRT_FL_PROG_SET
0118 BC (c5 12)
0119 . C. ----- Success Verify ? OK / NG ____
0120 C.
0121 C.
0122 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0123 C.
0124 +. DC 07-F0 MDP_XRT_MODE_OBSV
0125 BC (c2)
0126 +. TI 2014-11-13 10:34:02.0
0127 DC 07-F0 MDP_XRT_MODE_OBSV
0128 BC (c2)
0129 . C. ----- Success Verify ? OK / NG ____
0130 C.
0131 C. ***** XRT END *****
0132 C.
0133 . C. ***** MDP `uAInI»ô%YnEAð¹ªeDCBC·x²è *****
0134 C. (%â°i¥Ö¥Ä¥E¥P¥E¥â¥ç¥ëªE%¼ªª¼A»Üª¹ªè)
0135 . S. DC-BC dcbc-402:DCBC
0136 (MDP_known_event)
0137 C.
0138 C.
0139 . C. ***** ¥D¥¹·İ Daily±;İNªE´Øª¹ªeDCBC·x²è *****
0140 . S. DC-BC dcbc-153:DCBC
0141 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0142 C.
0143 C.
0144 . C. ;ãLOS¥Á¥S¥Ä¥¹¼A»Ü;ã
0145 C.
0146 . C. ***** LOS *****
0147 C.

```

*** OP Sequence for XRT ***

```

2014/11/13 10:38:30.0 XRT_Custom_430_OG [0x1ae]
2014/11/13 10:39:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO      1 07-F0 c0
2014/11/13 10:44:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU      1 07-F0 c1
2014/11/13 10:44:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU      1 07-F0 c1
2014/11/13 10:44:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION      4 07-F8 22 fe 97 00
2014/11/13 10:45:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM                  5 02-76 00 16 c8 53 02
2014/11/13 10:45:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA        1 07-F0 d8
2014/11/13 10:45:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA    1 07-F0 c8
2014/11/13 10:45:22.0 XRT_AEC_RESET_413_OG [0x19d]
                        MDP_XRT_AEC_RESET      1 07-F0 d0
2014/11/13 10:45:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS        1 07-F0 d5
2014/11/13 10:45:26.0 XRT_FLD_RESET_407_OG [0x197]
                        MDP_XRT_FLD_RESET      1 07-F0 da
2014/11/13 10:47:56.0 XRT_QT_PROG_SET_435_OG [0x1b3]
                        MDP_XRT_QT_PROG_SET    2 07-F0 c4 14
2014/11/13 10:47:58.0 XRT_FL_PROG_SET_414_OG [0x19e]
                        MDP_XRT_FL_PROG_SET    2 07-F0 c5 12
2014/11/13 10:48:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO      1 07-F0 c0
2014/11/13 14:20:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU      1 07-F0 c1
2014/11/13 14:20:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU      1 07-F0 c1
2014/11/13 14:20:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET      1 07-F0 da
2014/11/13 14:20:06.0 XRT_PREFLR_STRT_439_OG [0x1b7]
                        MDP_XRT_PREFLR_STRT    1 07-F0 e8
2014/11/13 14:23:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP    1 07-F0 e9
2014/11/13 14:28:30.0 XRT_Custom_430_OG [0x1ae]
2014/11/13 14:29:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO      1 07-F0 c0
2014/11/13 15:53:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU      1 07-F0 c1
2014/11/13 15:53:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU      1 07-F0 c1
2014/11/13 15:53:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET      1 07-F0 da
2014/11/13 15:53:36.0 XRT_PREFLR_STRT_439_OG [0x1b7]
                        MDP_XRT_PREFLR_STRT    1 07-F0 e8
2014/11/13 15:56:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP    1 07-F0 e9
2014/11/13 16:17:00.0 XRT_Custom_430_OG [0x1ae]
2014/11/13 16:18:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO      1 07-F0 c0
2014/11/13 16:59:42.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU      1 07-F0 c1
2014/11/13 16:59:44.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU      1 07-F0 c1
2014/11/13 16:59:46.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA        1 07-F0 d8
2014/11/13 16:59:48.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA    1 07-F0 c8
2014/11/13 16:59:50.0 XRT_AEC_RESET_413_OG [0x19d]
                        MDP_XRT_AEC_RESET      1 07-F0 d0
2014/11/13 16:59:52.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS        1 07-F0 d5
2014/11/13 16:59:54.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET      1 07-F0 da
2014/11/13 16:59:56.0 XRT_QT_PROG_SET_416_OG [0x1a0]
                        MDP_XRT_QT_PROG_SET    2 07-F0 c4 0f
2014/11/13 16:59:58.0 XRT_FL_PROG_SET_414_OG [0x19e]
                        MDP_XRT_FL_PROG_SET    2 07-F0 c5 12
2014/11/13 17:00:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO      1 07-F0 c0
2014/11/13 17:30:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU      1 07-F0 c1
2014/11/13 17:30:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU      1 07-F0 c1
2014/11/13 17:30:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET      1 07-F0 da
2014/11/13 17:30:36.0 XRT_PREFLR_STRT_439_OG [0x1b7]
                        MDP_XRT_PREFLR_STRT    1 07-F0 e8
2014/11/13 17:33:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP    1 07-F0 e9
2014/11/13 17:55:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU      1 07-F0 c1
2014/11/13 17:55:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU      1 07-F0 c1
2014/11/13 17:55:58.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION      4 07-F8 22 ff aa 00
2014/11/13 17:56:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM                  5 02-76 00 00 00 00 00

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2014/11/13	17:56:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/11/13	17:58:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/11/13	17:58:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/11/13	17:58:58.0	XRT_QT_PROG_SET_433_OG [0x1b1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2014/11/13	17:59:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/11/13	18:05:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/13	18:05:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/13	18:05:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/11/13	18:06:00.0	AOCS_ORe-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01 00 00 00 00				
2014/11/13	18:06:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/11/13	18:06:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/11/13	18:06:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/11/13	18:06:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/11/13	18:06:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/11/13	18:08:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2014/11/13	18:08:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 12				
2014/11/13	18:09:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/11/13	19:07:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/13	19:07:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/13	19:07:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/11/13	19:07:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/11/13	19:10:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/11/13	19:30:30.0	XRT_Custom_430_OG [0x1ae]							
2014/11/13	19:31:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/11/13	20:44:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/13	20:44:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/13	20:44:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/11/13	20:44:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/11/13	20:47:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/11/13	21:08:00.0	XRT_Custom_430_OG [0x1ae]							
2014/11/13	21:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/11/13	22:22:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/13	22:22:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/13	22:22:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/11/13	22:22:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/11/13	22:25:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/11/13	22:30:00.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/13	22:30:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/13	22:30:04.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/11/13	22:30:06.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/11/13	22:30:08.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/11/13	22:30:10.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/11/13	22:30:12.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/11/13	22:30:14.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2014/11/13	22:30:16.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 12				
2014/11/13	22:43:00.0	XRT_Custom_430_OG [0x1ae]							
2014/11/13	22:44:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/11/13	23:59:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				

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2014/11/13	23:59:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/13	23:59:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/11/13	23:59:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/11/14	00:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/11/14	00:06:00.5	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/14	00:06:02.5	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/14	00:06:04.5	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/11/14	00:06:06.5	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/11/14	00:06:08.5	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/11/14	00:06:10.5	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/11/14	00:06:12.5	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/11/14	00:06:14.5	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2014/11/14	00:06:16.5	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 12
2014/11/14	00:08:00.0	XRT_Custom_430_OG [0x1ae]			
2014/11/14	00:09:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/11/14	01:23:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/14	01:23:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/14	01:23:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/11/14	01:23:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/11/14	01:26:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/11/14	01:42:00.0	XRT_Custom_430_OG [0x1ae]			
2014/11/14	01:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/11/14	02:10:00.5	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/14	02:10:02.5	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/14	02:10:04.5	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/11/14	02:10:06.5	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/11/14	02:10:08.5	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/11/14	02:10:10.5	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/11/14	02:10:12.5	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/11/14	02:10:14.5	XRT_QT_PROG_SET_435_OG [0x1b3]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 14
2014/11/14	02:10:16.5	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 12
2014/11/14	02:10:18.5	XRT_CTRL_AUTO_408_OG [0x198]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/11/14	02:58:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/14	02:58:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/14	02:58:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/11/14	02:58:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/11/14	03:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/11/14	03:19:00.0	XRT_Custom_430_OG [0x1ae]			
2014/11/14	03:20:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/11/14	04:28:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/14	04:28:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/14	04:28:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/11/14	04:28:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/11/14	04:31:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/11/14	04:56:30.0	XRT_Custom_430_OG [0x1ae]			
2014/11/14	04:57:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/11/14	06:08:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/14	06:08:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/14	06:08:04.0	XRT_FLD_RESET_415_OG [0x19f]			

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2014/11/14	06:08:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/11/14	06:11:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/11/14	06:34:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/11/14	06:35:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/11/14	07:19:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/14	07:19:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/14	07:19:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00				
2014/11/14	07:20:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00 00 00 00 00				
2014/11/14	07:20:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/11/14	07:22:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/11/14	07:22:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/11/14	07:22:58.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04				
2014/11/14	07:23:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/11/14	07:29:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/14	07:29:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/14	07:29:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22 fe 97 00				
2014/11/14	07:30:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01 00 00 00 00				
2014/11/14	07:30:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/11/14	07:30:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/11/14	07:30:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/11/14	07:30:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/11/14	07:30:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/11/14	07:32:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 14				
2014/11/14	07:32:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5 12				
2014/11/14	07:33:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/11/14	07:48:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/14	07:48:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/14	07:48:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/11/14	07:48:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/11/14	07:51:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/11/14	08:10:30.0	XRT_Custom_430_OG [0x1ae]								
2014/11/14	08:11:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/11/14	09:28:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/14	09:28:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/14	09:28:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/11/14	09:28:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/11/14	09:31:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/11/14	09:45:30.0	XRT_Custom_430_OG [0x1ae]								
2014/11/14	09:46:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/11/14	11:59:42.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/14	11:59:44.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/14	11:59:46.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/11/14	11:59:48.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/11/14	11:59:50.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/11/14	11:59:52.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/11/14	11:59:54.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da				
2014/11/14	11:59:56.0	XRT_QT_PROG_SET_416_OG [0x1a0]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f				
2014/11/14	11:59:58.0	XRT_FL_PROG_SET_414_OG [0x19e]								

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2014/11/14	12:00:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	12	
			MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/11/14	14:54:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/11/14	14:54:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/11/14	14:54:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/11/14	14:54:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/11/14	14:57:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/11/14	15:00:00.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/11/14	15:00:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/11/14	15:00:04.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/11/14	15:00:06.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/11/14	15:00:08.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/11/14	15:00:10.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/11/14	15:00:12.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/11/14	15:00:14.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14	
2014/11/14	15:00:16.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	12	
2014/11/14	15:06:30.0	XRT_Custom_430_OG [0x1ae]						
2014/11/14	15:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/11/14	16:29:00.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/11/14	16:29:02.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/11/14	16:29:04.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/11/14	16:29:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8		
2014/11/14	16:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9		
2014/11/14	16:53:00.0	XRT_Custom_430_OG [0x1ae]						
2014/11/14	16:54:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/11/14	17:42:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/11/14	17:42:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/11/14	17:42:58.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22	ff aa 00	
2014/11/14	17:43:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	00	00 00 00 00	
2014/11/14	17:43:18.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9		
2014/11/14	17:45:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9		
2014/11/14	17:45:56.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/11/14	17:45:58.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	04	
2014/11/14	17:46:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/11/14	17:52:54.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/11/14	17:52:56.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/11/14	17:52:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]	XRT_FOCUS_POSITION	4	07-F8	22	fe 97 00	
2014/11/14	17:53:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	01	00 00 00 00	
2014/11/14	17:53:18.0	XRT_FLD_ENA_411_OG [0x19b]	MDP_XRT_FLD_ENA	1	07-F0	d8		
2014/11/14	17:53:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]	MDP_XRT_FLRCTRL_ENA	1	07-F0	c8		
2014/11/14	17:53:22.0	XRT_AEC_RESET_413_OG [0x19d]	MDP_XRT_AEC_RESET	1	07-F0	d0		
2014/11/14	17:53:24.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5		
2014/11/14	17:53:26.0	XRT_FLD_RESET_407_OG [0x197]	MDP_XRT_FLD_RESET	1	07-F0	da		
2014/11/14	17:55:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]	MDP_XRT_QT_PROG_SET	2	07-F0	c4	14	
2014/11/14	17:55:58.0	XRT_FL_PROG_SET_414_OG [0x19e]	MDP_XRT_FL_PROG_SET	2	07-F0	c5	12	
2014/11/14	17:56:00.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0		
2014/11/14	18:06:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1		
2014/11/14	18:06:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1		

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2014/11/14	18:06:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/11/14	18:06:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/11/14	18:09:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/11/14	18:29:30.0	XRT_Custom_430_OG [0x1ae]			
2014/11/14	18:30:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/11/14	19:43:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/14	19:43:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/14	19:43:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/11/14	19:43:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/11/14	19:46:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/11/14	20:06:30.0	XRT_Custom_430_OG [0x1ae]			
2014/11/14	20:07:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/11/14	21:20:30.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/14	21:20:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/14	21:20:34.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/11/14	21:20:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/11/14	21:23:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/11/14	21:43:30.0	XRT_Custom_430_OG [0x1ae]			
2014/11/14	21:44:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/11/14	22:58:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/14	22:58:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/14	22:58:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/11/14	22:58:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/11/14	23:01:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/11/14	23:05:00.5	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/14	23:05:02.5	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/14	23:05:04.5	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/11/14	23:05:06.5	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/11/14	23:05:08.5	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/11/14	23:05:10.5	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/11/14	23:05:12.5	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/11/14	23:05:14.5	XRT_QT_PROG_SET_416_OG [0x1a0]			
		MDP_XRT_QT_PROG_SET	2	07-F0	c4 0f
2014/11/14	23:05:16.5	XRT_FL_PROG_SET_414_OG [0x19e]			
		MDP_XRT_FL_PROG_SET	2	07-F0	c5 12
2014/11/14	23:16:30.0	XRT_Custom_430_OG [0x1ae]			
2014/11/14	23:17:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/11/15	00:35:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/15	00:35:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/15	00:35:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/11/15	00:35:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/11/15	00:38:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/11/15	00:42:00.0	XRT_Custom_430_OG [0x1ae]			
2014/11/15	00:43:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/11/15	01:20:00.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/15	01:20:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/15	01:20:04.0	XRT_FLD_ENA_411_OG [0x19b]			
		MDP_XRT_FLD_ENA	1	07-F0	d8
2014/11/15	01:20:06.0	XRT_FLRCTRL_ENA_412_OG [0x19c]			
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8
2014/11/15	01:20:08.0	XRT_AEC_RESET_413_OG [0x19d]			
		MDP_XRT_AEC_RESET	1	07-F0	d0
2014/11/15	01:20:10.0	XRT_ARS_DIS_423_OG [0x1a7]			
		MDP_XRT_ARS_DIS	1	07-F0	d5
2014/11/15	01:20:12.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da

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2014/11/15	01:20:14.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2014/11/15	01:20:16.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	12			
2014/11/15	01:20:18.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/11/15	01:57:30.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/15	01:57:32.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/15	01:57:34.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/11/15	01:57:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/11/15	02:00:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/11/15	02:17:30.0	XRT_Custom_430_OG [0x1ae]							
2014/11/15	02:18:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/11/15	03:29:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/15	03:29:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/15	03:29:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/11/15	03:29:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/11/15	03:32:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/11/15	03:55:00.0	XRT_Custom_430_OG [0x1ae]							
2014/11/15	03:56:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/11/15	05:05:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/15	05:05:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/15	05:05:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/11/15	05:05:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]							
		MDP_XRT_PREFLR_STRT	1	07-F0	e8				
2014/11/15	05:08:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]							
		MDP_XRT_PREFLR_STOP	1	07-F0	e9				
2014/11/15	05:32:30.0	XRT_Custom_430_OG [0x1ae]							
2014/11/15	05:33:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/11/15	05:59:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/15	05:59:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/15	05:59:58.0	XRT_FOCUS_POSITION_403_OG [0x193]							
		XRT_FOCUS_POSITION	4	07-F8	22	ff	aa	00	
2014/11/15	06:00:00.0	AOCS_Ore-point_Start_2_OG [0x098]							
		AOCU_NM	5	02-76	00	00	00	00	00
2014/11/15	06:00:18.0	XRT_FLD_DIS_422_OG [0x1a6]							
		MDP_XRT_FLD_DIS	1	07-F0	d9				
2014/11/15	06:02:54.0	XRT_FLRCTRL_DIS_405_OG [0x195]							
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9				
2014/11/15	06:02:56.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/11/15	06:02:58.0	XRT_QT_PROG_SET_433_OG [0x1b1]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	04			
2014/11/15	06:03:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/11/15	06:09:54.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/15	06:09:56.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/15	06:09:58.0	XRT_FOCUS_POSITION_410_OG [0x19a]							
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00	
2014/11/15	06:10:00.0	AOCS_Ore-point_Start_3_OG [0x099]							
		AOCU_NM	5	02-76	01	00	00	00	00
2014/11/15	06:10:18.0	XRT_FLD_ENA_411_OG [0x19b]							
		MDP_XRT_FLD_ENA	1	07-F0	d8				
2014/11/15	06:10:20.0	XRT_FLRCTRL_ENA_412_OG [0x19c]							
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8				
2014/11/15	06:10:22.0	XRT_AEC_RESET_413_OG [0x19d]							
		MDP_XRT_AEC_RESET	1	07-F0	d0				
2014/11/15	06:10:24.0	XRT_ARS_DIS_423_OG [0x1a7]							
		MDP_XRT_ARS_DIS	1	07-F0	d5				
2014/11/15	06:10:26.0	XRT_FLD_RESET_407_OG [0x197]							
		MDP_XRT_FLD_RESET	1	07-F0	da				
2014/11/15	06:12:56.0	XRT_QT_PROG_SET_435_OG [0x1b3]							
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	14			
2014/11/15	06:12:58.0	XRT_FL_PROG_SET_414_OG [0x19e]							
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	12			
2014/11/15	06:13:00.0	XRT_CTRL_AUTO_408_OG [0x198]							
		MDP_XRT_CTRL_AUTO	1	07-F0	c0				
2014/11/15	06:45:00.0	XRT_CTRL_MANU_400_OG [0x190]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/15	06:45:02.0	XRT_CTRL_MANU_402_OG [0x192]							
		MDP_XRT_CTRL_MANU	1	07-F0	c1				
2014/11/15	06:45:04.0	XRT_FLD_RESET_415_OG [0x19f]							
		MDP_XRT_FLD_RESET	1	07-F0	da				

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2014/11/15	06:45:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/11/15	06:48:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/11/15	07:09:30.0	XRT_Custom_430_OG [0x1ae]			
2014/11/15	07:10:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/11/15	08:25:00.0	XRT_CTRL_MANU_400_OG [0x190]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/15	08:25:02.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/15	08:25:04.0	XRT_FLD_RESET_415_OG [0x19f]			
		MDP_XRT_FLD_RESET	1	07-F0	da
2014/11/15	08:25:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]			
		MDP_XRT_PREFLR_STRT	1	07-F0	e8
2014/11/15	08:28:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]			
		MDP_XRT_PREFLR_STOP	1	07-F0	e9
2014/11/15	08:46:00.0	XRT_Custom_430_OG [0x1ae]			
2014/11/15	08:47:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]			
		MDP_XRT_CTRL_AUTO	1	07-F0	c0
2014/11/15	10:03:30.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/15	10:03:32.0	XRT_CTRL_MANU_402_OG [0x192]			
		MDP_XRT_CTRL_MANU	1	07-F0	c1
2014/11/15	10:19:00.0	AOCS_ORe-point_Start_2_OG [0x098]			
		AOCU_NM	5	02-76	00 00 00 00 00