

XRT Timeline to be uploaded on 2014/11/22

Period: 2014/11/22 09:15:00 - 2014/11/27 10:04:00

* * * * *

Normal mode

* * * * *

XOB #1A1B: HOP81/206 2-filter - Al/poly 6s, Al/mesh 4s 60s cadence, G-band - 384x384 33ms													
Term	Pointing (x, y)							Comment					
11/22 09:41:30 - 11/22 15:24:54	Fixed (-24.0, 885.0)							# OP start + 10min / HOP81 N-pole					
PROG= 14 Inf.-time(s)													
├─ Subr= 1 1-time(s) 2.0sec													
│ ├─ Seqn= 8 2-time(s) 2.0sec													
│ │ Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec													
├─ Subr= 2 1-time(s) 2.0sec													
│ ├─ Seqn= 48 1-time(s) 30.0sec													
│ │ Open/G-band Open/G-band open Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec													
├─ Subr= 3 30-time(s) 2.0sec													
│ ├─ Seqn= 57 1-time(s) 60.0sec													
│ │ Open/Al-mesh Open/Al-mesh close Safe Norm 4.00s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec													
│ │ Al-poly/Open Al-poly/Open close Safe Norm 5.66s Obs 1x1 384x384 (1064, 1048) Q=90 0 0 2.0sec													
└─ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #19E0: AR Standard-A(Filter-Ratio with Al/poly and thin-Be) with PFB, 384x384 at 1064 1048, thin-Be, thick-Al, and Al/Poly context, with G-band (33ms)													
Term	Pointing (x, y)							Comment					
11/22 15:28:00 - 11/22 17:37:24	Track (463.0, -303.5) @ 11/22 15:25:00							AR12209 tracking					
11/22 17:55:30 - 11/23 05:55:24	Track (482.0, -302.9) @ 11/22 17:52:30							AR12209 tracking					
PROG= 02 Inf.-time(s)													
├─ Subr= 1 1-time(s) 2.0sec													
│ ├─ Seqn= 8 2-time(s) 2.0sec													
│ │ Open/G-band Open/G-band close Safe Norm 44ms Obs 1x1 384x384 (1064, 1048) DPCM 0 0 2.0sec													
├─ Subr= 2 2-time(s) 2.0sec													
│ ├─ Seqn= 24 1-time(s) 2.0sec													
│ │ Open/Ti-poly Open/thick-Al close Safe Dark 16.0s Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec													
│ │ Open/G-band Open/G-band open Safe Norm 32ms Obs 1x1 384x384 (1064, 1048) Q=98 0 0 2.0sec													
│ ├─ Seqn= 42 4-time(s) 2.0sec													
│ │ Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec													
│ │ thin-Be/Open med-Be/Open close Safe Norm 5.66s Obs 1x1 512x512 (1064, 1048) Q=95 3 0 2.0sec													
│ │ Open/thick-Al Open/thick-Al close Safe Norm 16.0s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec													
│ ├─ Seqn= 62 36-time(s) 50.0sec													
│ │ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec													
│ │ Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 0 2.0sec													
│ │ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec													
│ │ Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 1 2.0sec													
│ │ thin-Be/Open med-Be/Open close Safe Norm 1.00s Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec													
│ │ Al-poly/Open thin-Be/Open close Safe Norm 500ms Obs 1x1 384x384 (1064, 1048) Q=95 3 2 2.0sec													
└─ Default Filter Thicker Filter VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval													

XOB #1A60: Synoptic 8 Filter w/ Triplet Exposures(Al-mesh, Ti-poly, Thin-Be) - Doublets(Al-poly, Al-poly+Ti-poly, Med-Al) - Singlet(Thick-Be) - 2x2 Q98 - G													
Term	Pointing (x, y)							Comment					
11/22 17:40:30 - 11/22 17:52:24	Fixed (0.0, 0.0)							synoptic, shifted -22.5 min					
PROG= 11 1-time(s)													
├─ Subr= 1 1-time(s) 12.0sec													
│ ├─ Seqn= 49 1-time(s) 4.0sec													
│ │ Open/Al-mesh Open/Al-mesh close Safe Norm 12ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
│ │ Open/Al-mesh Open/Al-mesh close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
│ │ Open/Al-mesh Open/Al-mesh close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
│ ├─ Seqn= 5 1-time(s) 2.0sec													
│ │ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
│ │ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
│ │ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
│ │ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec													
│ │ Open/Ti-poly Open/thick-Al close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec													
│ ├─ Seqn= 17 1-time(s) 4.0sec													
│ │ Open/Ti-poly Open/Ti-poly close Safe Norm 24ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
│ │ Open/Ti-poly Open/Ti-poly close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
│ │ Open/Ti-poly Open/Ti-poly close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
│ ├─ Seqn= 87 1-time(s) 2.0sec													
│ │ thin-Be/Open thin-Be/Open close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
│ │ thin-Be/Open thin-Be/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
│ │ thin-Be/Open thin-Be/Open close Safe Norm 5.66s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
│ ├─ Seqn= 6 1-time(s) 2.0sec													
│ │ Open/G-band Open/G-band open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec													
│ │ Open/G-band Open/G-band close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec													
├─ Subr= 2 2-time(s) 12.0sec													
│ ├─ Seqn= 21 1-time(s) 2.0sec													
│ │ Al-poly/Open Al-poly/thick-Al close Safe Norm 44ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
│ │ Al-poly/Open Al-poly/Open close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec													
│ ├─ Seqn= 59 1-time(s) 2.0sec													

Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	500ms	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Al-poly/Ti-poly	Al-poly/thick-Al	close	Safe	Norm	2.00s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 3		1-time(s)	4.0sec									
med-Al/Open	med-Al/Open	close	Safe	Norm	11.3s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
med-Al/Open	med-Al/Open	close	Safe	Norm	32.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Seqn= 46		1-time(s)	4.0sec									
Open/thick-Be	Open/thick-Be	close	Safe	Norm	64.0s	Obs	2x2	2048x2048 (1024, 1024)	Q=98	0	0	2.0sec
Default Filter	Thicker Filter	VLS	mode	image	Exp.	CCD	Bin	ROI: size (center)	Comp.	AEC Buffer	Interval	

XOB #1A59: Synoptic Q95 2x2 - Al/mesh(2/128/1024) + Dark cal(2x2 4x4 8x8 512 Q98) + Dark cal(1x1 512x2048 - 1x1 2048x512) + Ti-poly(8/362/2048) + Thin

Term	Pointing (x, y)	Comment
11/23 05:58:30 - 11/23 06:05:24	Fixed (0.0, 0.0)	synoptic, shifted -4.5 min
PROG= 04 1-time(s)		
Subr= 1 1-time(s) 12.0sec		
Seqn= 71 1-time(s) 4.0sec		
Open/Al-mesh	Open/Al-mesh	close Safe Norm 2ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Norm 125ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Al-mesh	Open/Al-mesh	close Safe Norm 1.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 5 1-time(s) 2.0sec		
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 2x2 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 4x4 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 8x8 2048x2048 (1024, 1024) Q=98 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 1x1 2048x512 (1024, 1024) DPCM 0 0 2.0sec
Open/Ti-poly	Open/thick-Al	close Safe Dark 500ms Obs 1x1 512x2048 (1024, 1024) DPCM 0 0 2.0sec
Seqn= 27 1-time(s) 4.0sec		
Open/Ti-poly	Open/Ti-poly	close Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly	close Safe Norm 354ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Open/Ti-poly	Open/Ti-poly	close Safe Norm 2.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 93 1-time(s) 2.0sec		
thin-Be/Open	thin-Be/Open	close Safe Norm 16ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open	close Safe Norm 500ms Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
thin-Be/Open	thin-Be/Open	close Safe Norm 4.00s Obs 2x2 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Seqn= 6 1-time(s) 2.0sec		
Open/G-band	Open/G-band	open Safe Norm 8ms Obs 2x2 2048x2048 (1024, 1024) Q=90 0 0 2.0sec
Open/G-band	Open/G-band	close Safe Norm 32ms Obs 1x1 2048x2048 (1024, 1024) Q=95 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

* * * * *

Flare mode

* * * * *

XOB #1A61: Flare - multifilter 26 sec cadence (Be/thin, Be/med, Al/thick), AEC 3(thin-Be AEC2), 384x384 + context (med-Al,thick-Be -384x384 + Al-poly 512

Term	Pointing (x, y)	Comment
11/22 09:41:30 - 11/22 15:24:54	Fixed (-24.0, 885.0)	# OP start + 10min / HOP81 N-pole
11/22 15:28:00 - 11/22 17:37:24	Track (463.0, -303.5) @ 11/22 15:25:00	AR12209 tracking
11/22 17:55:30 - 11/23 05:55:24	Track (482.0, -302.9) @ 11/22 17:52:30	AR12209 tracking
PROG= 19 30-time(s)		
Subr= 1 20-time(s) 2.0sec		
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn=100 1-time(s) 10.0sec		
thin-Be/Open	med-Be/Open	close Safe Norm 125ms Obs 1x1 384x384 (1024, 1024) Q=95 2 0 2.0sec
med-Be/Open	Open/thick-Al	close Safe Norm 250ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Al	Open/thick-Be	close Safe Norm 1.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Subr= 2 1-time(s) 2.0sec		
Seqn= 10 1-time(s) 2.0sec		
med-Al/Open	med-Al/thick-Al	close Safe Norm 500ms Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Open/thick-Be	Open/thick-Be	close Safe Norm 2.00s Obs 1x1 384x384 (1024, 1024) Q=95 3 0 2.0sec
Seqn= 11 1-time(s) 2.0sec		
Al-poly/Open	Al-poly/thick-Al	close Safe Norm 125ms Obs 2x2 512x512 (1024, 1024) Q=95 2 0 2.0sec
Seqn= 15 1-time(s) 2.0sec		
Open/G-band	Open/G-band	open Safe Norm 44ms Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 1x1 384x384 (1024, 1024) Q=98 0 0 2.0sec
Open/thick-Al	Open/thick-Al	close Safe Dark 1.00s Obs 2x2 512x512 (1024, 1024) Q=98 0 0 2.0sec
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval

* * * * *

Active Region Search

* * * * *

NOT USED

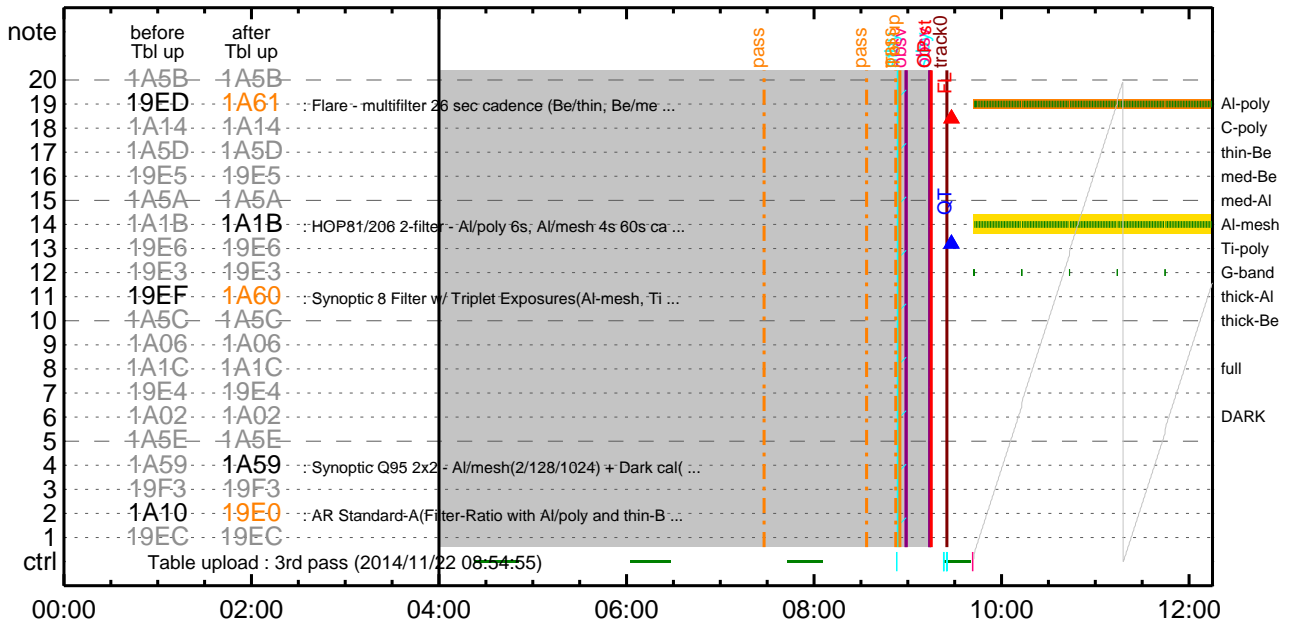
* * * * *

Flare Detection

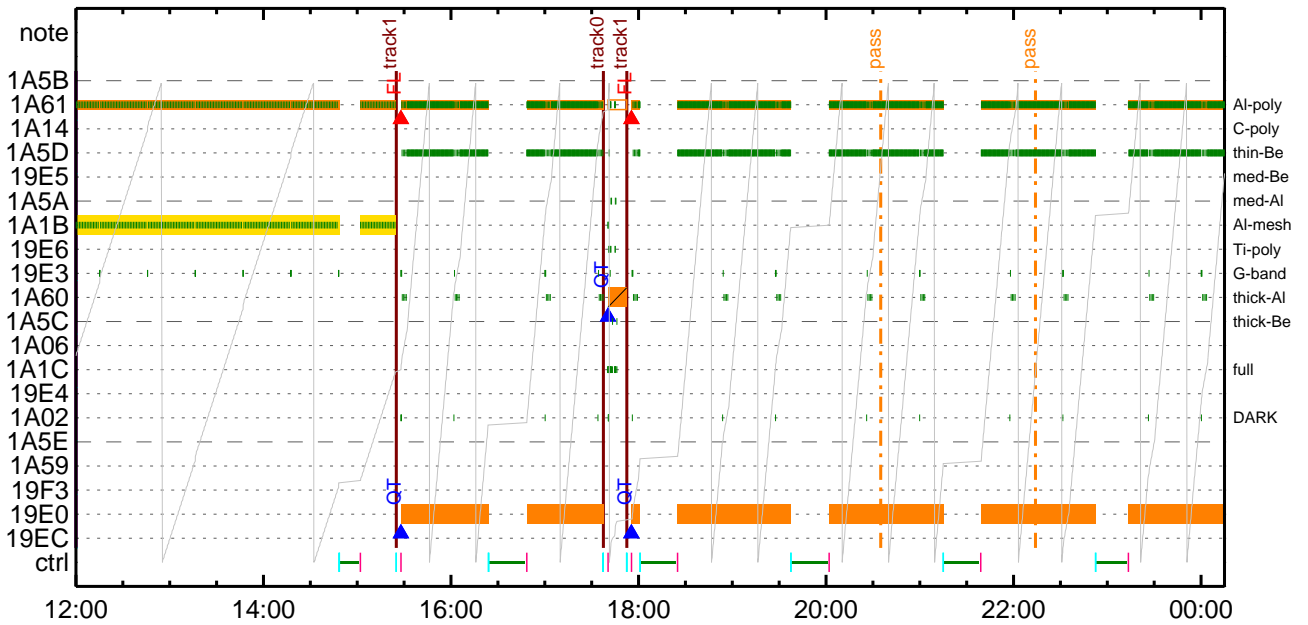
* * * * *

FLD Patrol												
Term	Pointing (x, y)	Comment										
11/22 17:52:48 - 11/23 05:55:48	Track (482.0, -302.9) @ 11/22 17:52:30	AR12209 tracking										
Open/Ti-poly	Open/thick-Al	close Safe Norm 8ms Obs 8x8 Q=50 30sec										
Default Filter	Thicker Filter	VLS mode image Exp. CCD Bin ROI: size (center) Comp. AEC Buffer Interval										

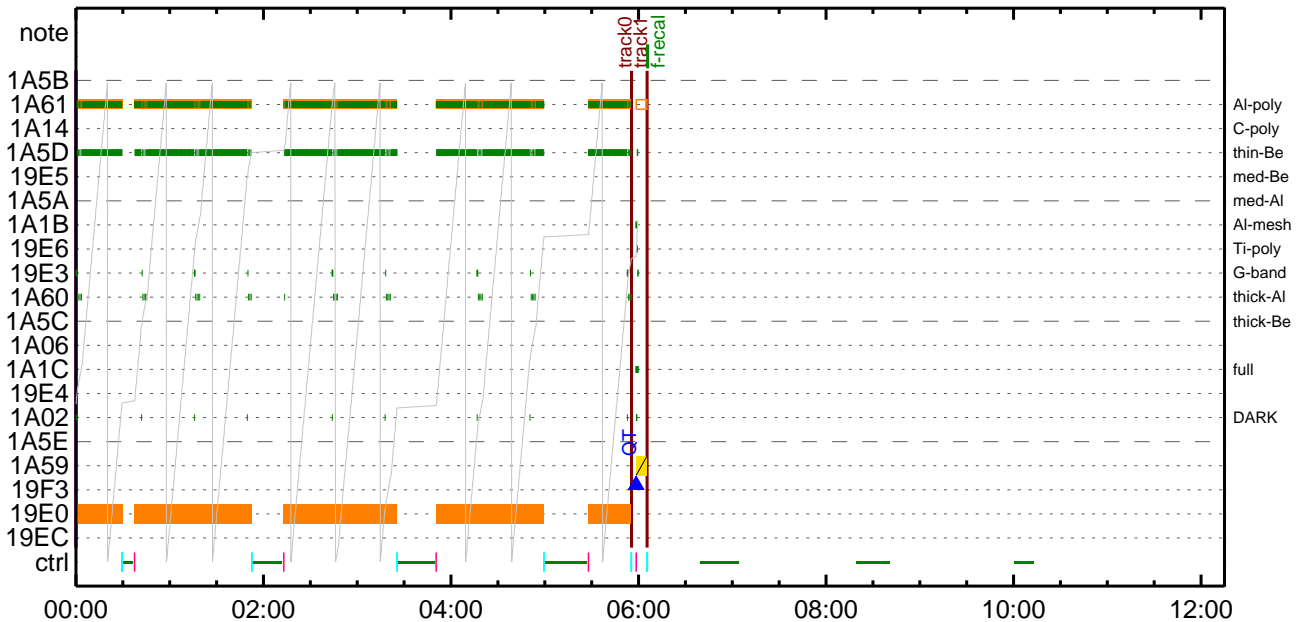
CMDI #0740 2014/11/22



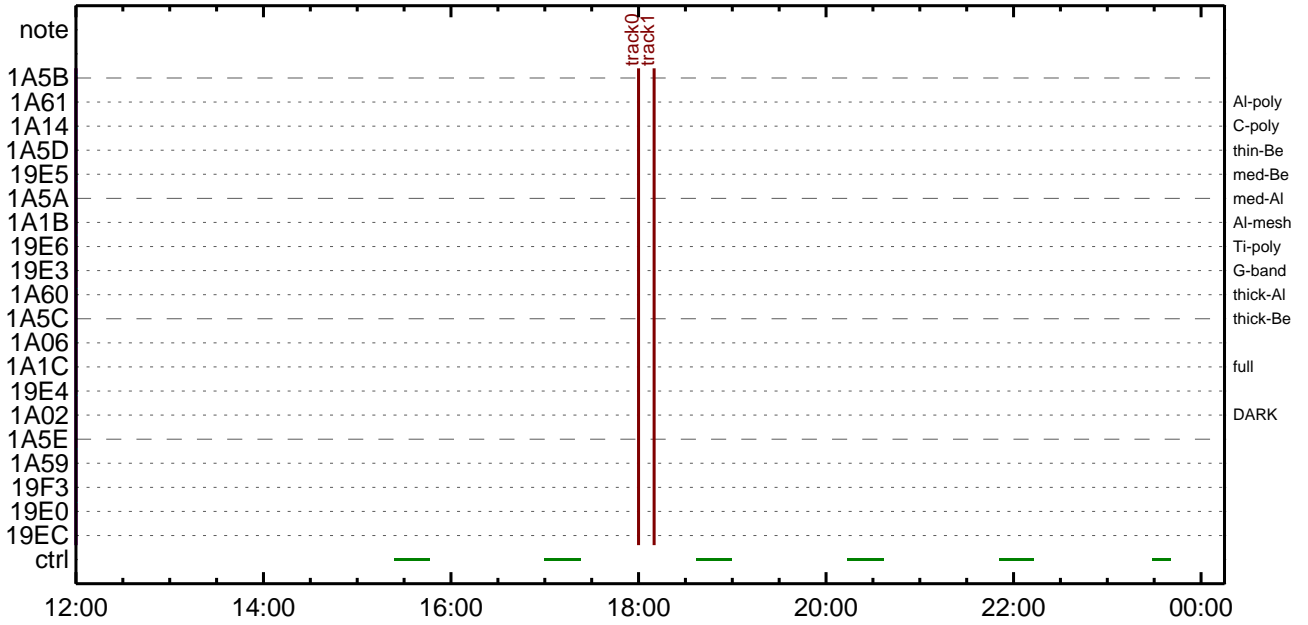
CMDI #0740 2014/11/22



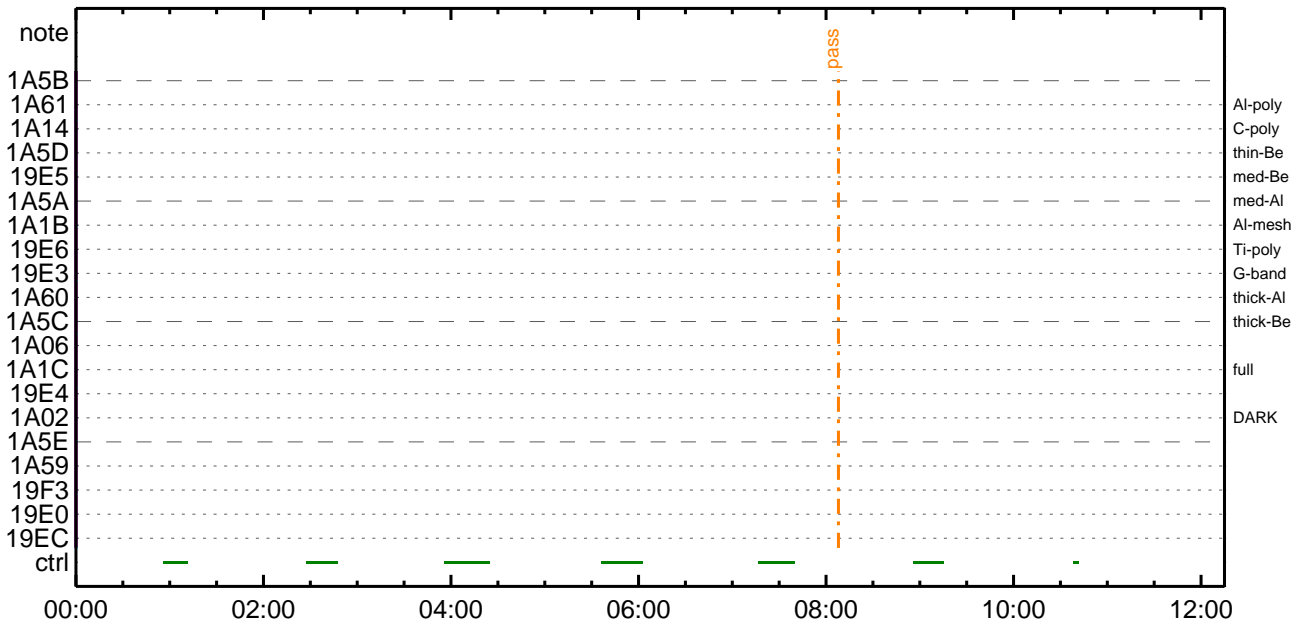
CMDI #0740 2014/11/23



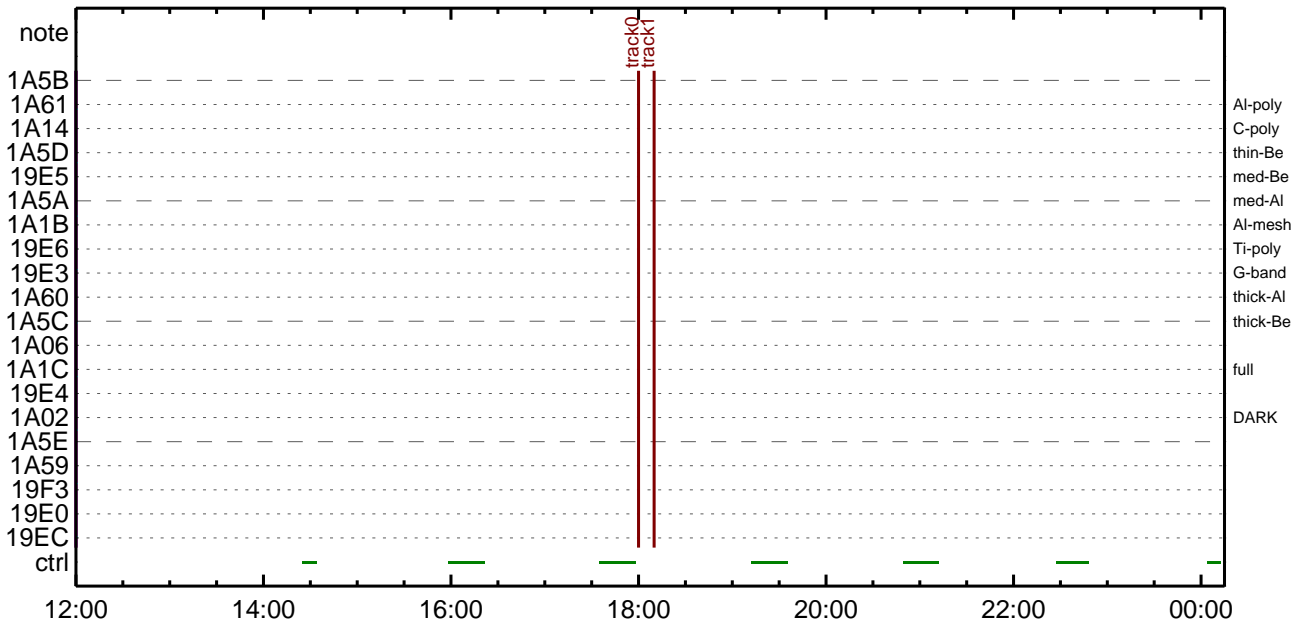
CMDI #0740 2014/11/23



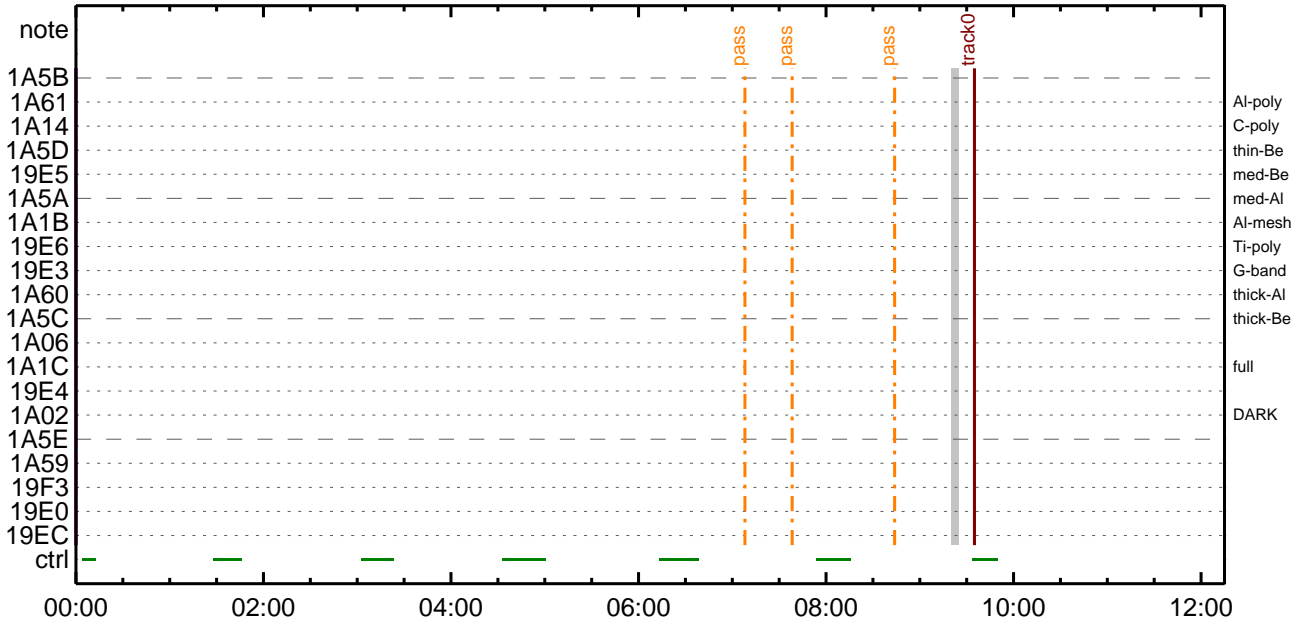
CMDI #0740 2014/11/24



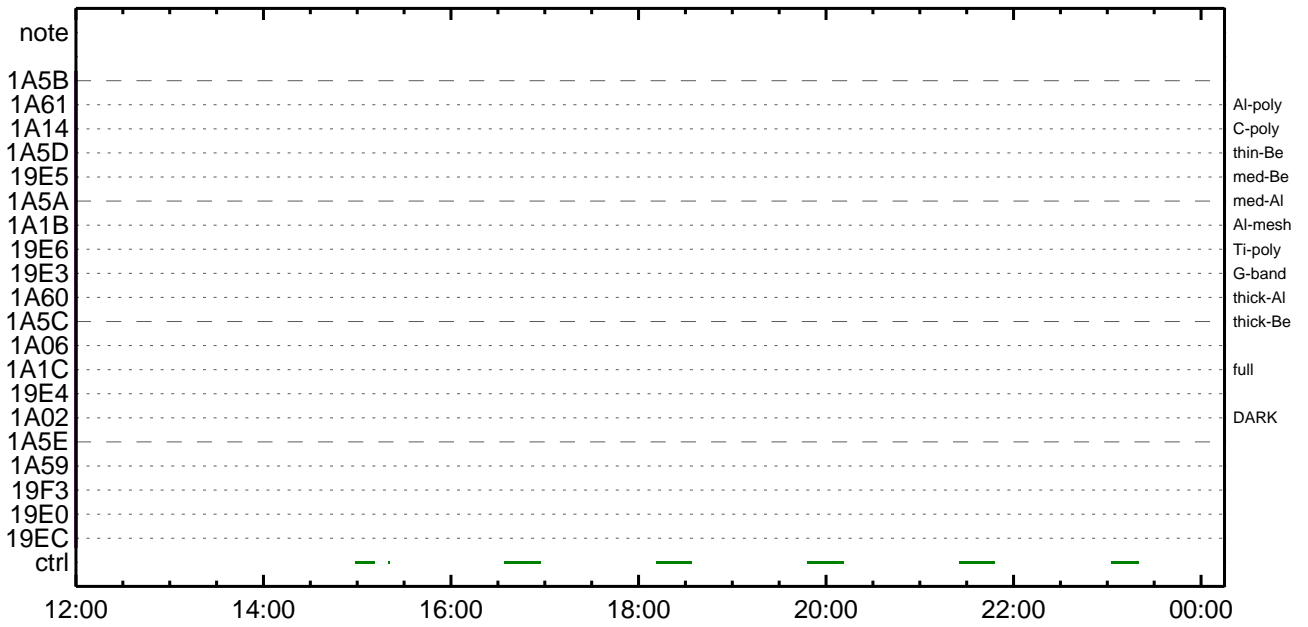
CMDI #0740 2014/11/24



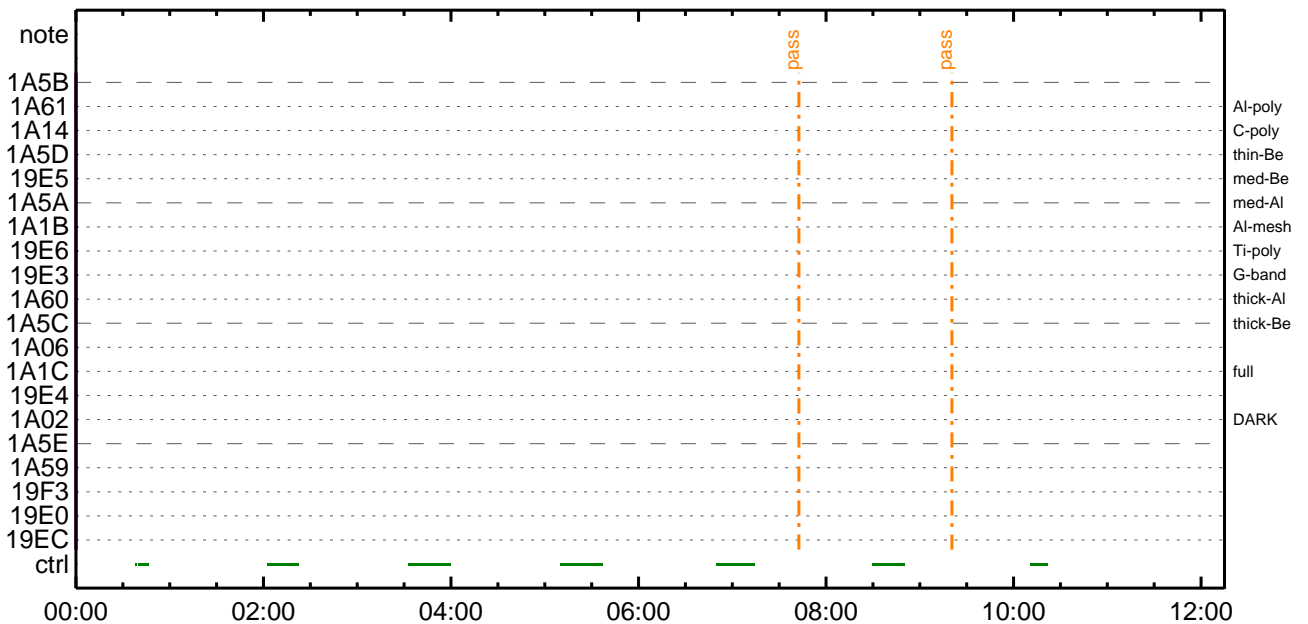
CMDI #0740 2014/11/25



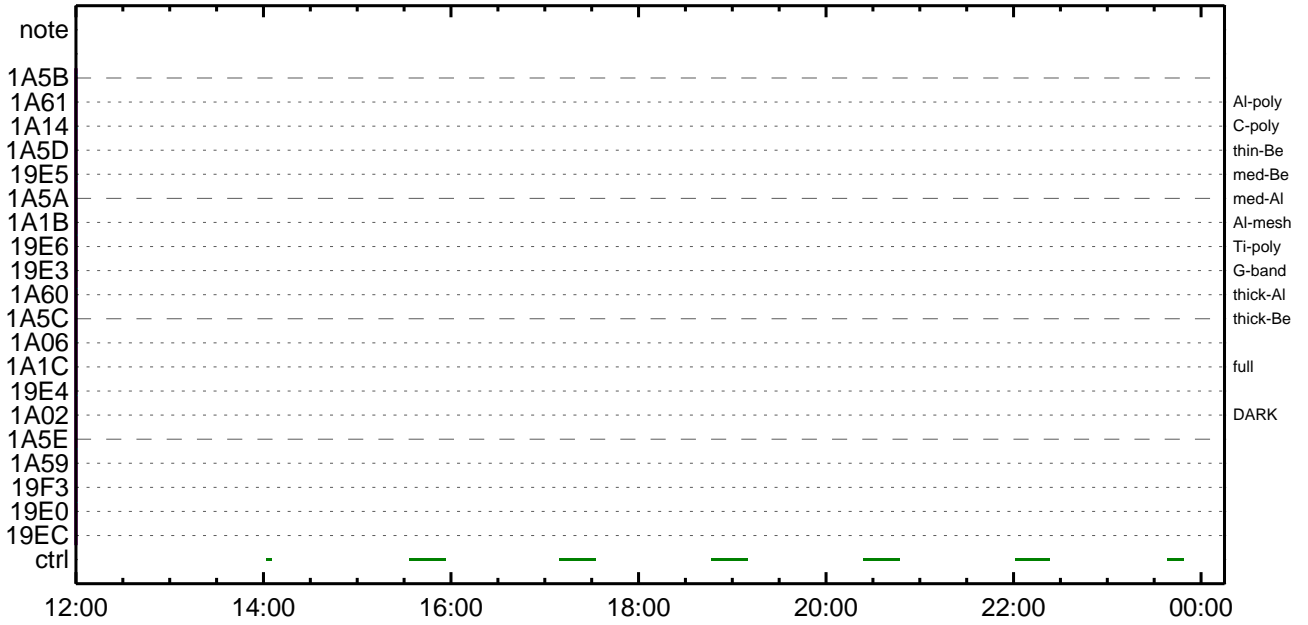
CMDI #0740 2014/11/25



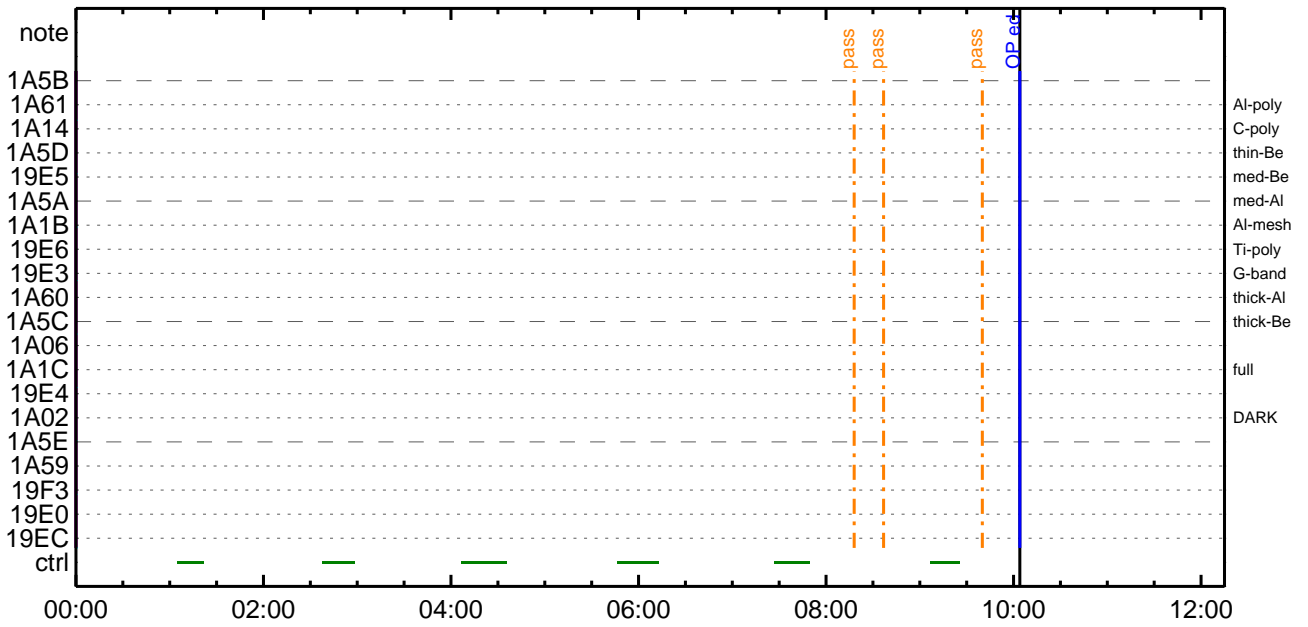
CMDI #0740 2014/11/26



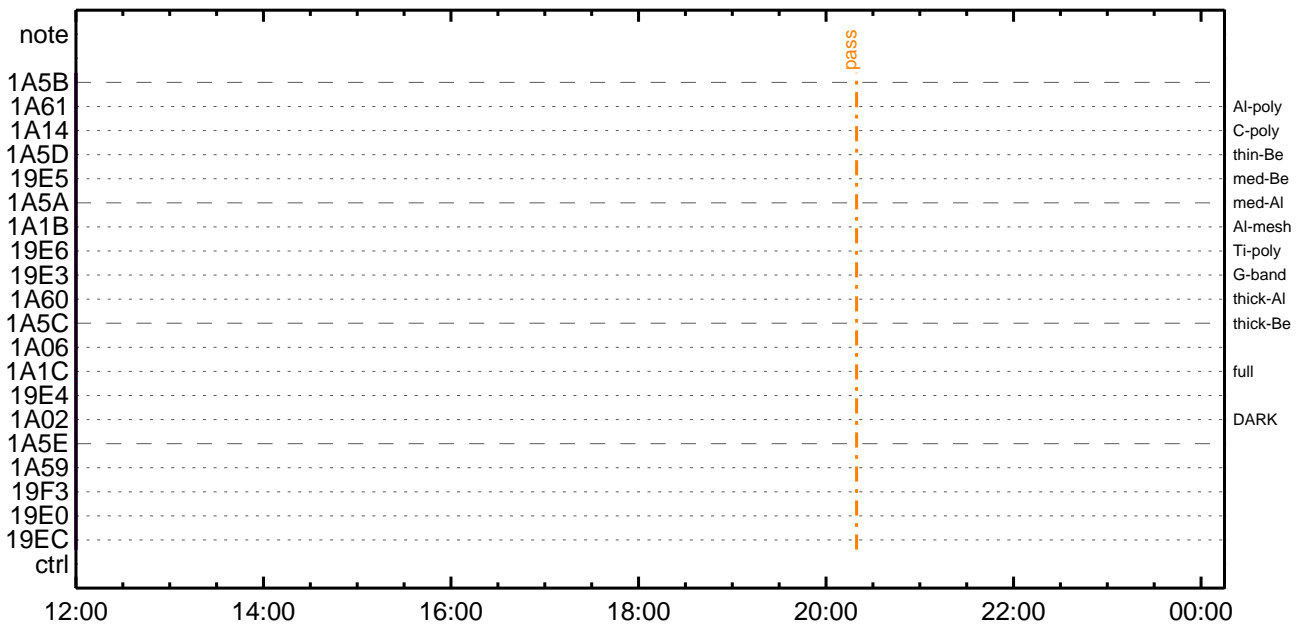
CMDI #0740 2014/11/26



CMDI #0740 2014/11/27



CMDI #0740 2014/11/27




```

0096 C.                0300; SET EDUMP 0100 0100 0100 0100 0100 0100
0097 C.
0098 C. TI 2014-11-22 09:10:00.0
0099 +. TI 2014-11-22 09:10:00.0
0100 DC 01-B3 DHU_OP_STOP
0101 C.                00[HK1_TI_CMD_NUM] EQ 1COUNTUP
0102 C.
0103 +. TI 2014-11-22 09:10:01.0
0104 DC 01-B4 DHU_OP_COPY
0105 C.                00[HK1_TI_CMD_NUM] EQ 1COUNTUP
0106 C.
0107 +. TI 2014-11-22 09:10:01.0
0108 DC 01-B5 DHU_OPOG_COPY
0109 C.                00[HK1_TI_CMD_NUM] EQ 1COUNTUP
0110 C.
0111 +. TI 2014-11-22 09:14:59.5
0112 DC 01-B2 DHU_OP_START
0113 C.                00[HK1_TI_CMD_NUM] EQ 1COUNTUP
0114 C.
0115 C.                00[HK1_TI_CMD_ENA/DIS] EQ ENA
0116 C.                00[HK1_TI_CMD_NUM] EQ 4
0117 C.                00[HK1_NEXT_EXEC_PIM] EQ DHU
0118 C.                00[HK1_NEXT_EXEC_DC] EQ 0xB3
0119 C.
0120 C.
0121 C. *****
0122 C. TI 2014-11-22 09:14:59.5
0123 C. *****
0124 C.
0125 C. TI_TBL(0x03AB00-0x03AEFF; 1024byte)
0126 +. DC 01-23 DHU_DMA_DMP_PRM_SET
0127 BC (03 ab 03 01 02)
0128 C.                00[HK1_DMP_TOP_ADRS_1] EQ 07
0129 C.                00[HK1_DMP_TOP_ADRS_0] EQ 2B
0130 C.                00[HK1_DMP_BLOCK_NUM] EQ 3
0131 C.                00[HK1_DMP_REPEAT_NUM] EQ 0
0132 C.                00[HK1_DMA_DMP_PIM] EQ DHU
0133 +. DC 01-22 DHU_MODE_CHNG
0134 BC (07 0b f8)
0135 C.                00[HK1_PKT_FORM_NO] EQ 7
0136 C.                00[HK1_PKT_GEN_TIME] EQ 0.25 s
0137 C.                00[HK1_S_TLM_BIT_RATE] EQ 32k
0138 C.                00[HK1_X_TLM_BIT_RATE] EQ 4M
0139 C.                00[HK1_DMP_CHK_FLG] EQ EXEC
0140 C.
0141 C.                00[HK1_DMP_CHK_FLG] EQ NON
0142 C.
0143 C.
0144 C. RAM ID=TI_TBL 0100 0100 0100 0100 0100 0100
0145 C.
0146 C. DHU 2014-11-22 09:14:59.5
0147 +. DC 01-22 DHU_MODE_CHNG
0148 BC (02 0a f8)
0149 C.                00[HK1_PKT_FORM_NO] EQ 2
0150 C.                00[HK1_PKT_GEN_TIME] EQ 0.5S
0151 C.                00[HK1_S_TLM_BIT_RATE] EQ 32K
0152 C.                00[HK1_X_TLM_BIT_RATE] EQ 4M
0153 C.
0154 C. *****
0155 C. SOT TI command set
0156 C. *****
0157 C. Execute, after the success of OP upload.
0158 +. TI 2014-11-22 09:14:16.0
0159 DC 07-F0 MDP_SOT_MODE_STBY
0160 BC (41)
0161 C.
0162 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0163 C.
0164 C. ***** SOT END *****
0165 C.
0166 C. ***** XRT START *****
0167 C. Execute, after the success of OP upload.
0168 +. TI 2014-11-22 09:14:00.0
0169 DC 07-F0 MDP_XRT_MODE_STBY
0170 BC (c3)
0171 C.                [ ] [HK1_TI_CMD_NUM] EQ 1COUNTUP
0172 C.
0173 C. ***** XRT END *****
0174 C. Stop EIS observation and temporarily disable EIS mode changes
0175 C.
0176 C.
0177 C. ***** Start EIS operation (TI set) *****
0178 C. Execute, after the success of OP upload.
0179 C. Set EIS TI-commands
0180 +. TI 2014-11-22 09:14:30.0
0181 DC 07-FC EIS_MODE_MANU
0182 BC (21 02)
0183 +. TI 2014-11-22 09:14:40.0
0184 DC 07-FC EIS_MODE_CHG_DIS
0185 BC (22)
0186 C.                [ ] [HK1_TI_CMD_NUM] EQ 2 COUNTUP
0187 C. ***** End EIS operation (TI set) *****
0188 C.
0189 C.
0190 C.
0191 C. ***** MDP 2014-11-22 09:14:40.0 *****
0192 C. (%00 01 02 03 04 05 06 07 08 09 0a 0b 0c 0d 0e 0f)
0193 S. DC-BC dcbc-402:DCBC

```



```
0194 (MDP_known_event)
0195 C.
0196 C.
0197 . C. ***** ¥ÐŸ!•İ Daily±;İÑøĒ'Øσ¹αēDCBC•x²è *****
0198 . S. DC-BC dcbc-153:DCBC
0199 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0200 C.
0201 C.
0202 . C. ;ãLOS¥Á¥S¥Ã¥~¼Â»Ü;ã
0203 C.
0204 . C. ***** LOS *****
0205 C.
```


(a) Spacecraft Operation Procedure (real-commands)

```

main-773 2014-11-22 13:44:58 144 33 SOLAR-B MAIN //
0001 C.
0002 . C. ***** AOS *****
0003 C.
0004 . C. ;ãAOSYÁYŞYÁY-¼Á»Û;ã
0005 C.
0006 C. YÁYŞ;¼Y³YFÏÖYÉÁ+¿®
0007 +. DC 00-00 NULL_DUMMY_CMD
0008 C.
0009 . C. ***** AOCS : Reload orbital element (send every contact) *****
0010 C. Áí;È¿µÁµ•µ°È»Í×ÁÇµÍYÇYÁY×YÍ;¼YÉ;ÈÈ%µ•íÉ;ÈÈ%°ÇÖµ•µ¿¼l¹ÇµÍ;ÇÁ®, ùµ¹µèµµÇÁ+¿®µ•µÈµµµ³µÈ;f
0011 +. DC 02-8E AOCU_ORB_UPD
0012 C.
0013 C.
0014 . C. *****
0015 C. SOT table upload
0016 C. *****
0017 . C. < Stop FG table >
0018 +. DC 07-F0 MDP_FG_CTRL_MANU
0019 BC (51)
0020 . C. -----
0021 C. MDP_FG_CTRL_MODE = MANU [ ]
0022 C. -----
0023 C.
0024 . C. <Upload FG Observation Table>
0025 . S. RAM ram-266:MDP_OBS_F
0026 ( )
0027 C.
0028 . C. < Dump RAMID=MDP_OBS_F >
0029 +. DC 07-F0 MDP_DUMP_FGTBL
0030 BC (82 07 00 00 00 38 b8)
0031 C. -----
0032 C. MDP_OBS_F verify = OK/NG [ ]
0033 C. -----
0034 C.
0035 . C. < Stop SP table >
0036 +. DC 07-F0 MDP_SP_CTRL_MANU
0037 BC (61)
0038 C. -----
0039 C. MDP_SP_CTRL_MODE = MANU [ ]
0040 C. -----
0041 C.
0042 . C. <Upload SP Observation Table>
0043 . S. RAM ram-287:MDP_OBS_S
0044 ( )
0045 C.
0046 . C. < Dump RAMID=MDP_OBS_S >
0047 +. DC 07-F0 MDP_DUMP_SPTBL
0048 BC (83 07 00 00 00 38 b8)
0049 C. -----
0050 C. MDP_OBS_S verify = OK/NG [ ]
0051 C. -----
0052 C.
0053 C. *****
0054 C. SOT TI command set
0055 C. *****
0056 C. Execute, after the success of TBL upload.
0057 +. TI 2014-11-22 09:14:18.0
0058 DC 07-F0 MDP_SOT_MODE_OBSV
0059 BC (40)
0060 . C. -----
0061 C. HK1_TI_CMD_NUM = 1 CNTUP [ ]
0062 C. -----
0063 C.
0064 C.
0065 C. ***** XRT START *****
0066 C.
0067 +. DC 07-F0 MDP_XRT_CTRL_MANU
0068 BC (c1)
0069 + DC 07-F0 MDP_XRT_MODE_STBY
0070 BC (c3)
0071 . C. ----- Success Verify ? OK / NG_____
0072 C.
0073 C. XRT Obs. Table Upload
0074 . S. RAM ram-291:MDP_OBS_X
0075 ( )
0076 C.
0077 +. DC 07-F0 MDP_DUMP_XRTTBL
0078 BC (84 07 00 00 00 3a d4)
0079 . C. ----- Comparison Check ? OK / ERR _____
0080 C.
0081 C.
0082 +. DC 07-F0 MDP_XRT_ROI_SET
0083 BC (cd 01 b1 b1 04 04)
0084 + DC 07-F0 MDP_XRT_ROI_SET
0085 BC (cd 02 b1 b1 08 08)
0086 + DC 07-F0 MDP_XRT_ROI_SET
0087 BC (cd 03 b1 b1 08 08)
0088 + DC 07-F0 MDP_XRT_ROI_SET
0089 BC (cd 04 b1 b1 06 06)
0090 + DC 07-F0 MDP_XRT_ROI_SET
0091 BC (cd 05 85 83 06 06)
0092 + DC 07-F0 MDP_XRT_ROI_SET
0093 BC (cd 06 85 83 06 06)
0094 + DC 07-F0 MDP_XRT_ROI_SET
0095 BC (cd 07 85 83 08 08)

```

```
0096 + DC 07-F0 MDP_XRT_ROI_SET
0097 BC (cd 08 80 80 20 20)
0098 + DC 07-F0 MDP_XRT_ROI_SET
0099 BC (cd 09 80 80 20 08)
0100 + DC 07-F0 MDP_XRT_ROI_SET
0101 BC (cd 0a 80 80 08 20)
0102 + DC 07-F0 MDP_XRT_ROI_SET
0103 BC (cd 0f 80 80 06 06)
0104 + DC 07-F0 MDP_XRT_ROI_SET
0105 BC (cd 10 80 80 08 08)
0106 + DC 07-F0 MDP_XRT_FLD_ENA
0107 BC (d8)
0108 + DC 07-F0 MDP_XRT_FLRCTRL_ENA
0109 BC (c8)
0110 + DC 07-F0 MDP_XRT_AEC_RESET
0111 BC (d0)
0112 + DC 07-F0 MDP_XRT_ARS_DIS
0113 BC (d5)
0114 + DC 07-F0 MDP_XRT_FLD_RESET
0115 BC (da)
0116 . C. ----- Success Verify ? OK / NG ____
0117 C.
0118 C.
0119 . C. All OK? Yes--> Please Proceed. / No --> Stop here.
0120 C.
0121 +. DC 07-F0 MDP_XRT_MODE_OBSV
0122 BC (c2)
0123 +. TI 2014-11-22 09:14:02.0
0124 DC 07-F0 MDP_XRT_MODE_OBSV
0125 BC (c2)
0126 . C. ----- Success Verify ? OK / NG ____
0127 C.
0128 C. ***** XRT END *****
0129 C.
0130 . C. ***** MDP 'uAÎaî»ô%ÿaÊÂDâ¹aèDCBC•x²è *****
0131 C. (%â°îÿÓÿÃÿÈÿPÿËÿâÿçÿèaE%¼aa¼A»Ûa¹aè)
0132 . S. DC-BC dcbc-402:DCBC
0133 (MDP_known_event)
0134 C.
0135 C.
0136 . C. ***** ÿDÿ¹.Ï Daily±;îÑaÊ´Øa¹aèDCBC•x²è *****
0137 . S. DC-BC dcbc-153:DCBC
0138 (SPECIAL-CMD_DAILY_OPERATIN_DCB)
0139 C.
0140 C.
0141 . C. ;ãLOSÿÃÿSÿËÿ¼A»Û;ã
0142 C.
0143 . C. ***** LOS *****
0144 C.
```

*** OP Sequence for XRT ***

```

2014/11/22 09:23:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2014/11/22 09:23:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2014/11/22 09:23:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2014/11/22 09:23:06.0 XRT_PREFLR_STRT_439_OG [0x1b7]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2014/11/22 09:24:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2014/11/22 09:24:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2014/11/22 09:24:58.0 XRT_FOCUS_POSITION_443_OG [0x1bb]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2014/11/22 09:25:00.0 AOCs_OrE-point_Start_1_OG [0x097]
                        AOCU_NM 5 02-76 00 b1 59 02 24
2014/11/22 09:25:18.0 XRT_FLD_ENA_421_OG [0x1a5]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2014/11/22 09:26:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2014/11/22 09:27:48.0 XRT_FLRCTRL_ENA_444_OG [0x1bc]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2014/11/22 09:27:50.0 XRT_AEC_RESET_413_OG [0x19d]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2014/11/22 09:27:52.0 XRT_ARS_DIS_446_OG [0x1be]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2014/11/22 09:27:54.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2014/11/22 09:27:56.0 XRT_QT_PROG_SET_427_OG [0x1ab]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 0e
2014/11/22 09:27:58.0 XRT_FL_PROG_SET_401_OG [0x191]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 13
2014/11/22 09:40:30.5 XRT_Custom_430_OG [0x1ae]
2014/11/22 09:41:30.5 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2014/11/22 14:48:30.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2014/11/22 14:48:32.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2014/11/22 14:48:34.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2014/11/22 14:48:36.0 XRT_PREFLR_STRT_439_OG [0x1b7]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2014/11/22 14:51:44.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2014/11/22 15:01:00.0 XRT_Custom_430_OG [0x1ae]
2014/11/22 15:02:00.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2014/11/22 15:24:54.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2014/11/22 15:24:56.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2014/11/22 15:24:58.0 XRT_FOCUS_POSITION_410_OG [0x19a]
                        XRT_FOCUS_POSITION 4 07-F8 22 fe 97 00
2014/11/22 15:25:00.0 AOCs_OrE-point_Start_2_OG [0x098]
                        AOCU_NM 5 02-76 01 00 00 00 00
2014/11/22 15:25:18.0 XRT_FLD_ENA_411_OG [0x19b]
                        MDP_XRT_FLD_ENA 1 07-F0 d8
2014/11/22 15:25:20.0 XRT_FLRCTRL_ENA_412_OG [0x19c]
                        MDP_XRT_FLRCTRL_ENA 1 07-F0 c8
2014/11/22 15:25:22.0 XRT_AEC_RESET_413_OG [0x19d]
                        MDP_XRT_AEC_RESET 1 07-F0 d0
2014/11/22 15:25:24.0 XRT_ARS_DIS_423_OG [0x1a7]
                        MDP_XRT_ARS_DIS 1 07-F0 d5
2014/11/22 15:25:26.0 XRT_FLD_RESET_407_OG [0x197]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2014/11/22 15:27:56.0 XRT_QT_PROG_SET_431_OG [0x1af]
                        MDP_XRT_QT_PROG_SET 2 07-F0 c4 02
2014/11/22 15:27:58.0 XRT_FL_PROG_SET_401_OG [0x191]
                        MDP_XRT_FL_PROG_SET 2 07-F0 c5 13
2014/11/22 15:28:00.0 XRT_CTRL_AUTO_408_OG [0x198]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2014/11/22 16:24:00.0 XRT_CTRL_MANU_400_OG [0x190]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2014/11/22 16:24:02.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2014/11/22 16:24:04.0 XRT_FLD_RESET_415_OG [0x19f]
                        MDP_XRT_FLD_RESET 1 07-F0 da
2014/11/22 16:24:06.0 XRT_PREFLR_STRT_439_OG [0x1b7]
                        MDP_XRT_PREFLR_STRT 1 07-F0 e8
2014/11/22 16:27:14.0 XRT_PREFLR_STOP_419_OG [0x1a3]
                        MDP_XRT_PREFLR_STOP 1 07-F0 e9
2014/11/22 16:47:30.0 XRT_Custom_430_OG [0x1ae]
2014/11/22 16:48:30.0 XRT_CTRL_AUTO_424_OG [0x1a8]
                        MDP_XRT_CTRL_AUTO 1 07-F0 c0
2014/11/22 17:37:24.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2014/11/22 17:37:26.0 XRT_CTRL_MANU_402_OG [0x192]
                        MDP_XRT_CTRL_MANU 1 07-F0 c1
2014/11/22 17:37:28.0 XRT_FOCUS_POSITION_403_OG [0x193]
                        XRT_FOCUS_POSITION 4 07-F8 22 ff aa 00

```

Nov 22, 14 13:45

XRT_OGLIST_0740.chk

Page 2/3

2014/11/22	17:37:30.0	AOCS_ORe-point_Start_3_OG [0x099]								
		AOCU_NM	5	02-76	00	00	00	00	00	
2014/11/22	17:37:48.0	XRT_FLD_DIS_422_OG [0x1a6]								
		MDP_XRT_FLD_DIS	1	07-F0	d9					
2014/11/22	17:40:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]								
		MDP_XRT_FLRCTRL_DIS	1	07-F0	c9					
2014/11/22	17:40:26.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2014/11/22	17:40:28.0	XRT_QT_PROG_SET_418_OG [0x1a2]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	0b				
2014/11/22	17:40:30.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2014/11/22	17:52:24.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/11/22	17:52:26.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/11/22	17:52:28.0	XRT_FOCUS_POSITION_410_OG [0x19a]								
		XRT_FOCUS_POSITION	4	07-F8	22	fe	97	00		
2014/11/22	17:52:30.0	AOCS_ORe-point_Start_2_OG [0x098]								
		AOCU_NM	5	02-76	01	00	00	00	00	
2014/11/22	17:52:48.0	XRT_FLD_ENA_411_OG [0x19b]								
		MDP_XRT_FLD_ENA	1	07-F0	d8					
2014/11/22	17:52:50.0	XRT_FLRCTRL_ENA_412_OG [0x19c]								
		MDP_XRT_FLRCTRL_ENA	1	07-F0	c8					
2014/11/22	17:52:52.0	XRT_AEC_RESET_413_OG [0x19d]								
		MDP_XRT_AEC_RESET	1	07-F0	d0					
2014/11/22	17:52:54.0	XRT_ARS_DIS_423_OG [0x1a7]								
		MDP_XRT_ARS_DIS	1	07-F0	d5					
2014/11/22	17:52:56.0	XRT_FLD_RESET_407_OG [0x197]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2014/11/22	17:55:26.0	XRT_QT_PROG_SET_431_OG [0x1af]								
		MDP_XRT_QT_PROG_SET	2	07-F0	c4	02				
2014/11/22	17:55:28.0	XRT_FL_PROG_SET_401_OG [0x191]								
		MDP_XRT_FL_PROG_SET	2	07-F0	c5	13				
2014/11/22	17:55:30.0	XRT_CTRL_AUTO_408_OG [0x198]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2014/11/22	18:01:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/11/22	18:01:02.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/11/22	18:01:04.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2014/11/22	18:01:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2014/11/22	18:04:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2014/11/22	18:24:00.0	XRT_Custom_430_OG [0x1ae]								
2014/11/22	18:25:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2014/11/22	19:37:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/11/22	19:37:32.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/11/22	19:37:34.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2014/11/22	19:37:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2014/11/22	19:40:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2014/11/22	20:01:00.0	XRT_Custom_430_OG [0x1ae]								
2014/11/22	20:02:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2014/11/22	21:15:00.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/11/22	21:15:02.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/11/22	21:15:04.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2014/11/22	21:15:06.0	XRT_PREFLR_STRT_439_OG [0x1b7]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2014/11/22	21:18:14.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2014/11/22	21:38:00.0	XRT_Custom_430_OG [0x1ae]								
2014/11/22	21:39:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2014/11/22	22:52:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/11/22	22:52:32.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/11/22	22:52:34.0	XRT_FLD_RESET_415_OG [0x19f]								
		MDP_XRT_FLD_RESET	1	07-F0	da					
2014/11/22	22:52:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]								
		MDP_XRT_PREFLR_STRT	1	07-F0	e8					
2014/11/22	22:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]								
		MDP_XRT_PREFLR_STOP	1	07-F0	e9					
2014/11/22	23:12:30.0	XRT_Custom_430_OG [0x1ae]								
2014/11/22	23:13:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]								
		MDP_XRT_CTRL_AUTO	1	07-F0	c0					
2014/11/23	00:29:30.0	XRT_CTRL_MANU_400_OG [0x190]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/11/23	00:29:32.0	XRT_CTRL_MANU_402_OG [0x192]								
		MDP_XRT_CTRL_MANU	1	07-F0	c1					
2014/11/23	00:29:34.0	XRT_FLD_RESET_415_OG [0x19f]								

2014/11/23	00:29:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/11/23	00:32:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/11/23	00:36:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/11/23	00:37:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/11/23	01:52:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/23	01:52:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/23	01:52:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/11/23	01:52:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/11/23	01:55:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/11/23	02:12:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/11/23	02:13:00.5	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/23	03:25:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/23	03:25:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/23	03:25:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/11/23	03:25:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/11/23	03:28:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/11/23	03:49:30.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/11/23	03:50:30.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/23	04:59:30.0	XRT_CTRL_MANU_400_OG [0x190]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/23	04:59:32.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/23	04:59:34.0	XRT_FLD_RESET_415_OG [0x19f]	MDP_XRT_FLD_RESET	1	07-F0	da			
2014/11/23	04:59:36.0	XRT_PREFLR_STRT_439_OG [0x1b7]	MDP_XRT_PREFLR_STRT	1	07-F0	e8			
2014/11/23	05:02:44.0	XRT_PREFLR_STOP_419_OG [0x1a3]	MDP_XRT_PREFLR_STOP	1	07-F0	e9			
2014/11/23	05:27:00.0	XRT_Custom_430_OG [0x1ae]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/11/23	05:28:00.0	XRT_CTRL_AUTO_424_OG [0x1a8]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/23	05:55:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/23	05:55:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/23	05:55:28.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2014/11/23	05:55:30.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00			
2014/11/23	05:55:48.0	XRT_FLD_DIS_422_OG [0x1a6]	MDP_XRT_FLD_DIS	1	07-F0	d9			
2014/11/23	05:58:24.0	XRT_FLRCTRL_DIS_405_OG [0x195]	MDP_XRT_FLRCTRL_DIS	1	07-F0	c9			
2014/11/23	05:58:26.0	XRT_ARS_DIS_423_OG [0x1a7]	MDP_XRT_ARS_DIS	1	07-F0	d5			
2014/11/23	05:58:28.0	XRT_QT_PROG_SET_433_OG [0x1b1]	MDP_XRT_QT_PROG_SET	2	07-F0	c4 04			
2014/11/23	05:58:30.0	XRT_CTRL_AUTO_408_OG [0x198]	MDP_XRT_CTRL_AUTO	1	07-F0	c0			
2014/11/23	06:05:24.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/23	06:05:26.0	XRT_CTRL_MANU_402_OG [0x192]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/23	06:05:30.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00			
2014/11/23	06:05:40.0	XRT_CTRL_MANU_404_OG [0x194]	MDP_XRT_CTRL_MANU	1	07-F0	c1			
2014/11/23	06:05:50.0	XRT_FOCUS_RECALIBRATE_445_OG [0x1bd]	XRT_FOCUS_RECAL	2	07-F8	78 00			
2014/11/23	06:09:50.0	XRT_FOCUS_POSITION_403_OG [0x193]	XRT_FOCUS_POSITION	4	07-F8	22 ff aa 00			
2014/11/23	06:10:30.0	XRT_TCIB_XRT_S_HTR_A_ENA_426_OG [0x1aa]	TCIB_XRT_S_HTR_A_ENA	0	04-BC				
2014/11/23	18:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00			
2014/11/23	18:10:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00			
2014/11/24	18:00:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00			
2014/11/24	18:10:00.0	AOCS_Ore-point_Start_2_OG [0x098]	AOCU_NM	5	02-76	01 00 00 00 00			
2014/11/25	09:35:00.0	AOCS_Ore-point_Start_3_OG [0x099]	AOCU_NM	5	02-76	00 00 00 00 00			